

Structure Silicon Monolithic Integrated Circuit

Product Name 4ch constant-current driver for mobile phone

Type **BD1754HFN**

- Features
- 1) Current regulation for LED up to 4 parallels
 - 2) Adjustable constant current 64steps
 - 3) High accurate and matching of each current channel (0.5% Typ)
 - 4) Brightness control via a signal-line digital control
(Uni-Port Interface Control=UPIC)
 - 5) 2.9mm x 3.0mm HSON8 Small package

○ Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Rating	Unit	Condition
Power supply voltage	VMAX	7.0	V	
Power Dissipation	Pd	630 (*1)	mW	
Operating Temperature Range	Topr	-30 ~ +85	°C	
Storage Temperature Range	Tstr	-55 ~ +150	°C	

(*1) This value is the measurement value that was mounted on the PCB by ROHM.
(70mm x 70mm x 1.6mm glass epoxy Board)
Temperature delecting: 5.04mW/deg from Ta>25deg

○ Operating conditions (Ta=-30 °C~+85 °C)

Parameter	Symbol	Rating			Unit	Condition
		Min.	Typ.	Max.		
Power supply voltage	Vin	2.7	3.6	5.5	V	
Driver pin voltage range	VDRV	0.2	-	Vin-1.4	V	When Current driver power on

This product isn't designed to protect itself against radioactive rays.

○ Electrical Characteristics

(Unless otherwise noted, Ta = +25°C, Vin =3.6V)

Parameter	Symbol	Rating			Units	Condition
		Min.	Typ.	Max.		
Overall						
Quiescent current	Iq	-	0.1	1	μA	EN=0V
Circuit current	IDD	-	1.2	2.0	mA	Except LED current
[Current driver]						
Maximum current	I _{LED-max}	29.76	32.0	34.24	mA	R _{ISET} = 120kΩ
LED Current accuracy	I _{LED-diff}	-	-	7.0	%	When current 16.5mA setting and ISET resistance 120kΩ
LED Current matching	I _{LED-match}	-	0.5	3.0 ^{*1}	%	When current 16.5mA setting and ISET resistance 120kΩ
[Logic controller]						
Low threshold voltage	V _{IL}	-	-	0.4	V	
High threshold voltage	V _{IH}	1.4	-	-	V	
'H' level input current	I _{IH}	-	0	2	μA	EN=Vin
'L' level input current	I _{IL}	-2	0	-	μA	EN=0V
EN 'H' time	T _{HI}	0.05	-	100	μsec	
EN 'L' time	T _{LO}	0.3	-	100	μsec	
EN Off time-out	T _{OFF}	1	-	-	msec	
VIN supply → EN active time	T _{VINON}	1	-	-	msec	
EN stand-by → VBAT Off time	T _{VINOFF}	0	-	-	msec	

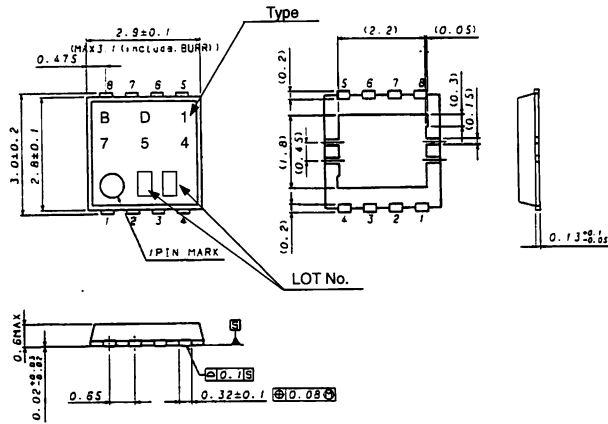
*1) The following expression is used for calculation:

$$I_{LED-match} = \{(I_{max}-I_{min})/(I_{max}+I_{min})\} \times 100$$

I_{max} = Current value in a channel with the maximum current value among all channels

I_{min} = Current value in a channel with the minimum current value among all channels

External dimensions

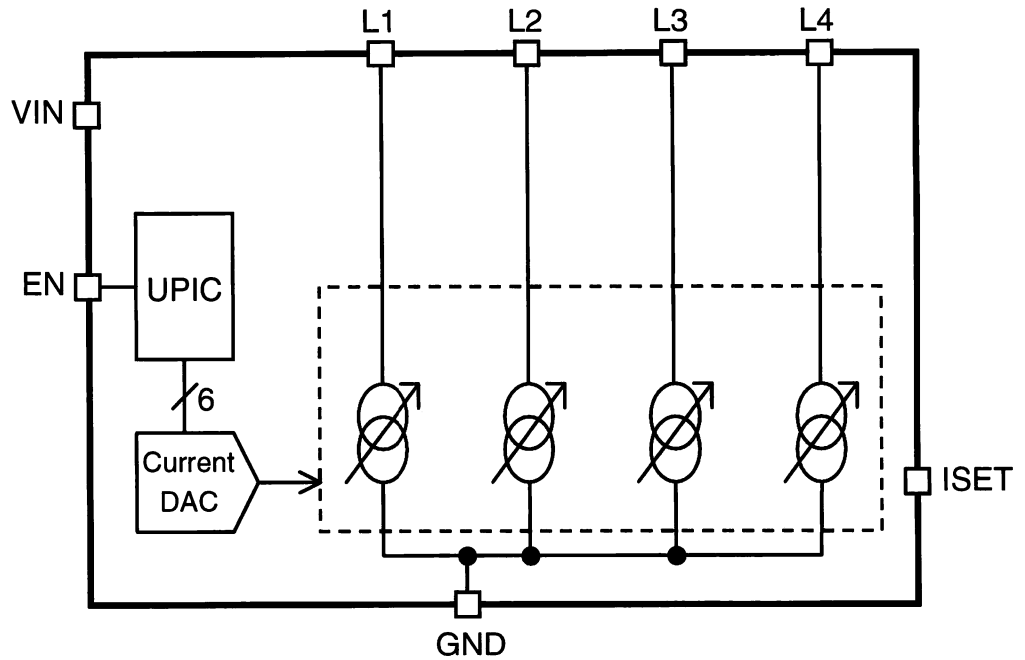


Terminals

PIN	Pin Name
1	EN
2	GND
3	ISET
4	VIN
5	L1
6	L2
7	L3
8	L4

HSON8 (8 PIN) (Unit : mm)

Block diagram



○Cautions on use

(1) Absolute Maximum Ratings

An excess in the absolute maximum ratings, such as supply voltage, temperature range of operating conditions, etc., can break down devices, thus making impossible to identify breaking mode such as a short circuit or an open circuit. If any special mode exceeding the absolute maximum ratings is assumed, consideration should be given to take physical safety measures including the use of fuses, etc.

(2) Power supply and GND line

Design PCB pattern to provide low impedance for the wiring between the power supply and the GND lines. Pay attention to the interference by common impedance of layout pattern when there are plural power supplies and GND lines. Especially, when there are GND pattern for small signal and GND pattern for large current included the external circuits, please separate each GND pattern. Furthermore, for all power supply terminals to ICs, mount a capacitor between the power supply and the GND terminal. At the same time, in order to use a capacitor, thoroughly check to be sure the characteristics of the capacitor to be used present no problem including the occurrence of capacity dropout at a low temperature, thus determining the constant.

(3) GND voltage

Make setting of the potential of the GND terminal so that it will be maintained at the minimum in any operating state. Furthermore, check to be sure no terminals are at a potential lower than the GND voltage including an actual electric transient.

(4) Short circuit between terminals and erroneous mounting

In order to mount ICs on a set PCB, pay thorough attention to the direction and offset of the ICs. Erroneous mounting can break down the ICs. Furthermore, if a short circuit occurs due to foreign matters entering between terminals or between the terminal and the power supply or the GND terminal, the ICs can break down.

(5) Operation in strong electromagnetic field

Be noted that using ICs in the strong electromagnetic field can malfunction them.

(6) Input terminals

In terms of the construction of IC, parasitic elements are inevitably formed in relation to potential. The operation of the parasitic element can cause interference with circuit operation, thus resulting in a malfunction and then breakdown of the input terminal. Therefore, pay thorough attention not to handle the input terminals, such as to apply to the input terminals a voltage lower than the GND respectively, so that any parasitic element will operate. Furthermore, do not apply a voltage to the input terminals when no power supply voltage is applied to the IC. In addition, even if the power supply voltage is applied, apply to the input terminals a voltage lower than the power supply voltage or within the guaranteed value of electrical characteristics.

(7) External capacitor

In order to use a ceramic capacitor as the external capacitor, determine the constant with consideration given to a degradation in the nominal capacitance due to DC bias and changes in the capacitance due to temperature, etc.

(8) Thermal design

Perform thermal design in which there are adequate margins by taking into account the permissible dissipation (P_d) in actual states of use.

(9) Other cautions on use

Please consult supplementary documents such as technical notebook, function manual and application design guide of this LSI.

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