Kapalua Schematic

REV : X02

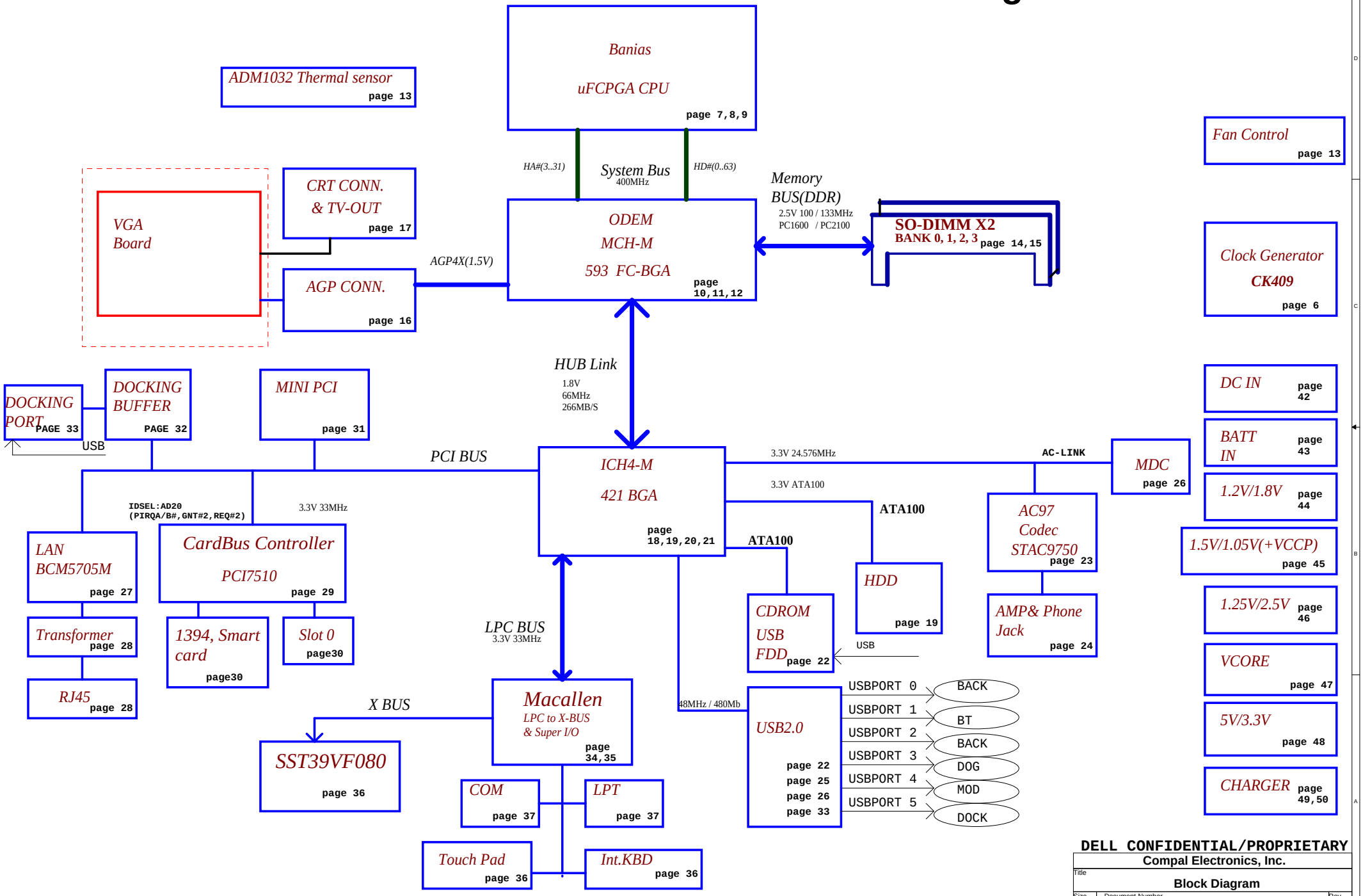
BDQ11/LA1601 Schematic with Capture CIS and Function field
uFCPGA Banias
12-27-2002
REV: 0.4

```
@ : Depop Component
1@ : Depop Component
2@ : Pop Component
```


Compal Electronics, Inc.

Title	Cover Sheet	
Size	Document Number: BDQ11/LA-1601	Rev: 0.4
Date: 星期五, 十二月 27, 2002	Sheet 1 of 56	

Block Diagram



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Title	Block Diagram
Size	Document Number: BDQ11/LA-1601
Rev	0.4
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PM TABLE

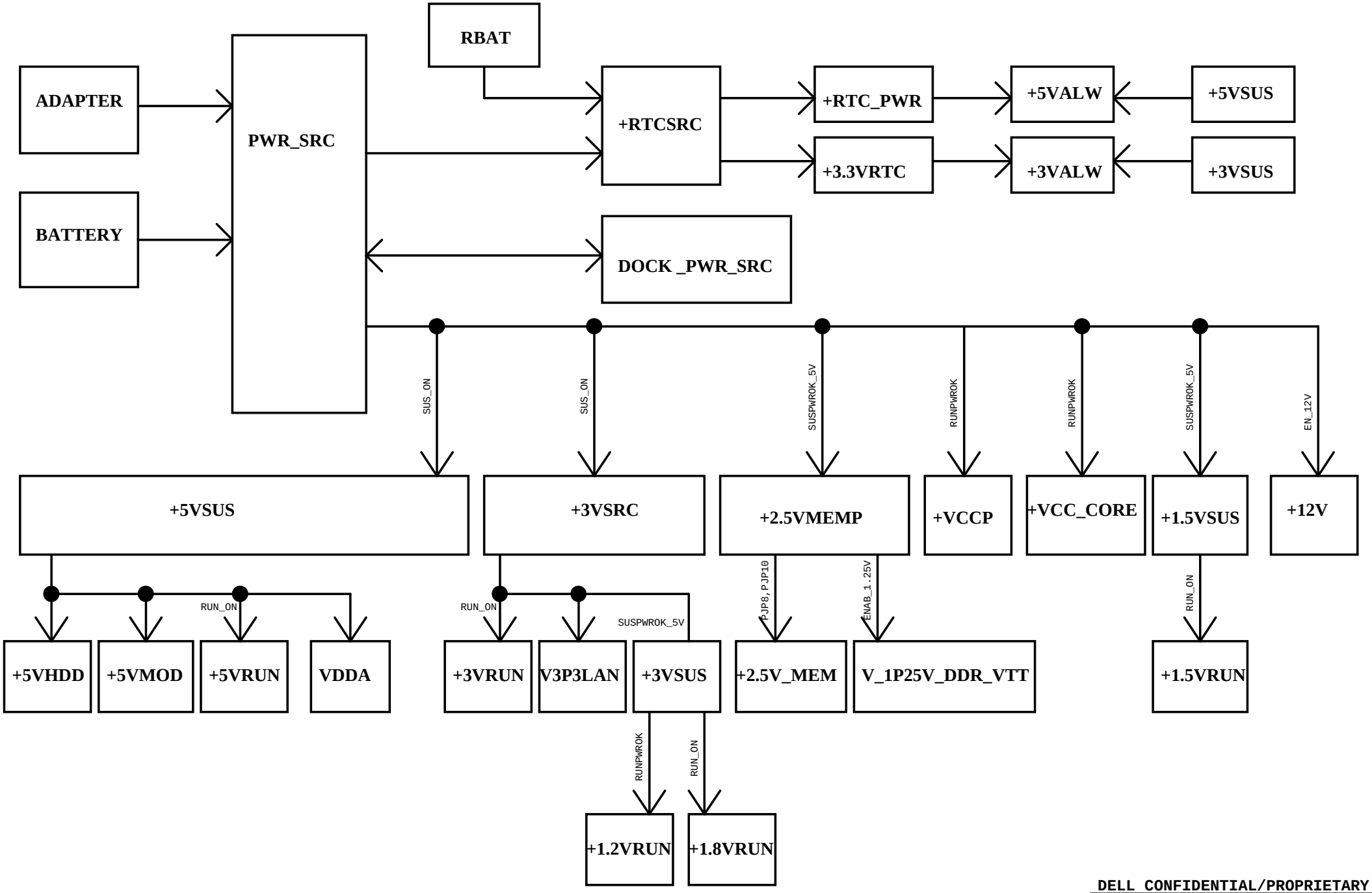
State \ power plane	+3VALW +5VALW	+3VSUS +5VSUS +2.5V_MEM +1.5VSUS	+5VRUN +3VRUN +1.8VRUN +1.5VRUN +1.2VRUN +VCC_CORE +VCCP +12V
S0	ON	ON	ON
S1	ON	ON	ON
S3	ON	ON	OFF
S5 S4/AC	ON	OFF	OFF
S5 S4/AC don't exist	OFF	OFF	OFF

PCI TABLE

PCI DEVICE	IDSEL	REQ#/GNT#	PIRQ
CARD BUS	AD17	1	D,C
LAN	AD16	4	C
DOCK	AD24	0	B
MINI PCI	AD19	3	D,B(NP)
VGA			A,B(NP)

USB TABLE

USB PORT#	DESTINATION
0	BACK
1	BLUETOOTH
2	BACK
3	DOG
4	MOD
5	DOCK

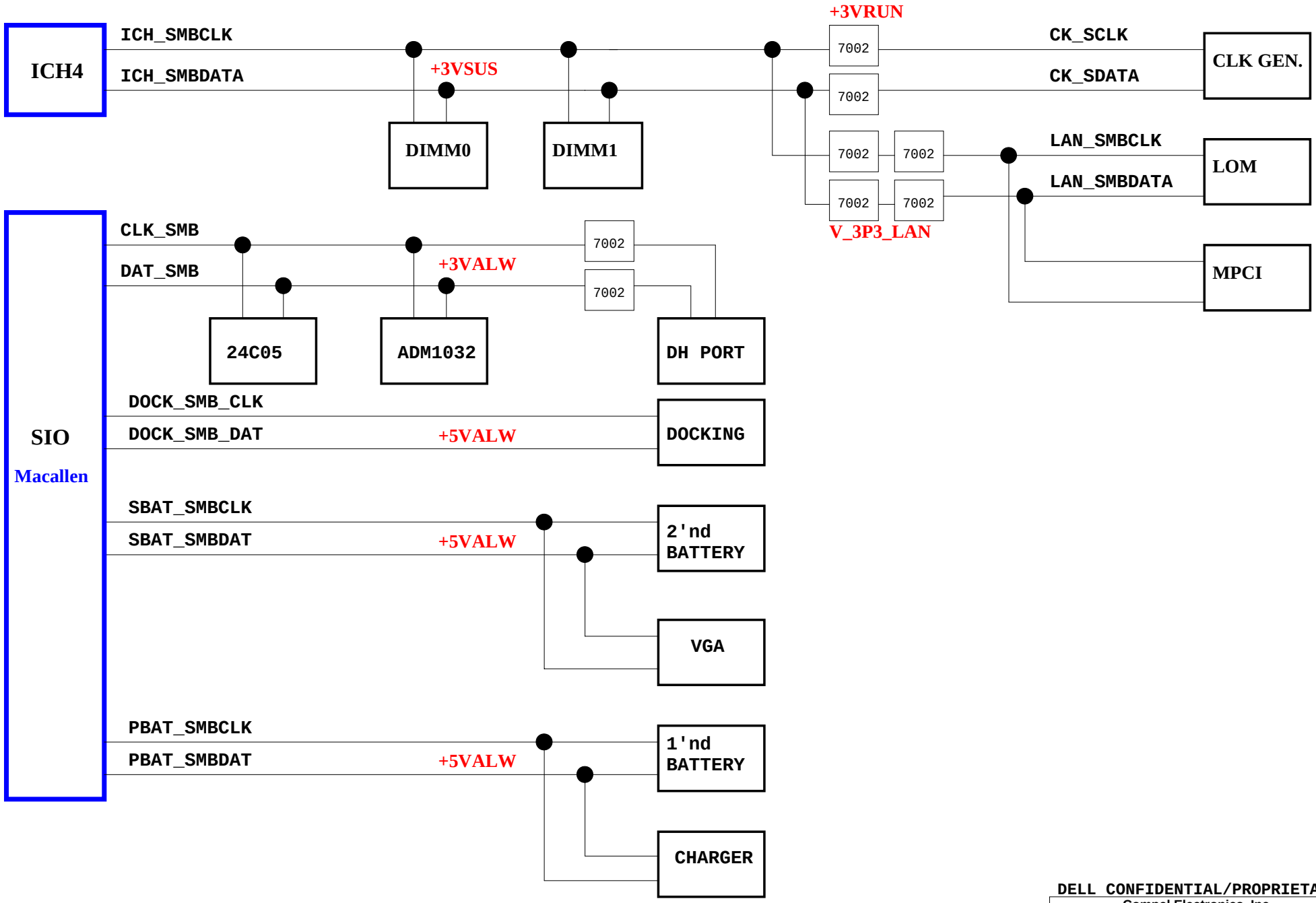


Compal Electronics, Inc.

Title: Power Rail

Size | Document Number: BDQ11/LA-1601 | Rev: 0.4

Date: 星期五, 十二月 27, 2002 | Sheet 4 of 56



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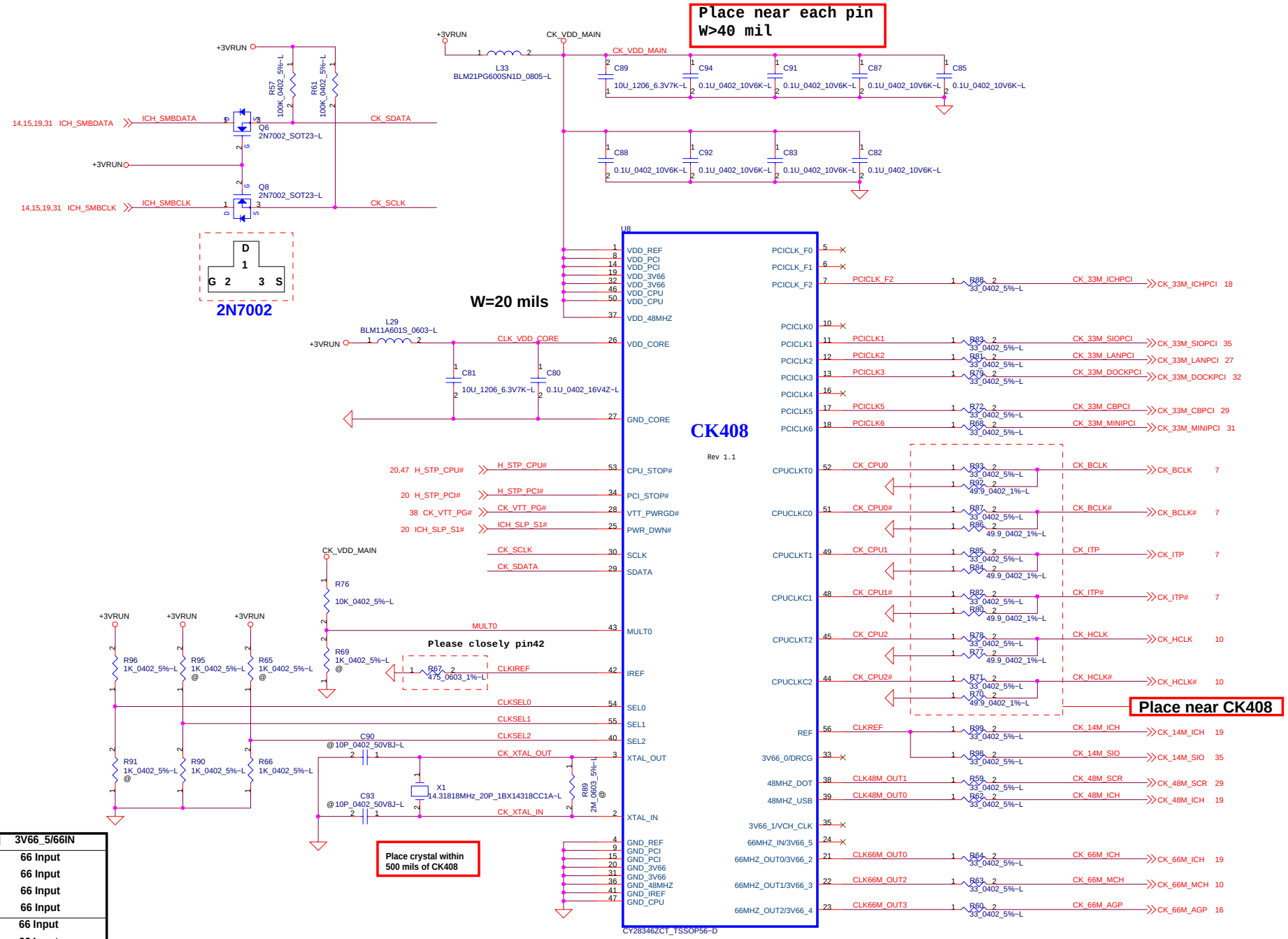
Title: SMBUS TOPOLOGY

Size | Document Number: BDQ11/LA-1601 | Rev: 0.4

Date: 星期五, 十二月 27, 2002 | Sheet 5 of 56



S2	S1	S0	CPU	3V66[0..4]	3V66_5/66IN
1	0	0	66	66IN	66 Input
1	0	1	100	66IN	66 Input
1	1	0	200	66IN	66 Input
1	1	1	133	66IN	66 Input
0	0	0	66	66	66 Input
0	0	1	100	66	66 Input
0	1	0	200	66	66 Input
0	1	1	133	66	66 Input
Mid	0	0	Hi-z	Hi-z	Hi-z
Mid	0	1	TCLK/2	TCLK/2	TCLK/2
Mid	1	0	Reserve	Reserve	Reserve
Mid	1	1	Reserve	Reserve	Reserve

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Title	Clock Generator	
Size	Document Number: BDQ11/LA-1601	Rev: 0.4
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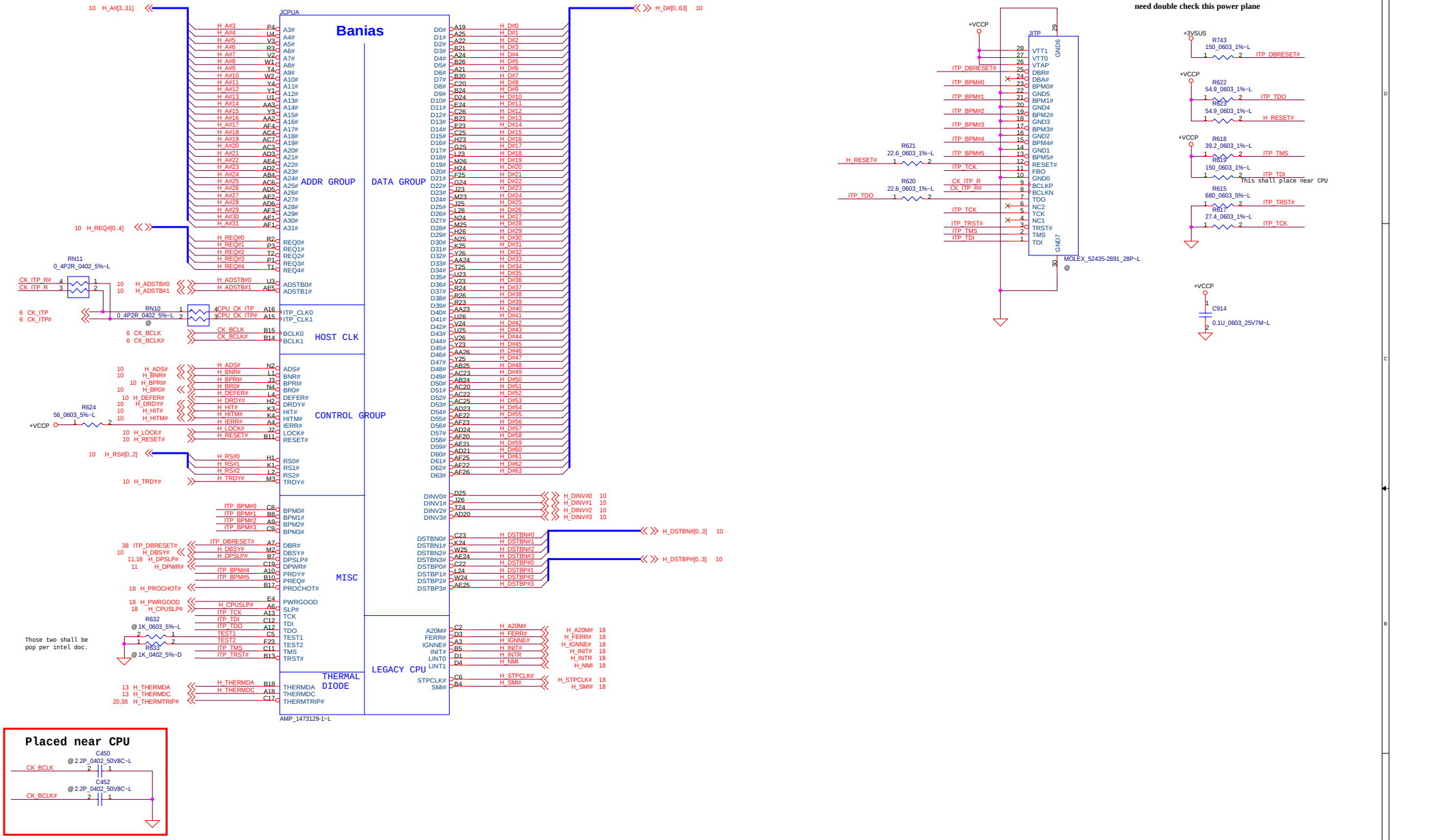

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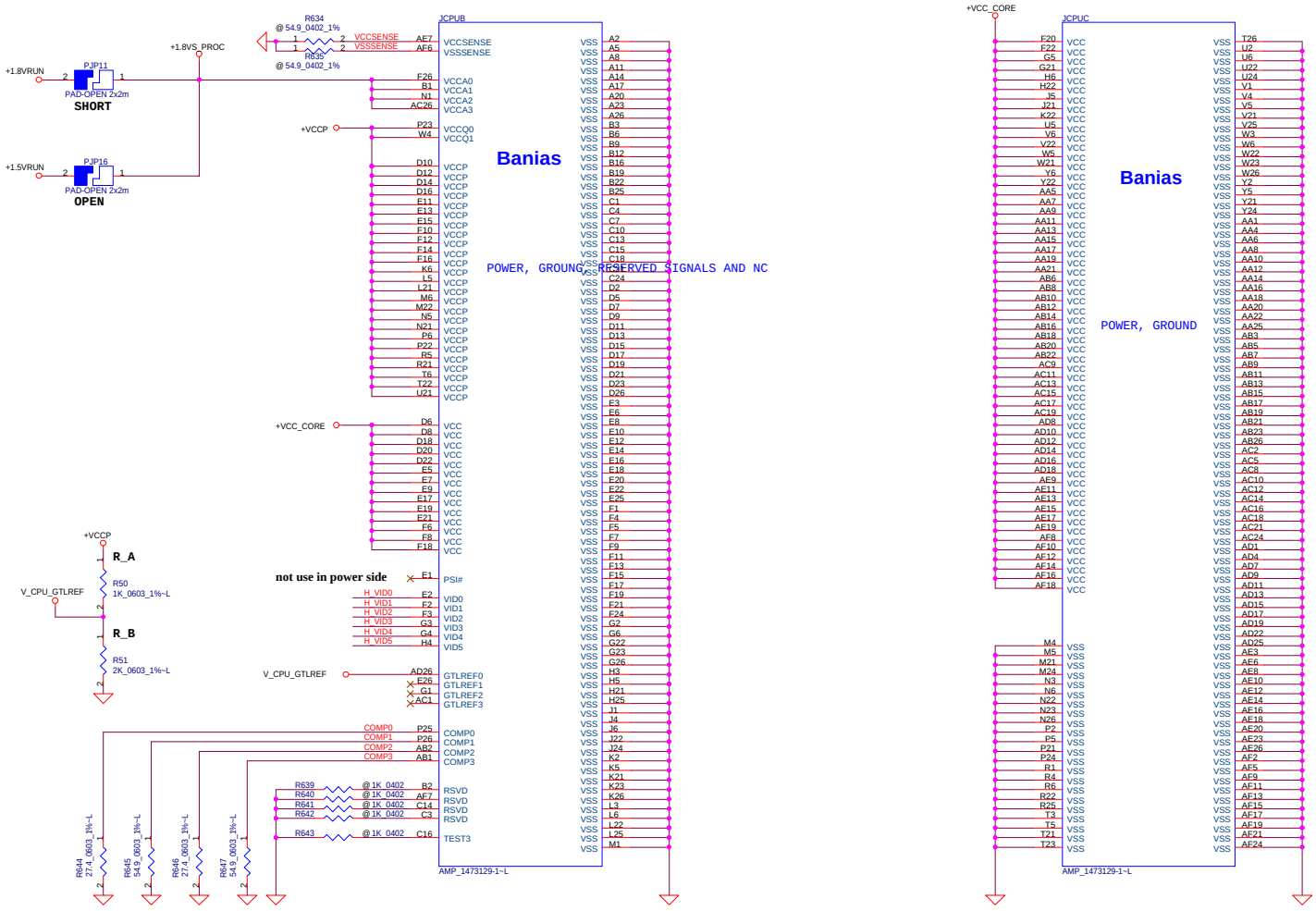
Compal Electronics, Inc.

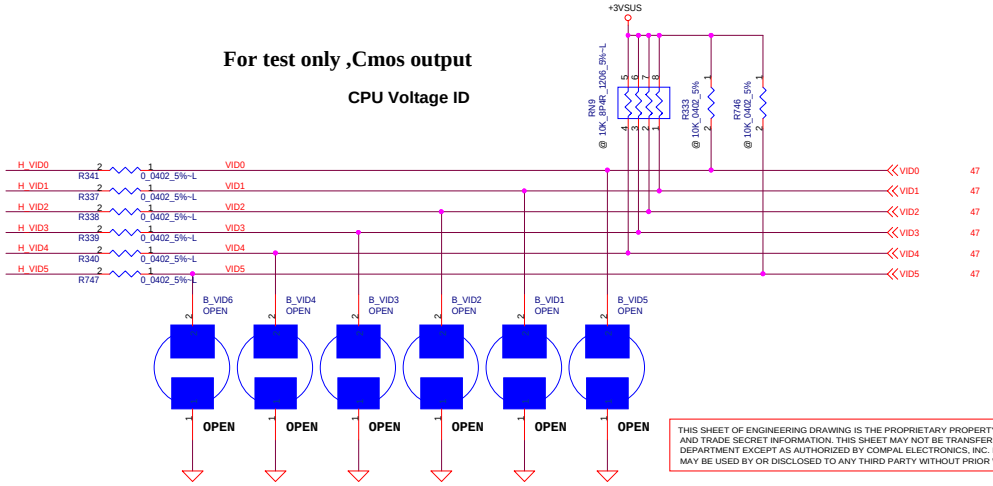
Title	Banias Processor in mFCPGA479		
Size: C	Document Number: BDQ11/LA-1601		Rev: 0.4
Date: 星期五, 十二月 27, 2002	Sheet 7 of 56		



Resistor placed within 0.5" of CPU pin.Trace should be at least 25 miles away from any other toggling signal.

For test only ,Cmos output

CPU Voltage ID



DELL CONFIDENTIAL/PROPRIETARY

Compal Electronics, Inc.

Title: Banias Processor in mFCPGA479		
Size: C	Document Number: BDQ11/LA-1601	Rev: 0.4
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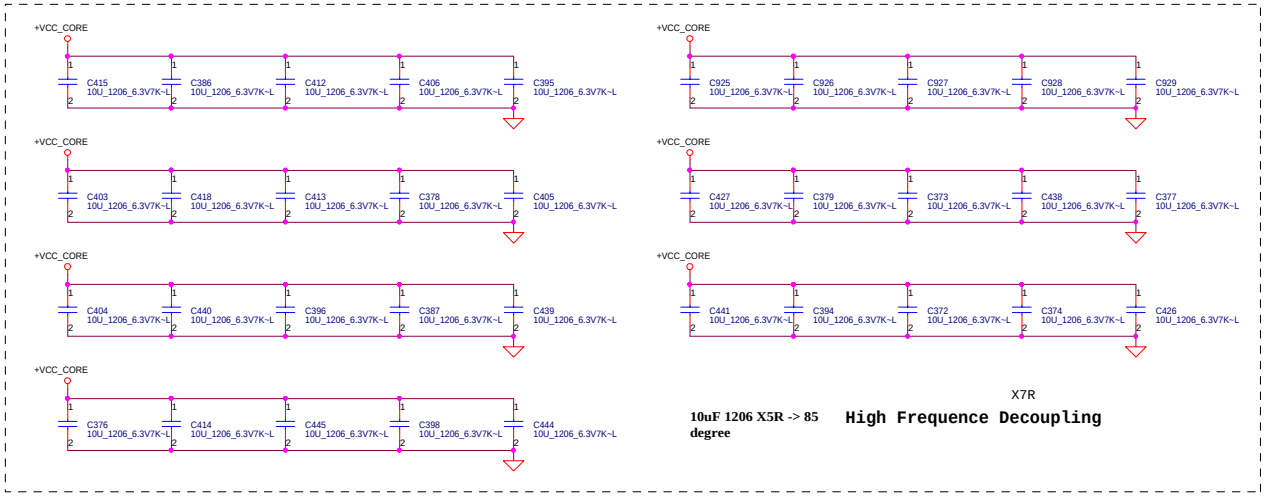

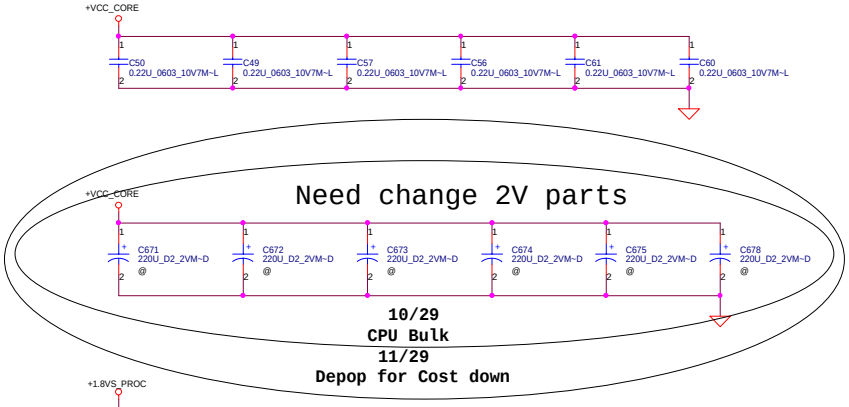

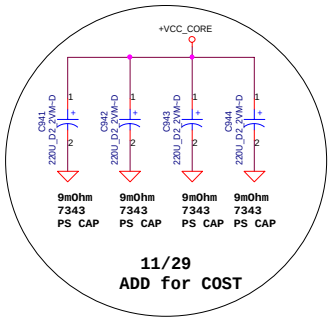

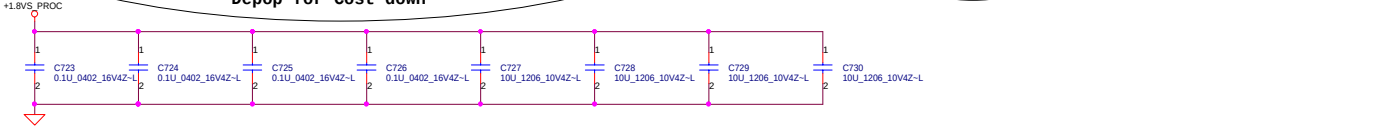

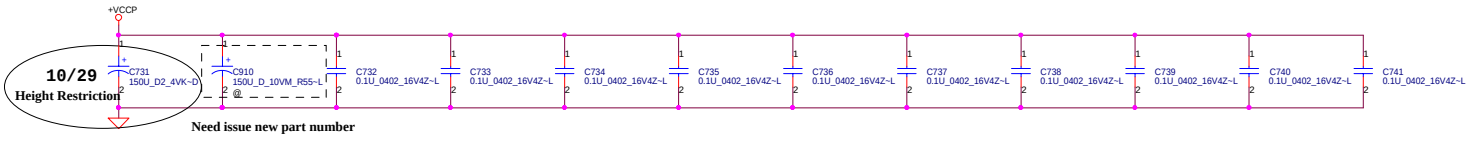

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Title	CPU Bypass	
Size C	Document Number BDQ11/LA-1601	Rev 0.4
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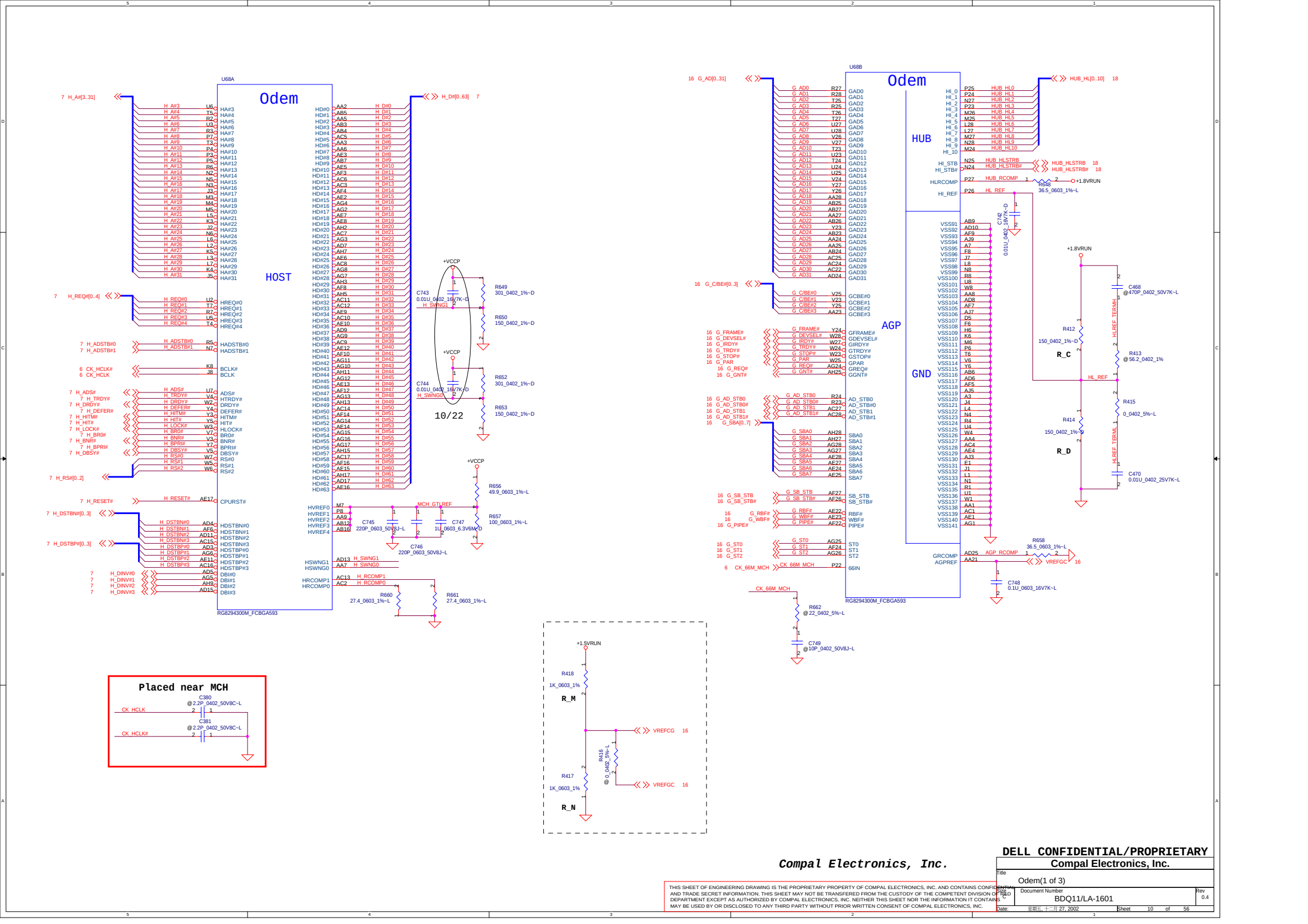

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All of thsoe schematic shall be changed to
un-pop

NEED CHECK THIS POWER
PLANE

NEED CHECK THIS POWER
PLANE

+12V

+2.5V_MEM

U69A
@ LM358M_SO8~L

DDR_VREF_MCH

C751
@10U_1206_10V4Z

R664
@10K_0603_1%~L

R666
@10K_0603_1%~L

C750
@0.1U_0603_16V7K~L

R760
@0_0402_5%~D

U68C

Odem

MEMORY

RG8294300M_FCBGA593

14,15 DDR_MA[0..12]

DDR_MA0 DDR_MA1 DDR_MA2 DDR_MA3 DDR_MA4 DDR_MA5 DDR_MA6 DDR_MA7 DDR_MA8 DDR_MA9 DDR_MA10 DDR_MA11 DDR_MA12

SMA0 SMA1 SMA2 SMA3 SMA4 SMA5 SMA6 SMA7 SMA8 SMA9 SMA10 SMA11 SMA12 RSVD2

T281
PAD

14 DDR_DS[0..8]

DDR_DS5 DDR_DS4 DDR_DS6 DDR_DS7 DDR_DS3 DDR_DS2 DDR_DS1 DDR_DS0 DDR_DS8

SDQS0 SDQS1 SDQS2 SDQS3 SDQS4 SDQS5 SDQS6 SDQS7 SDQS8

14,15 DDR_WE#
14,15 DDR_RAS#
14,15 DDR_CAS#

DDR_WE# DDR_RAS# DDR_CAS#

SWE# SRAS# SCAS#

14 CK_DDR_CK0
14 CK_DDR_CK0#
14 CK_DDR_CK1
14 CK_DDR_CK1#
14 CK_DDR_CK2
14 CK_DDR_CK2#
15 CK_DDR_CK3
15 CK_DDR_CK3#
15 CK_DDR_CK4
15 CK_DDR_CK4#
15 CK_DDR_CK5
15 CK_DDR_CK5#

CK_DDR_CK0 CK_DDR_CK0# CK_DDR_CK1 CK_DDR_CK1# CK_DDR_CK2 CK_DDR_CK2# CK_DDR_CK3 CK_DDR_CK3# CK_DDR_CK4 CK_DDR_CK4# CK_DDR_CK5 CK_DDR_CK5#

SCK0 SCK#0 SCK1 SCK#1 SCK2 SCK#2 SCK3 SCK#3 SCK4 SCK#4 SCK5 SCK#5

14 DDR_CKE0_DIMM0
14 DDR_CKE1_DIMM0
15 DDR_CKE2_DIMM1
15 DDR_CKE3_DIMM1

DDR_CKE0_DIMM0 DDR_CKE1_DIMM0 DDR_CKE2_DIMM1 DDR_CKE3_DIMM1

SCKE0 SCKE1 SCKE2 SCKE3

14 DDR_CS0_DIMM0#
14 DDR_CS1_DIMM0#
15 DDR_CS2_DIMM1#
15 DDR_CS3_DIMM1#

DDR_CS0_DIMM0# DDR_CS1_DIMM0# DDR_CS2_DIMM1# DDR_CS3_DIMM1#

SCS#0 SCS#1 SCS#2 SCS#3

14,15 DDR_BS0
14,15 DDR_BS1

DDR_BS0 DDR_BS1

SBS0 SBS1

R748
0_0603_5%~L

14,15,46 V_DDR_MCH_REF

DDR_VREF_MCH

SMVREF0 SMVREF1

V_1P25V_DDR_VTT

R667
30.1_0603_1%~L

DDR_RCOMP

100mils

SMRCOMP

C752
0.1U_0603_16V7K~L

C753
0.1U_0603_16V7K~L

C754
0.1U_0603_16V7K~L

M_RCV#

RCVENIN#

RCVENOUT#

T5
PAD

7,18 H_DPSLP#
7 H_DPWR#

DPSLP# DPWR#

NC0 NC1

T6
PAD

RSTIN#
RSVD1
TESTIN#

PCIRST_1# 18,27

MCH_TEST#

+1.5VRUN

R668
@4.7K_0603_5%~L

T282
PAD

DDR_D[0..63] 14

DDR_CB[0..7] 14

Title: Odem(2 of 3)

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change to .01u 7/22

10/29
Height
Restriction

10/29
Height
Restriction

need have 2.2u,220n,47n,22n,15n,10n
need creat OEM part number

Need to add

0402

0402

Odem

POWER

GND

one .01uf and 10
uf each two pins

internal pull up

MMBT3904_SOT23
MMBT3906_SOT23

Transistoe
illustration

Odem 1.5V Rail Leakage
Current Issue Solution

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Title	Odem(3 of 3)		
Size C	Document Number BDQ11/LA-1601		Rev 0.4
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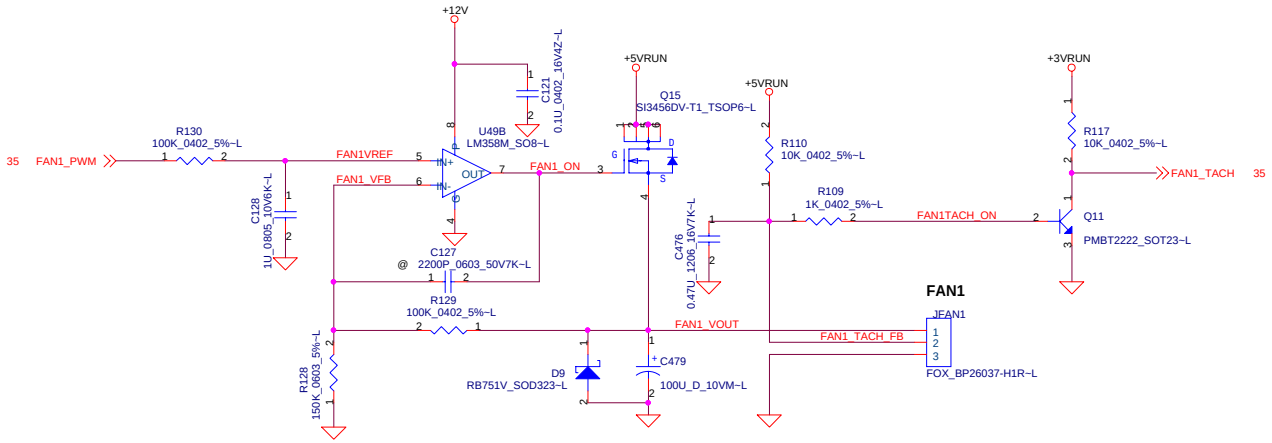
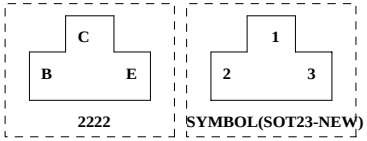
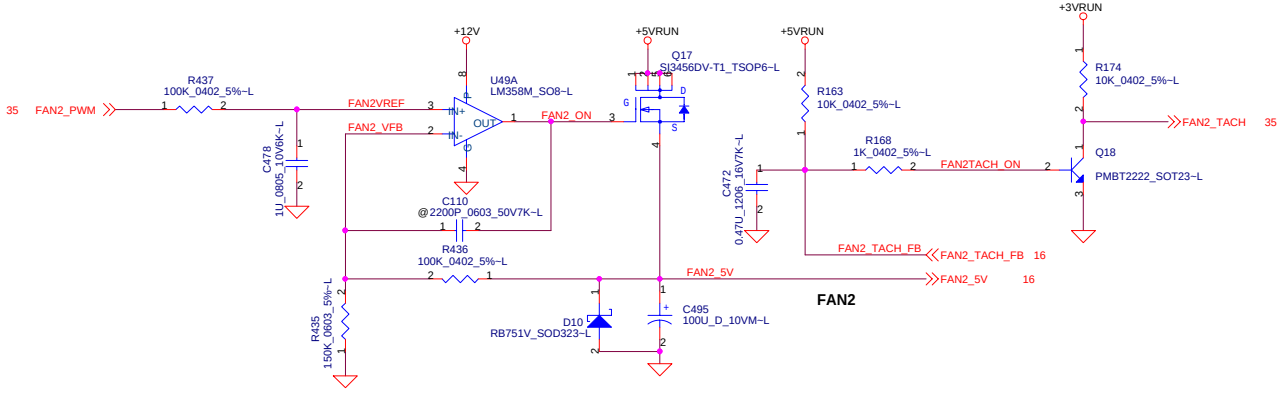
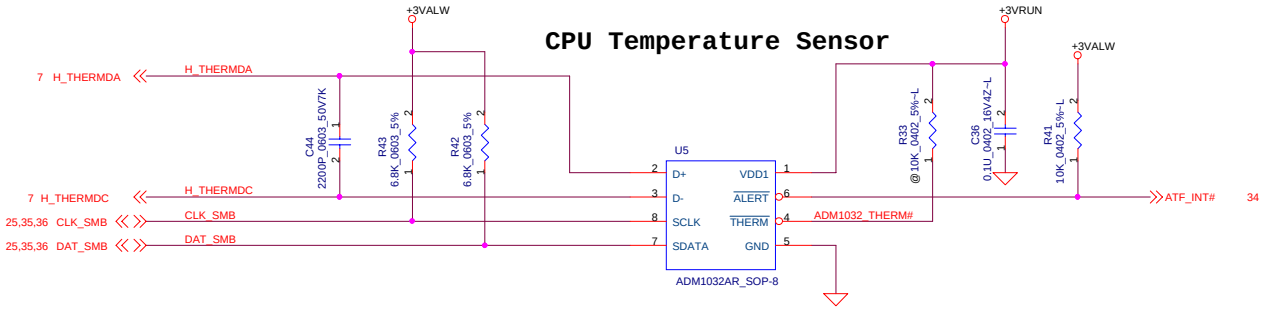
FAN1 Control and Tachometer







CPU Temperature Sensor



Compal Electronics, Inc.	
Title	ITP Debug CONN. & FAN
Size	Document Number: BDQ11/LA-1601 — Rev: 0.4
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11 DDR_D[0..63]
11 DDR_CB[0..7]
11 DDR_DS[0..8]

Layout Note:
Place these resistor closely DIMM0,all trace length<750 mil

RN20 DDR_CB4 DDR_CB0 — DDR_CB4_R DDR_CB0_R — 10_4P2R_0402_5%~L
RN40 DDR_D31 DDR_D27 — DDR_D31_R DDR_D27_R — 10_4P2R_0402_5%~L
RN19 DDR_D30 DDR_D26 — DDR_D30_R DDR_D26_R — 10_4P2R_0402_5%~L
RN39 DDR_DS3 DDR_D29 — DDR_DS3_R DDR_D29_R — 10_4P2R_0402_5%~L
RN18 DDR_D25 DDR_D28 — DDR_D25_R DDR_D28_R — 10_4P2R_0402_5%~L
RN38 DDR_D24 DDR_D23 — DDR_D24_R DDR_D23_R — 10_4P2R_0402_5%~L
RN17 DDR_D19 DDR_D22 — DDR_D19_R DDR_D22_R — 10_4P2R_0402_5%~L
RN37 DDR_D18 DDR_DS2 — DDR_D18_R DDR_DS2_R — 10_4P2R_0402_5%~L
RN16 DDR_D21 DDR_D17 — DDR_D21_R DDR_D17_R — 10_4P2R_0402_5%~L
RN36 DDR_D20 DDR_D16 — DDR_D20_R DDR_D16_R — 10_4P2R_0402_5%~L
RN35 DDR_D15 DDR_D11 — DDR_D15_R DDR_D11_R — 10_4P2R_0402_5%~L
RN15 DDR_D14 DDR_D10 — DDR_D14_R DDR_D10_R — 10_4P2R_0402_5%~L
RN34 DDR_DS1 DDR_D13 — DDR_DS1_R DDR_D13_R — 10_4P2R_0402_5%~L
RN14 DDR_D9 DDR_D12 — DDR_D9_R DDR_D12_R — 10_4P2R_0402_5%~L
RN33 DDR_D8 DDR_D7 — DDR_D8_R DDR_D7_R — 10_4P2R_0402_5%~L
RN13 DDR_D3 DDR_D6 — DDR_D3_R DDR_D6_R — 10_4P2R_0402_5%~L
RN32 DDR_D2 DDR_DS0 — DDR_D2_R DDR_DS0_R — 10_4P2R_0402_5%~L
RN12 DDR_D5 DDR_D1 — DDR_D5_R DDR_D1_R — 10_4P2R_0402_5%~L
RN31 DDR_D4 DDR_D0 — DDR_D4_R DDR_D0_R — 10_4P2R_0402_5%~L
RN22 DDR_CB7 DDR_CB3 — DDR_CB7_R DDR_CB3_R — 10_4P2R_0402_5%~L
RN42 DDR_CB6 — DDR_CB6_R — 10_4P2R_0402_5%~L
RN21 DDR_CB2 DDR_DS8 — DDR_CB2_R DDR_DS8_R — 10_4P2R_0402_5%~L
RN41 DDR_CB5 DDR_CB1 — DDR_CB5_R DDR_CB1_R — 10_4P2R_0402_5%~L
RN57 DDR_D63 DDR_D59 — DDR_D63_R DDR_D59_R — 10_4P2R_0402_5%~L
RN30 DDR_D62 DDR_D58 — DDR_D62_R DDR_D58_R — 10_4P2R_0402_5%~L
RN56 DDR_DS7 DDR_D61 — DDR_DS7_R DDR_D61_R — 10_4P2R_0402_5%~L

RN29 DDR_D57 DDR_D60 — DDR_D57_R DDR_D60_R — 10_4P2R_0402_5%~L
RN55 DDR_D56 DDR_D55 — DDR_D56_R DDR_D55_R — 10_4P2R_0402_5%~L
RN28 DDR_D51 DDR_D54 — DDR_D51_R DDR_D54_R — 10_4P2R_0402_5%~L
RN54 DDR_D50 DDR_DS6 — DDR_D50_R DDR_DS6_R — 10_4P2R_0402_5%~L
RN27 DDR_D53 DDR_D49 — DDR_D53_R DDR_D49_R — 10_4P2R_0402_5%~L
RN53 DDR_D52 DDR_D48 — DDR_D52_R DDR_D48_R — 10_4P2R_0402_5%~L
RN52 DDR_D47 DDR_D43 — DDR_D47_R DDR_D43_R — 10_4P2R_0402_5%~L
RN26 DDR_D46 DDR_D42 — DDR_D46_R DDR_D42_R — 10_4P2R_0402_5%~L
RN51 DDR_DS5 DDR_D45 — DDR_DS5_R DDR_D45_R — 10_4P2R_0402_5%~L
RN25 DDR_D41 DDR_D44 — DDR_D41_R DDR_D44_R — 10_4P2R_0402_5%~L
RN50 DDR_D40 DDR_D39 — DDR_D40_R DDR_D39_R — 10_4P2R_0402_5%~L
RN24 DDR_D35 DDR_D38 — DDR_D35_R DDR_D38_R — 10_4P2R_0402_5%~L
RN49 DDR_D34 DDR_DS4 — DDR_D34_R DDR_DS4_R — 10_4P2R_0402_5%~L
RN23 DDR_D37 DDR_D33 — DDR_D37_R DDR_D33_R — 10_4P2R_0402_5%~L
RN48 DDR_D36 DDR_D32 — DDR_D36_R DDR_D32_R — 10_4P2R_0402_5%~L

Layout Note:
Place these resistor closely DIMM0,all trace length Max=1.3"

V_1P25V_DDR_VTT
RN75 — DDR_CKE0_DIMM0 DDR_CKE1_DIMM0 — 56_4P2R_0402_5%~L
RN81 — DDR_CS0_DIMM0# DDR_CS1_DIMM0# — 56_4P2R_0402_5%~L

11,15 DDR_MA[0..12]

Layout Note:
Place these resistor closely DIMM0,all trace length<750 mil

11,15 DDR_BS0 DDR_WE# — RN61 DDR_BS0_R DDR_WE_R# — 10_4P2R_0402_5%~L
11,15 DDR_CAS# DDR_RAS# — RN47 DDR_CAS_R# DDR_RAS_R# — 10_4P2R_0402_5%~L
11,15 DDR_BS1 — RN46 DDR_BS1_R DDR_MA0_R — 10_4P2R_0402_5%~L
RN60 DDR_MA1 DDR_MA10 — DDR_MA1_R DDR_MA10_R — 10_4P2R_0402_5%~L
RN45 DDR_MA2 DDR_MA4 — DDR_MA2_R DDR_MA4_R — 10_4P2R_0402_5%~L
RN62 DDR_MA5 DDR_MA3 — DDR_MA5_R DDR_MA3_R — 10_4P2R_0402_5%~L
RN44 DDR_MA6 DDR_MA8 — DDR_MA6_R DDR_MA8_R — 10_4P2R_0402_5%~L
RN59 DDR_MA9 DDR_MA7 — DDR_MA9_R DDR_MA7_R — 10_4P2R_0402_5%~L
RN43 DDR_MA11 — DDR_MA11_R — 10_4P2R_0402_5%~L
RN58 DDR_MA12 — DDR_MA12_R — 10_4P2R_0402_5%~L

+2.5V_MEM
+2.5V_MEM C114 0.1U_0402_16V4Z~L
V_DDR_MCH_REF — V_DDR_MCH_REF 11,15,46
C98 0.1U_0402_16V4Z~L

JDIM1 — AMP_1565917-1~L

	VREF	VREF	
DDR_D0_R DDR_D1_R	VSS	VSS	DDR_D4_R DDR_D5_R
	DQ0	DQ4	
	DQ1	DQ5	
	VDD	VDD	
DDR_DS0_R DDR_D2_R	DQS0	DM0	DDR_D6_R
	DQ2	DQ6	
	VSS	VSS	
DDR_D3_R DDR_D8_R	DQ3	DQ7	DDR_D7_R DDR_D12_R
	DQ8	DQ12	
	VDD	VDD	
DDR_D9_R DDR_DS1_R	DQ9	DQ13	DDR_D13_R
	DQS1	DM1	
	VSS	VSS	
DDR_D10_R DDR_D11_R	DQ10	DQ14	DDR_D14_R DDR_D15_R
	DQ11	DQ15	
	VDD	VDD	
11 CK_DDR_CK1 / CK_DDR_CK1 — 11 CK_DDR_CK1# / CK_DDR_CK1#	CK0	VDD	
	CK0#	VSS	
	VSS		
DDR_D16_R DDR_D17_R	DQ16	DQ20	DDR_D20_R DDR_D21_R
	DQ17	DQ21	
	VDD	VDD	
DDR_DS2_R DDR_D18_R	DQS2	DM2	DDR_D22_R
	DQ18	DQ22	
	VSS	VSS	
DDR_D19_R DDR_D24_R	DQ19	DQ23	DDR_D23_R DDR_D28_R
	DQ24	DQ28	
	VDD	VDD	
DDR_D25_R DDR_DS3_R	DQ25	DQ29	DDR_D29_R
	DQS3	DM3	
	VSS	VSS	
DDR_D26_R DDR_D27_R	DQ26	DQ30	DDR_D30_R DDR_D31_R
	DQ27	DQ31	
	VDD	VDD	
DDR_CB0_R DDR_CB1_R	CB0	CB4	DDR_CB4_R DDR_CB5_R
	CB1	CB5	
	VSS	VSS	
DDR_DS8_R DDR_CB2_R	DQS8	DM8	DDR_CB6_R
	CB2	CB6	
	VDD	VDD	
DDR_CB3_R	CB3	CB7	DDR_CB7_R
	DU	DU/RESET#	
	VSS	VSS	
11 CK_DDR_CK2 / CK_DDR_CK2#	CK2	VSS	
	CK2#	VDD	
	VDD	VDD	DDR_CKE0_DIMM0 — DDR_CKE0_DIMM0 11
11 DDR_CKE1_DIMM0 — DDR_CKE1_DIMM0	CKE1	CKE0	
	DU/A13	DU/BA2	
DDR_MA12_R DDR_MA9_R	A12	A11	DDR_MA11_R DDR_MA8_R
	A9	A8	
	VSS	VSS	
DDR_MA7_R DDR_MA5_R DDR_MA3_R DDR_MA1_R	A7	A6	DDR_MA6_R DDR_MA4_R DDR_MA2_R DDR_MA0_R
	A5	A4	
	A3	A2	
	A1	A0	
	VDD	VDD	
DDR_MA10_R DDR_BS0_R DDR_WE_R# DDR_CS0_DIMM0# — 11 DDR_CS0_DIMM0#	A10/AP	BA1	DDR_BS1_R DDR_RAS_R# DDR_CAS_R# DDR_CS1_DIMM0# — DDR_CS1_DIMM0# 11
	BA0	RAS#	
	WE#	CAS#	
	S0#	S1#	
	DU	DU	
	VSS	VSS	
DDR_D32_R DDR_D33_R	DQ32	DQ36	DDR_D36_R DDR_D37_R
	DQ33	DQ37	
	VDD	VDD	
DDR_DS4_R DDR_D34_R	DQS4	DM4	
	DQ34	DQ38	DDR_D38_R
	VSS	VSS	
DDR_D35_R DDR_D40_R	DQ35	DQ39	DDR_D39_R DDR_D44_R
	DQ40	DQ44	
	VDD	VDD	
DDR_D41_R DDR_DS5_R	DQ41	DQ45	DDR_D45_R
	DQS5	DM5	
	VSS	VSS	
DDR_D42_R DDR_D43_R	DQ42	DQ46	DDR_D46_R DDR_D47_R
	DQ43	DQ47	
	VDD	VDD	
	CK1#	VDD	CK_DDR_CK0# — CK_DDR_CK0# 11
	CK1	CK_DDR_CK0	CK_DDR_CK0 11
	VSS	VSS	
DDR_D48_R DDR_D49_R	DQ48	DQ52	DDR_D52_R DDR_D53_R
	DQ49	DQ53	
	VDD	VDD	
DDR_DS6_R DDR_D50_R	DQS6	DM6	
	DQ50	DQ54	DDR_D54_R
	VSS	VSS	
DDR_D51_R DDR_D56_R	DQ51	DQ55	DDR_D55_R DDR_D60_R
	DQ56	DQ60	
	VDD	VDD	
DDR_D57_R DDR_DS7_R	DQ57	DQ61	DDR_D61_R
	DQS7	DM7	
	VSS	VSS	
DDR_D58_R DDR_D59_R	DQ58	DQ62	DDR_D62_R DDR_D63_R
	DQ59	DQ63	
	VDD	VDD	
6,15,19,31 ICH_SMBDATA / 6,15,19,31 ICH_SMBCLK	SDA	SA0	
	SCL	SA1	
+3VSUS	VDD_SPD	SA2	
	VDD_ID	DU	

+2.5V_MEM
R119 — 1 — 2 — DIMM0_ID
10K_0402_5%~L

DIMM0 STANDARD

DDR_D0_R DDR_D1_R DDR_D2_R DDR_D3_R DDR_D4_R DDR_D5_R DDR_D6_R DDR_D7_R DDR_D8_R DDR_D9_R DDR_D10_R DDR_D11_R DDR_D12_R DDR_D13_R DDR_D14_R DDR_D15_R DDR_D16_R DDR_D17_R DDR_D18_R DDR_D19_R DDR_D20_R DDR_D21_R DDR_D22_R DDR_D23_R DDR_D24_R DDR_D25_R DDR_D26_R DDR_D27_R DDR_D28_R DDR_D29_R DDR_D30_R DDR_D31_R DDR_D32_R DDR_D33_R DDR_D34_R DDR_D35_R DDR_D36_R DDR_D37_R DDR_D38_R DDR_D39_R DDR_D40_R DDR_D41_R DDR_D42_R DDR_D43_R DDR_D44_R DDR_D45_R DDR_D46_R DDR_D47_R DDR_D48_R DDR_D49_R DDR_D50_R DDR_D51_R DDR_D52_R DDR_D53_R DDR_D54_R DDR_D55_R DDR_D56_R DDR_D57_R DDR_D58_R DDR_D59_R DDR_D60_R DDR_D61_R DDR_D62_R DDR_D63_R — 15

DDR_CB0_R DDR_CB1_R DDR_CB2_R DDR_CB3_R DDR_CB4_R DDR_CB5_R DDR_CB6_R DDR_CB7_R — 15

DDR_DS0_R DDR_DS1_R DDR_DS2_R DDR_DS3_R DDR_DS4_R DDR_DS5_R DDR_DS6_R DDR_DS7_R DDR_DS8_R — 15

DELL CONFIDENTIAL/PROPRIETARY
Compal Electronics, Inc.

Title	DDR-SODIMM SLOT1	
Size	Document Number BDQ11/LA-1601	Rev 0.4
Date: 星期五, 十二月 27, 2002	Sheet 14 of 56	

Layout Note:
Place these resistors closely DIMM1,all trace length<=800 mil

Layout Note:
Place CAP near SODIMM

One .1uF cap per power pin. Place each cap close to pin.

Layout Note:
Place these resistor closely DIMM1,all trace length Max=1.3"

Place by pin197 of each SODIMM

11,14 DDR_MA[0..12]

V_1P25V_DDR_VTT

+2.5V_MEM

+3VSUS

JDIM2

AMP_1565918-1-L

V_DDR_MCH_REF 11,14,46

DDR_CKE2_DIMM1 11

DDR_CS3_DIMM1# 11

CK_DDR_CK3 11

CK_DDR_CK3# 11

ICH_SMBDATA 6,14,19,31

ICH_SMBCLK 6,14,19,31

DIMM1_ID

R151 10K_0402_5%~L

C165 0.1U_0402_16V4Z~L

C141 0.1U_0402_16V4Z~L

DIMM1 REVERSE

10/29

C933+ 150U_D2_4VK~D

C934+ 150U_D2_4VK~D

C935+ 150U_D_10VM_R55~D

C936+ 150U_D_10VM_R55~D

Height Restriction

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Compal Electronics, Inc.

Title: DDR-SODIMM SLOT2

Size: | Document Number: BDQ11/LA-1601 | Rev: 0.4

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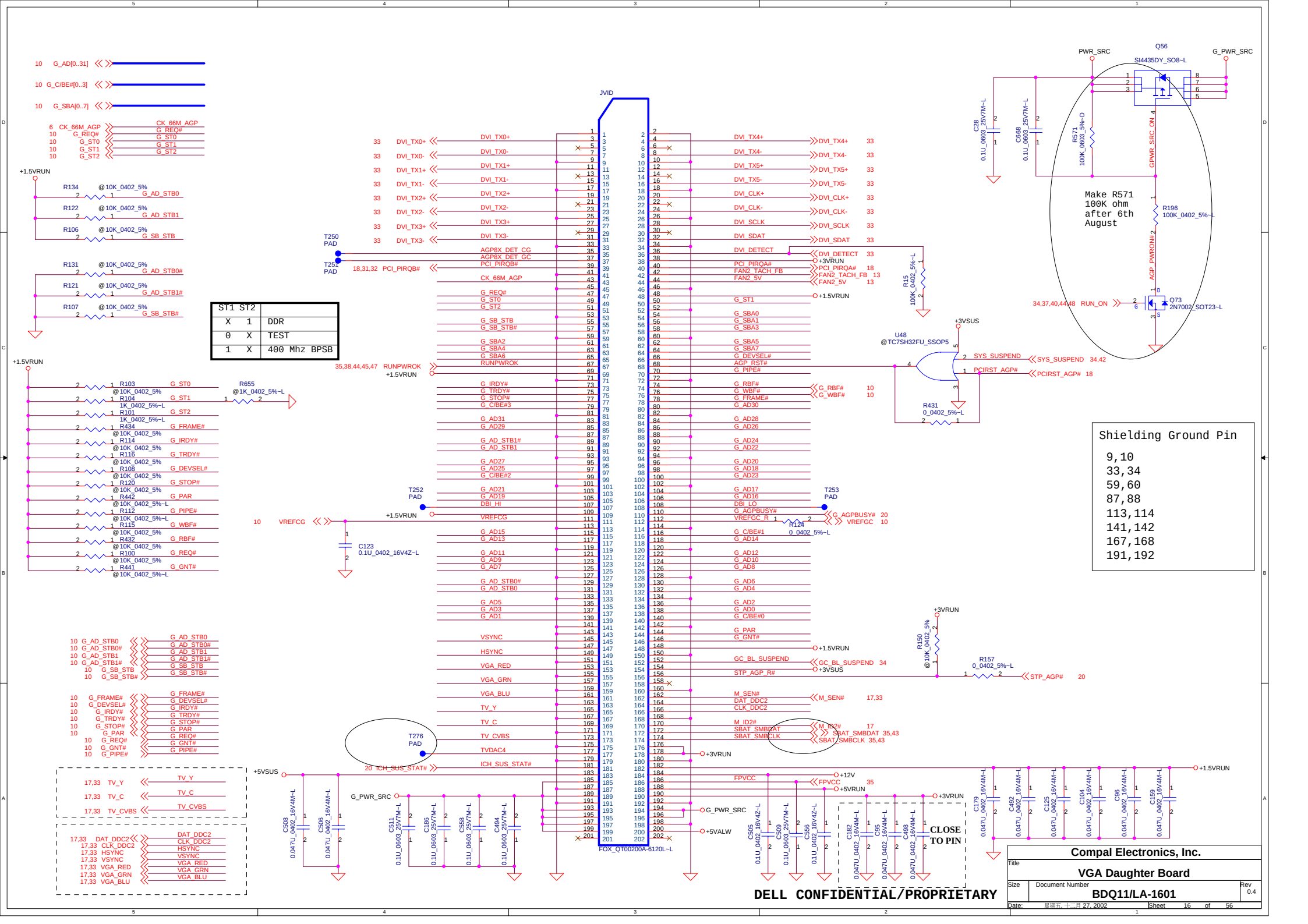
10 G_AD[0..31]
10 G_C/BE#[0..3]
10 G_SBA[0..7]
6 CK_66M_AGP
10 G_REQ#
10 G_ST0
10 G_ST1
10 G_ST2
+1.5VRUN
R134 @10K_0402_5%
R122 @10K_0402_5%
R106 @10K_0402_5%
R131 @10K_0402_5%
R121 @10K_0402_5%
R107 @10K_0402_5%
ST1 ST2
X 1 DDR
0 X TEST
1 X 400 Mhz BPSB
R103 @10K_0402_5%
R104 1K_0402_5%~L
R101 1K_0402_5%~L
R434 @10K_0402_5%
R114 @10K_0402_5%
R116 @10K_0402_5%
R108 @10K_0402_5%
R120 @10K_0402_5%
R442 @10K_0402_5%~L
R112 @10K_0402_5%~L
R115 @10K_0402_5%
R432 @10K_0402_5%
R100 @10K_0402_5%
R441 @10K_0402_5%~L
R655 @1K_0402_5%~L
JVID
FOX_QT00200A-6120L~L
T250 PAD
T251 PAD
T252 PAD
T253 PAD
T276 PAD
C123 0.1U_0402_16V4Z~L
R15 100K_0402_5%~L
R124 0_0402_5%~L
U48 @TC7SH32FU_SSOP5
R431 0_0402_5%~L
Q56 SI4435DY_SO8~L
C28 0.1U_0603_25V7M~L
C668 0.1U_0603_25V7M~L
R571 100K_0603_5%~D
R196 100K_0402_5%~L
Q73 2N7002_SOT23~L
Make R571 100K ohm after 6th August
R150 @10K_0402_5%
R157 0_0402_5%~L
C508 0.047U_0402_16V4M~L
C506 0.047U_0402_16V4M~L
C511 0.1U_0603_25V7M~L
C186 0.1U_0603_25V7M~L
C558 0.1U_0603_25V7M~L
C494 0.1U_0603_25V7M~L
C505 0.1U_0402_16V4Z~L
C509 0.1U_0603_25V7M~L
C556 0.1U_0402_16V4Z~L
C182 0.047U_0402_16V4M~L
C95 0.047U_0402_16V4M~L
C498 0.047U_0402_16V4M~L
C179 0.047U_0402_16V4M~L
C492 0.047U_0402_16V4M~L
C125 0.047U_0402_16V4M~L
C104 0.047U_0402_16V4M~L
C96 0.047U_0402_16V4M~L
C159 0.047U_0402_16V4M~L
CLOSE TO PIN
Shielding Ground Pin
9,10
33,34
59,60
87,88
113,114
141,142
167,168
191,192
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Compal Electronics, Inc.
VGA Daughter Board
BDQ11/LA-1601
Rev 0.4
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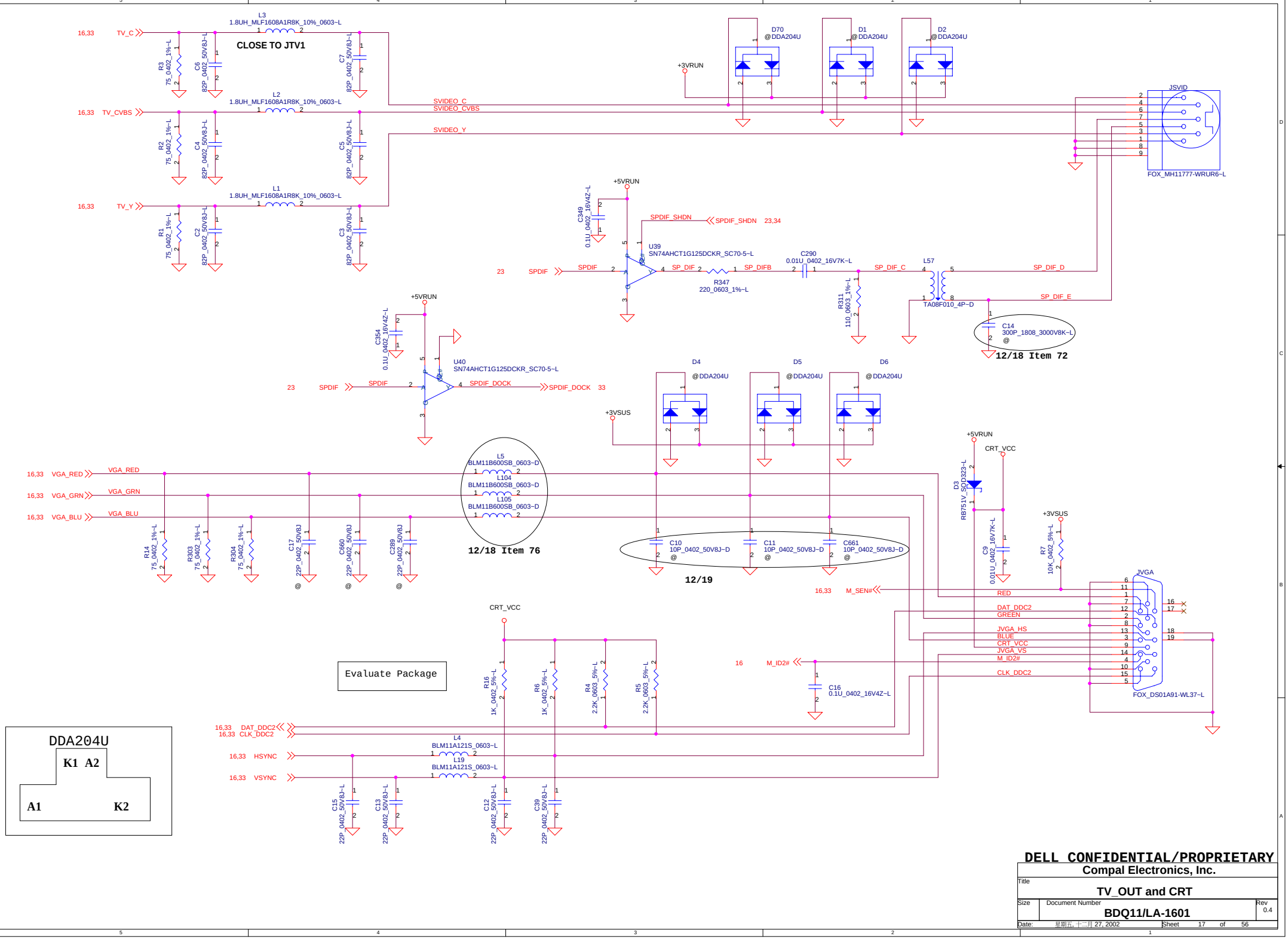

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Compal Electronics, Inc.

Title	TV_OUT and CRT	
Size	Document Number: BDQ11/LA-1601	Rev: 0.4
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Hub interface Layout:
Route signal with 5/15
trace/space,<< 8 inchs
Signal must match +/- 0.1" of HL_STB/STB# signals.

ICH4

FW82801DBM_BGA421_ADQ00~L

Place near ICH

Level shifter

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Compal Electronics, Inc.

Title	ICH4(1/4)	
Size	Document Number: BDQ11/LA-1601	Rev: 0.4
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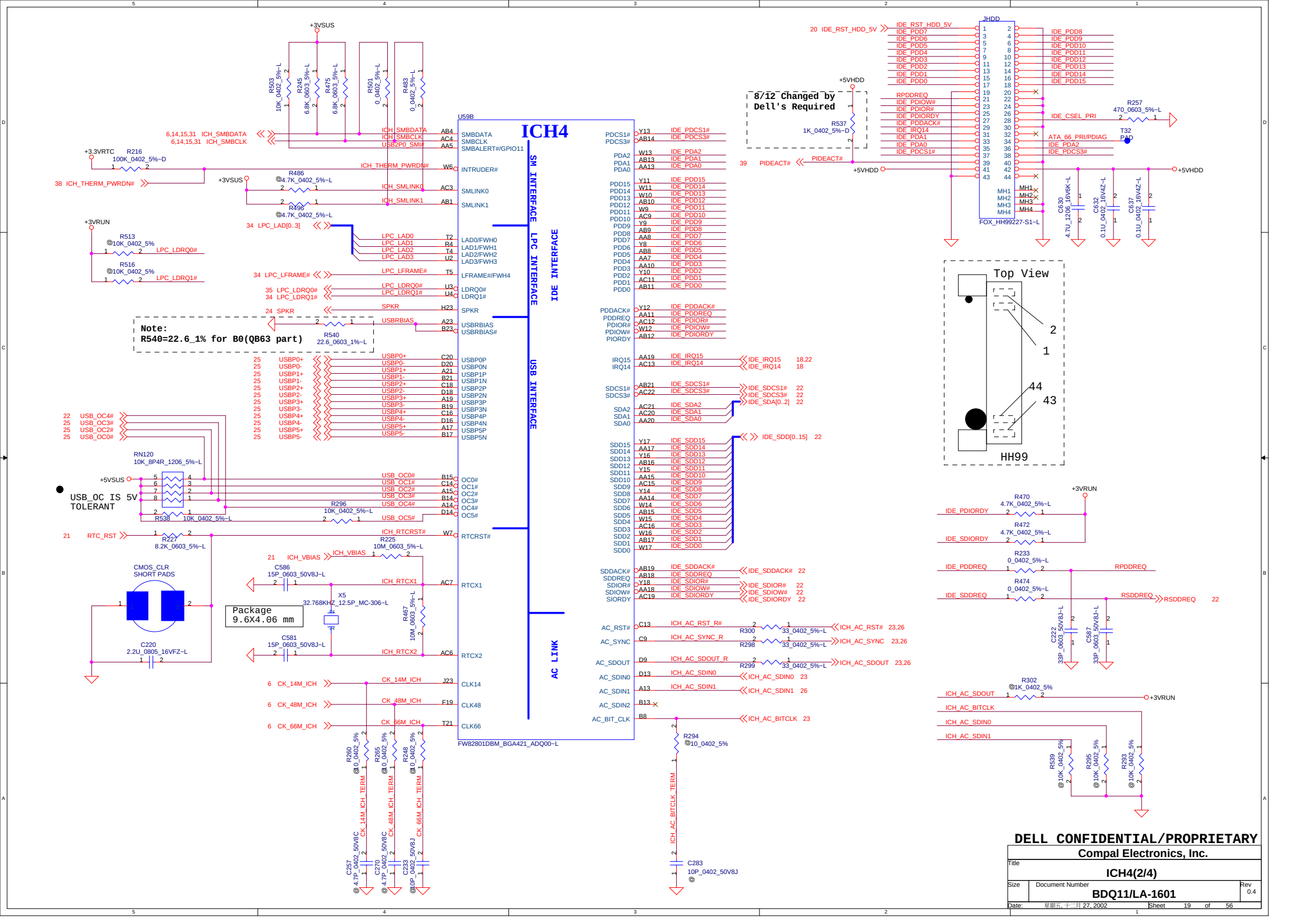
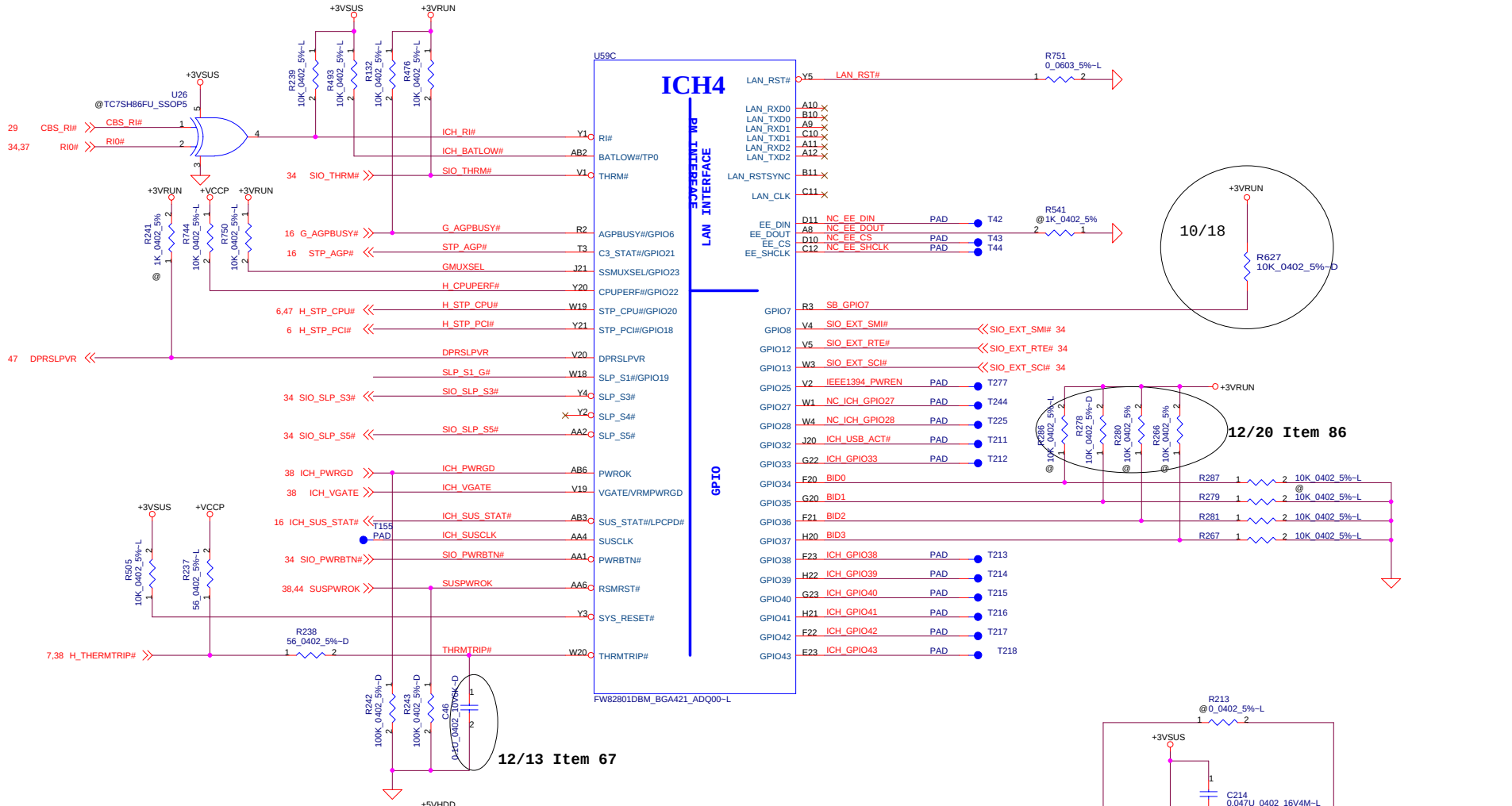
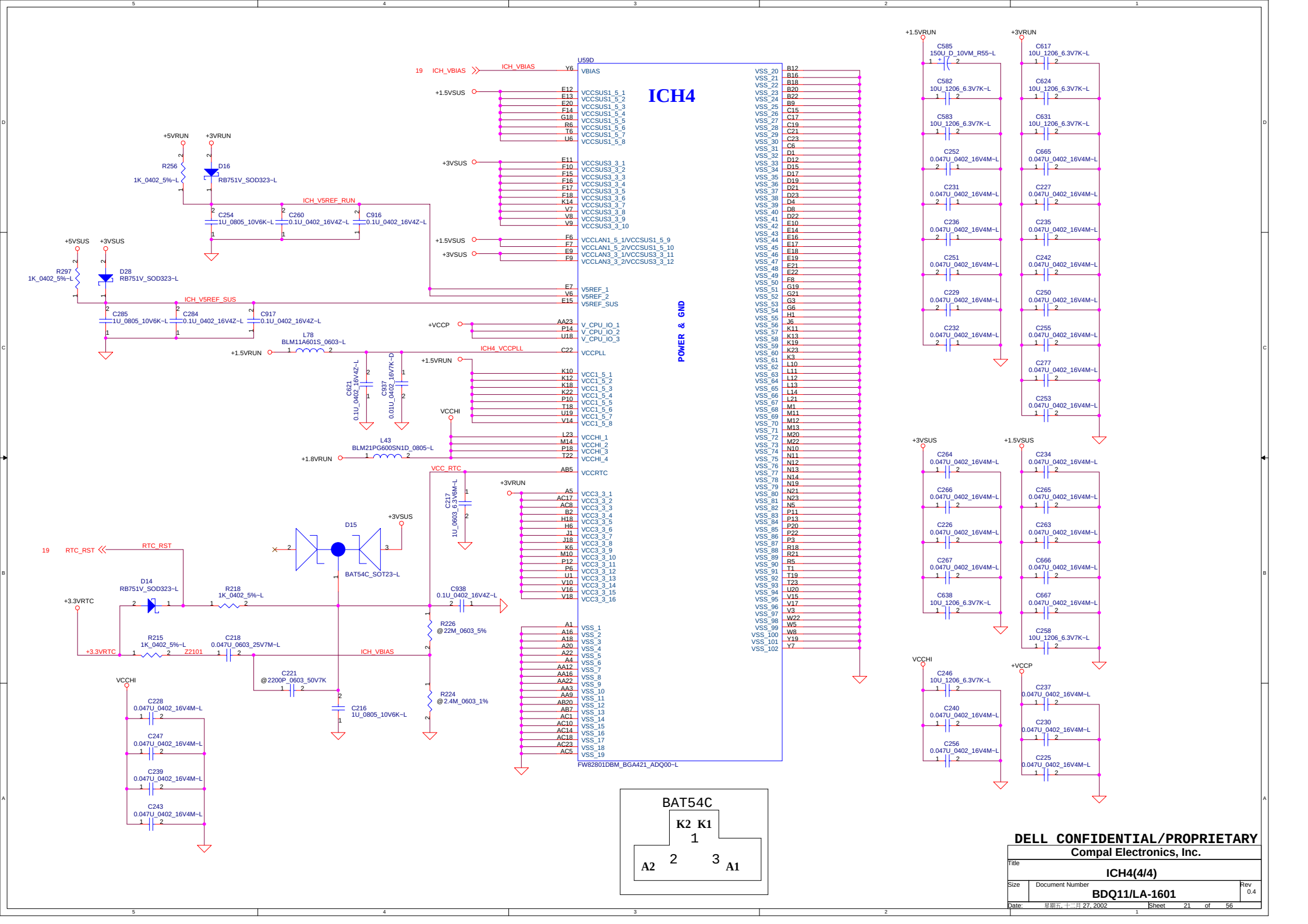

ICH4
U59B
FW82801DBM_BGA421_ADQ00~L
SM INTERFACE
LPC INTERFACE
IDE INTERFACE
USB INTERFACE
AC LINK
Note:
R540=22.6_1% for B0(QB63 part)
USB_OC IS 5V TOLERANT
Package 9.6X4.06 mm
CMOS_CLR SHORT PADS
8/12 Changed by Dell's Required
JHDD
FOX_HH99227-S1~L
Top View
HH99
DELL CONFIDENTIAL/PROPRIETARY
Compal Electronics, Inc.
ICH4(2/4)
BDQ11/LA-1601
Rev 0.4
Sheet 19 of 56
Date: 星期五, 十二月 27, 2002

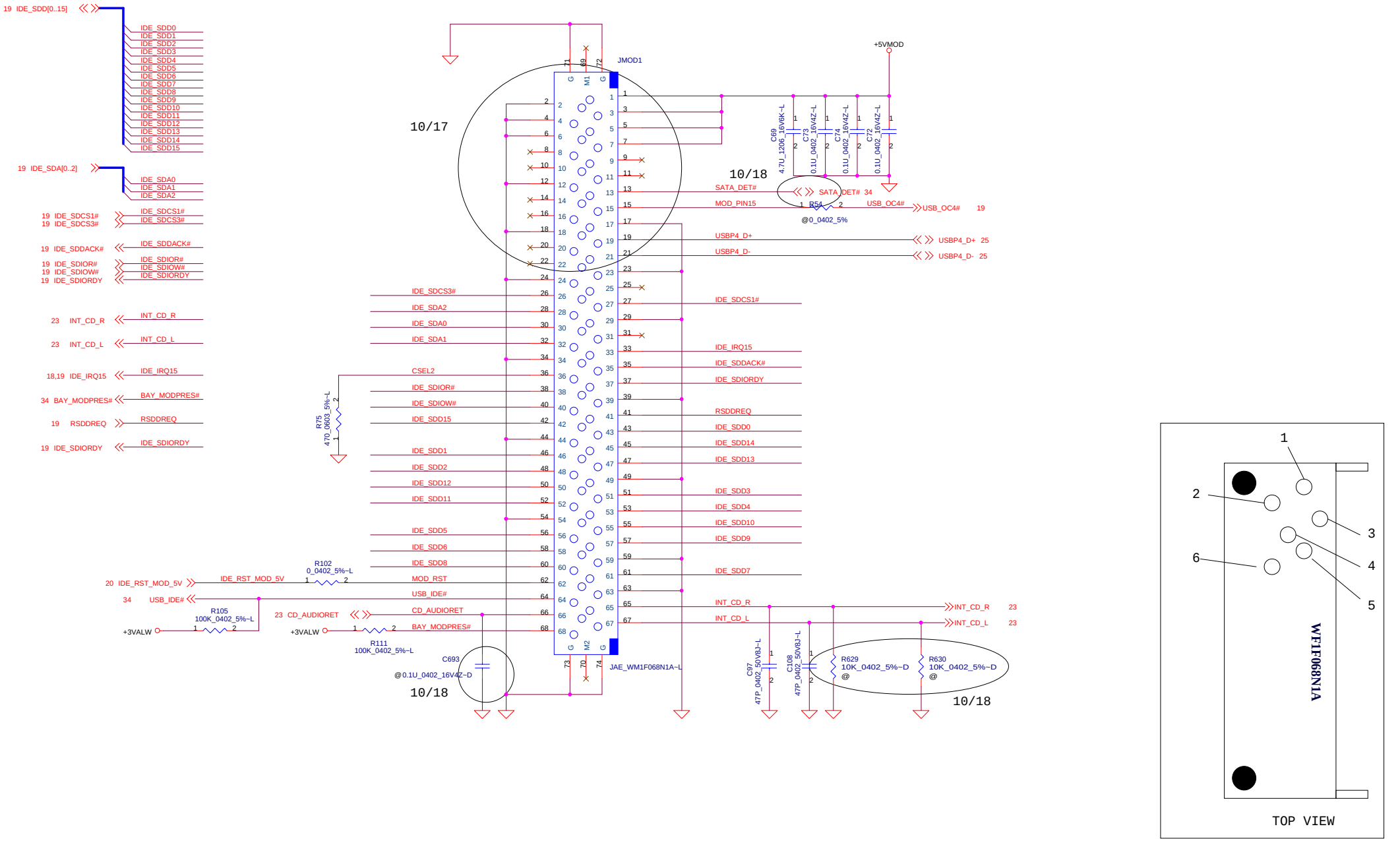


BID3	BID2	BID1	BID0	REV
0	0	0	0	X00
0	0	0	1	X01
0	0	1	0	X02

DELL CONFIDENTIAL/PROPRIETARY
Compal Electronics, Inc.

Title	ICH4(3/4)	
Size	Document Number: BDQ11/LA-1601	Rev: 0.4
Date: 星期五, 十二月 27, 2002	Sheet 20 of 56	

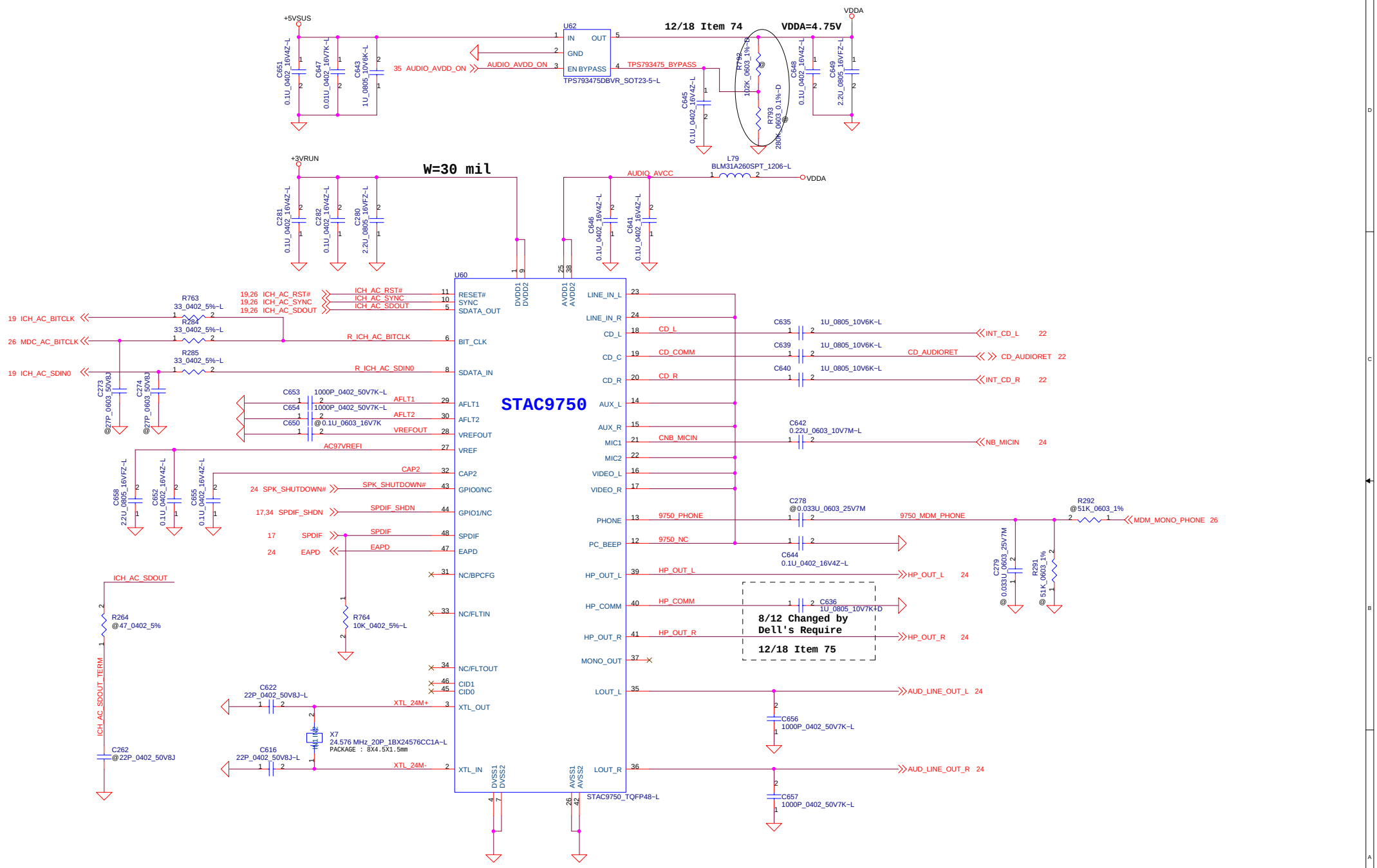

ICH4
U59D
POWER & GND
FW82801DBM_BGA421_ADQ00-L
ICH_VBIAS
ICH_V5REF_RUN
ICH_V5REF_SUS
ICH4_VCCPLL
VCC_RTC
RTC_RST
BAT54C_SOT23-L
BAT54C
K2 K1
1
A2 2 3 A1
DELL CONFIDENTIAL/PROPRIETARY
Compal Electronics, Inc.
Title
ICH4(4/4)
Size
Document Number
BDQ11/LA-1601
Rev
0.4
Date: 星期五, 十二月 27, 2002
Sheet 21 of 56



DELL CONFIDENTIAL/PROPRIETARY

Compal Electronics, Inc.

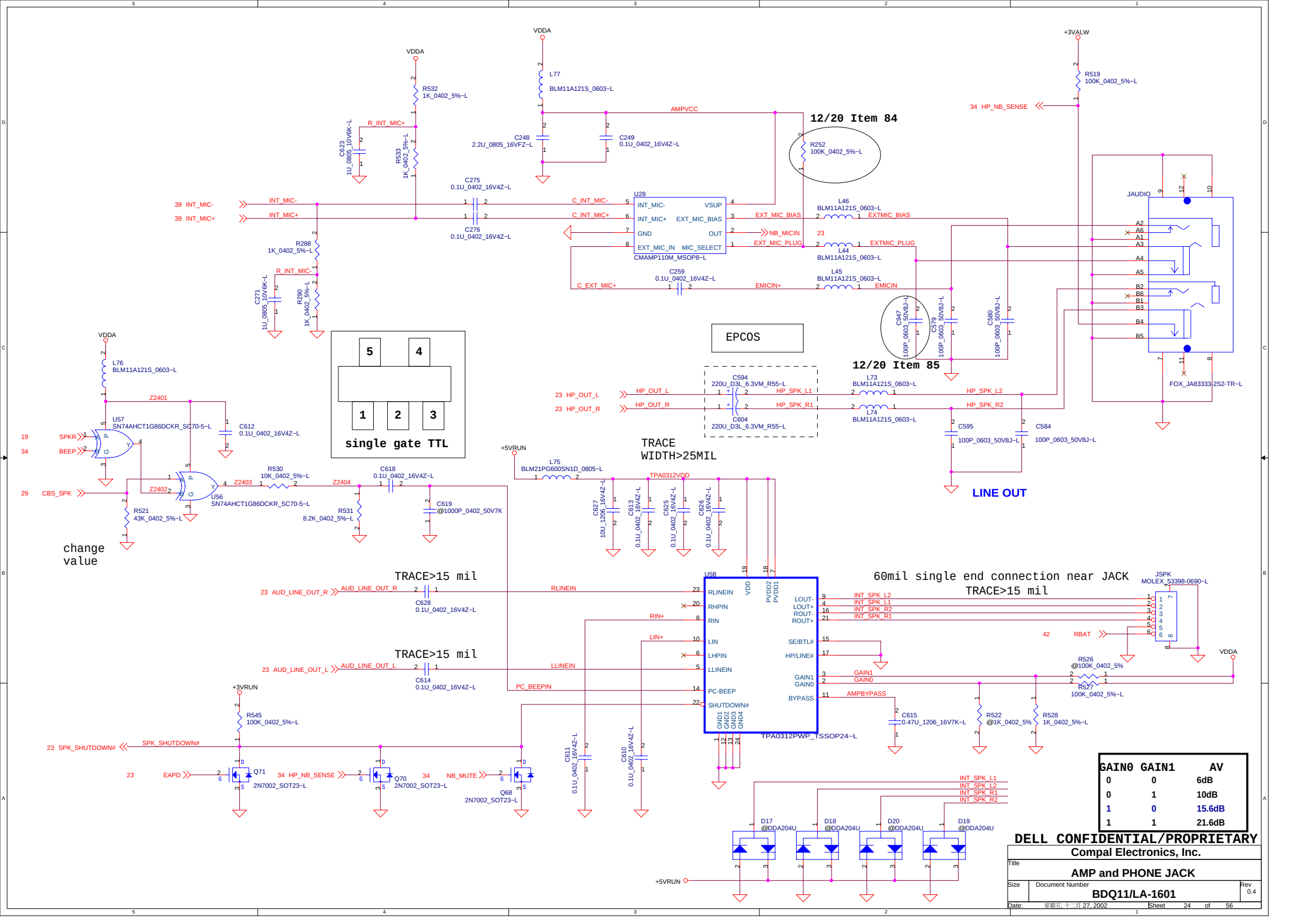
Title	DVD MODULE	
Size	Document Number: BDQ11/LA-1601	Rev: 0.4
Date: 星期五, 十二月 27, 2002	Sheet 22 of 56	



DELL CONFIDENTIAL/PROPRIETARY

Compal Electronics, Inc.

Title	AC97	
Size	Document Number BDQ11/LA-1601	Rev 0.4
Date: 星期五, 十二月 27, 2002	Sheet 23 of 56	



GAIN0	GAIN1	AV
0	0	6dB
0	1	10dB
1	0	15.6dB
1	1	21.6dB

Compal Electronics, Inc.

Title: AMP and PHONE JACK

Size | Document Number: BDQ11/LA-1601 | Rev: 0.4

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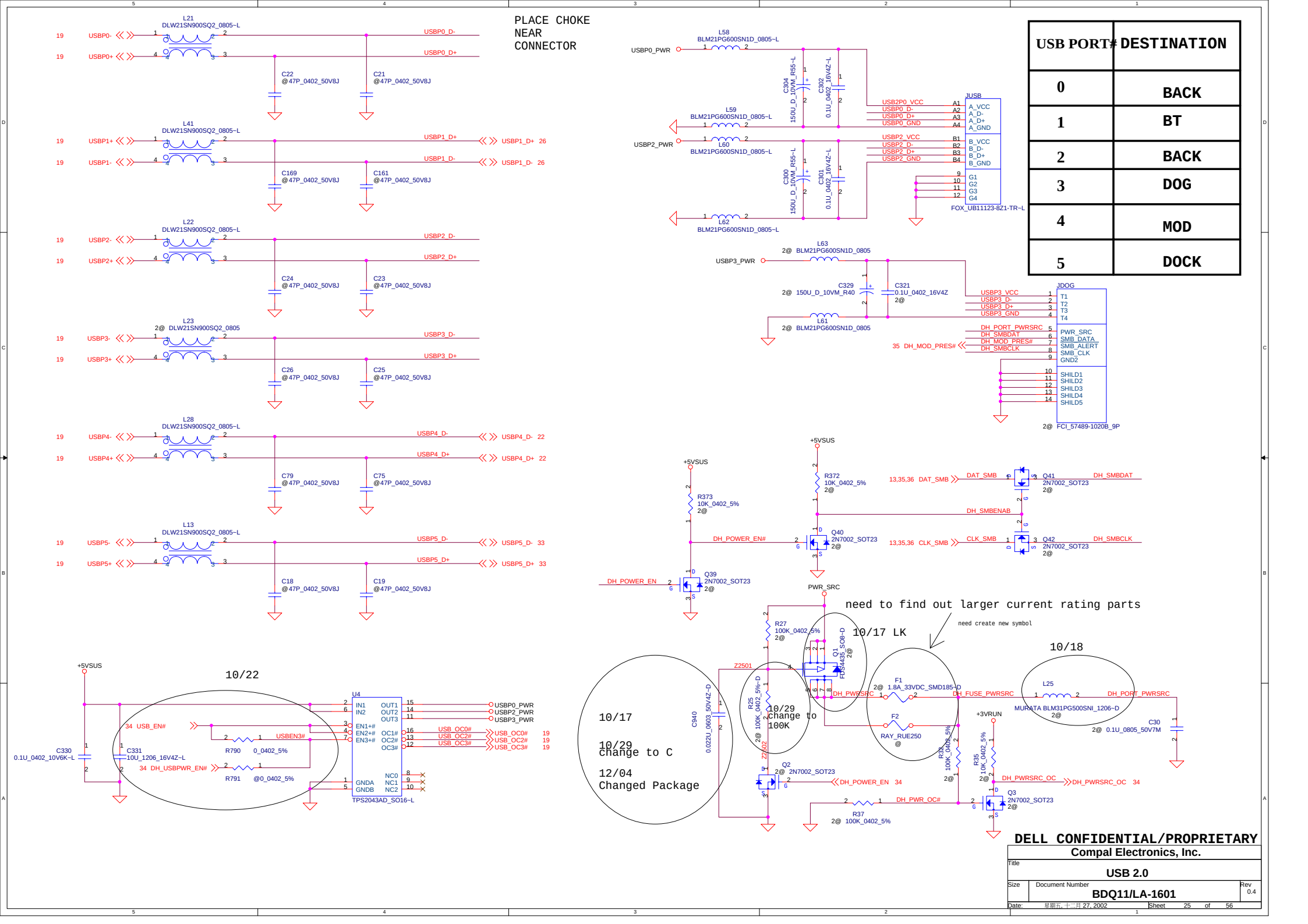
PLACE CHOKE NEAR CONNECTOR
L21 DLW21SN900SQ2_0805~L
USBP0-
USBP0+
USBP0_D-
USBP0_D+
C22 @47P_0402_50V8J
C21 @47P_0402_50V8J
L41 DLW21SN900SQ2_0805~L
USBP1+
USBP1-
USBP1_D+
USBP1_D-
C169 @47P_0402_50V8J
C161 @47P_0402_50V8J
L22 DLW21SN900SQ2_0805~L
USBP2-
USBP2+
USBP2_D-
USBP2_D+
C24 @47P_0402_50V8J
C23 @47P_0402_50V8J
L23 2@ DLW21SN900SQ2_0805
USBP3-
USBP3+
USBP3_D-
USBP3_D+
C26 @47P_0402_50V8J
C25 @47P_0402_50V8J
L28 DLW21SN900SQ2_0805~L
USBP4-
USBP4+
USBP4_D-
USBP4_D+
C79 @47P_0402_50V8J
C75 @47P_0402_50V8J
L13 DLW21SN900SQ2_0805~L
USBP5-
USBP5+
USBP5_D-
USBP5_D+
C18 @47P_0402_50V8J
C19 @47P_0402_50V8J
L58 BLM21PG600SN1D_0805~L
L59 BLM21PG600SN1D_0805~L
L60 BLM21PG600SN1D_0805~L
L62 BLM21PG600SN1D_0805~L
USBP0_PWR
USBP2_PWR
C304 150U_D_10VM_R55~L
C302 0.1U_0402_16V4Z~L
C300 150U_D_10VM_R55~L
C301 0.1U_0402_16V4Z~L
USB2P0_VCC
USBP0_D-
USBP0_D+
USBP0_GND
USB2P2_VCC
USBP2_D-
USBP2_D+
USBP2_GND
JUSB
A_VCC
A_D-
A_D+
A_GND
B_VCC
B_D-
B_D+
B_GND
G1
G2
G3
G4
FOX_UB11123-8Z1-TR~L
L63 2@ BLM21PG600SN1D_0805
L61 2@ BLM21PG600SN1D_0805
USBP3_PWR
C329 2@ 150U_D_10VM_R40
C321 0.1U_0402_16V4Z 2@
USBP3_VCC
USBP3_D-
USBP3_D+
USBP3_GND
JDOG
T1
T2
T3
T4
DH_PORT_PWRSRC
DH_SMBDAT
DH_MOD_PRES#
DH_SMBCLK
35 DH_MOD_PRES#
PWR_SRC
SMB_DATA
SMB_ALERT
SMB_CLK
GND2
SHILD1
SHILD2
SHILD3
SHILD4
SHILD5
2@ FCI_57489-1020B_9P
USB PORT# DESTINATION
0 BACK
1 BT
2 BACK
3 DOG
4 MOD
5 DOCK
+5VSUS
R373 10K_0402_5%
R372 10K_0402_5%
13,35,36 DAT_SMB
DAT_SMB
Q41 2N7002_SOT23
DH_SMBDAT
DH_SMBENAB
DH_POWER_EN#
Q40 2N7002_SOT23
13,35,36 CLK_SMB
CLK_SMB
Q42 2N7002_SOT23
DH_SMBCLK
DH_POWER_EN
Q39 2N7002_SOT23
PWR_SRC
need to find out larger current rating parts
need create new symbol
10/17 LK
10/18
R27 100K_0402_5%
Q1 FDS4435_SO8~D
Z2501
R25 100K_0402_5%~D
C940 0.022U_0603_50V4Z~D
10/17
10/29 change to C
12/04 Changed Package
10/29 change to 100K
F1 1.8A_33VDC_SMD185~D
DH_PWRSRC
DH_FUSE_PWRSRC
F2 RAY_RUE250
L25 MURATA BLM31PG500SN1_1206~D
DH_PORT_PWRSRC
C30 2@ 0.1U_0805_50V7M
+3VRUN
R32 100K_0402_5%
R35 10K_0402_5%
Q2 2@ 2N7002_SOT23
DH_POWER_EN 34
DH_PWR_OC#
R37 2@ 100K_0402_5%
DH_PWRSRC_OC
DH_PWRSRC_OC 34
Q3 2N7002_SOT23
10/22
+5VSUS
C330 0.1U_0402_10V6K~L
C331 10U_1206_16V4Z~L
34 USB_EN#
USBEN3#
R790 0_0402_5%
34 DH_USBPWR_EN#
R791 @0_0402_5%
U4
IN1
IN2
EN1#
EN2#
EN3#
OUT1
OUT2
OUT3
OC1#
OC2#
OC3#
GNDA
GNDB
NC0
NC1
NC2
USB_OC0#
USB_OC2#
USB_OC3#
USBP0_PWR
USBP2_PWR
USBP3_PWR
TPS2043AD_SO16~L
DELL CONFIDENTIAL/PROPRIETARY
Compal Electronics, Inc.
Title
USB 2.0
Size
Document Number
BDQ11/LA-1601
Rev
0.4
Date:
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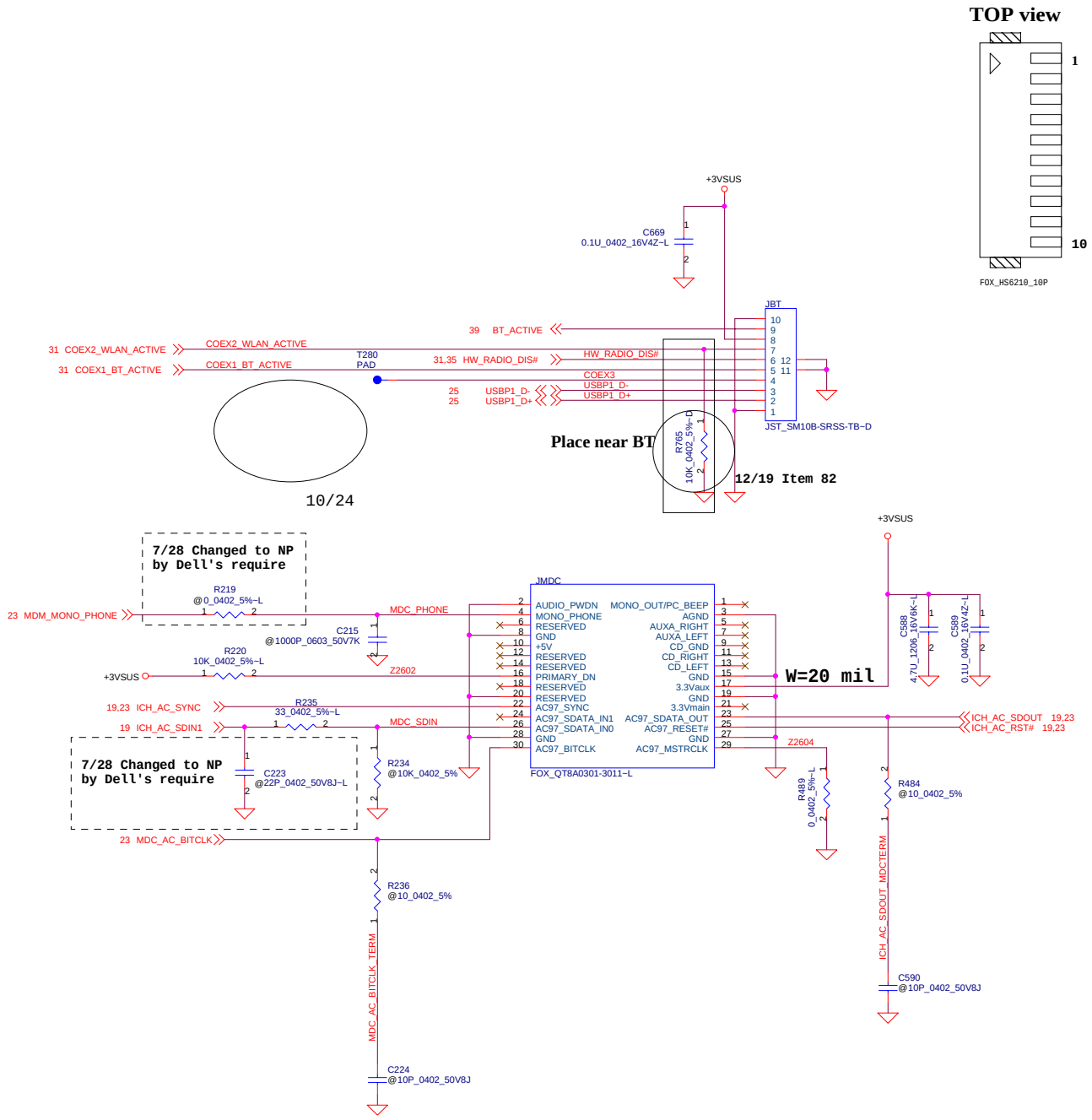
56



Compal Electronics, Inc.

Title	BT PORT and MDC	
Size	Document Number: BDQ11/LA-1601	Rev: 0.4
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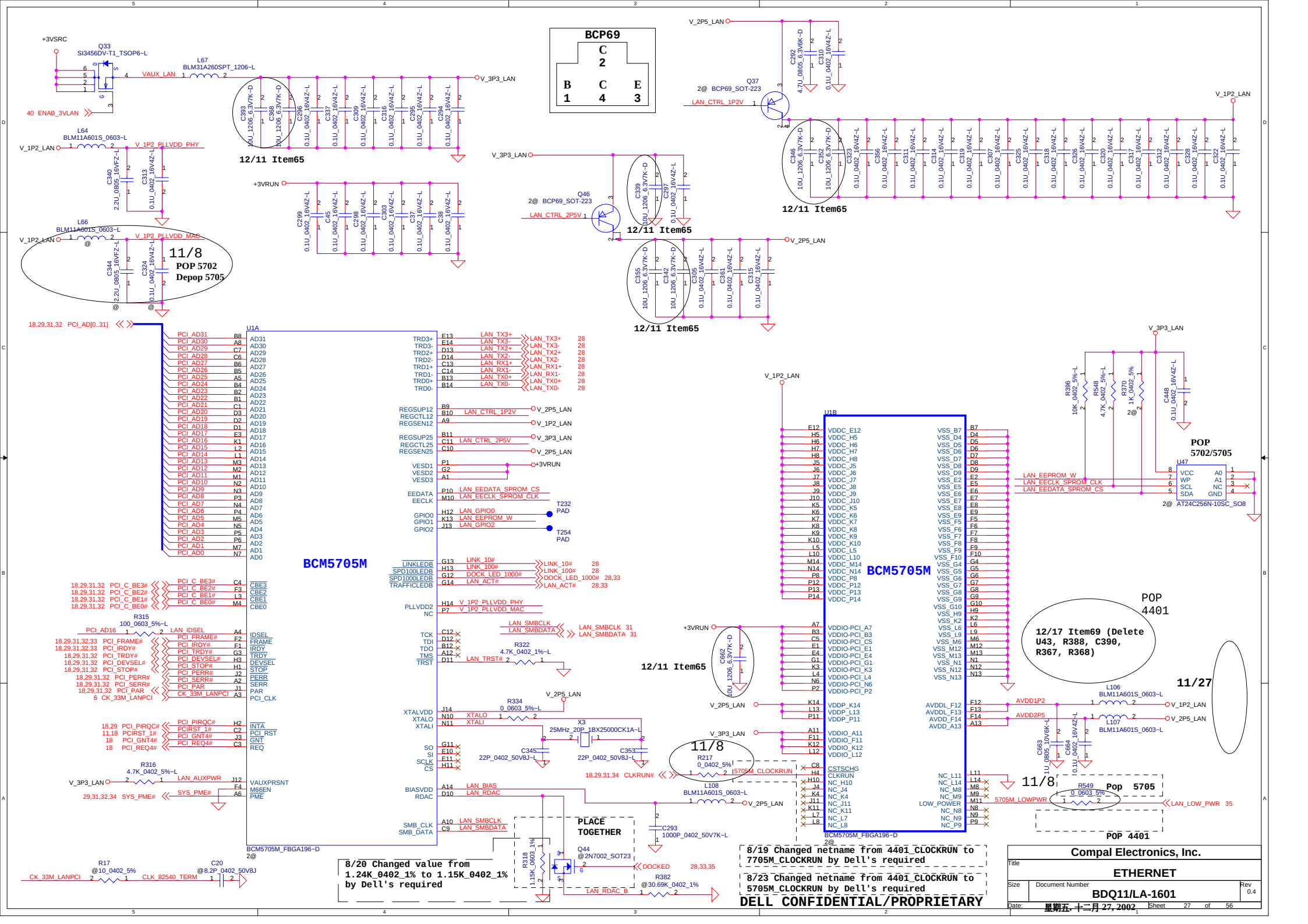
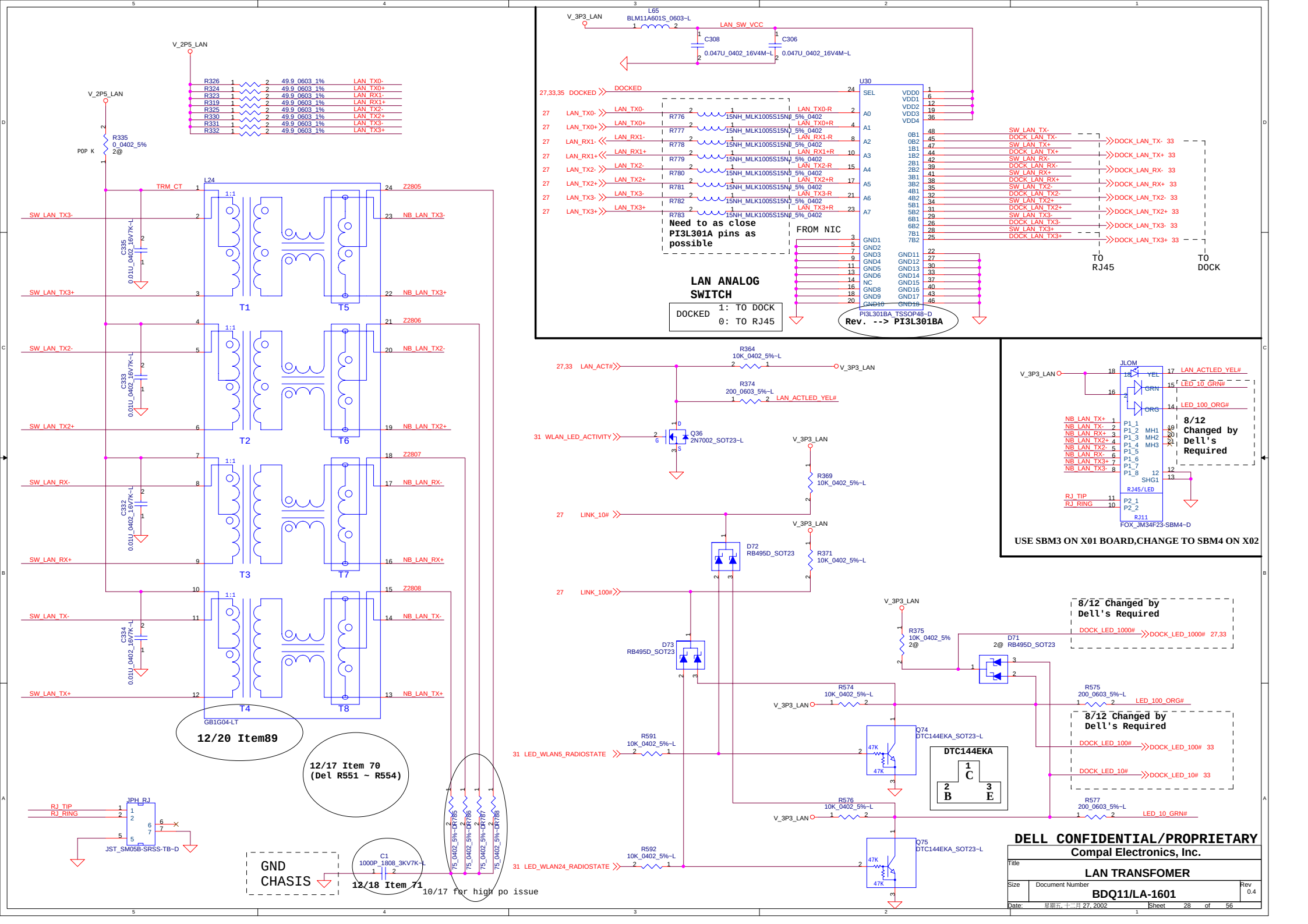
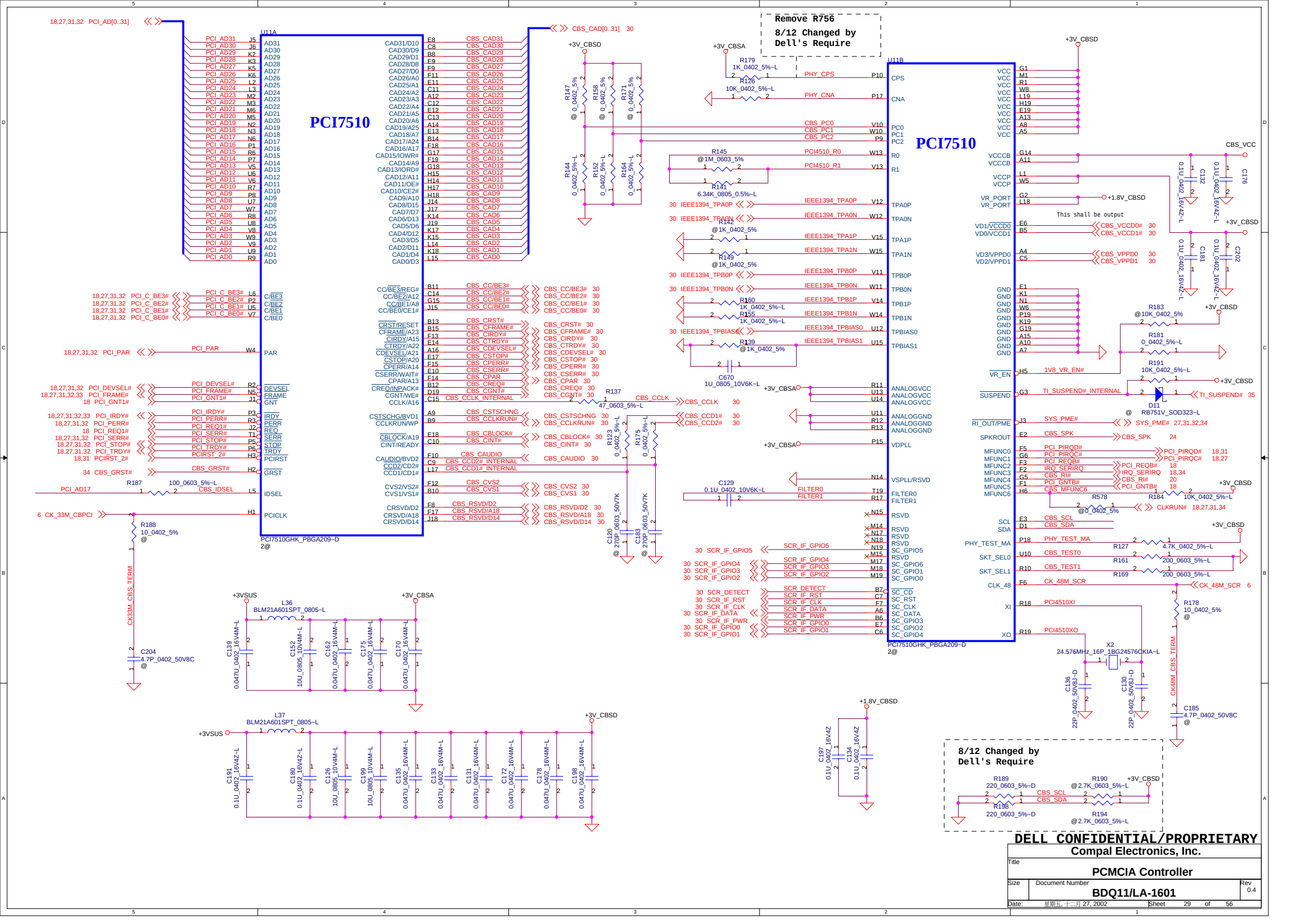

BCP69
C
2
B C E
1 4 3
Q33
SI3456DV-T1_TSOP6-L
+3VSRC
VAUX_LAN
L67
BLM31A260SPT_1206-L
V_3P3_LAN
40 ENAB_3VLAN
L64
BLM11A601S_0603-L
V_1P2_LAN
V_1P2_PLLVDD_PHY
12/11 Item65
+3VRUN
L66
BLM11A601S_0603-L
V_1P2_PLLVDD_MAC
11/8
POP 5702
Depop 5705
Q46
2@ BCP69_SOT-223
LAN_CTRL_2P5V
V_2P5_LAN
Q37
2@ BCP69_SOT-223
LAN_CTRL_1P2V
18,29,31,32 PCI_AD[0..31]
U1A
BCM5705M
BCM5705M_FBGA196~D
2@
TRD3+
TRD3-
TRD2+
TRD2-
TRD1+
TRD1-
TRD0+
TRD0-
REGSUP12
REGCTL12
REGSEN12
REGSUP25
REGCTL25
REGSEN25
VESD1
VESD2
VESD3
EEDATA
EECLK
GPIO0
GPIO1
GPIO2
T232 PAD
T254 PAD
LINKLEDB
SPD100LEDB
SPD1000LEDB
TRAFFICLEDB
PLLVDD2
NC
TCK
TDI
TDO
TMS
TRST
XTALVDD
XTALO
XTALI
SO
SI
SCLK
CS
BIASVDD
RDAC
SMB_CLK
SMB_DATA
R315
100_0603_5%~L
R316
4.7K_0402_5%~L
R334
0_0603_5%~L
R322
4.7K_0402_1%~L
X3
25MHz_20P_1BX25000CK1A~L
22P_0402_50V8J~L
R217
0_0402_5%
R318
1.15K_0603_1%
Q44
@2N7002_SOT23
R382
@30.69K_0402_1%
R17
@10_0402_5%
C20
@8.2P_0402_50V8J
8/20 Changed value from 1.24K_0402_1% to 1.15K_0402_1% by Dell's required
PLACE TOGETHER
U1B
BCM5705M
BCM5705M_FBGA196~D
2@
POP 5702/5705
U47
AT24C256N-10SC_SO8
POP 4401
12/17 Item69 (Delete U43, R388, C390, R367, R368)
11/27
L106
BLM11A601S_0603-L
L107
BLM11A601S_0603-L
R549
0_0603_5%
Pop 5705
POP 4401
8/19 Changed netname from 4401_CLOCKRUN to 7705M_CLOCKRUN by Dell's required
8/23 Changed netname from 4401_CLOCKRUN to 5705M_CLOCKRUN by Dell's required
DELL CONFIDENTIAL/PROPRIETARY
Compal Electronics, Inc.
Title
ETHERNET
Size
Document Number
BDQ11/LA-1601
Rev
0.4
Date:
星期五, 十二月 27, 2002
Sheet
27
of
56

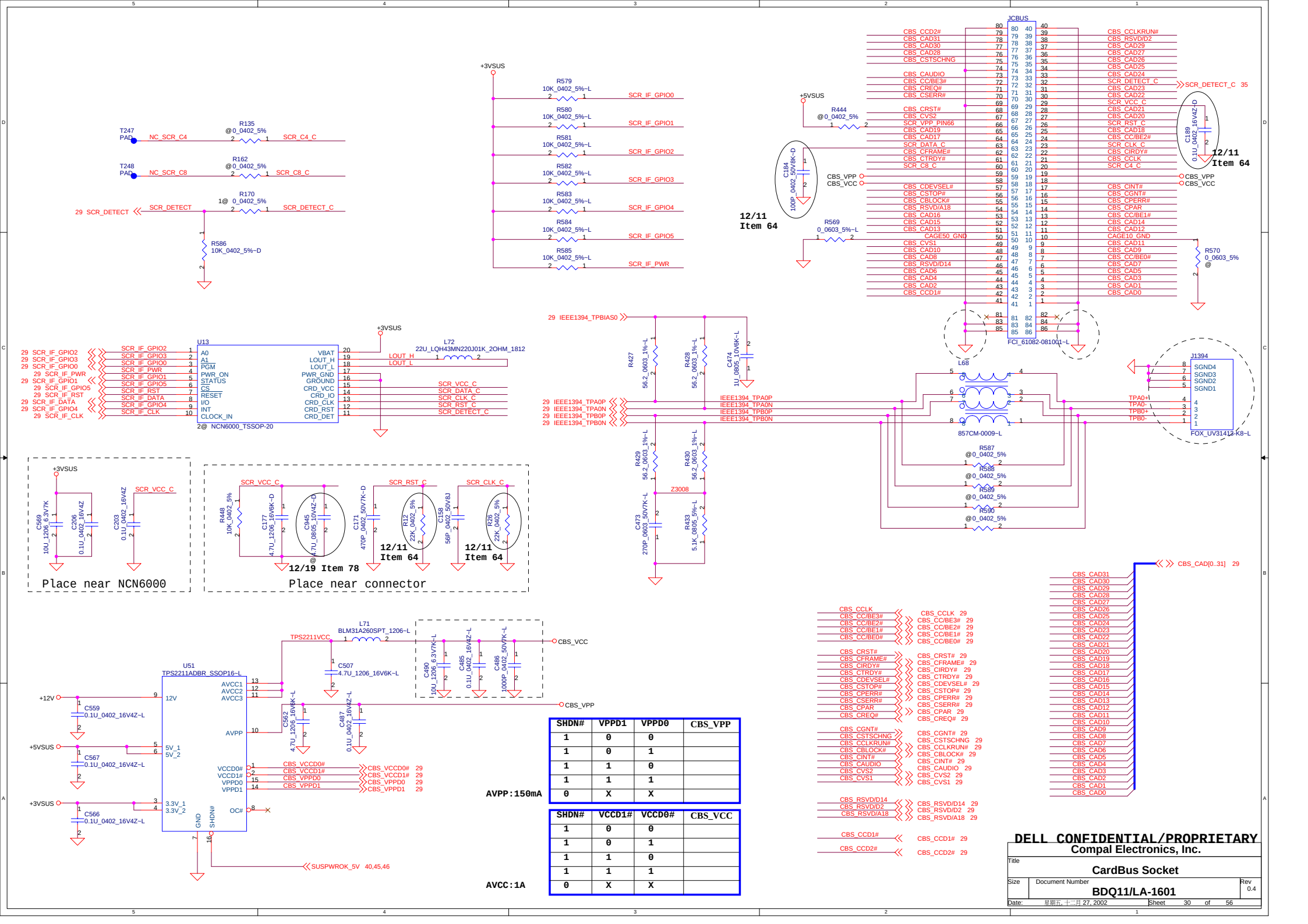


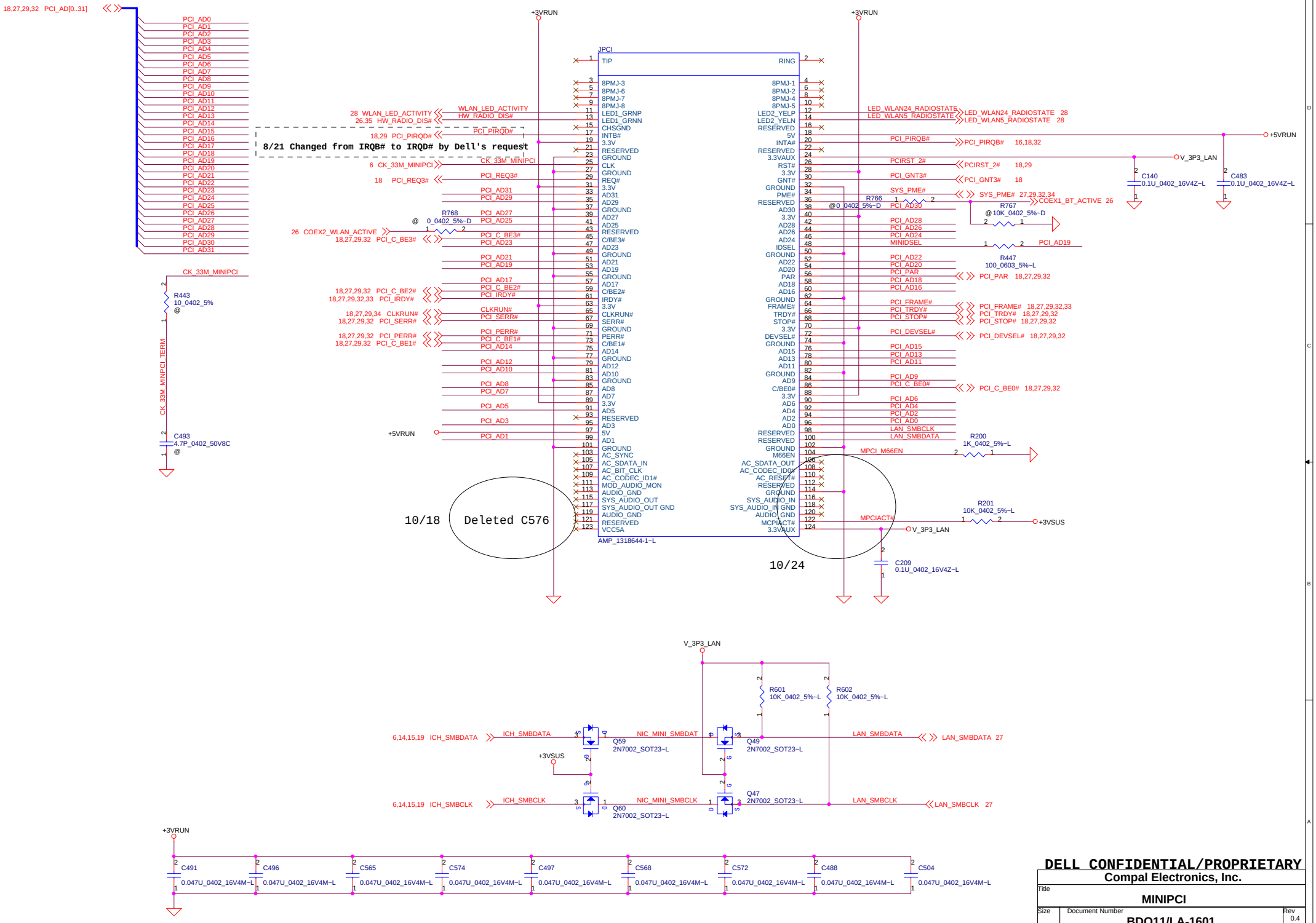
DELL CONFIDENTIAL/PROPRIETARY

Compal Electronics, Inc.

Title	LAN TRANSFOMER	
Size	Document Number: BDQ11/LA-1601	Rev: 0.4
Date: 星期五, 十二月 27, 2002	Sheet 28 of 56	


Remove R756
8/12 Changed by Dell's Require
PCI7510
U11A
U11B
PCI7510GHK_PBGA209~D
8/12 Changed by Dell's Require
DELL CONFIDENTIAL/PROPRIETARY
Compal Electronics, Inc.
PCMCIA Controller
BDQ11/LA-1601
Rev 0.4
Date: 星期五, 十二月 27, 2002
Sheet 29 of 56

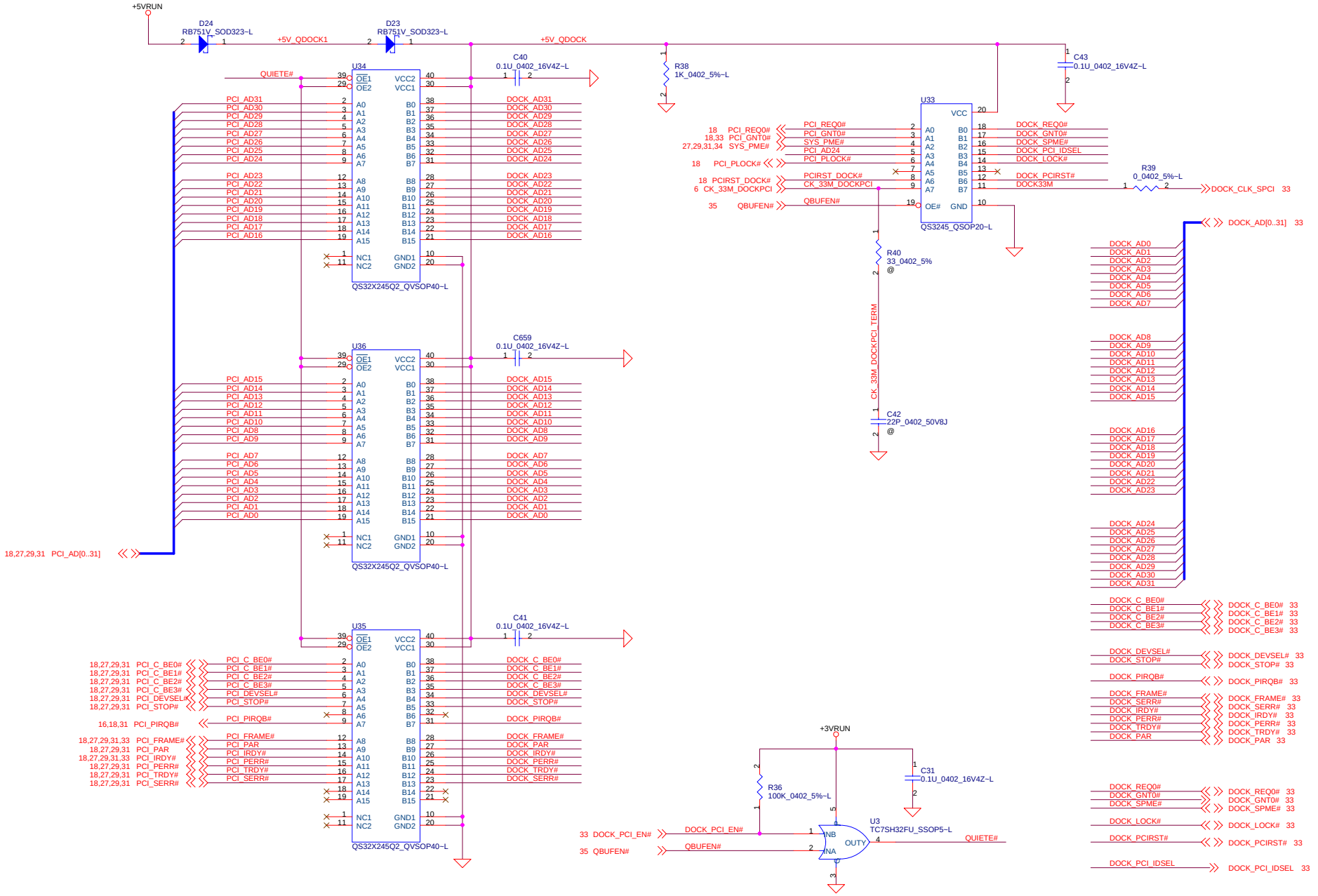

JCBUS
U13
TPS2211ADBR_SSOP16-L
U51
TPS2211ADBR_SSOP16-L
FCI_61082-081001-L
FOX_UV31412-K8-L
857CM-0009-L
Place near NCN6000
Place near connector
12/19 Item 78
12/11 Item 64
12/11 Item 64
12/11 Item 64
AVPP:150mA
AVCC:1A
SHDN# VPPD1 VPPD0 VCCD1# VCCD0# CBS_VCC
SHDN# VPPD1 VPPD0 CBS_VPP
DELL CONFIDENTIAL/PROPRIETARY
Compal Electronics, Inc.
CardBus Socket
BDQ11/LA-1601
Rev 0.4
Sheet 30 of 56



DELL CONFIDENTIAL/PROPRIETARY

Compal Electronics, Inc.

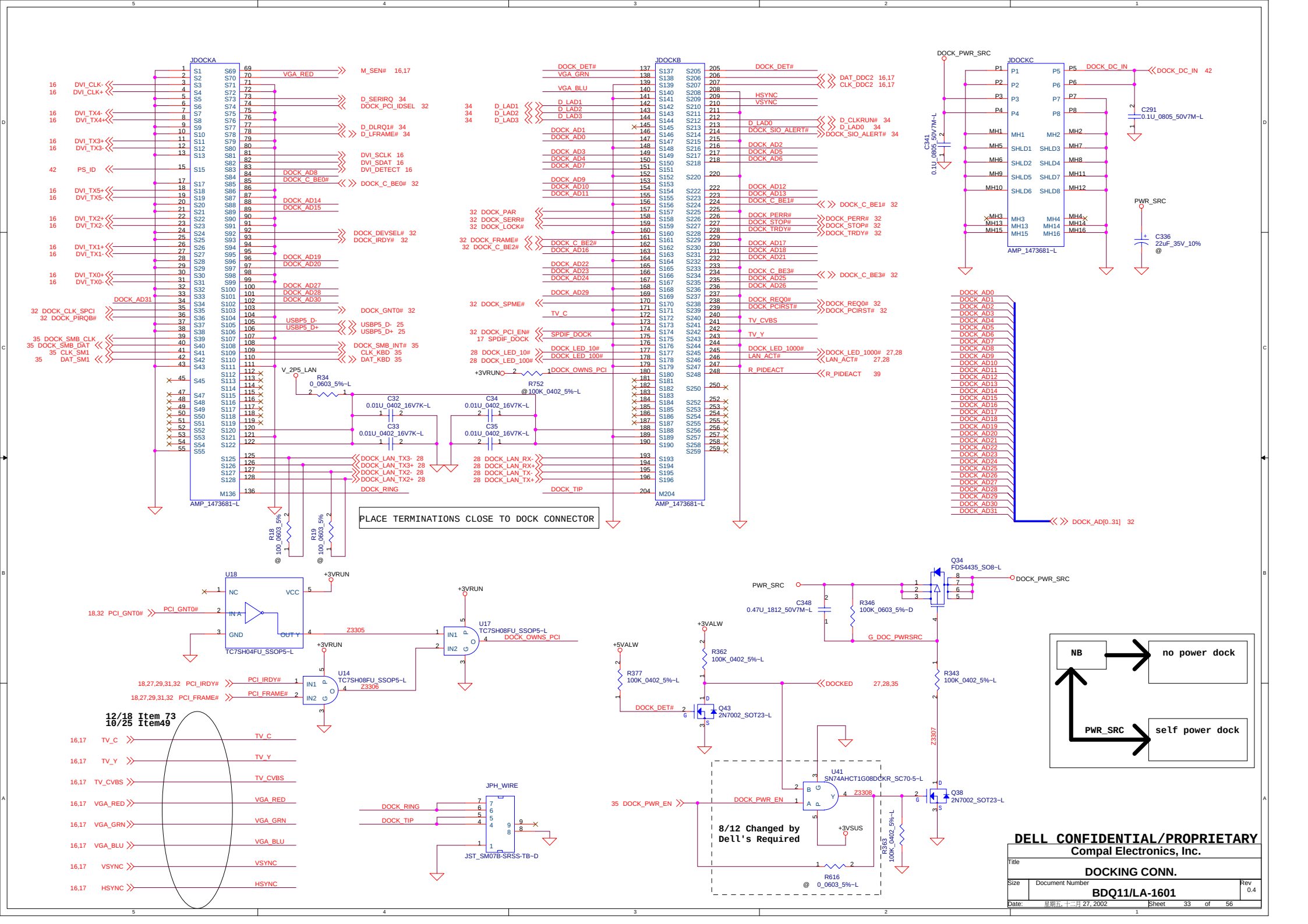
Title	MINIPCI	
Size	Document Number: BDQ11/LA-1601	Rev: 0.4
Date: 星期五, 十二月 27, 2002	Sheet 31 of 56	

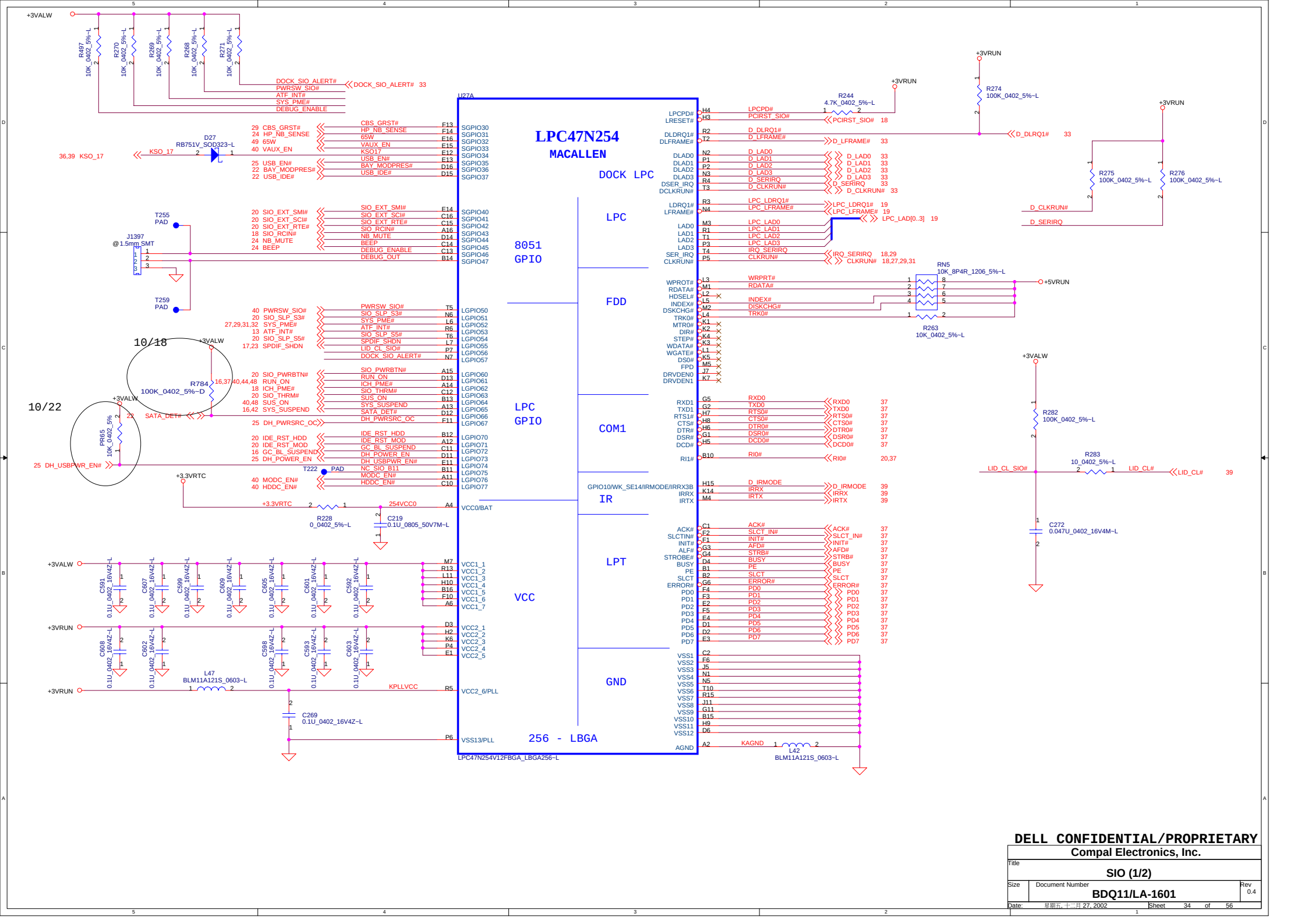


DELL CONFIDENTIAL/PROPRIETARY

Compal Electronics, Inc.

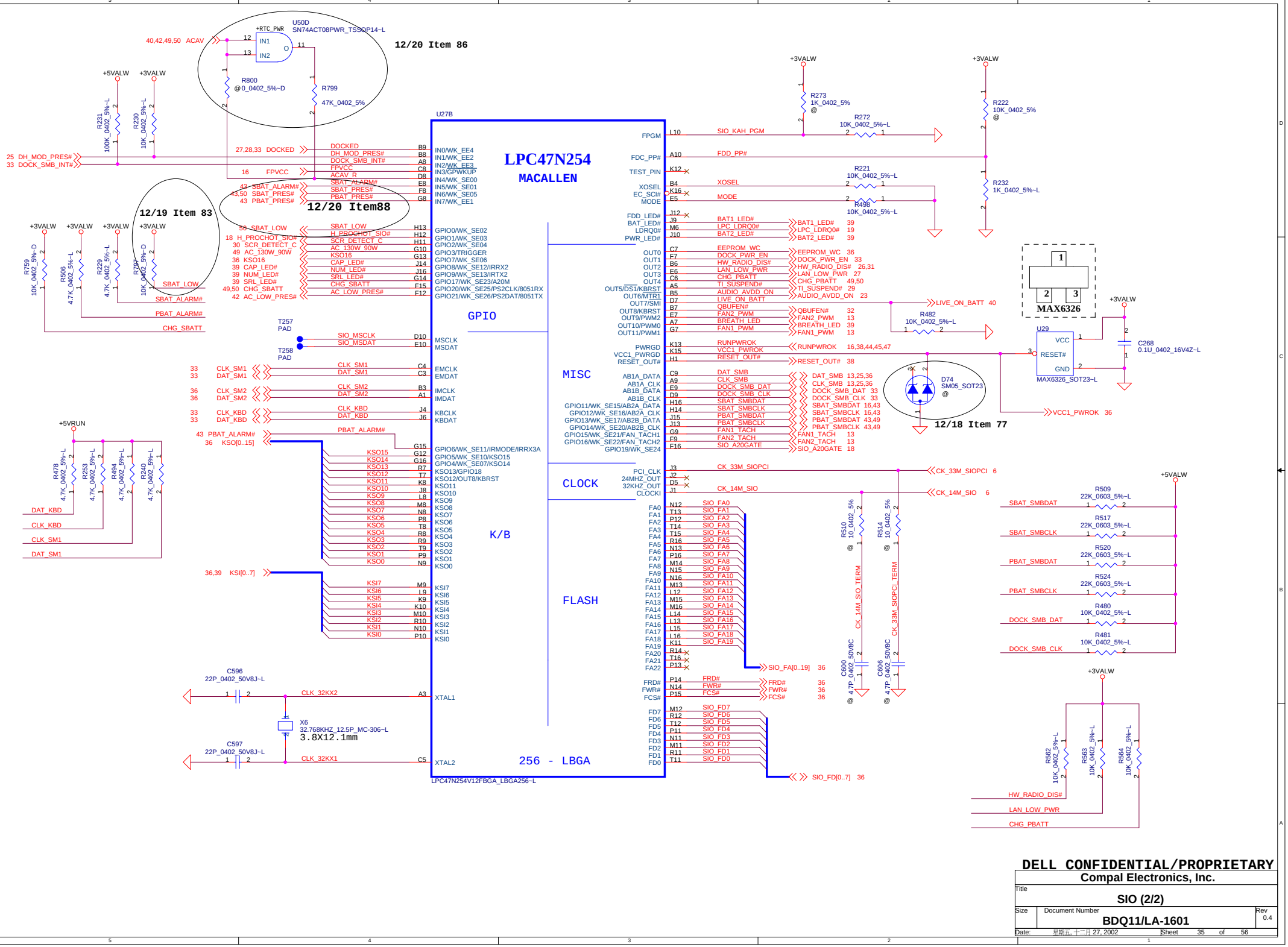
Title	DOCKING BUFFER	
Size	Document Number BDQ11/LA-1601	Rev 0.4
Date: 星期五, 十二月 27, 2002	Sheet 32 of 56	


JDOCKA
JDOCKB
JDOCKC
AMP_1473681-L
PLACE TERMINATIONS CLOSE TO DOCK CONNECTOR
DOCK_PWR_SRC
PWR_SRC
DOCK_AD[0..31] 32
U18
TC7SH04FU_SSOP5-L
U17
TC7SH08FU_SSOP5-L
U14
TC7SH08FU_SSOP5-L
Q34
FDS4435_SO8-L
Q43
2N7002_SOT23-L
Q38
2N7002_SOT23-L
U41
SN74AHCT1G08DCKR_SC70-5-L
JPH_WIRE
JST_SM07B-SRSS-TB-D
12/18 Item 73
10/25 Item49
8/12 Changed by
Dell's Required
NB
no power dock
PWR_SRC
self power dock
DELL CONFIDENTIAL/PROPRIETARY
Compal Electronics, Inc.
DOCKING CONN.
BDQ11/LA-1601
Rev 0.4
Sheet 33 of 56



Compal Electronics, Inc.

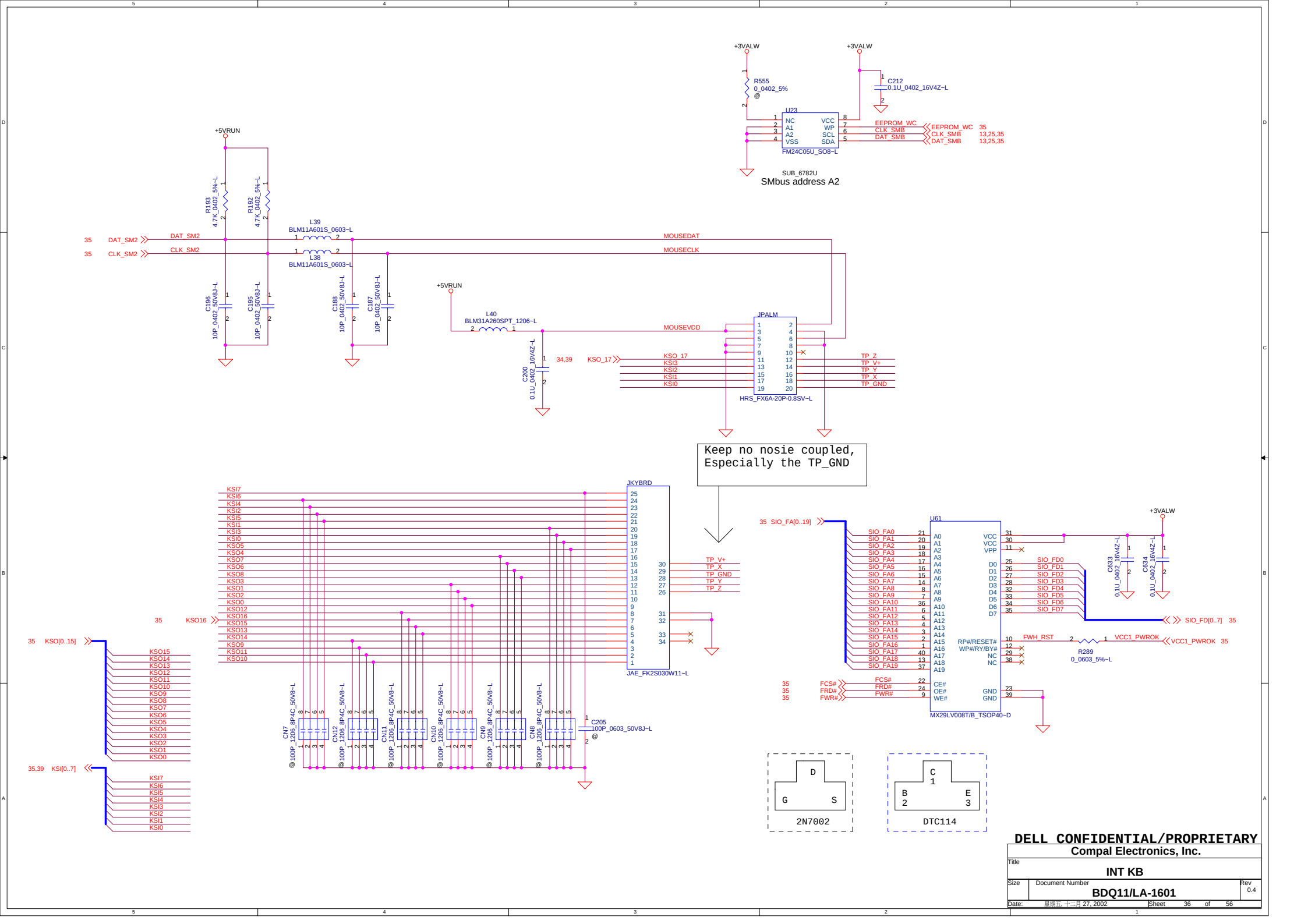
Title	SIO (1/2)	
Size	Document Number: BDQ11/LA-1601	Rev: 0.4
Date: 星期五, 十二月 27, 2002	Sheet 34 of 56	



DELL CONFIDENTIAL/PROPRIETARY

Compal Electronics, Inc.

Title	SIO (2/2)	
Size	Document Number: BDQ11/LA-1601	Rev: 0.4
Date: 星期五, 十二月 27, 2002	Sheet 35 of 56	

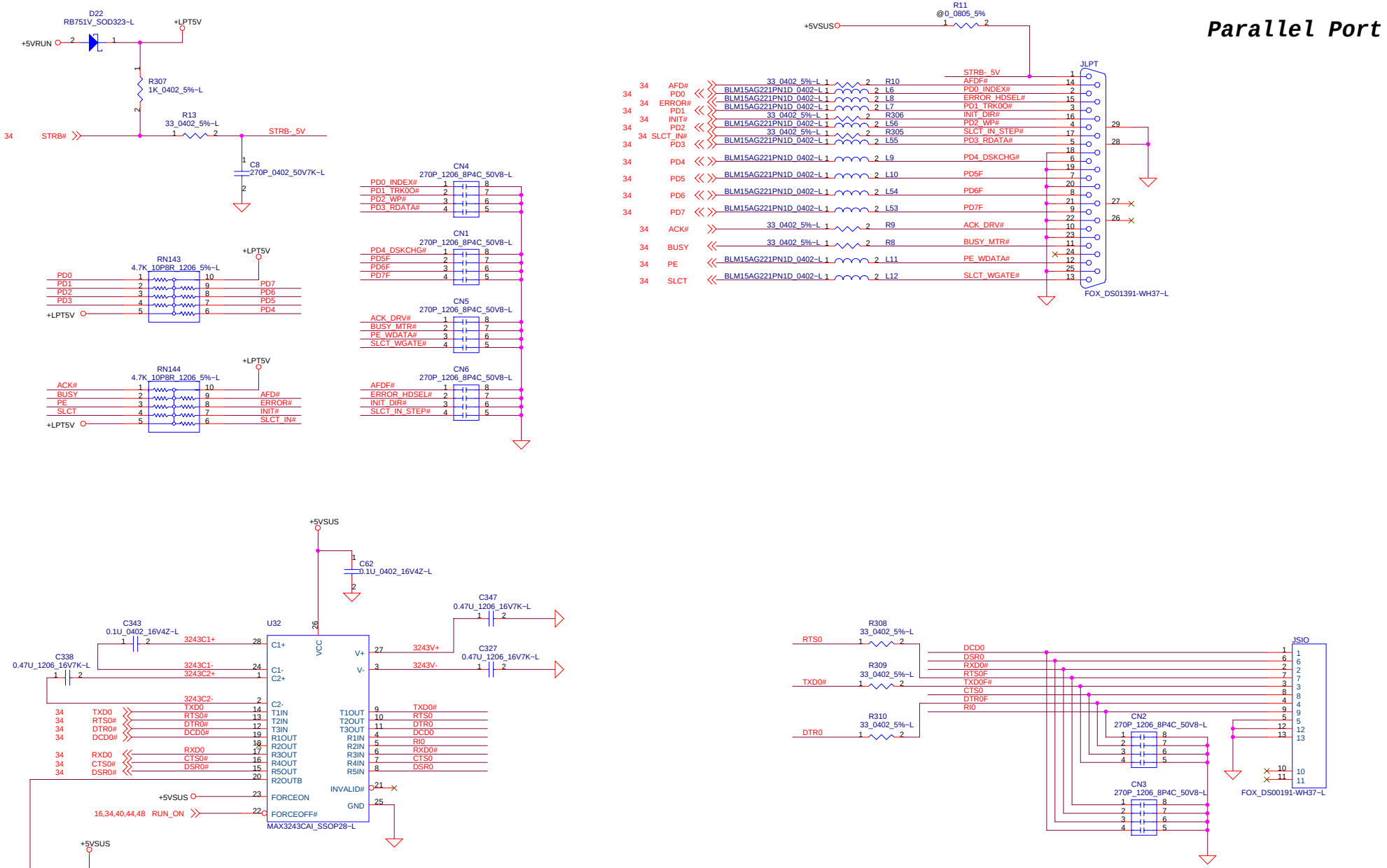


DELL CONFIDENTIAL/PROPRIETARY

Compal Electronics, Inc.

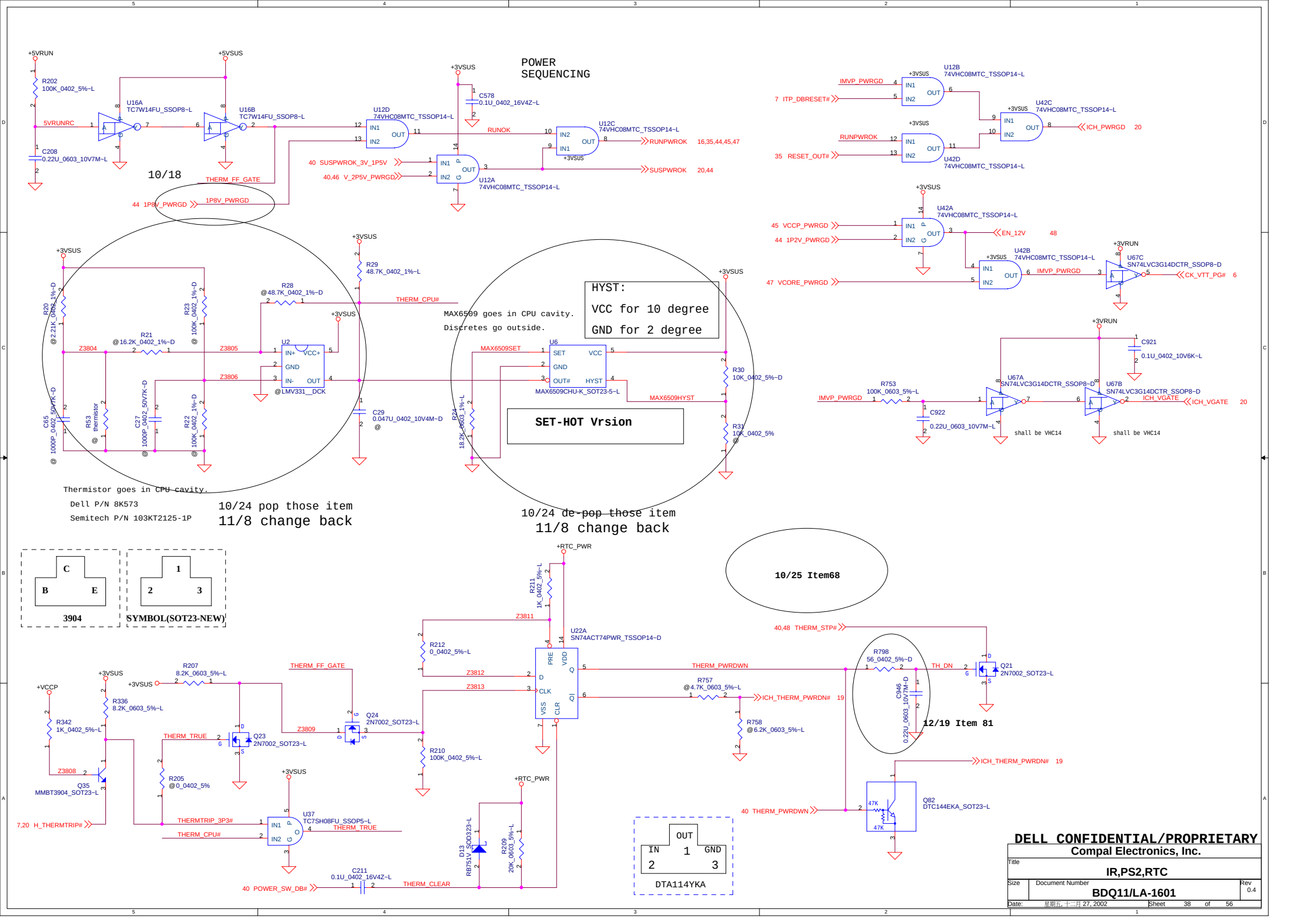
Title	INT KB	
Size	Document Number: BDQ11/LA-1601	Rev: 0.4
Date: 星期五, 十二月 27, 2002	Sheet 36 of 56	

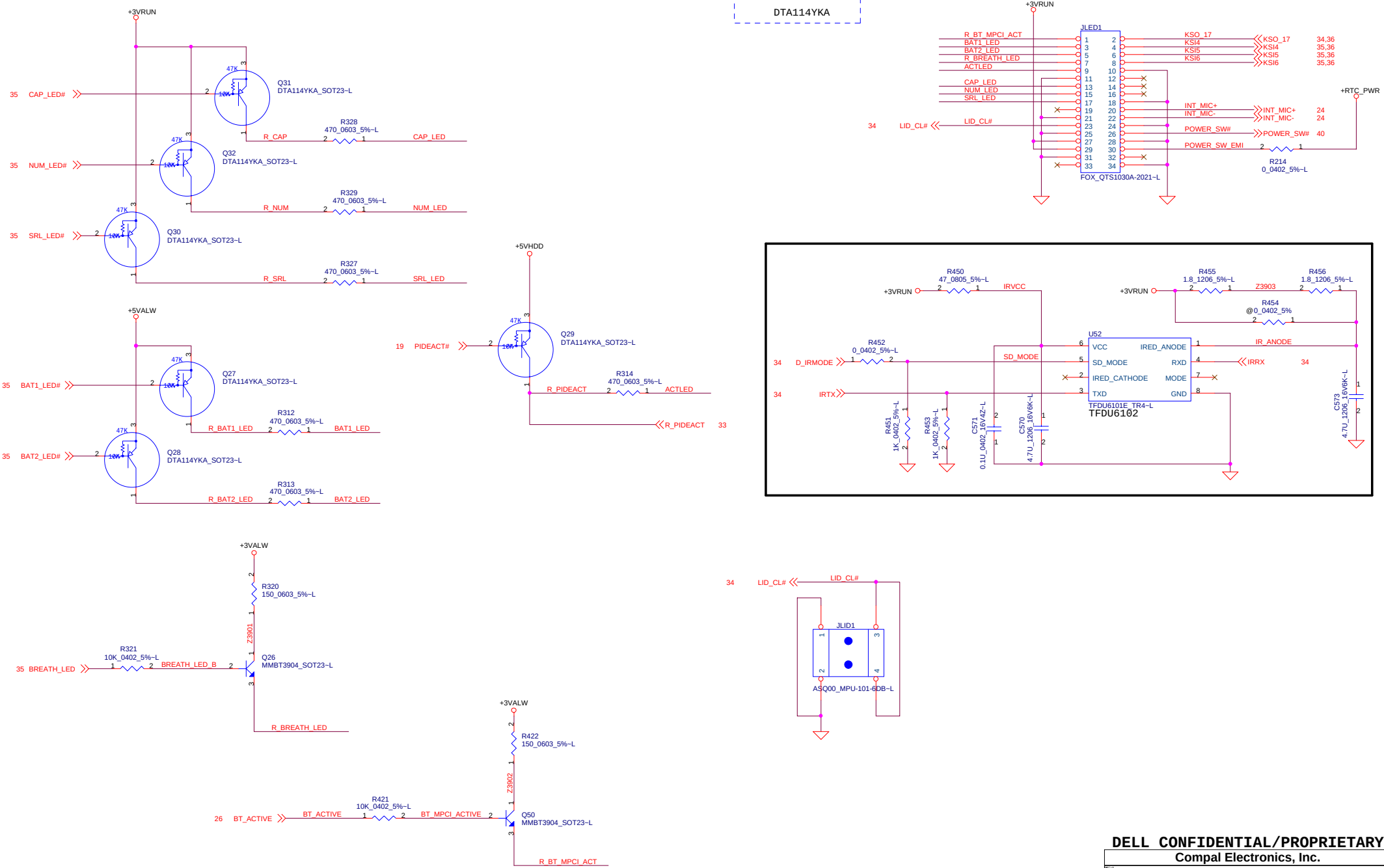
Parallel Port



Compal Electronics, Inc.

Title	LPT	
Size	Document Number: BDQ11/LA-1601	Rev: 0.4
Date: 星期五, 十二月 27, 2002	Sheet 37 of 56	

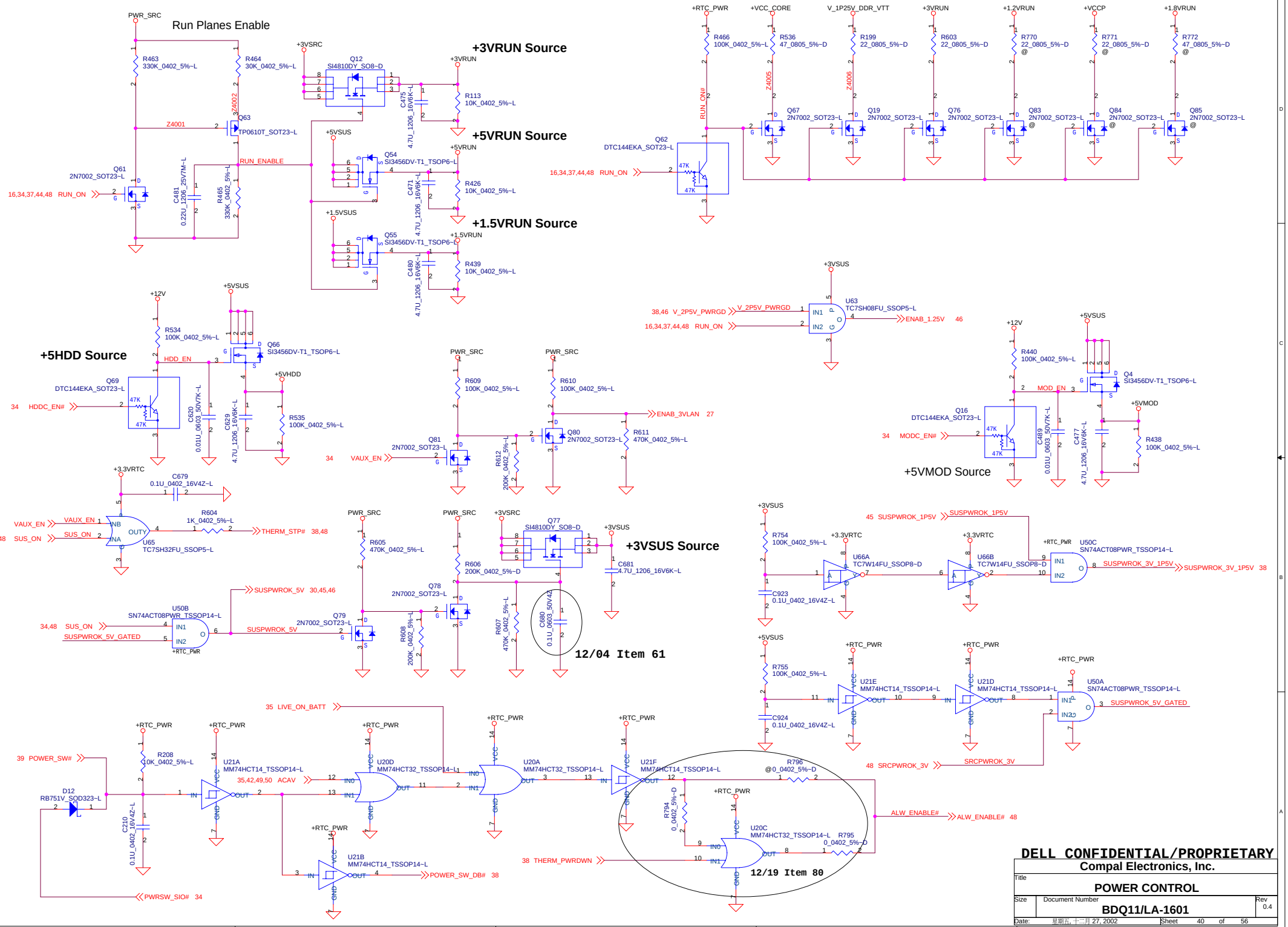

POWER SEQUENCING
+5VRUN
+5VSUS
+3VSUS
+3VRUN
R202 100K_0402_5%~L
C208 0.22U_0603_10V7M~L
5VRUNRC
U16A TC7W14FU_SSOP8~L
U16B TC7W14FU_SSOP8~L
10/18
THERM_FF_GATE
44 1P8V_PWRGD
1P8V_PWRGD
U12D 74VHC08MTC_TSSOP14~L
C578 0.1U_0402_16V4Z~L
RUNOK
U12C 74VHC08MTC_TSSOP14~L
RUNPWROK 16,35,44,45,47
40 SUSPWROK_3V_1P5V
40,46 V_2P5V_PWRGD
U12A 74VHC08MTC_TSSOP14~L
SUSPWROK 20,44
U12B 74VHC08MTC_TSSOP14~L
IMVP_PWRGD
7 ITP_DBRESET#
U42C 74VHC08MTC_TSSOP14~L
ICH_PWRGD 20
RUNPWROK
35 RESET_OUT#
U42D 74VHC08MTC_TSSOP14~L
U42A 74VHC08MTC_TSSOP14~L
45 VCCP_PWRGD
44 1P2V_PWRGD
EN_12V 48
U42B 74VHC08MTC_TSSOP14~L
47 VCORE_PWRGD
U67C SN74LVC3G14DCTR_SSOP8~D
CK_VTT_PG# 6
R29 48.7K_0402_1%~L
THERM_CPU#
R28 @48.7K_0402_1%~D
R20 @2.21K_0402_1%~D
R21 @16.2K_0402_1%~D
R23 @100K_0402_1%~D
Z3804
Z3805
Z3806
U2 @LMV331_DCK
C65 1000P_0402_50V7K~D
R53 thermistor
C27 1000P_0402_50V7K~D
R22 100K_0402_1%~D
C29 0.047U_0402_10V4M~D
Thermistor goes in CPU cavity.
Dell P/N 8K573
Semitech P/N 103KT2125-1P
10/24 pop those item
11/8 change back
MAX6509 goes in CPU cavity.
Discretes go outside.
HYST:
VCC for 10 degree
GND for 2 degree
U6 MAX6509CHU-K_SOT23-5~L
MAX6509SET
MAX6509HYST
R24 18.2K_0603_1%~L
R30 10K_0402_5%~D
R31 10K_0402_5%
SET-HOT Vrsion
10/24 de-pop those item
11/8 change back
C921 0.1U_0402_10V6K~L
R753 100K_0603_5%~L
C922 0.22U_0603_10V7M~L
U67A SN74LVC3G14DCTR_SSOP8~D
U67B SN74LVC3G14DCTR_SSOP8~D
ICH_VGATE
ICH_VGATE 20
shall be VHC14
shall be VHC14
C
B
E
3904
1
2
3
SYMBOL(SOT23-NEW)
+RTC_PWR
R211 1K_0402_5%~L
Z3811
U22A SN74ACT74PWR_TSSOP14~D
R212 0_0402_5%~L
Z3812
Z3813
10/25 Item68
40,48 THERM_STP#
R798 56_0402_5%~D
TH_DN
Q21 2N7002_SOT23~L
C946 0.22U_0603_10V7M~D
12/19 Item 81
THERM_PWRDWN
R757 @4.7K_0603_5%~L
ICH_THERM_PWRDN# 19
R758 @6.2K_0603_5%~L
+VCCP
R207 8.2K_0603_5%~L
R336 8.2K_0603_5%~L
R342 1K_0402_5%~L
THERM_TRUE
Q23 2N7002_SOT23~L
Q24 2N7002_SOT23~L
Z3809
R210 100K_0402_5%~L
Z3808
Q35 MMBT3904_SOT23~L
R205 @0_0402_5%
7,20 H_THERMTRIP#
THERMTRIP_3P3#
THERM_CPU#
U37 TC7SH08FU_SSOP5~L
D13 RB751V_SOD323~L
R209 20K_0603_5%~L
C211 0.1U_0402_16V4Z~L
40 POWER_SW_DB#
THERM_CLEAR
OUT
IN 1 GND
2 3
DTA114YKA
40 THERM_PWRDWN
Q82 DTC144EKA_SOT23~L
47K
47K
DELL CONFIDENTIAL/PROPRIETARY
Compal Electronics, Inc.
Title
IR,PS2,RTC
Size
Document Number
BDQ11/LA-1601
Rev 0.4
Date: 星期五, 十二月 27, 2002
Sheet 38 of 56

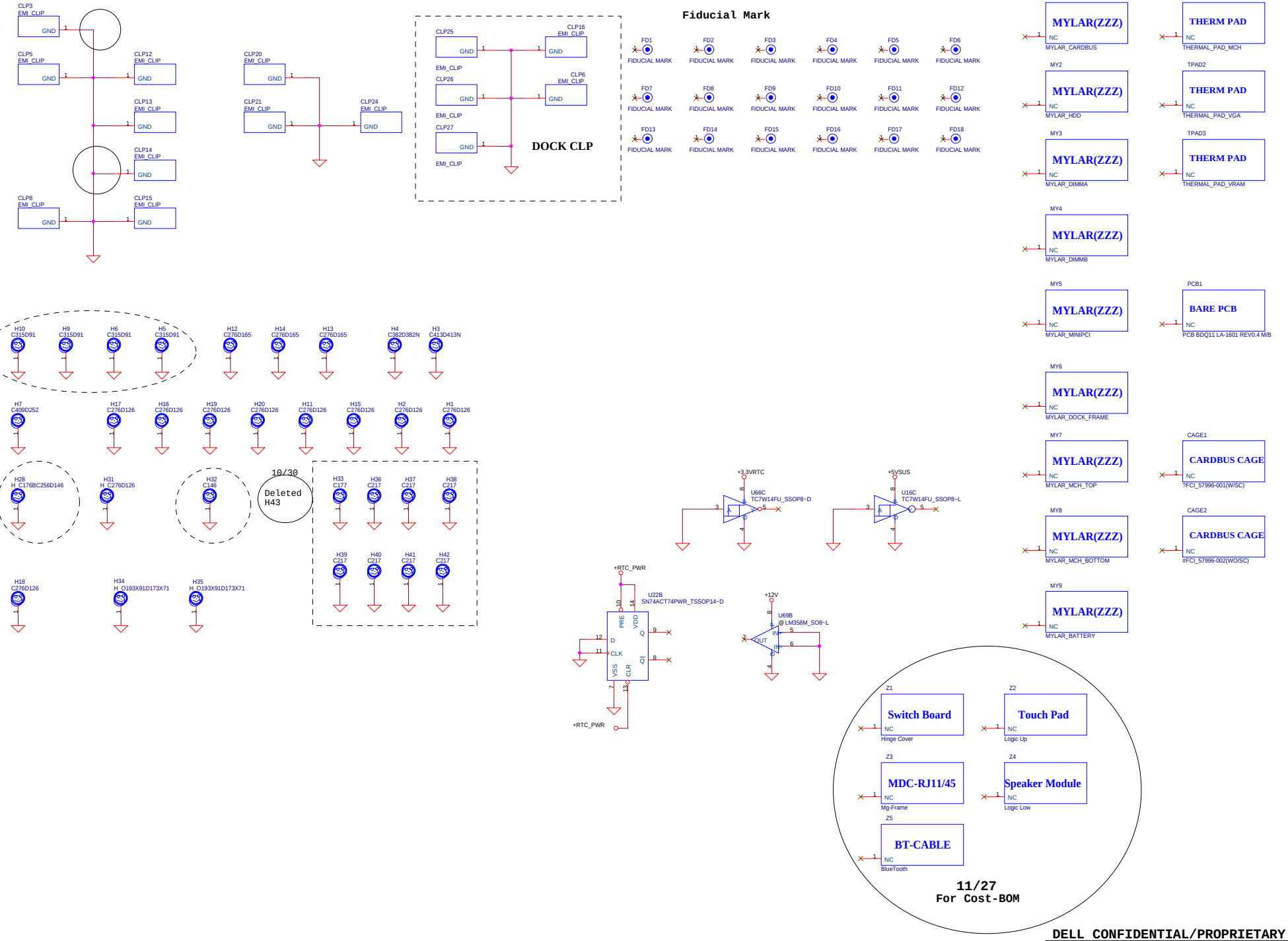


DELL CONFIDENTIAL/PROPRIETARY

Compal Electronics, Inc.

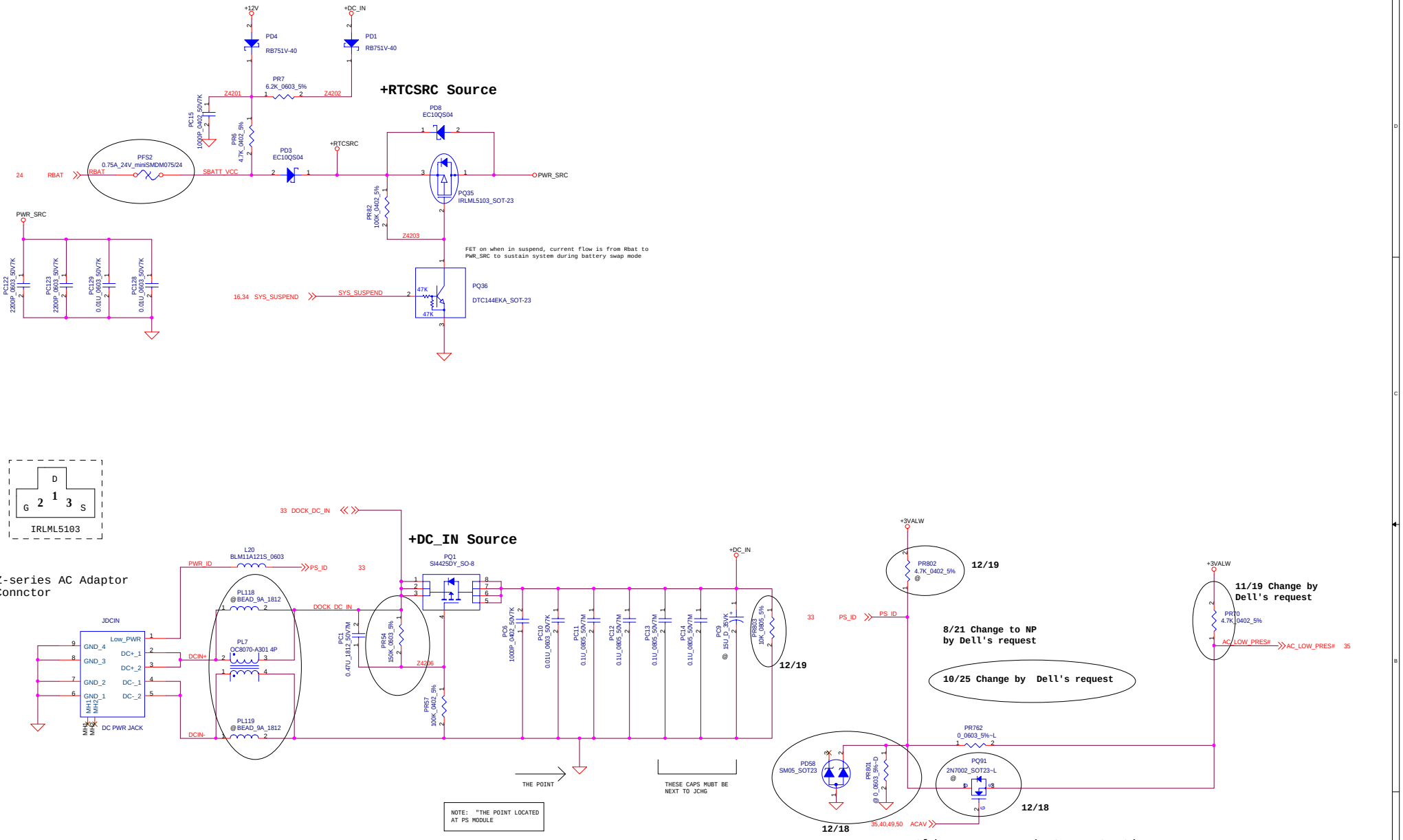
Title	JLED/IR/PS2	
Size	Document Number: BDQ11/LA-1601	Rev: 0.4
Date: 星期五, 十二月 27, 2002	Sheet 39 of 56	


Run Planes Enable
+3VRUN Source
+5VRUN Source
+1.5VRUN Source
+5HDD Source
+5VMOD Source
+3VSUS Source
12/04 Item 61
12/19 Item 80
PWR_SRC
+3VSRC
+3VRUN
+5VSUS
+5VRUN
+1.5VSUS
+1.5VRUN
+12V
+5VHDD
+3.3VRTC
+3VSUS
+RTC_PWR
+VCC_CORE
V_1P25V_DDR_VTT
+1.2VRUN
+VCCP
+1.8VRUN
+5VMOD
RUN_ON
RUN_ENABLE
HDD_EN
MOD_EN
VAUX_EN
SUS_ON
SUSPWROK_5V
SUSPWROK_5V_GATED
SUSPWROK_1P5V
SUSPWROK_3V_1P5V
SRCPWROK_3V
ALW_ENABLE#
ENAB_3VLAN
ENAB_1.25V
V_2P5V_PWRGD
HDDC_EN#
MODC_EN#
VAUX_EN
THERM_STP#
THERM_PWRDWN
POWER_SW#
POWER_SW_DB#
PWRSW_SIO#
ACAV
LIVE_ON_BATT
DELL CONFIDENTIAL/PROPRIETARY
Compal Electronics, Inc.
POWER CONTROL
BDQ11/LA-1601
Rev 0.4
Sheet 40 of 56



Compal Electronics, Inc.

Title	PAD and Standoff	
Size	Document Number: BDQ11/LA-1601	Rev: 0.4
Date: 星期五, 十二月 27, 2002	Sheet 41 of 56	



DELL CONFIDENTIAL/PROPRIETARY

Compal Electronics, Inc.

Title: DC-IN		
Size	Document Number: BDQ11/LA-1601	Rev: 0.4
Date: 星期五, 十二月 27, 2002	Sheet 42 of 56	

Secondary Battery Connector

ESD Diodes

+5VALW

PD18 @DDA204U

PD19 @DDA204U

PD20 @DDA204U

PD39 @DDA204U

JSBAT

BATT1+ 1
BATT2+ 2
SMB_CLK 3
SMB_DAT 4
BATT_PRES# 5
SYSPRES# 6
BATT_VOLT 7
GND 8
GND 9
BATT1- 10
BATT2- 11

SUYIN-20175A-09G1

PC67 2200P_0603_50V7K

Z4301
Z4302
Z4303

PR45 100_0603_5%
PR46 100_0603_5%
PR47 100_0603_5%

SBAT_SMBCLK 16,35
SBAT_SMBDAT 16,35
SBAT_ALARM# 35

SBATT+ 50

PC154 0.1U_0805_50V7M

+5VALW

PR44 10K_0402_5%

SBAT_PRES# 35,50

SUYIN_20175A-09G1

TRACE

THE POINT

Primary Battery Connector

ESD Diodes

+5VALW

PD32 @DDA204U

PD33 @DDA204U

PD34 @DDA204U

PD40 @DDA204U

JPBAT

BATT1+ 1
BATT2+ 2
SMB_CLK 3
SMB_DAT 4
BATT_PRES# 5
SYSPRES# 6
BATT_VOLT 7
GND 8
GND 9
BATT1- 10
BATT2- 11

SUYIN-20175A-09G1

PC56 2200P_0603_50V7K

Z4304
Z4305
Z4306

PR93 100_0603_5%
PR94 100_0603_5%
PR95 100_0603_5%

PBAT_SMBCLK 35,49
PBAT_SMBDAT 35,49
PBAT_ALARM# 35

PBATT+ 50

PC155 0.1U_0805_50V7M

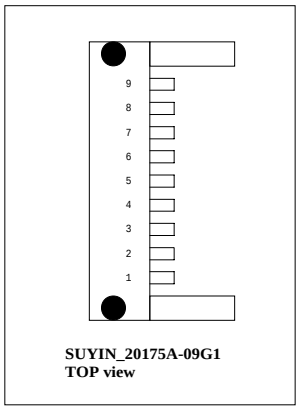
+5VALW

PR96 10K_0402_5%

PBAT_PRES# 35

TRACE

THE POINT



SUYIN_20175A-09G1
TOP view

DELL CONFIDENTIAL/PROPRIETARY

Compal Electronics, Inc.		
Title: Battery CONN.		
Size	Document Number: BDQ11/LA-1601	Rev: 0.4
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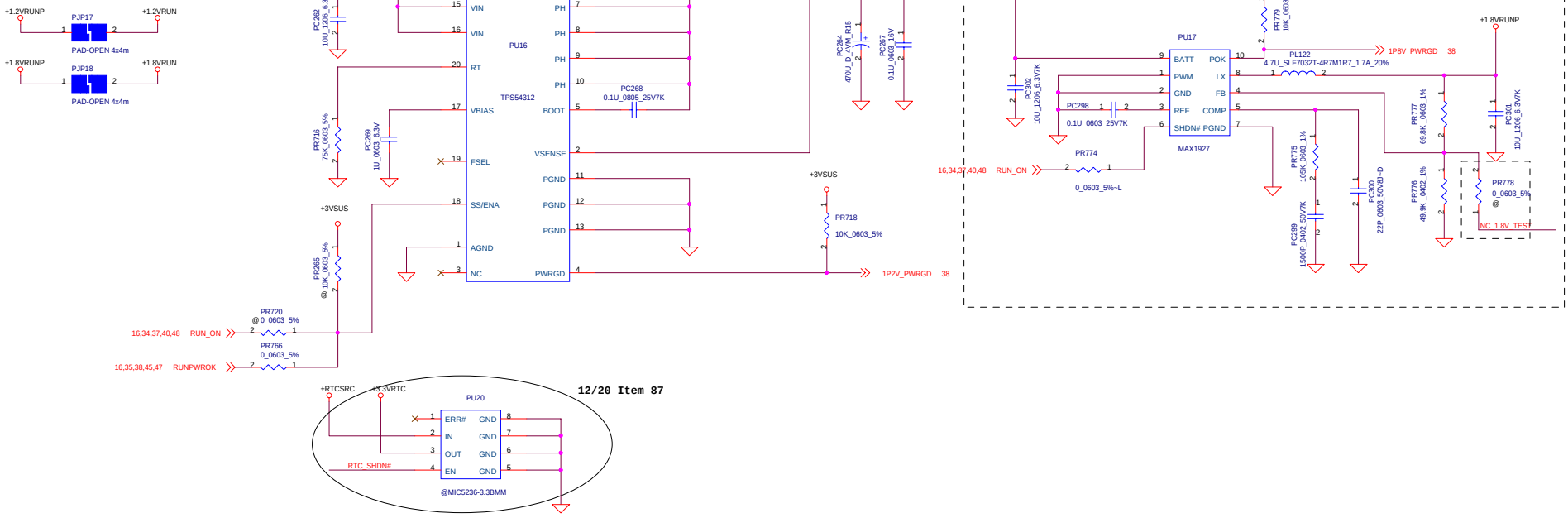
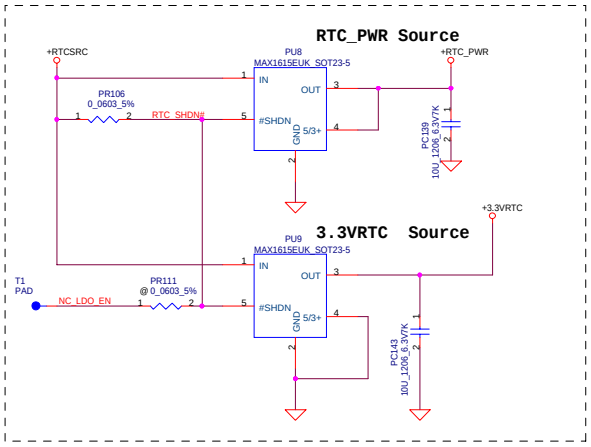
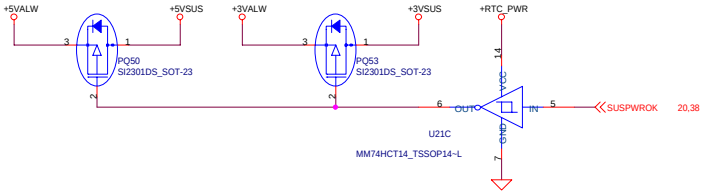
+1.2VRUN/+1.8VRUN SOURCE





Allows SUS planes to power ALW planes when possible



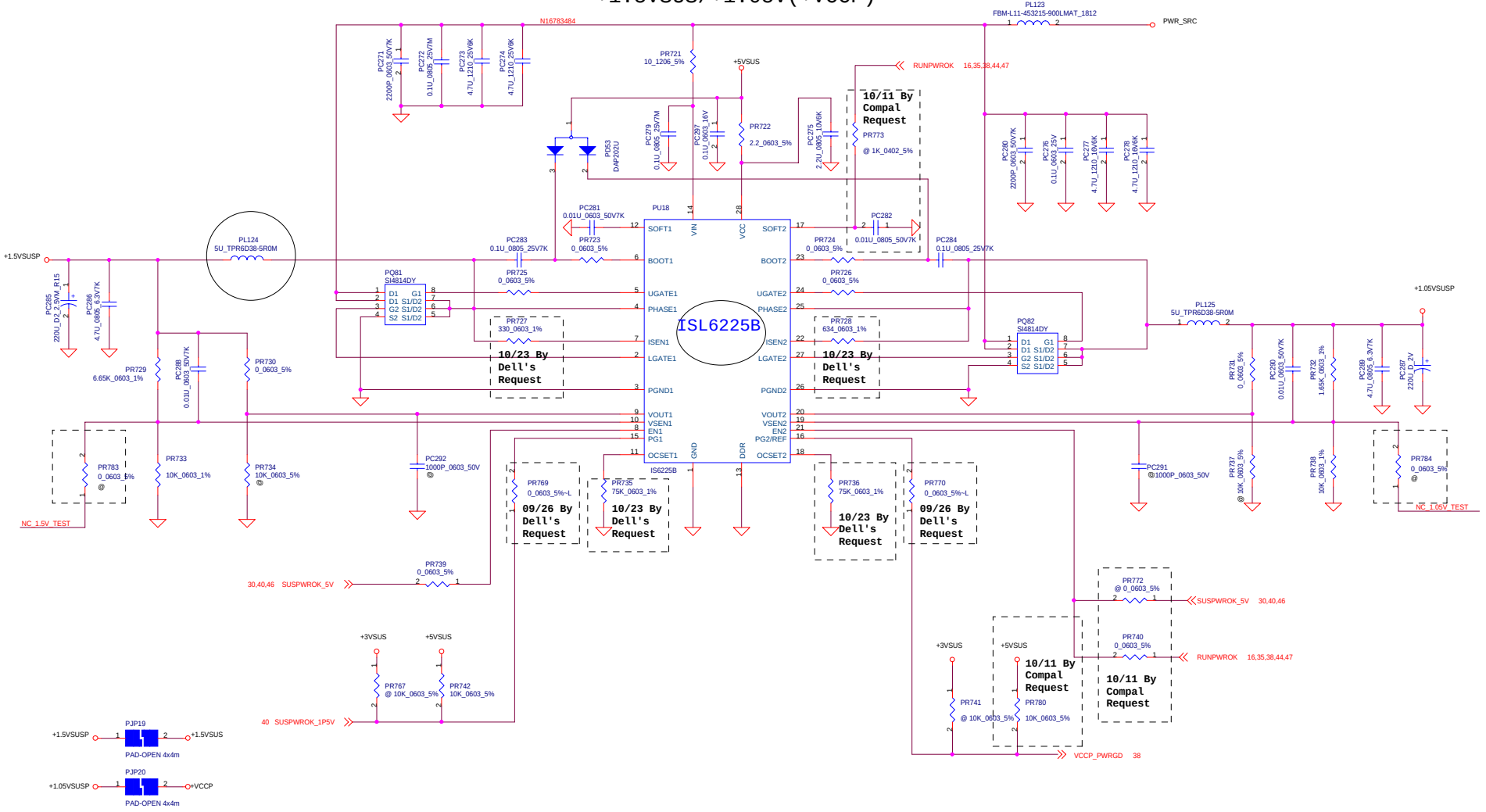
DA0JM2MBCA1

Dell-Compal Confidential

DELL CONFIDENTIAL/PROPRIETARY

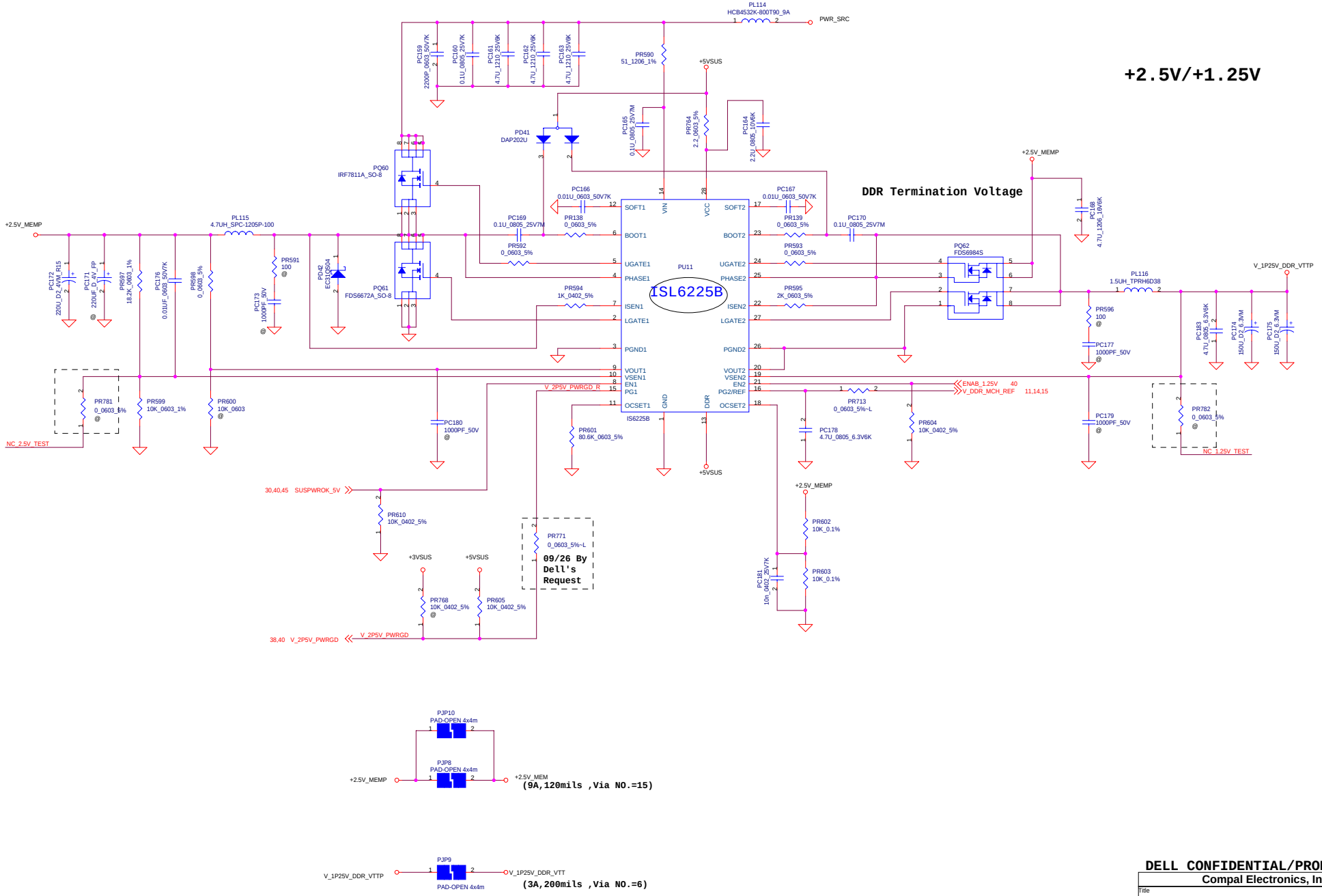
COMPAL ELECTRONICS, INC		
Title: **+1.2V & +1.8V**		
Size	Document Number: **BDQ11/LA-1601**	Rev: 0.4
Date: 星期五, 十二月 27, 2002	Sheet 44 of 56	

+1.5VSUS/+1.05V(+VCCP)



DELL CONFIDENTIAL/PROPRIETARY

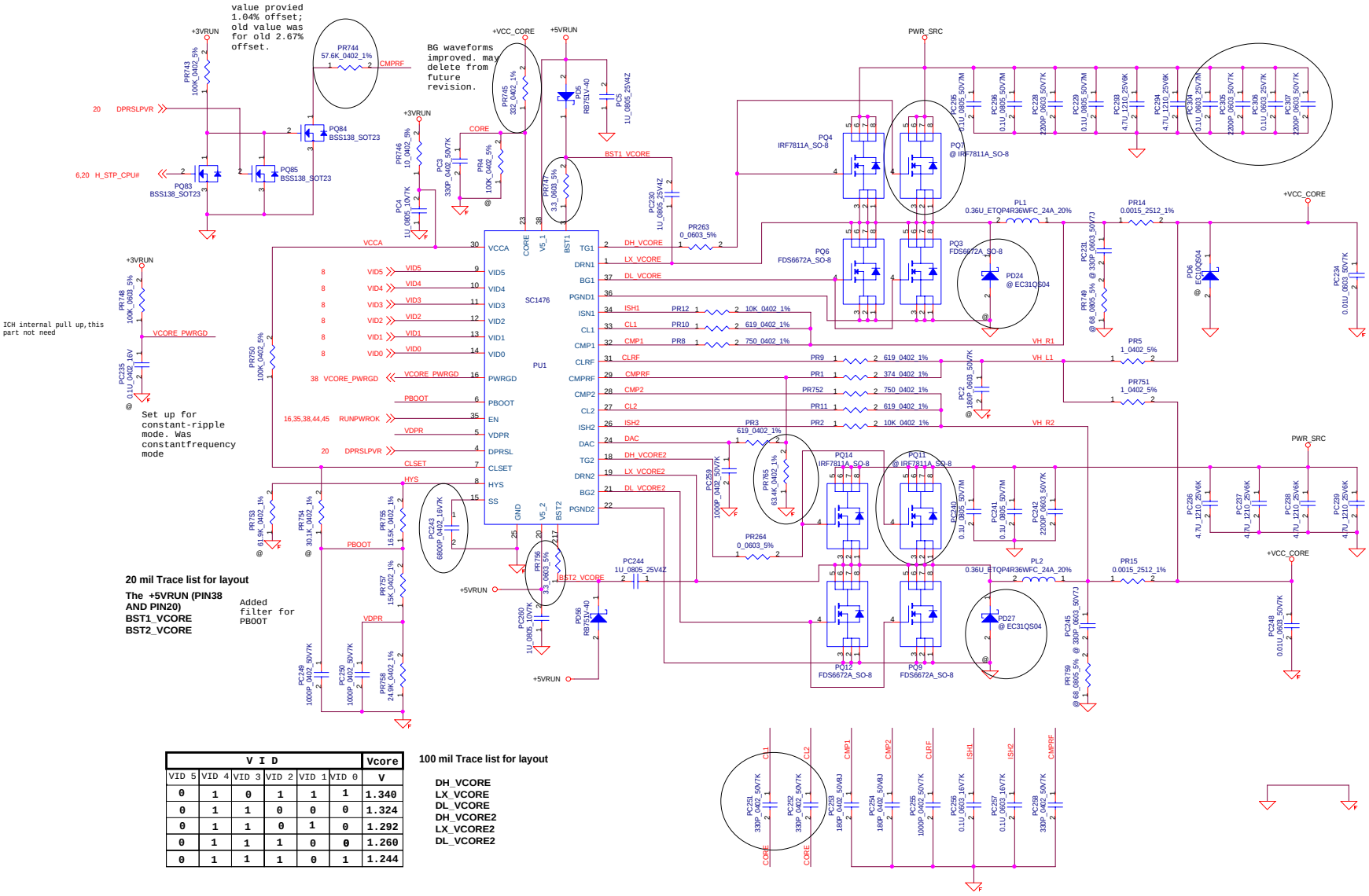
COMPAL ELECTRONICS, INC		
Title: +1.5V & +1.05V		
Size: B	Document Number: BDQ11/LA-1601	Rev: 0.4
Date: 星期五, 十二月 27, 2002	Sheet 45 of 56	



DELL CONFIDENTIAL/PROPRIETARY

Compal Electronics, Inc.

Title	1.25V/2.5V	
Size	Document Number BDQ11/LA-1601	Rev 0.4
Date: 星期五, 十二月 27, 2002	Sheet 46 of 56	



20 mil Trace list for layout

The +5VRUN (PIN38 AND PIN20)
BST1_VCORE
BST2_VCORE

V I D						Vcore
VID 5	VID 4	VID 3	VID 2	VID 1	VID 0	V
0	1	0	1	1	1	1.340
0	1	1	0	0	0	1.324
0	1	1	0	1	0	1.292
0	1	1	1	0	0	1.260
0	1	1	1	0	1	1.244

100 mil Trace list for layout

DH_VCORE
LX_VCORE
DL_VCORE
DH_VCORE2
LX_VCORE2
DL_VCORE2

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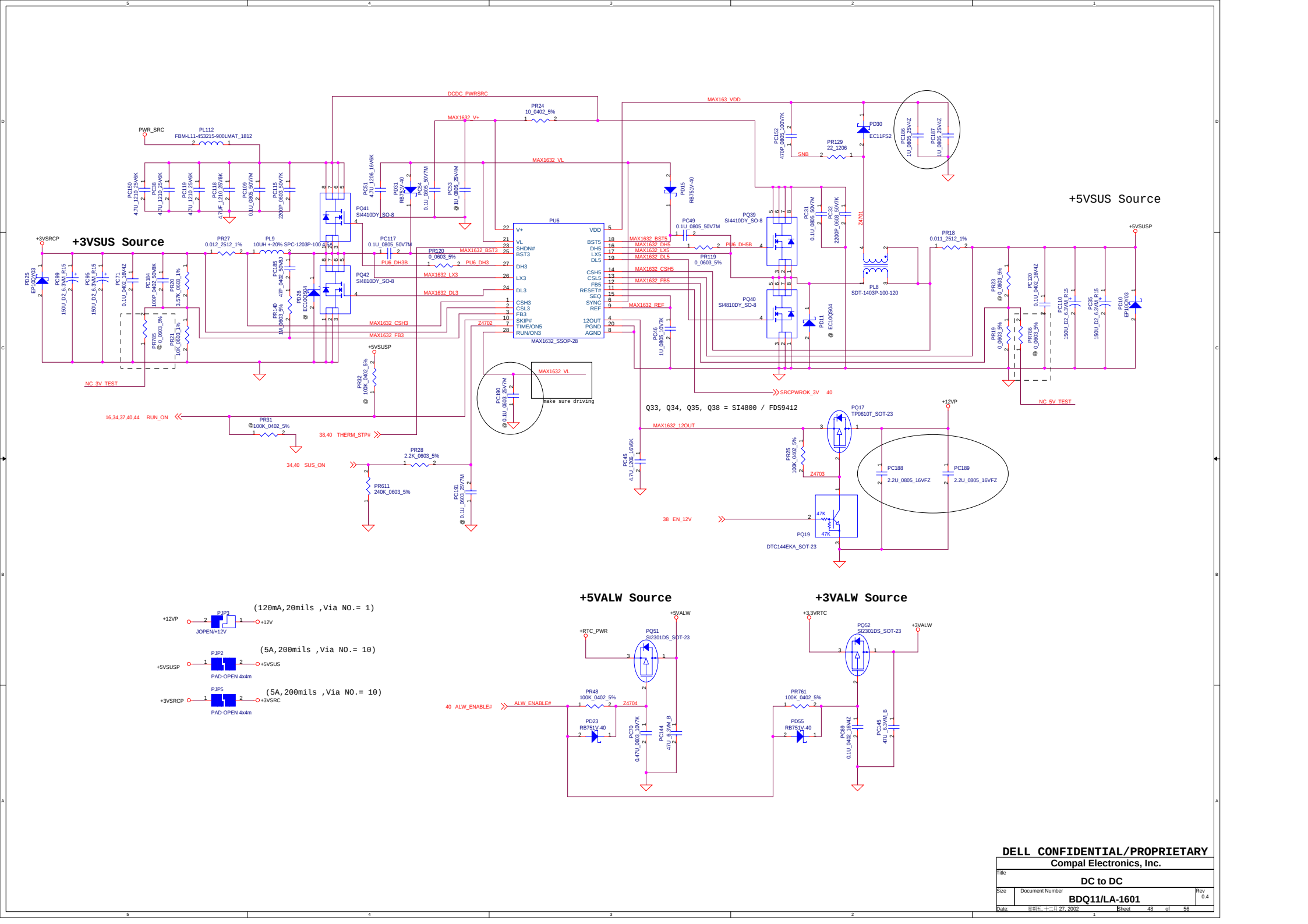
Compal Electronics, Inc.

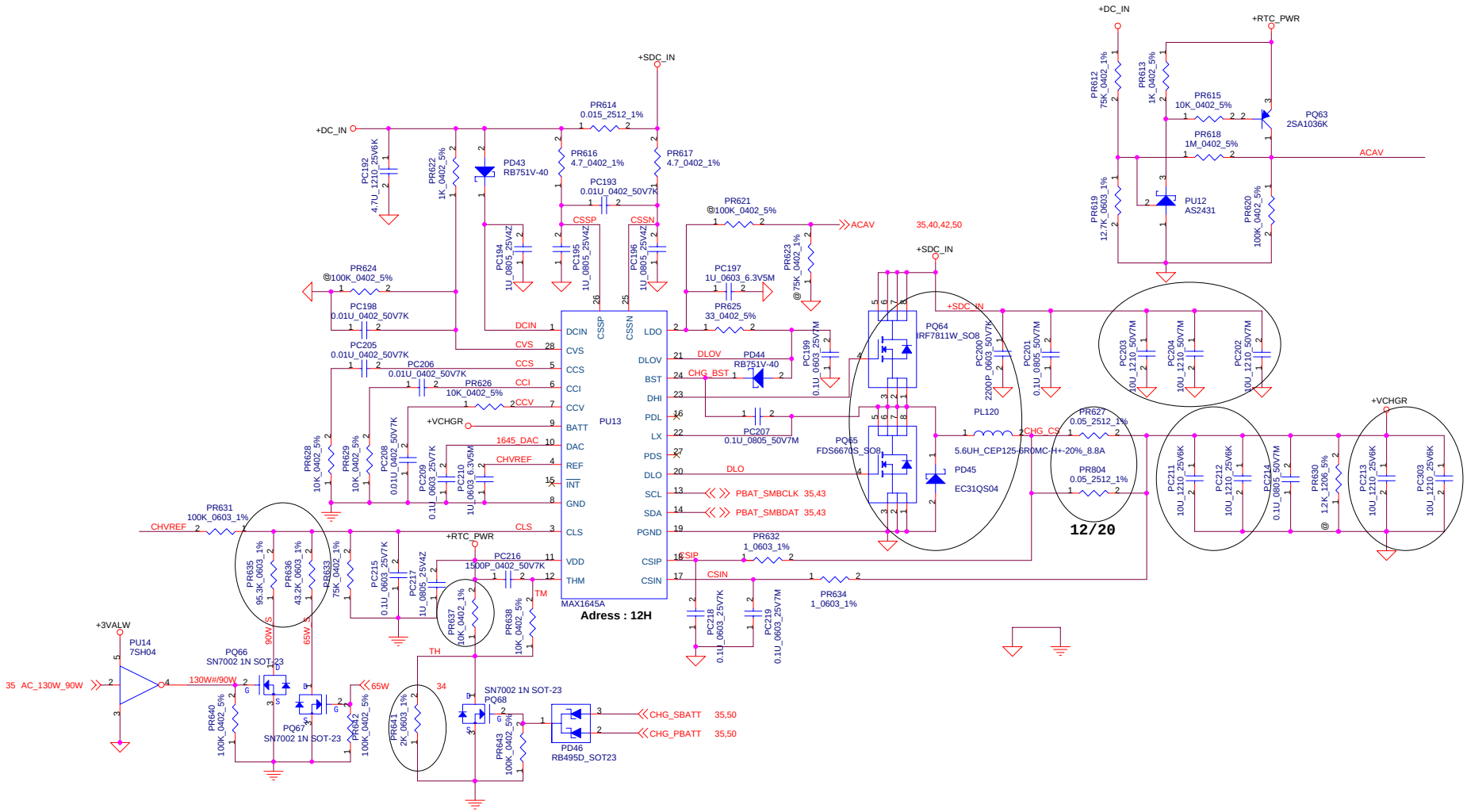
Title	VCC_CORE	
Size	Document Number BDQ11/LA-1601	Rev 0.4
Date: 星期五, 十二月 27, 2002	Sheet 47 of 56	



DELL CONFIDENTIAL/PROPRIETARY

Compal Electronics, Inc.

Title	DC to DC	
Size	Document Number: BDQ11/LA-1601	Rev: 0.4
Date: 星期五, 十二月 27, 2002	Sheet 48 of 56	



	65W (2.93A)	90W (4.0A)	130W (5.85A)
65W	H	L	L
AC_130W_90W	H	L	H

DELL CONFIDENTIAL/PROPRIETARY

Compal Electronics, Inc.

Title	CHARGER CONTROL	
Size	Document Number: BDQ11/LA-1601	Rev: 0.4
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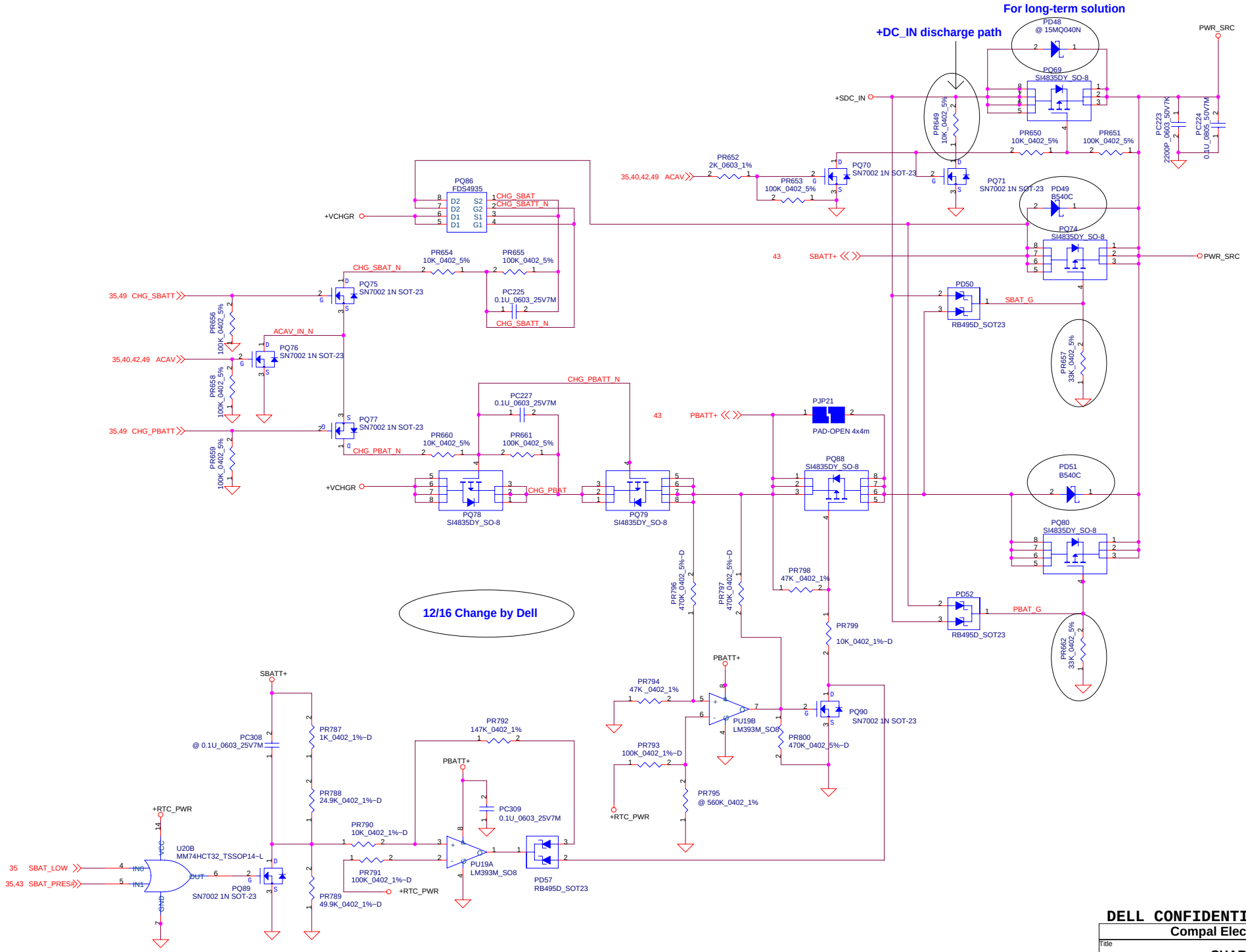

DELL CONFIDENTIAL/PROPRIETARY

Compal Electronics, Inc.

Title	CHARGER	
Size	Document Number: BDQ11/LA-1601	Rev: 0.4
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Version Change List (P. I. R. List)

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	27	Ethernet	8/18/2002	Dell	R318 use the same value as Lindbergh	R318 change the value from 1240Ω to 1140Ω	X00
2	27	Ethernet	8/18/2002	Dell	Netname change for A-2 chip	Change netname from 4401_CLOCKRUN to 7705M_CLOCKRUN	X00
3	27	Ethernet	8/18/2002	Dell	Populate R549 for 5705m controller	5702 controller do not need to change	X00
4	27	Ethernet	8/18/2002	Dell	Delete "POP SMBUS ISOLATION FOR 5702" text	Delete text	X00
5	28	LAN Transformer	8/18/2002	Dell	No need for RN1 pins 1,2,3,4 to ground connection	Delete RN1 pins 1,2,3,4 to ground connection	X00
6	28	LAN Transformer	8/18/2002	Dell	Delete the net name LIN_1000# and rename net of Pin G12 of U1A, Page 27, to DOCK_LED_1000#	Delete the net name LIN_1000#, rename net of Pin G12 of U1A to DOCK_LED_1000#	X00
7	8	Banias Processor	8/20/2002	Dell	PJP11 & PJP16 text discribe error	Change PJP11 to Short, and PJP16 to Open.	X00
8	27	Ethernet	8/20/2002	Dell	So for now we can use 1150 Ohm 1% resistor for R318 and we will continue with the 5702 controller	R318 change the value from 1140Ω to 1150Ω	X00
9	31	MINI PCI	8/20/2002	Dell	Change pin 17 of JPCI from PCI_PIRQB#to PCI_PIRQD#	Change pin 17 of JPCI connection to PCI_PIRQD#	X00
10	42	Power DC-IN	8/20/2002	Dell	Change PQ32, PR73, PR74, PR77 to NP	Updated PQ32, PR73, PR74, PR77 to NP	X00
11	1	Cover page	8/22/2002	Dell	On the title page place the name of the project Kapalua	Updated Schematic cover page text	X00
12	27	Ethernet	8/23/2002	Dell	Changed netname from 7705M_CLOCKRUN to 5705M_CLOCKRUN	Updated netname from 7705M_CLOCKRUN to 5705M_CLOCKRUN	X00
13	28	LAN Transformer	8/23/2002	Dell	Add 0Ω (0402 resistors) resistors to LAN_TX0-/LAN_TX0+, LAN_RX1-, LAN_RX1+, LAN_TX2-, LAN_TX2+, LAN_TX3-, LAN_TX3+	Add R776, R777, R778, R779, R780, R781, R782 and R783	X00
14	40	Power Control	8/29/2002	Dell	Using wrong Control signal of 1.8V rail drain on Q85.2	change the control signal of 1.8V rail drain from RUN_ON# to RUN_ON	X00
15	44	1.8V / 1.2V	8/29/2002	Compal	Change the control signal of 1.2VRUN from RUN_ON to RUNPWROK	Add PR766 connect to RUNPWROK, and reserved PR720 to NP	X00
16	47	Vcc_core	9/03/2002	Dell	Banias update PBOOT voltage spec with 1.2V	change PR755 to 16.5K and PR757 to 15K	X00
17	40	Power Control	9/04/2002	Compal	System doesn't work in using battery only	change R606 value from 470K to 200K	X00
18	40	Power Control	9/04/2002	Compal	Reduce +3VSUS power rail up response time and solve unexpected step waveform	Depoplate C680	X00
19	16	VGA Board	9/04/2002	Compal	VGA no reference voltage detected	Poplate R124	X00
20	45	1.5V / 1.05V	9/05/2002	Compal	SUSPWROK_1P5V control signal detected glitch issue	Add PR767 and reserved PR742 to NP	X00
21	46	1.25V / 2.5V	9/05/2002	Compal	SUSPWROK_1P5V control signal detected glitch issue	Add PR768 and reserved PR605 to NP	X00
22	40	Power Control	9/26/2002	Dell	Fixes the 3VSRC to 3VRUN leakage problem	Change R603 to 22Ω	X01
23	40	Power Control	9/26/2002	Dell	We need a Soft Start feature	Add C680 to 0.1uF	X01
24	20	ICH4-M	9/26/2002	Dell	Change Board Rev resistor to X01	Depopp R287, and Pop R286	X01
25	45	1.5V / 1.05V	9/26/2002	Dell	Add 0 ohm resistor at PU18 pin 15 and pin 16	Add PR769 and PR770 to 0Ω	X01
26	46	1.25V / 2.5V	9/26/2002	Dell	Add a 0 Ohm resistor at PU11 pin 15	Add PR771 to 0Ω	X01

Version Change List (P. I. R. List)

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
27	28	NIC high po issue	10/17/2002	DELL	High po test fail because RN1 pitch too close	change RN1 to R785~r788 to enlarge the pitch	X01
28	26	Add PD for BT	10/17/2002	DELL	leverage from LK	Add R789 10K PD to COEX1_BT_ACTIVE	X01
29	25	Dog h power rating	10/17/2002	Compal	The current rating of MOS and Bead is too small in Dog house	change Q1,F1,F2,L25 part to meet Doghouse power current requirement (F1&F2 create new symbol now)	X01
30	13	Fan soft start	10/17/2002	Compal	No soft start will cause +5VRUN droop low	change C127 and C110 pop(don't do it per Bo's command)	X01
31	22	cd audio pull down	10/18/2002	Compal	when we don't plug in CDrom, audio trace will floating	add R, R to tie the INT_CD_L/R ground	X01
32	22	reserve series ATA	10/18/2002	DELL	change some power/gnd to NC for reserve series ATA interface for module bay	change JMOD1pin8,10,14,16,9,11, to NC,conecct SATA_DET# to pin13	X01
33	20,22,34	add SATA detect	10/18/2002	DELL	Add series ATA detect in module bay	Add net sdat_det# from module bay to EC and remove original sio_lid_ec#	X01
34	31	change MINI PCI pin NC	10/18/2002	DELL	change MINI-PCI pin NC (No use)	Change JPCI pin109 and pin 123 to NC and remove R457,C576	X01
35	22	cr_ref return cap.	10/18/2002	DELL	add a pull down cap. for cd_ref near JMOD	Add C from CD_REF to gnd near JMOD and let it unpop	X01
36	29	Reserve PCMCIA suspend pin	10/18/2002	DELL	Reserve PCMCIA suspend pin not use	depop D11 and reserve it for TI_SUSPEND#	X01
37	40	Power Q-SW	10/18/2002	DELL	Lindberg change Q12,Q55,Q77 to big rating, we will wait the EA report and make a decision	TBD	X01
38	38	Add 1.8V PWRGD	10/18/2002	DELL	Add the 1.8V power good to power sequence	change the U12 pin 13 from RUN_ON to 1P8V_PWRGD	X01
40	25,34	change DH enable	10/22/2002	DELL	change dog house power enable pin independent	Add R and R seprate the control signal of DH	X01
41	10	change to normal part	10/22/2002	Compal	change to vender normal specification part	change C743 and C744 from 50V to 16V rating	X01
42	31	remove pin of MINI-PCI	10/24/2002	DELL	change pin def. of MINI-PCI	romove pin 118, 120 ground for JPCI	X01
43	42	remove some PS_ID	10/24/2002	DELL	remove some reserve PS_ID component	Remove PQ32, PR77, PR74, PR73	X01
44	26,31	duplicate pull down	10/24/2002	DELL	remove duplicate pull down	Remove R789 , only use R767	X01
45	29	remove not use part	10/24/2002	DELL	remove the reserve part for carrbus controller	Delete R185	X01
46	38	change the thermaltrip	10/24/2002	DELL	change the thermal shut down solution from MAX to discrete	del U6, R30, R24, add U2, C29, R28, R20~R23, C27, R53, C65	X01
47	28	ESD Safty issue	10/24/2002	Compal	JPH_RJ layout change connection from GND to NC.	JPH_RJ pin 6 change connection from GND to NC.	X01
48	40	+3VRUN, +3VSUS FET ISSUE	10/25/2002	DELL	+3VSUS AND +3VRUN POWER RAIL MOSFET PARTS CHANGED	Q12 AND Q77 CHANGE PARTS FROM SI3456 TO SI4810	X01
49	33	RGB Beads value changed	10/25/2002	DELL	Using 0Ω instead of beads	Change the value of L101, L102, L103, L14, L15, L16, L17, L18	X01
50	33	Rev. changed	10/25/2002	Compal	PI3L301A revision change to PI3L301BA	U30 Rev. changed	X01
51	25	Dog House	10/29/2002	DELL	Must Design as Lindberg	R25 changed value to 100K and R26 (10K) change to C940 (0.022u_0805)	X01
52	9,12,15	Bulk Cap.	10/29/2002	Compal	Height Restriction	Changed Height limit of C933,C934,C731,C779,C788,C789 to 1.9mm	X01
53	9	CPU Bypass	10/29/2002	DELL	Changed Voltage Limit of Bulk Cap. to 2V	C671~C675,C678 Changed Voltage Limit to 2V	X01

Version Change List (P. I. R. List)

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
54	41	PAD & ME Parts	10/30/2002	Compal	Need to BLOW OUT 2.5mm Radius from Vias used to transition Tip and Ring from InnerLayer 4 to Bottom layer	Deleted H43 to meet 2.5mm Radius gap	X01
55	28	Magnetic & RJ45	10/30/2002	DELL	LAN Analog Switch (PI3L301BA) serial Resistors change type to Inductors	R776~R783 changed value from 0Ω to 15nH Inductor (TDK MLK1005S15NJ)	X01
56	27	POP option to 5705	11/08/2002	Compal	change the POP option from 5702 to 5705	Depop L66, C344, C324 Pop R217, R549	X01
57	38	Thermal shut down to 6509	11/08/2002	DELL	change the thermal shut down solution from discrete to MAX	Add U6, R30, R24 Depop U2, C29, R28, R20~R23, C27, R53, C65	X01
58	27	LOM	11/27/2002	Compal	Depop dummy parts	Depop R388 (10K_0402) and C390 (0.01U_0402)	X02
59	41	PAD, ME, SPARE PARTS	11/27/2002	Compal	Add Cost-BOM parts	Add Z1(Switch-FPC), Z2(Touch_pad-FPC), Z3(MDC-Cable), Z4(Spk-mODULE), Z5(BT_Cable).	X02
60	9	Vcore Bypass CAP.	11/29/2002	Compal	Vcore by pass capacitors adjust for cost	Add C941~C944 (220UF_2V) Depop C671~C675, C678 (220UF_2V)	X02
61	40	Power Control	11/29/2002	Compal	package change for increase voltage tolerance	C680 changed package from 0402 to 0603	X02
62	20	ICH4	12/02/2002	Compal	Depop dummy part	Depop R529 (10K_0402)	X02
63	25	Dog House	12/04/2002	Compal	package change for conponent easy to buy	C940 changed package from 0805 to 0603	X02
64	30	Smart Card	12/11/2002	DELL	Need to implement 5 Smart Card changes.	Add R12, R26, C184 , C189 , Change C171 value from 390P to 470P	X02
65	27	BCM5705 By Pass	12/11/2002	DELL	Need to change package from 0805(4.7UF) to 1206(10UF) for cost	C339, C346, C352, C355, C393, C662, C368, C342	X02
66	All Pages	Page Symbol	12/13/2002	DELL	Put “DELL CONFIDENTIAL/PROPRIETARY” on Schematics	DELL CONFIDENTIAL/PROPRIETARY	X02
67	20	ICH4	12/13/2002	Compal	Delay Thermtrip to ICH4 so Intruder have time to get latch	Add C46 (0.1UF_0402)	X02
68	38	Thermtrip	12/17/2002	DELL	For Charger Selector Used (SIO_THERM_PWRDN net changed to SBAT_LOW)	Delete R206 & Q22	X02
69	27	BCM5705	12/17/2002	DELL	Delete 4401 Conponents	Delete U43, R388, C390, R367, R368	X02
70	28	LAN Transformer	12/17/2002	DELL	Delete 4401 Conponents	Delete R551 ~ R554	X02
71	28	LAN Transformer	12/18/2002	DELL	Changed Spec 2KV to 3KV	C1	X02
72	17	TV OUT	12/18/2002	DELL	Depop C14 for Cost	Depop C14	X02
73	33	DOCKING CONN.	12/18/2002	DELL	Remove RGB Bead	Delete L101, L102, L103, L14 ~ L18 & Trace	X02
74	23	AC,97 CODEC	12/18/2002	Compal	Added Resistors for TPS793475 LDO	Add R792, R793 (@)	X02
75	23	AC,97 CODEC	12/18/2002	DELL	Change the Value from 0 ohm to 1UF_0805	C636	X02
76	17	TV OUT	12/18/2002	DELL	Change RGB Bead Value & Package	Changed L5, L104, L105	X02
77	35	SIO	12/18/2002	Compal	ESD Cap is Needed on Reset to McCallen	Add D74 for ESD (@)	X02
78	30	Smart Card	12/19/2002	DELL	Added Cap (4.7uF_0805) for SCR_VCC_C to Depop	Add C945 for depop (@)	X02
79	17	TV OUT	12/19/2002	Compal	Depop C10, C11, C661 for RGB Signals	Depop C10, C11, C661(@)	X02

Compal Electronics, Inc.	
Title	Changed-List History
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Version Change List (P. I. R. List)

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
80	40	Power Control	12/19/2002	Dell	Change ALW_ENABLE# control methos through OR gate	Added R794, R795, R796	X02
81	38	Thermtrip	12/19/2002	Dell	THERM_PWRDN add RC delay	Added R798, C946	X02
82	26	MDC, BT	12/19/2002	Dell	Pop JPT pin 7 resistor	Added R765	X02
83	35	SIO	12/19/2002	Dell	SBAT_LOW pull-up through 4.7K resistor	Added R797	X02
84	24	Phone JACK	12/20/2002	Dell	R252 to be connected to EXT_MIC_PLUG instead of EXTMIC_PLUG	R252	X02
85	24	Phone JACK	12/20/2002	Dell	add a 100PF cap to GND on Sheet 24 on the EXTMIC_PLUG so that signal is terminated as well for protection purposes	Added C947	X02
86	35	SIO	12/20/2002	Compal	ACAV Leakage Current	Added R799, R800 & ACAV through AND gate	X02
87	20	ICH4M	12/20/2002	Dell	Board ID X02	Pop R278, R287 & Depop R279, R286	X02
88	35	SIO	12/20/2002	Dell	Add Battery Selector Circuit	U27.H13 netname changed to SBAT_LOW	X02
89	28	Magantic	12/21/2002	Dell	Change Magantic vendor to Transpower	L24 (GB1G04-LT)	X02

Version Change List (P. I. R. List) for Power Circuit

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	42	Power DC-IN	8/20/2002	Dell	Change PQ32, PR73, PR74, PR77 to NP	R318 change the value from 1240Ω to 1140Ω	X00
2	44	1.8V / 1.2V	8/29/2002	Compal	Change the control signal of 1.2VRUN from RUN_ON to RUNPWROK	Change netname from 4401_CLOCKRUN to 7705M_CLOCKRUN	X00
3	47	Vcc_core	9/03/2002	Dell	Banias update PBOOT voltage spec with 1.2V	Change PR755 to 16.5K and PR757 to 15K	X00
4	45	1.5V / 1.05V	9/05/2002	Compal	SUSPWROK_1P5V control signal detected glitch issue	Add PR767 and reserved PR742 to NP	X00
5	46	1.25V / 2.5V	9/05/2002	Compal	SUSPWROK_1P5V control signal detected glitch issue	Add PR768 and reserved PR605 to NP	X00
6	45	1.5V / 1.05V	9/26/2002	Dell	Add 0 ohm resistor at PU18 pin 15 and pin 16	Add PR769 and PR770 to 0Ω	X01
7	46	1.25V / 2.5V	9/26/2002	Dell	Add a 0 Ohm resistor at PU11 pin 15	Add PR771 to 0Ω	X01
8	45	1.5V / 1.05V	10/17/2002	Compal	Turn off have glitch because +3VSUS issue, so pull high to +5VSUS can improved	Add PR780 to 10KΩ	X01
9	45	1.5V / 1.05V	10/17/2002	Compal	Intersil Issue can solved by Fairchild solution Change the control signal of 1.05VSUS from RUNPWROK to SUSPWROK_5V	Add PR772, PR773 and change PC282 to 0.01U_0805 size	X01
10	44	1.8V / 1.2V	10/17/2002	Dell	Change the 1.8VRUN Regulator from CM3718 to MAX1927	Change +1.8V Regulator	X01
11	45	1.5V / 1.05V	10/17/2002	Dell	+1.5VSUS POWER GOOD ISSUE	change PL124 to 5.0U_20%_3.0A to 4.7U_20%_3.9A	X01
12	44-48	All Regulaters	10/17/2002	Dell	Add test point for DELL testing	Add PR778, PR783, PR784, PR781, PR782, PR785, PR786 to NP for Test point	X01
13	49	Battery charger	10/23/2002	Dell	Fast Charger for short time and Cost-Down	Add P303 and Change PC202, PC203, PC204, PC211, PC212, PC213 to 10U_1210 and Delete PU15's circuit Change PR636 to 43.2K ohms for 65W Adpater plug in	X01
14	45	1.5V / 1.05V	10/23/2002	Dell	Change OCSETt and SEN Resistor	change PR735, PR736 to 75K ohms, Change PR727 to 330 ohms, Change PR728 to 634 ohms	X01
15	47	Vcc_core	10/28/2002	Dell	Cost-Down and change deepsleep offset	Change PR744, PC243, PC251, PC252, PR765 For adjusts slope and sleep offset PQ7, PQ11, PD24, PD27 to NP for Cost-Down	X01
16	47	Vcc_core	10/28/2002	Dell	EMI Broad band ISSUE	Add PC304 PC306 to 0.1U, Add PC305 PC307 to 2200P change PR747 PR756 to 3.3 ohms	X01
17	50	PWR_Selector	11/14/2002	Compal	Charger selector ISSUE	Add PR649, Del PD48,Change PD49 PD51 to B540C and Change PR657 PR662 to 33K	X02
18	42	Power DC-IN	11/20/2002	Dell	AC-IN Detect Issue	Change PR70 to 4.7K	X02
19	49	Battery charger	11/26/2002	Dell	Charger Current to 5.3A	Change PR627 to 28m ohms	X02

Compal Electronics, Inc.		
Title: Changed-List History		
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Version Change List (P. I. R. List) for Power Circuit

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
20	47	Vcc_core	11/29/2002	Compal	Delete V-core Cap for Cost-Down	Delete PC232 PC233 PC246 PC247 for Cost-Down	X02
21	50	PWR_Selector	12/16/2002	Dell	New Battery selector switch design to sopport secondry battery discharge before primary	Delete PQ72, PQ73 Change PQ86 to Dual-Mosfet ,Add PQ88 Mosfet Add PU19, PC308, PC309, U20B, PQ89, PQ90, PD57 And PR787, PR788, PR789, PR790, PR791, PR792, PR793 PR794, PR795@, PR796, PR797, PR798, PR799, PR800	X02
22	42	Power DC-IN	12/17/2002	Dell	EFT Issue and Dell request	Add PQ91, PD58 PR762 to depop	X02
23	42	Power DC-IN	12/18/2002	Compal	EFT Issue	Add PR801 to depop	X02
24	42	Power DC-IN	12/19/2002	Dell	Added PS_ID Pull-up	Add PR802 (4.7K_0402)@	X02
25	42	Power DC-IN	12/19/2002	Dell	Added +DC_IN 10K_0805 Pull-down to GND to solve potential adapter insertion secqurncing issue	Add PR803 (10K_0805)	X02
26	49	Chaqrger	12/20/2002	Dell	Added mΩ resistor in palleal with PR627 to support 6.0Amp maximun charging current	Add PR804 0.05mΩ_2512)	X02
27	49	Chaqrger	12/20/2002	Dell	Changed the Value of PR627 from 0.028 to 0.05	PR627 chande to 50m ohms	X02
28	44	1.8V / 1.2V	12/20/2002	Compal	Prevent 3.3ALW voltage droop during OTP & ThermTrip shutdown	Added PU20 (@)	X02
29	49	Chaqrger	12/20/2002	Dell	Changed the Value of PL120 from 6.0U to 5.6U	PL120 chande to 5.6UH_+-20%_8.8A	X02
30	42	Power DC-IN	12/20/2002	Dell	Prevent Bondi PS_ID Pin Issue	Add PR762 and PQ91@	X02