

ZZZ1



PCB
DA2@

ZZZ2



PCB
DA2@
LA-4011P

ZZZ3



PCB
DA2@
LS-4011P

ZZZ4



PCB
DA2@
LS-4012P

ZZZ5



PCB
DA2@
LS-4013P

Compal Confidential

JAW91 Schematics Document

AMD S1/ ATI RS690M(C) / SB600

2008/11/20

Rev:1.0

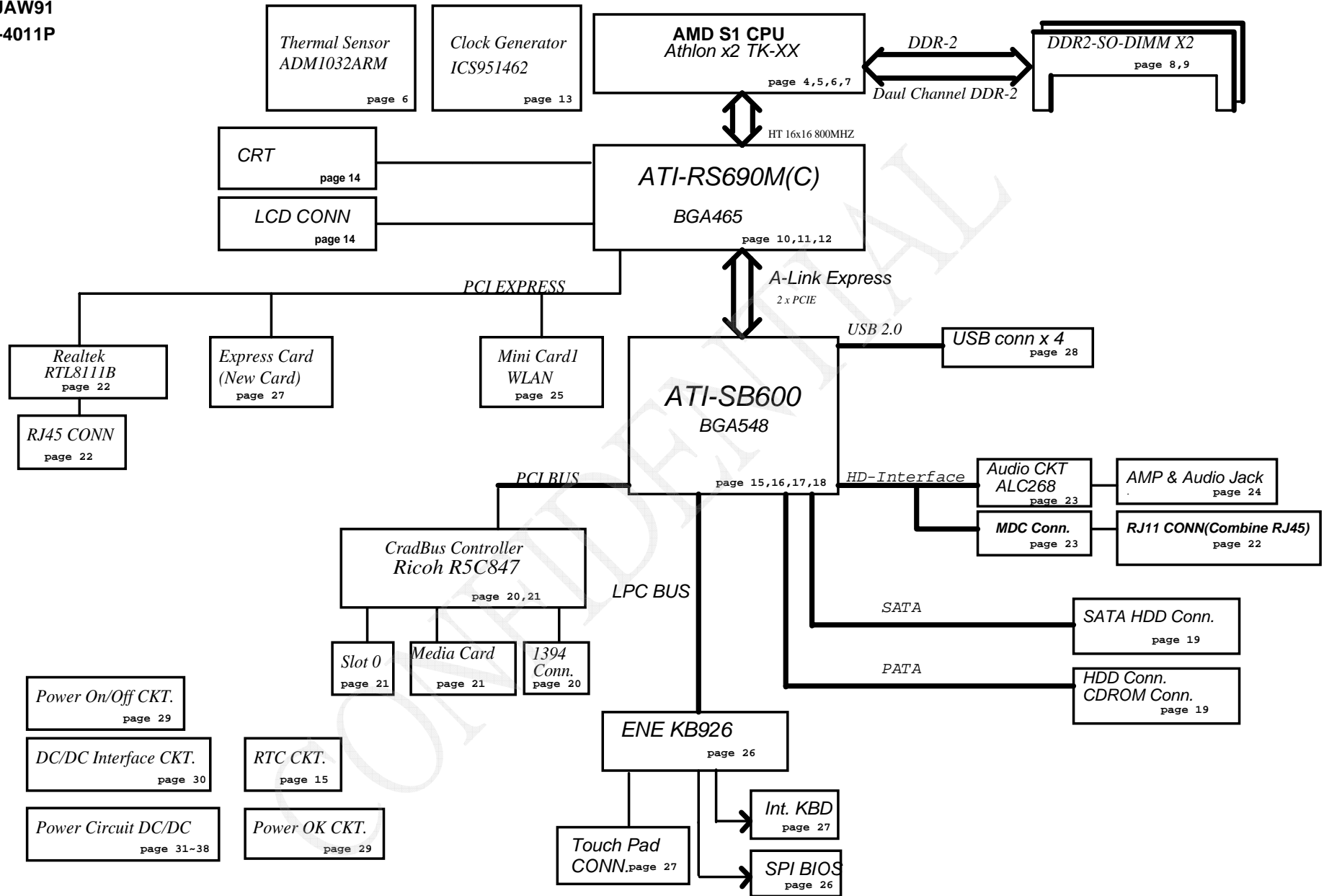
CONFIDENTIAL

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Project Code: JAW91

File Name : LA-4011P

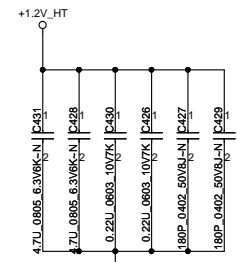
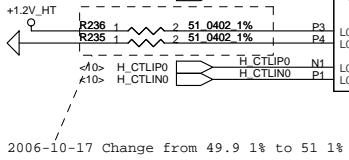
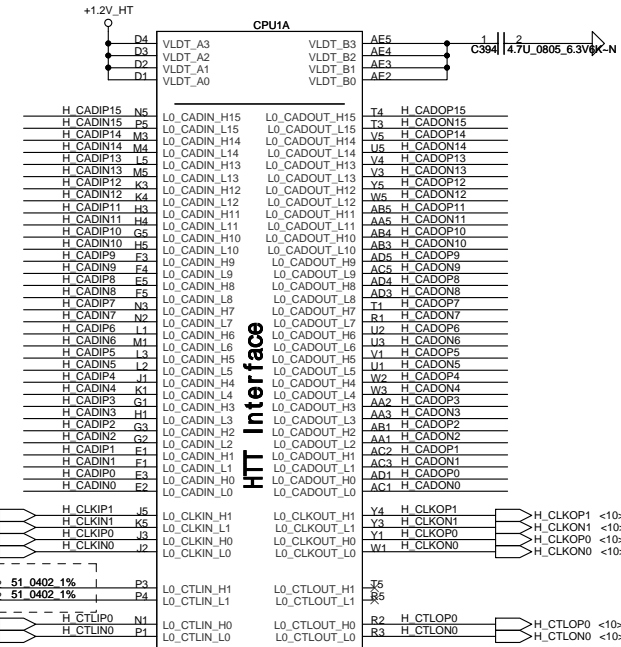


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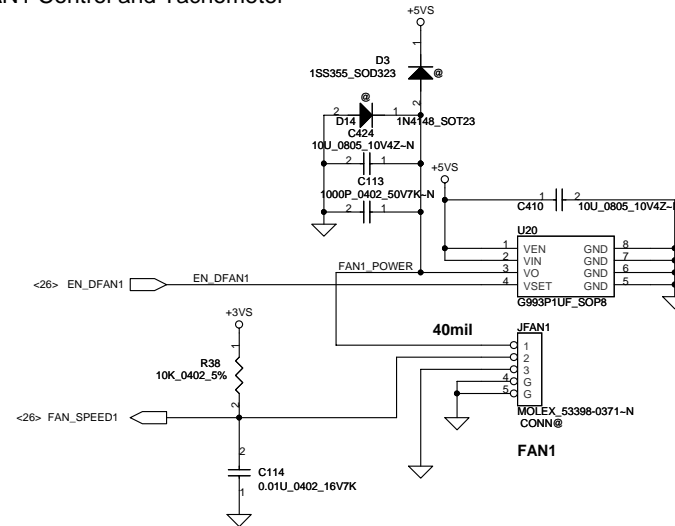


PROCESSOR HYPERTRANSPORT INTERFACE

VLDT_Ax AND VLDT_Bx ARE CONNECTED TO THE LDT_RUN POWER SUPPLY THROUGH THE PACKAGE OR ON THE DIE. IT IS ONLY CONNECTED ON THE BOARD TO DECOUPLING NEAR THE CPU PACKAGE



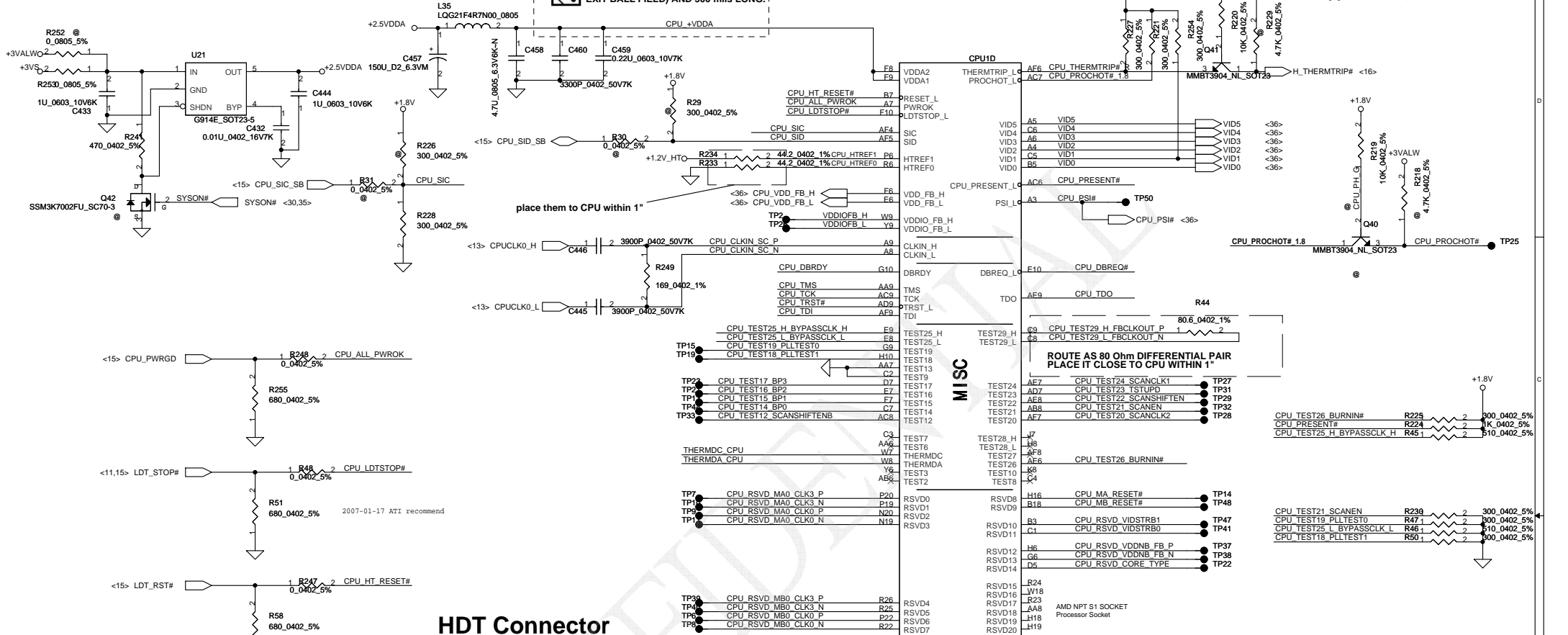
FAN1 Control and Tachometer



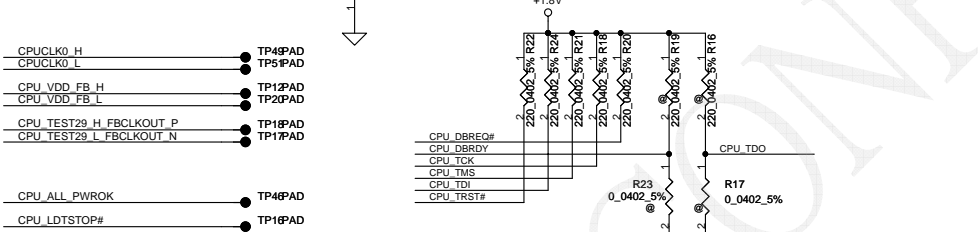
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ATHLON Control and Debug

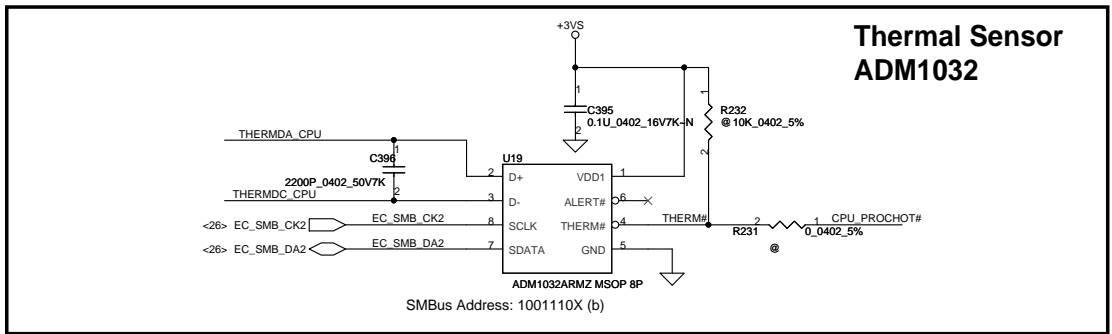
LAYOUT: ROUTE VDDA TRACE APPROX. 50 MILS WIDE (USE 2x25 MIL TRACES TO EXIT BALL FIELD) AND 500 MILS LONG.



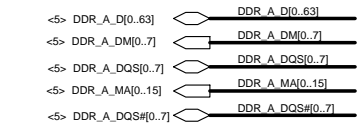
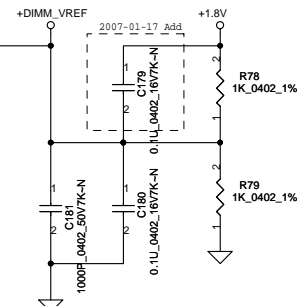
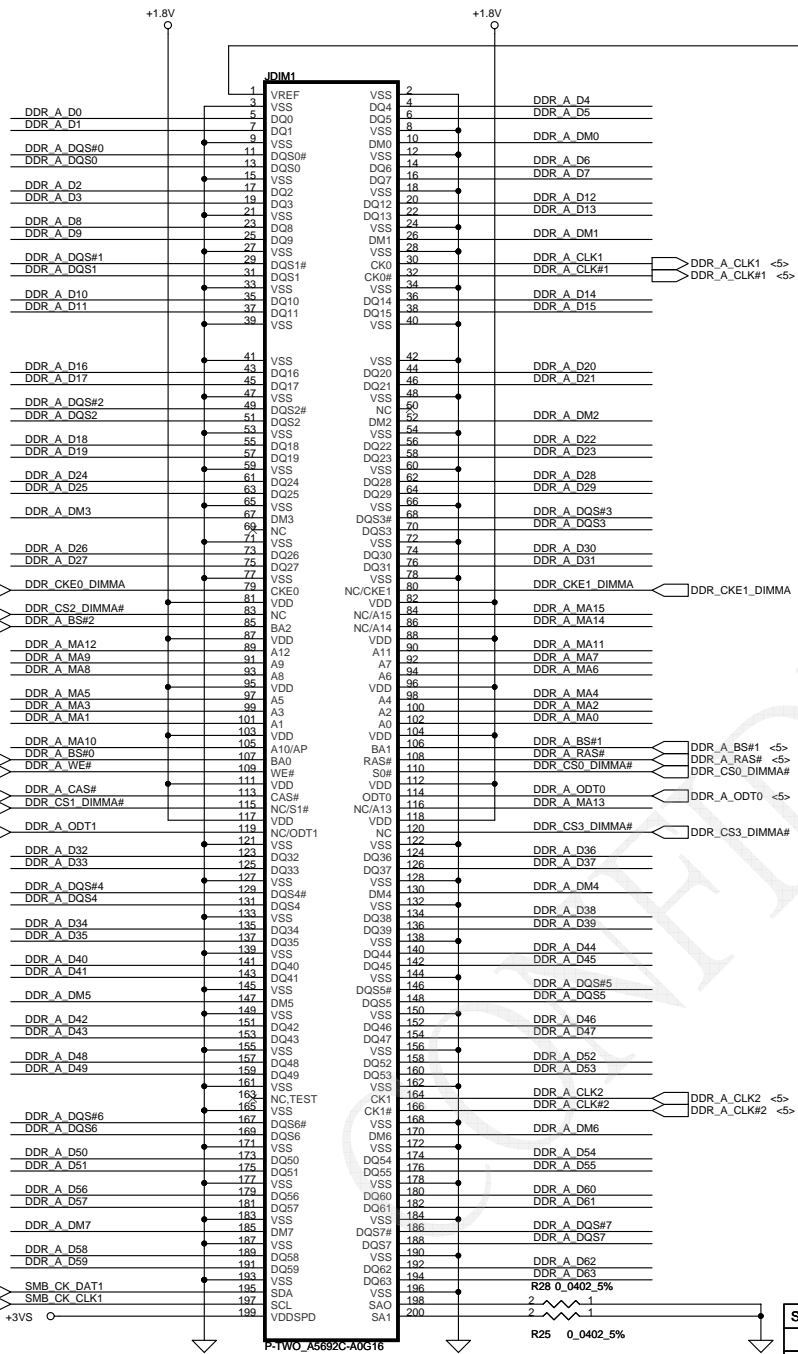
HDT Connector



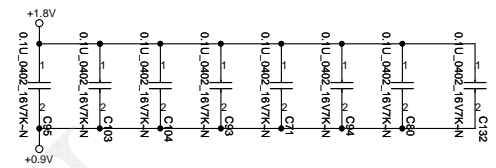
NOTE: HDT TERMINATION IS REQUIRED FOR REV. Ax SILICON ONLY.



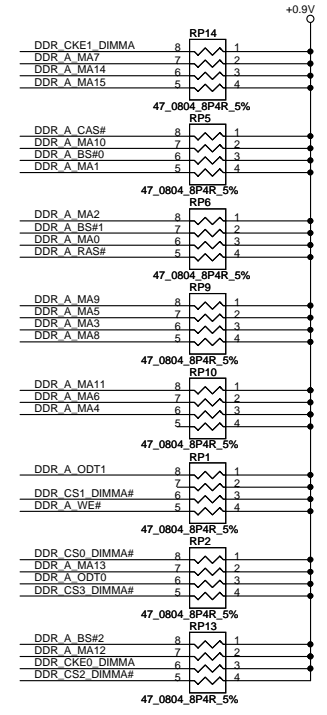
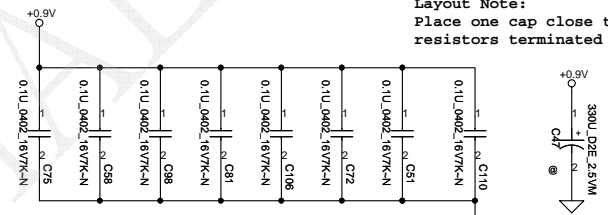
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Layout Note:
Place one cap close to every 2 pullup resistors terminated to +0.9V



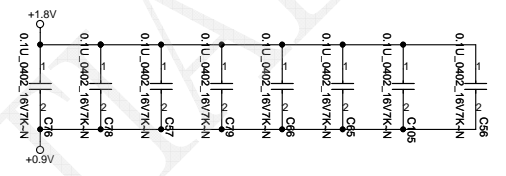
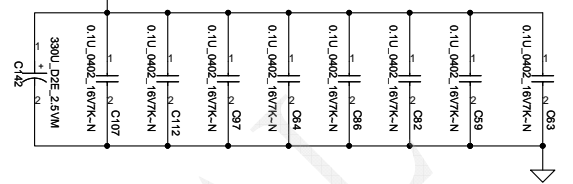
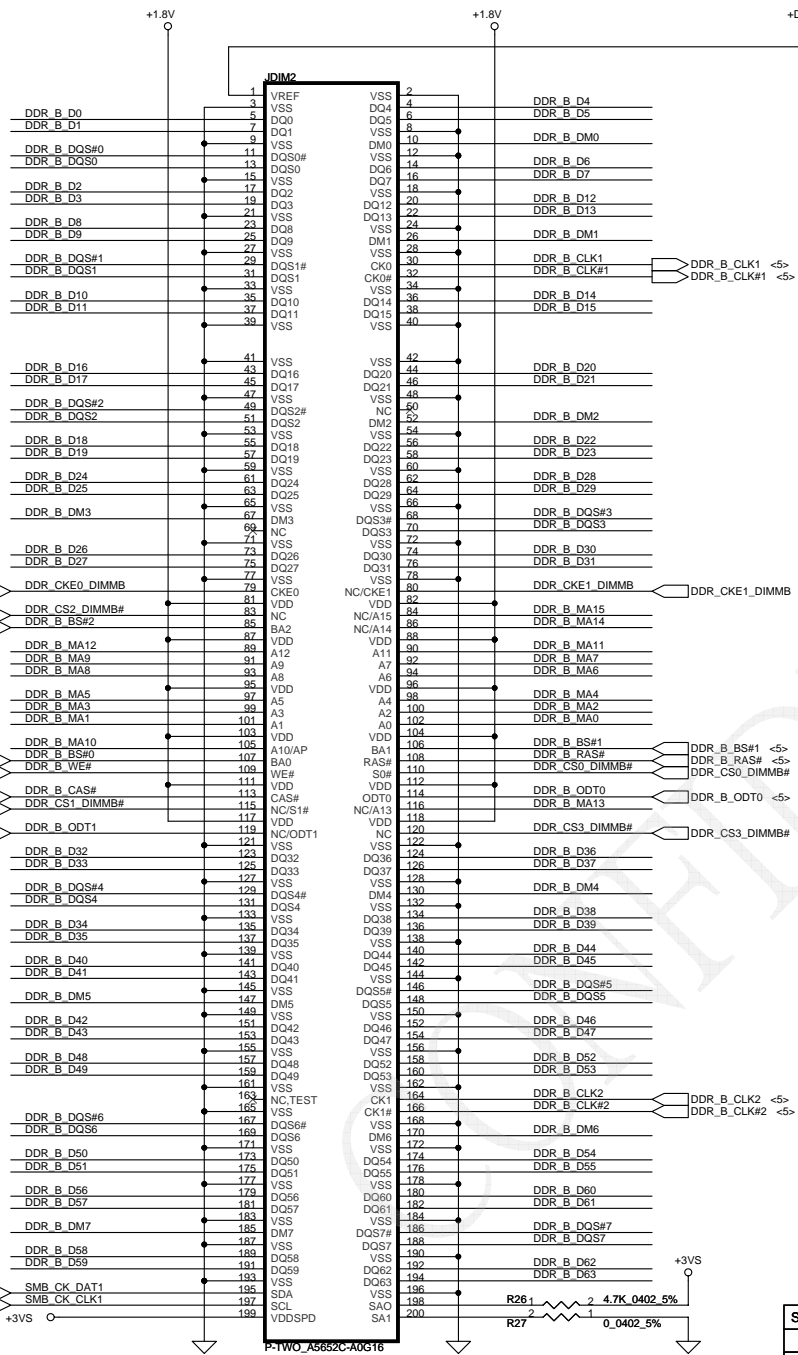
Layout Note:
Place one cap close to every 2 pullup resistors terminated to +0.9V



<9,13,16,27> SMB_CK_DAT1
<9,13,16,27> SMB_CK_CLK1

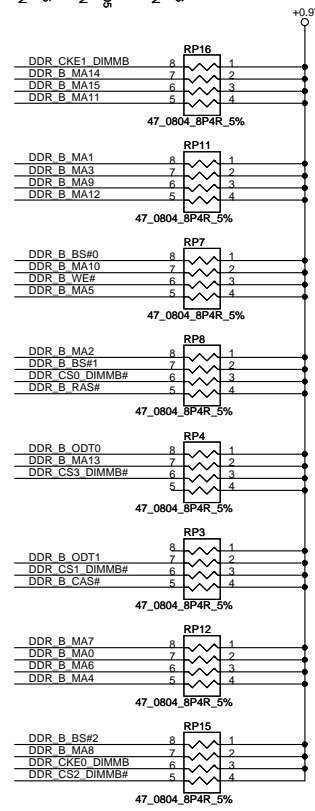
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Layout Note:
Place one cap close to every 2 pullup resistors terminated to +0.9V

Layout Note:
Place one cap close to every 2 pullup resistors terminated to +0.9V



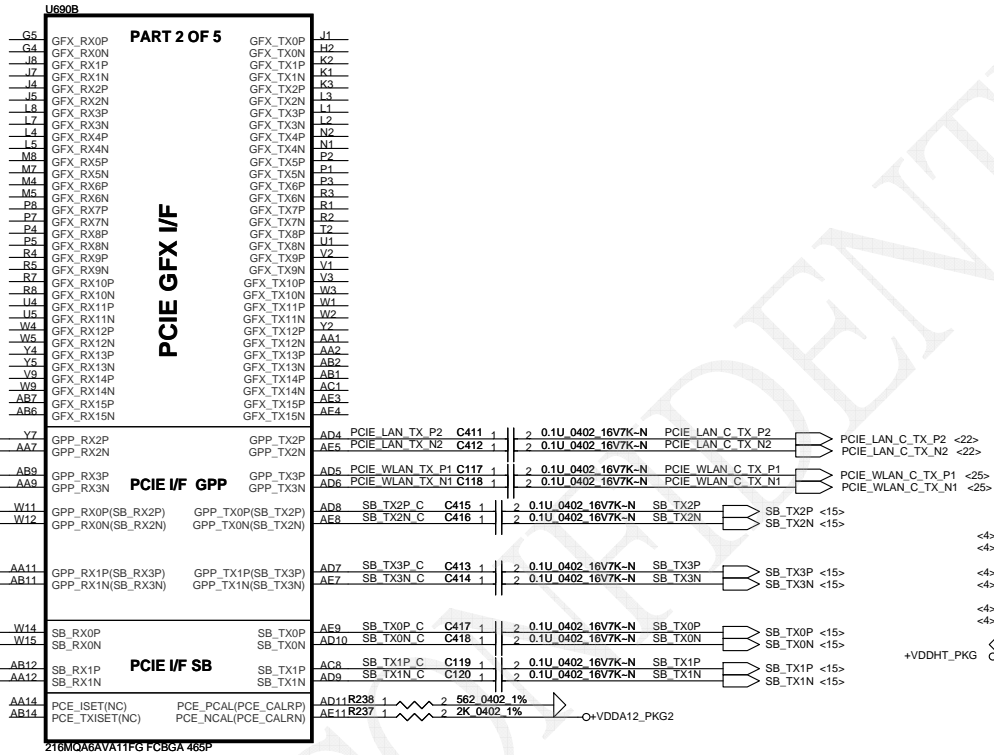
<8,13,16,27> SMB_CK_DAT1
<8,13,16,27> SMB_CK_CLK1

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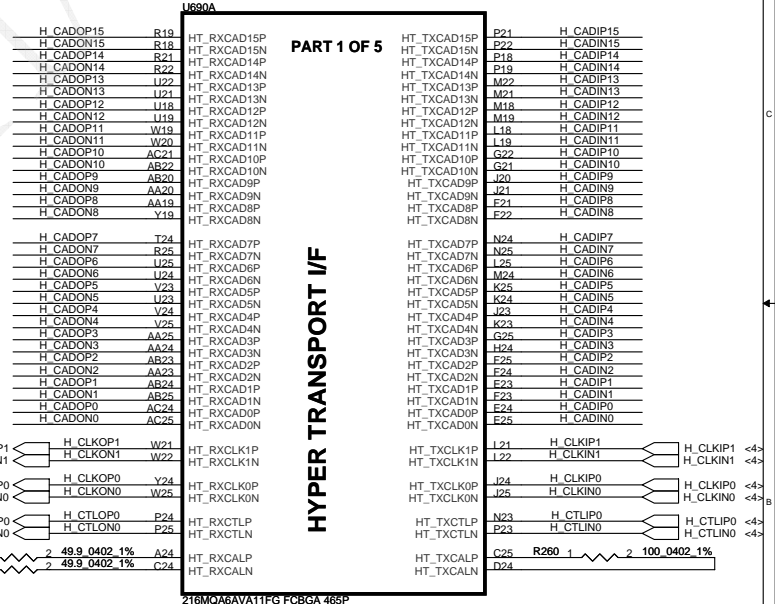
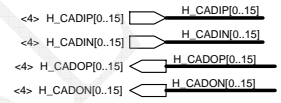


PART 2 OF 5

PCIE GFX I/F

PCIE I/F GPP

PCIE I/F SB

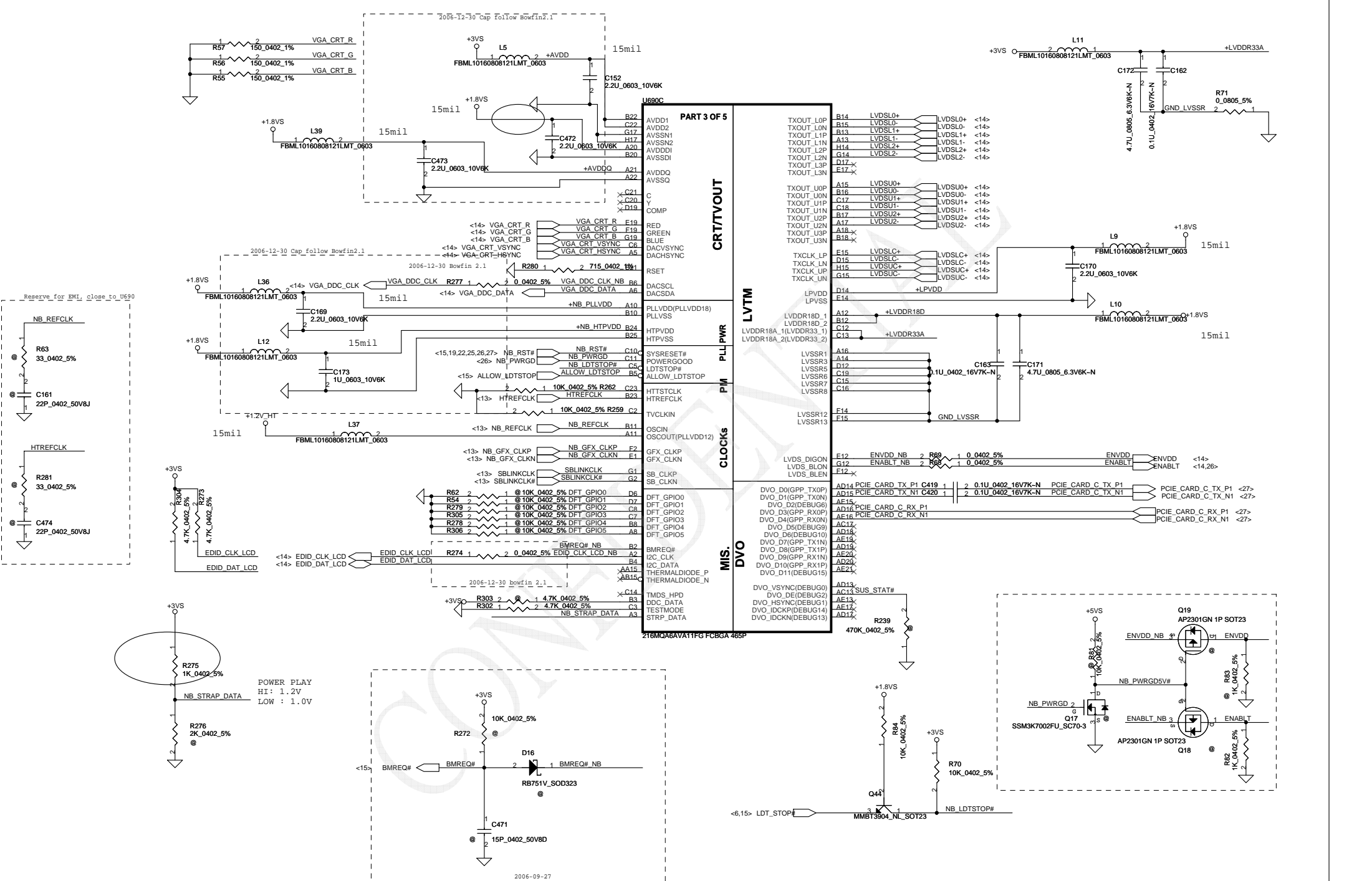


PART 1 OF 5

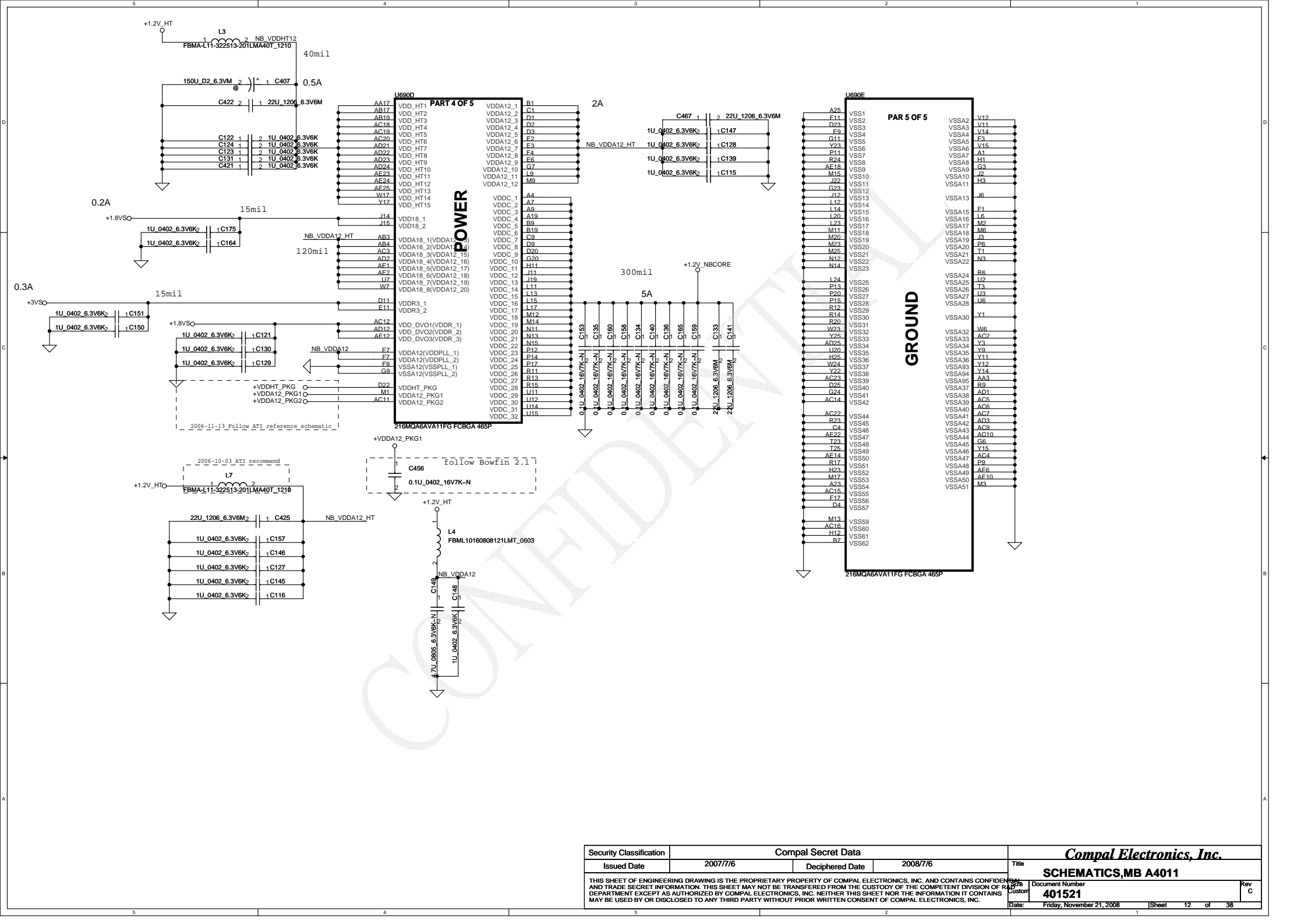
HYPER TRANSPORT I/F

216M0A6AVAT11FG FCBGA 465P

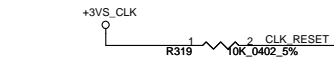
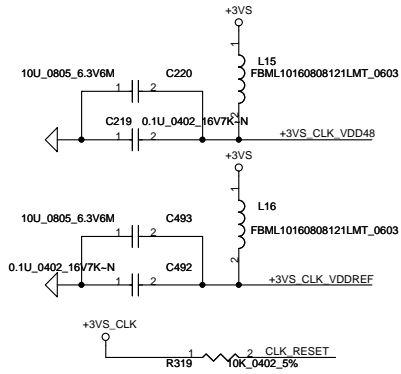
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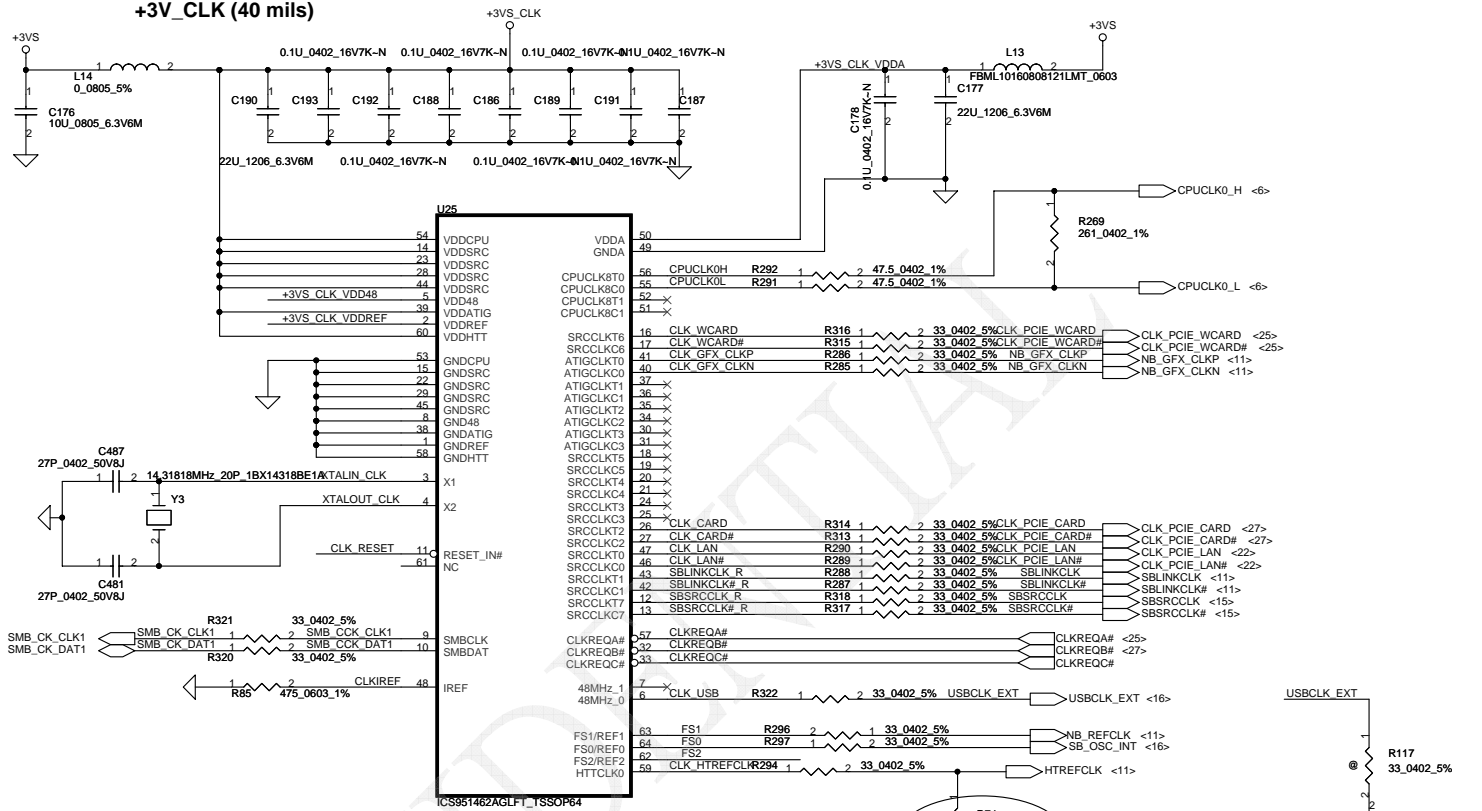
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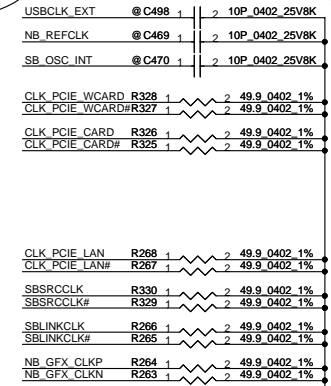
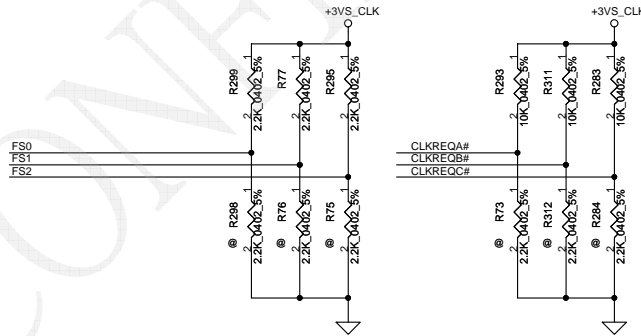


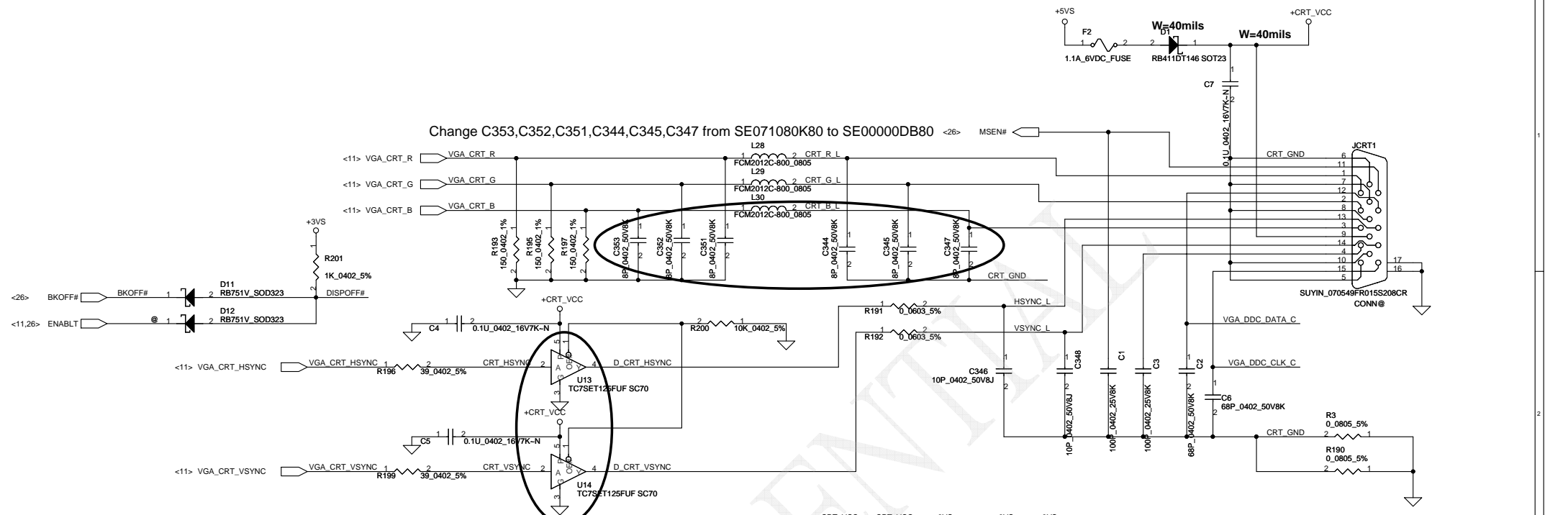
+3VS_CLK (40 mils)



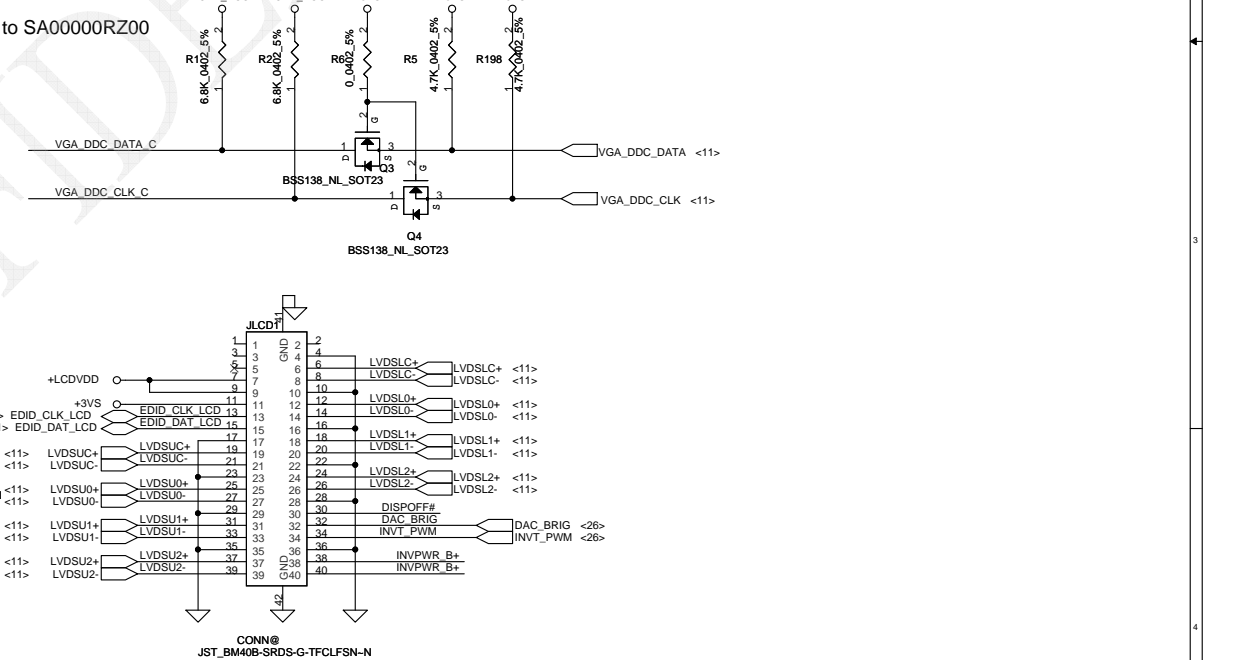
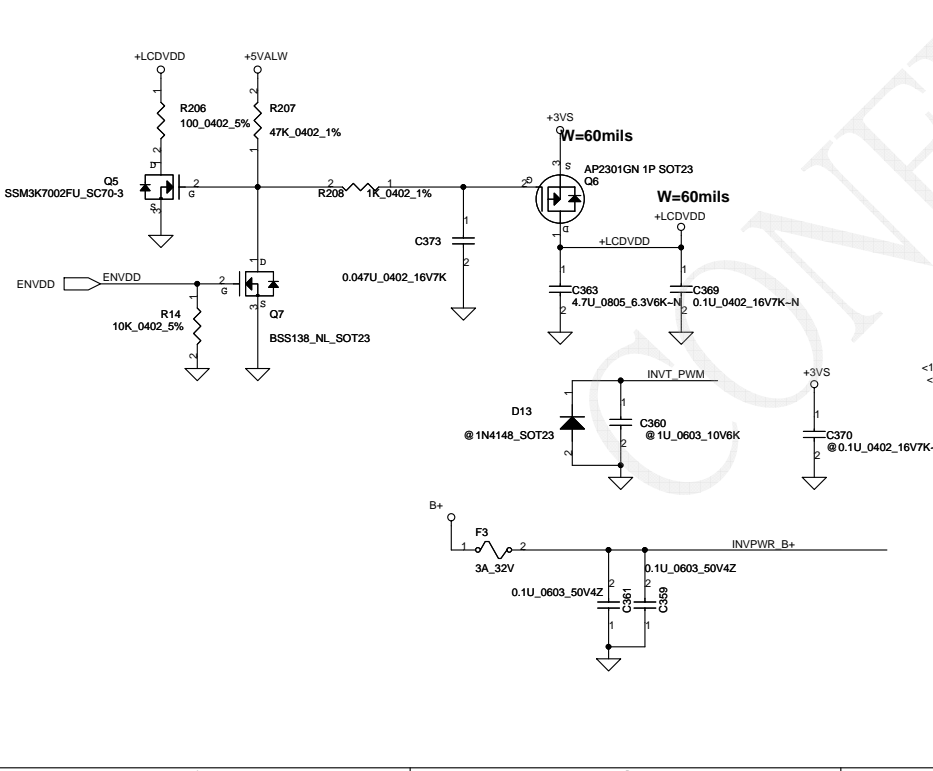
EXT CLK FREQUENCY SELECT TABLE(MHZ)

FS2	FS1	FS0	CPU	SRCCLK [2:1]	HTT	PCI	USB	COMMENT
0	0	0	Hi-Z	100.00	Hi-Z	Hi-Z	48.00	Reserved
0	0	1	X	100.00	X/3	X/6	48.00	Reserved
0	1	0	180.00	100.00	60.00	30.00	48.00	Reserved
0	1	1	220.00	100.00	36.56	73.12	48.00	Reserved
1	0	0	100.00	100.00	66.66	33.33	48.00	Reserved
1	0	1	133.33	100.00	66.66	33.33	48.00	Reserved
1	1	1	200.00	100.00	66.66	33.33	48.00	Normal ATHLON64 operator

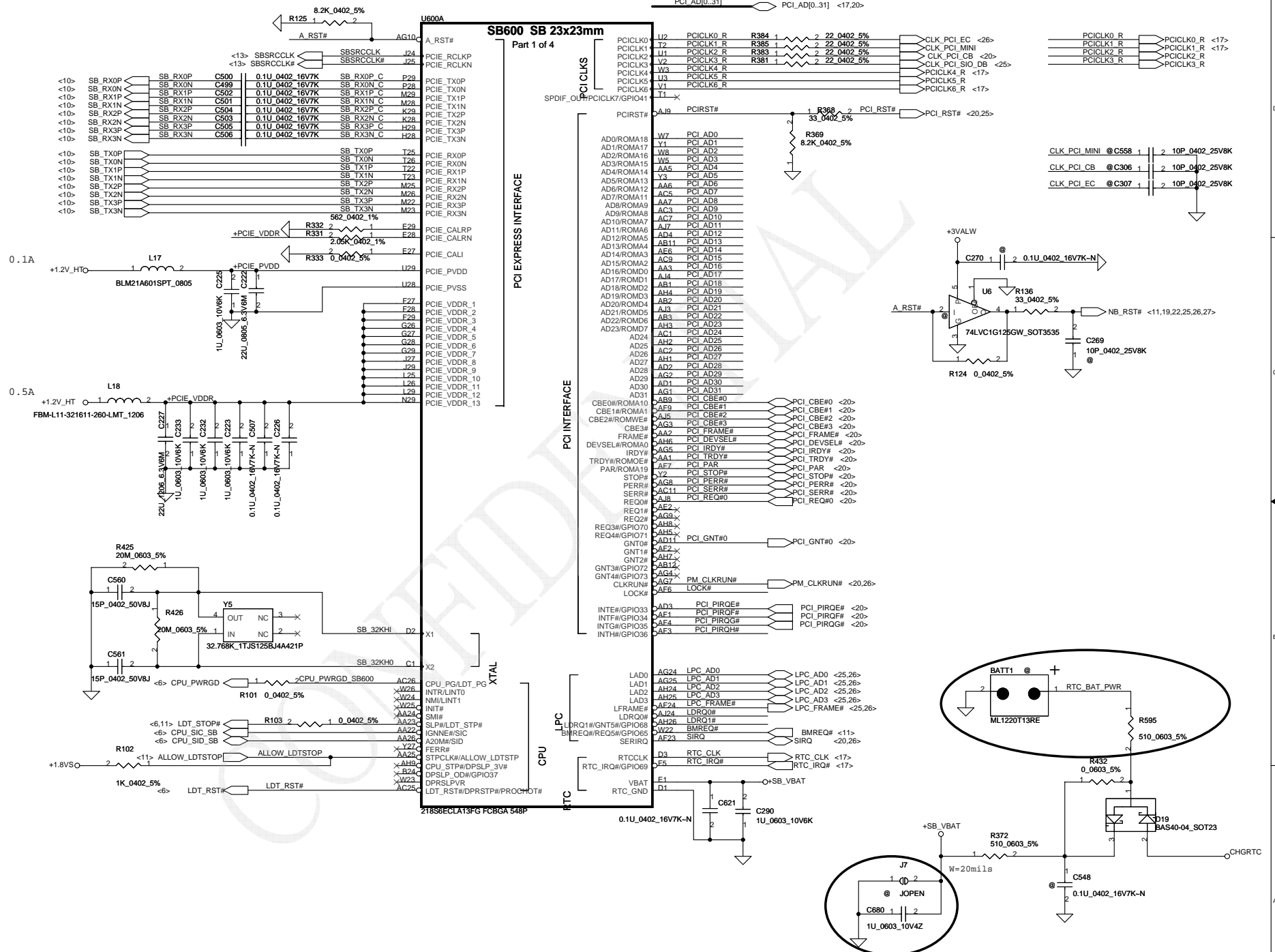




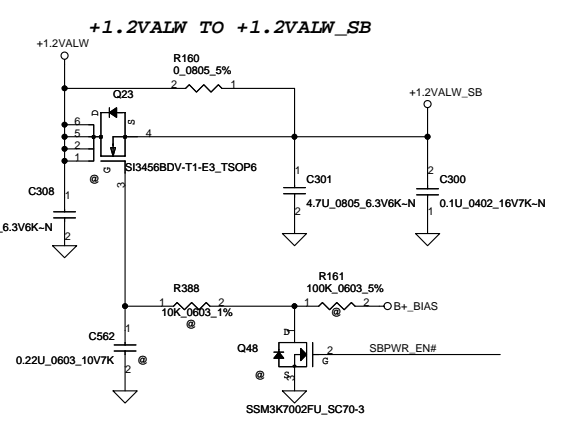
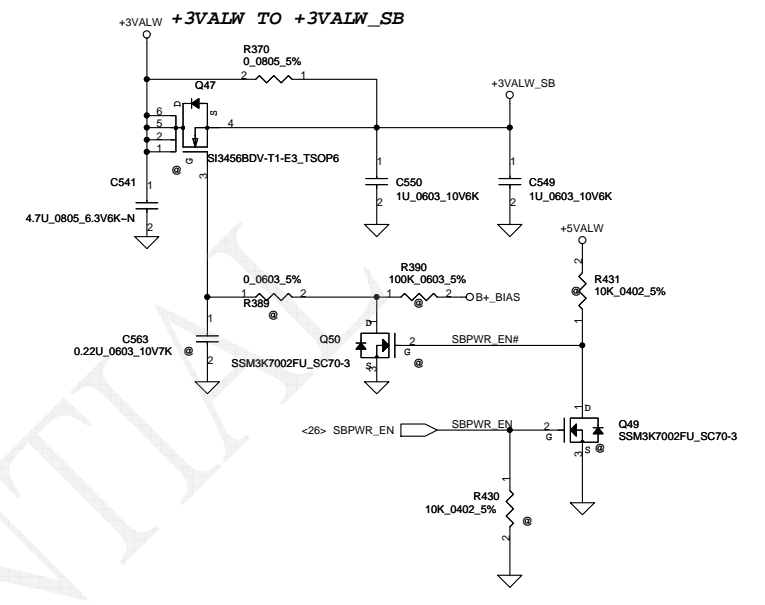
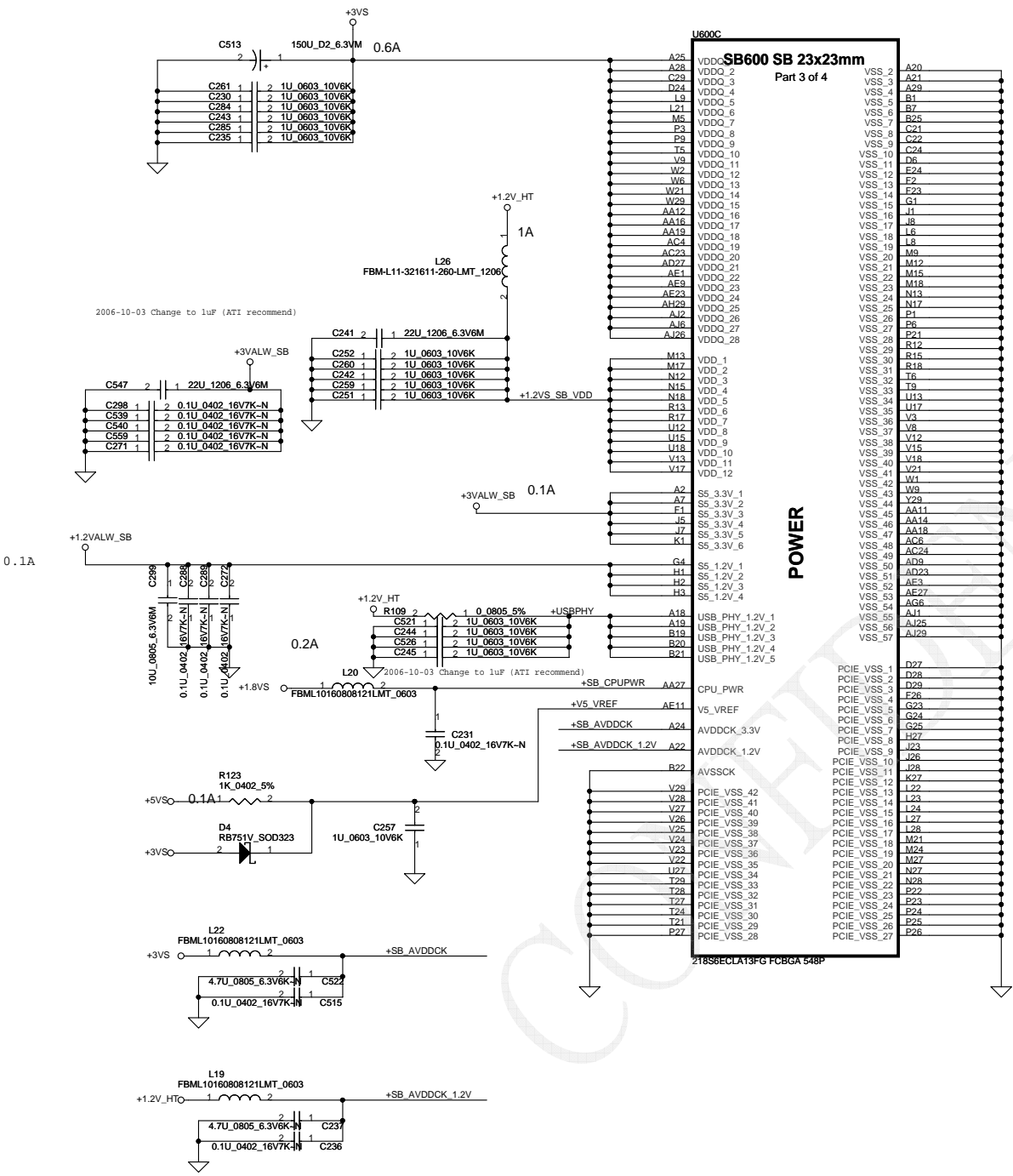
LCD POWER CIRCUIT



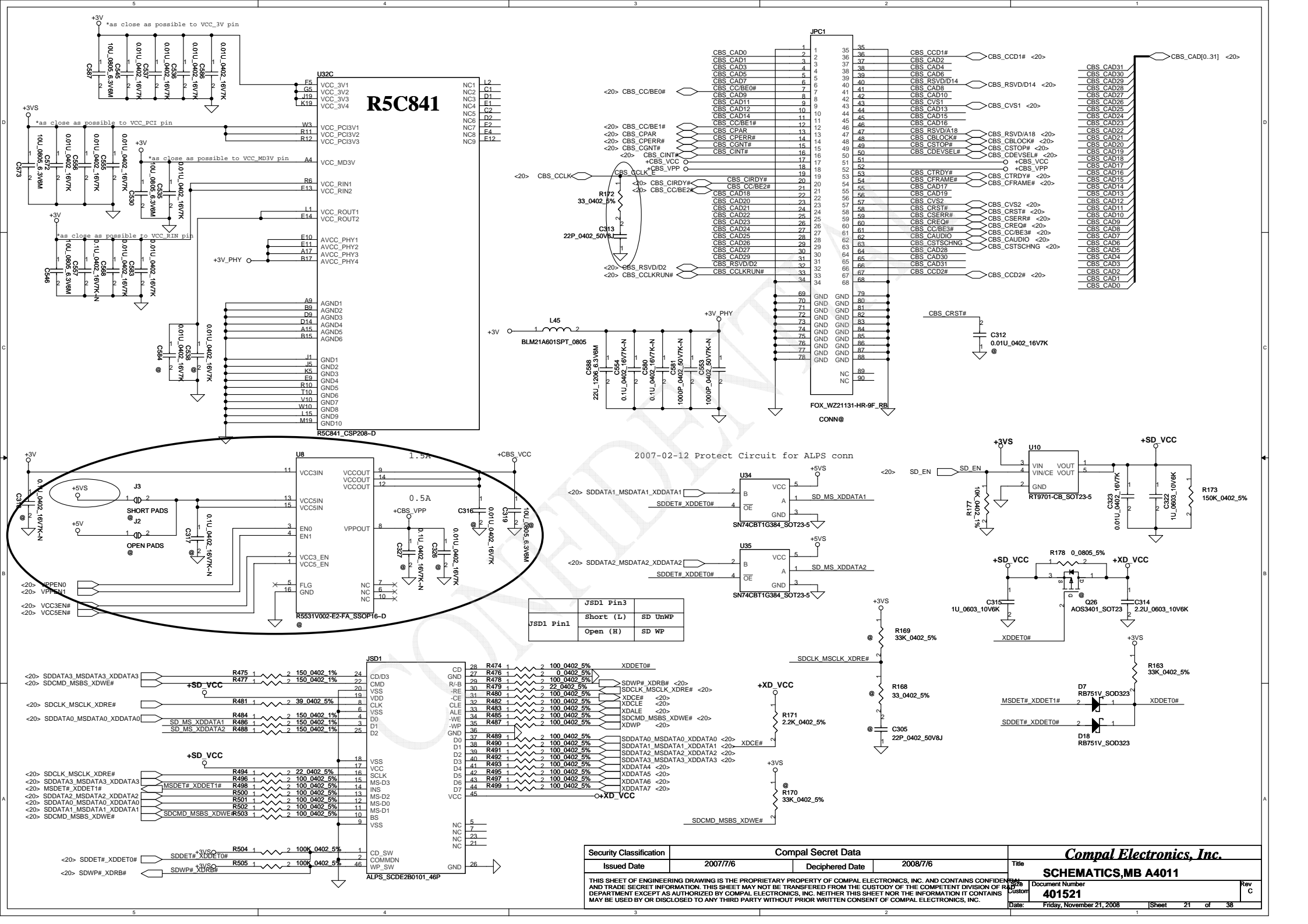
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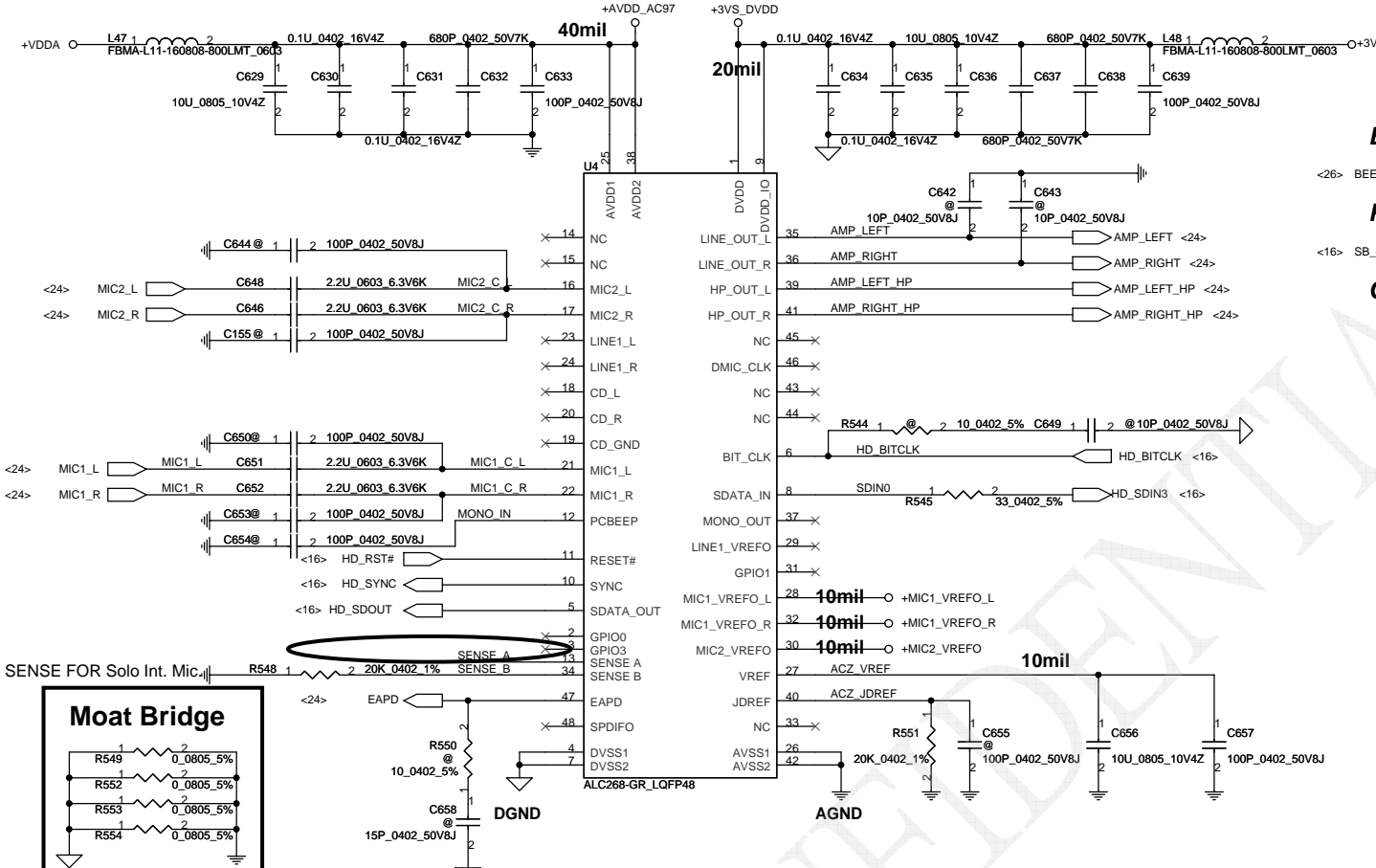
R5C841

2007-02-12 Protect Circuit for ALPS conn

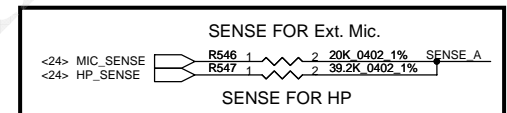
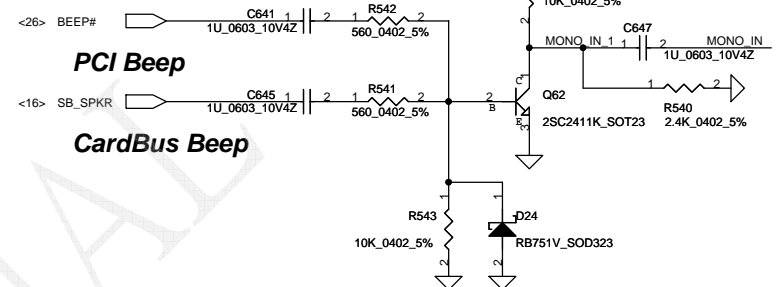
JSD1 Pin3	Short (L)	SD UnWP
JSD1 Pin1	Open (H)	SD WP

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HD Audio Codec

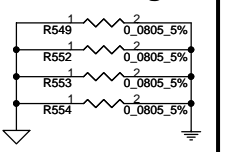


EC Beep

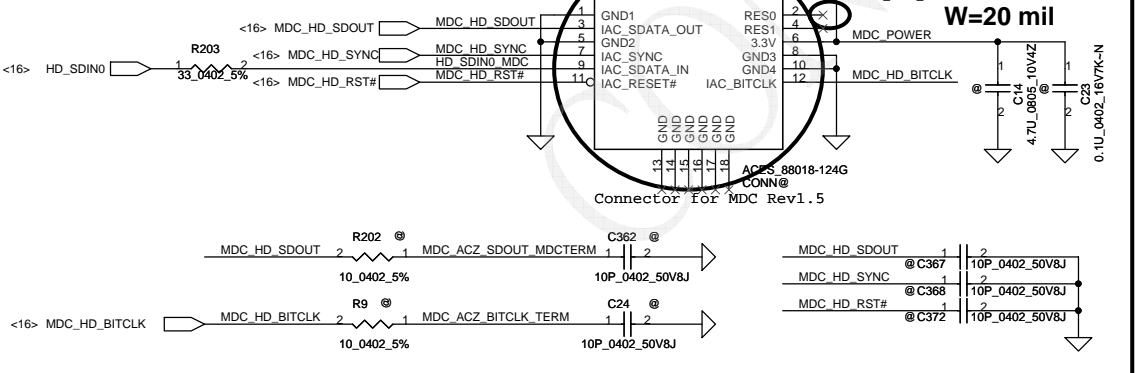


Sense Pin	Impedance	Codec Signals	Function
SENSE A / B	39.2K	PORT-A (PIN 39, 41)	HP
	20K	PORT-B (PIN 21, 22)	MIC
	10K	PORT-C (PIN 23, 24)	LINE IN
SENSE B	5.1K	PORT-D (PIN 35, 36)	LINE Out
	39.2K	PORT-E (PIN 14, 15)	HP
	20K	PORT-F (PIN 16, 17)	MIC
	10K	PORT-G (PIN 43, 44)	LINE IN
	5.1K	PORT-H (PIN 45, 46)	LINE Out

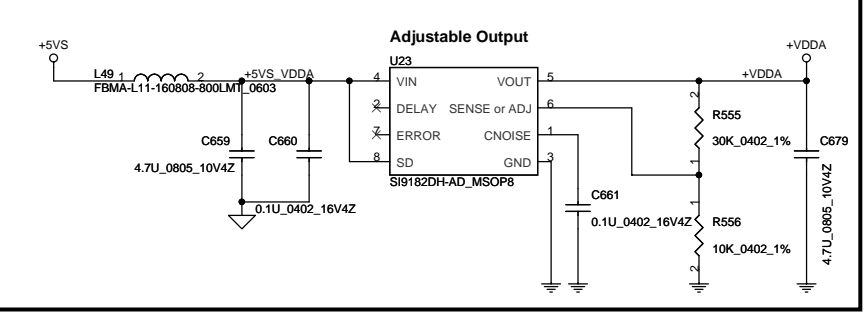
Moat Bridge



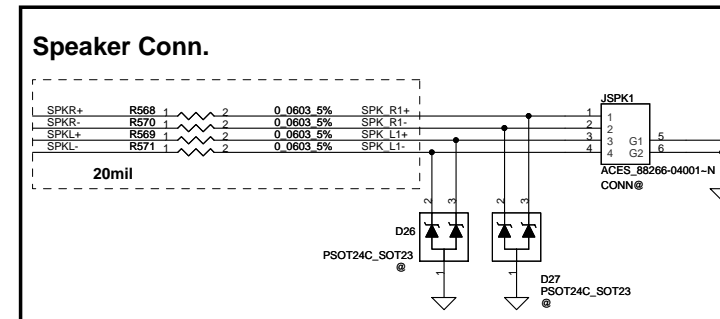
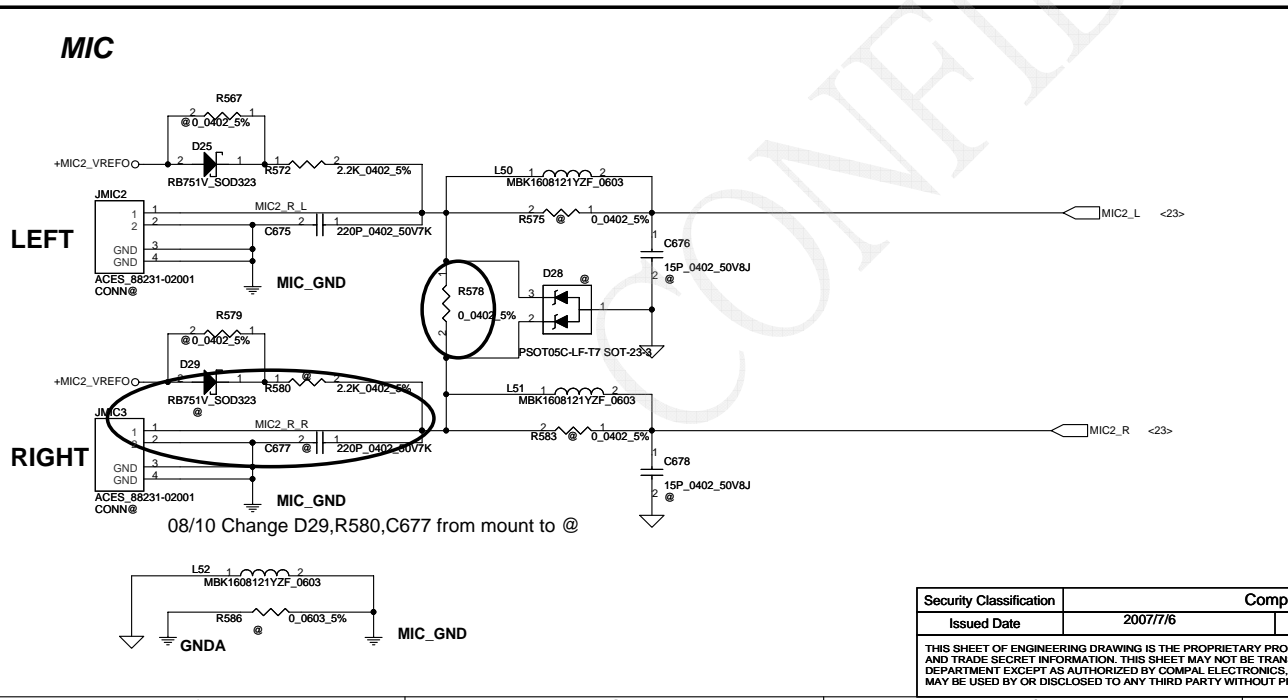
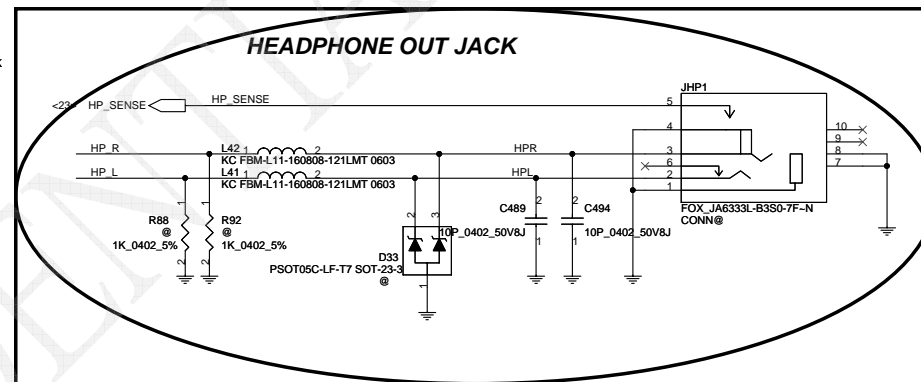
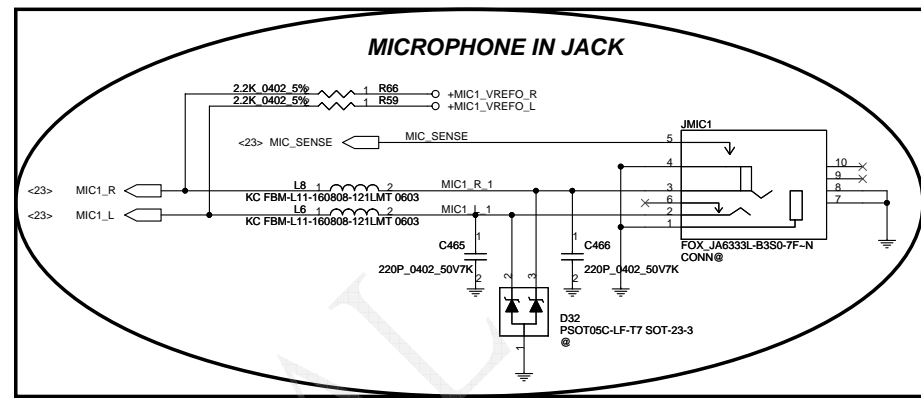
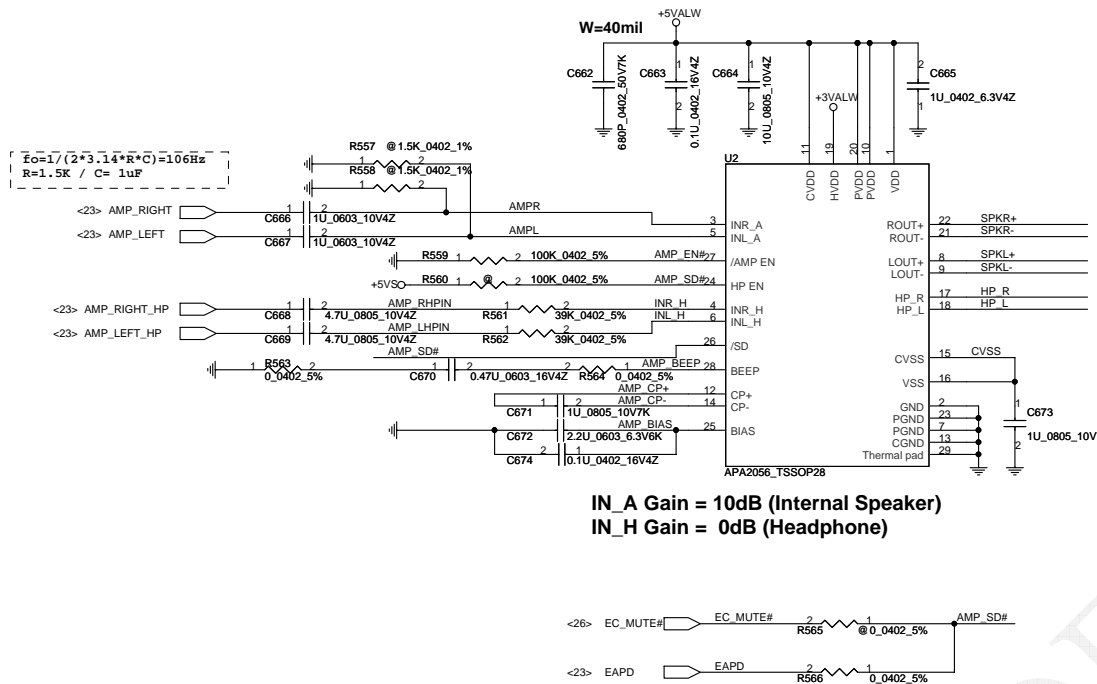
MDC CONN.



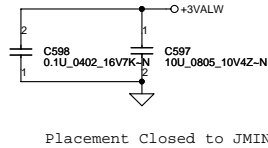
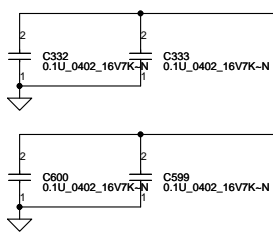
Regulator for CODEC



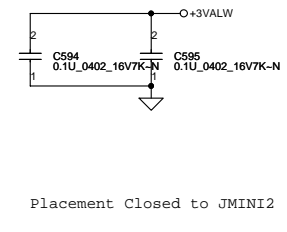
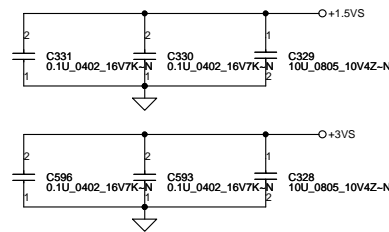
APA2056 SPK/HP Amplifier



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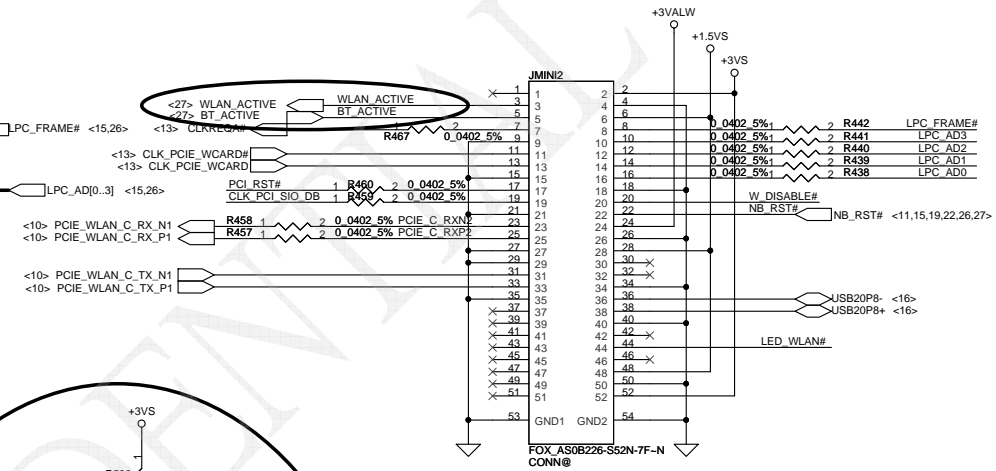
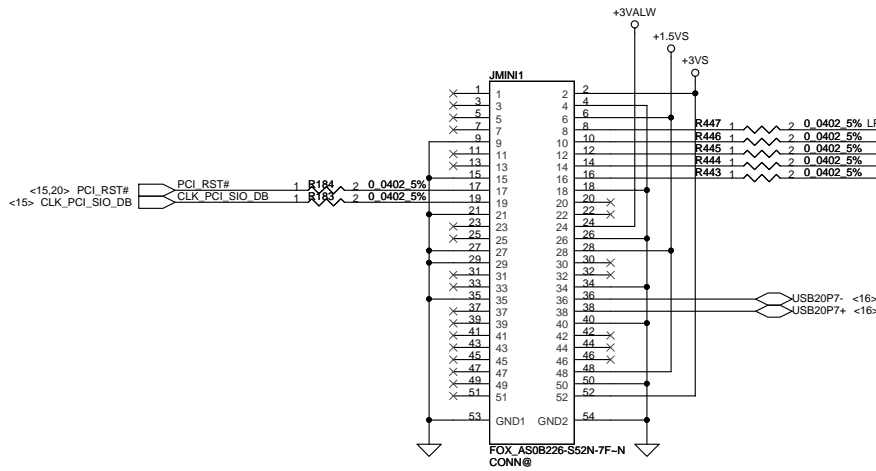
Placement Closed to JMINI1



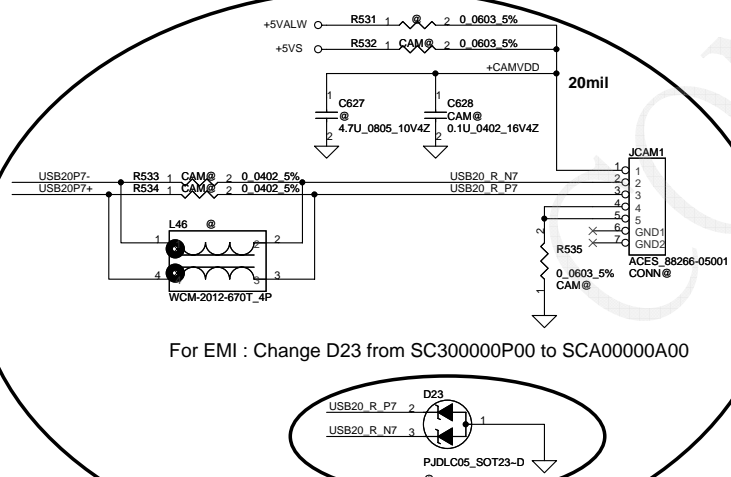
Placement Closed to JMINI2

Mini-Express Card (Reserve for debug or TV)

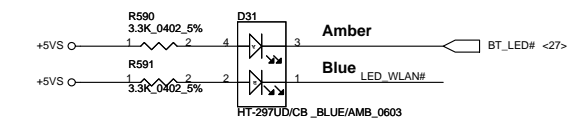
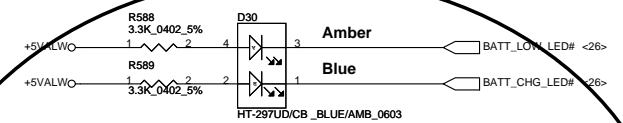
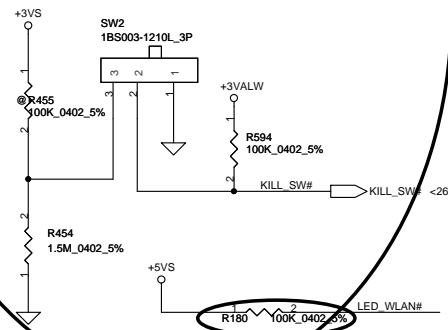
Mini-Express Card



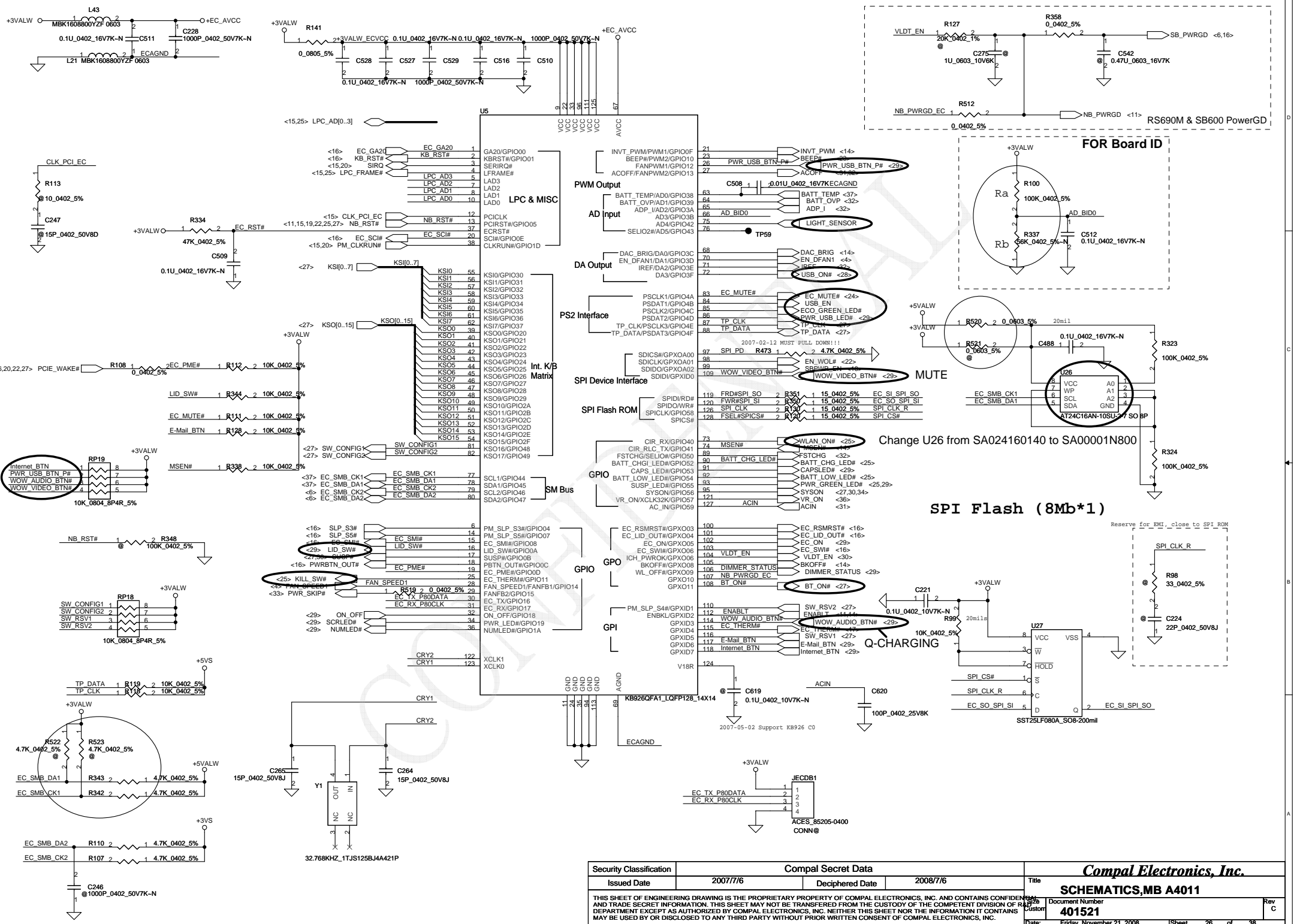
CAMERA



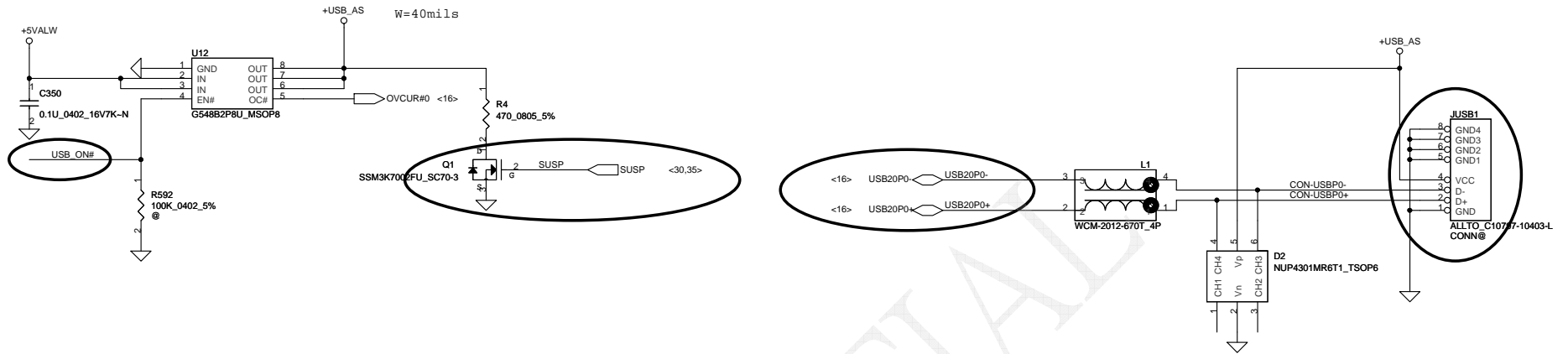
Wireless BTN Killer switch



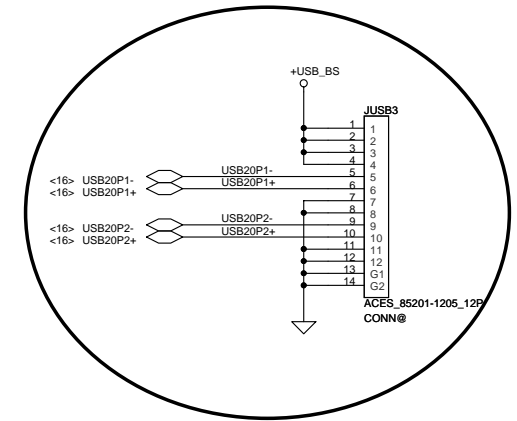
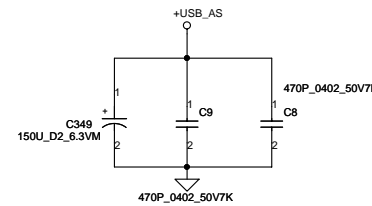
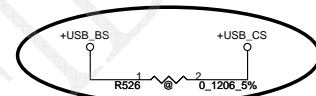
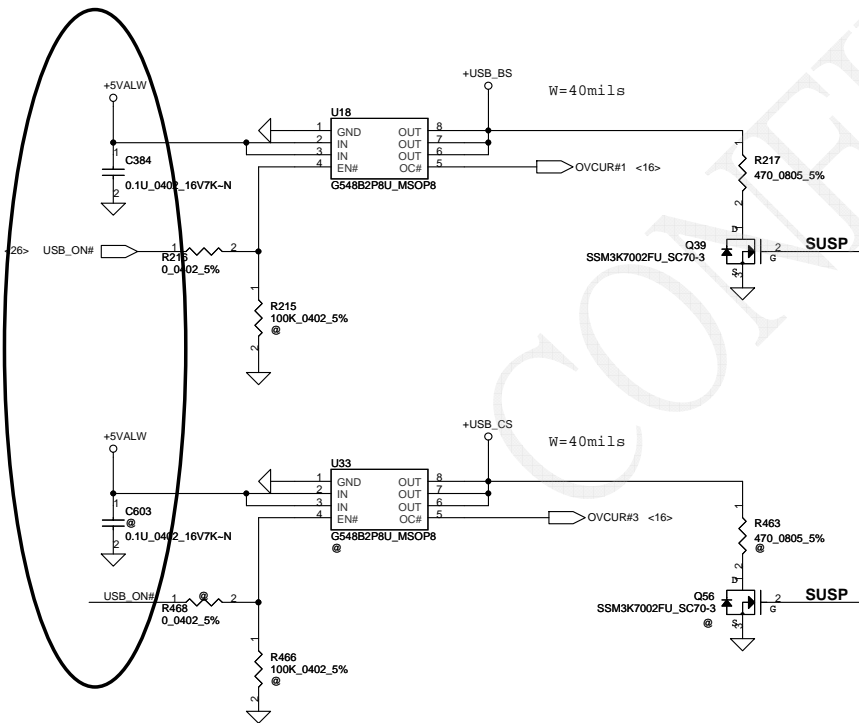
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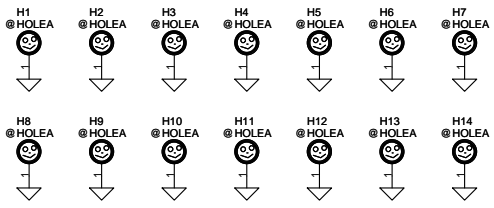
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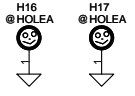
Change JUSB1 from DC233105800 to DC233002A00



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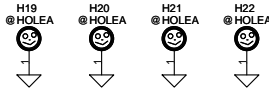
H_S354D118



H_C216D144



H_C236D157



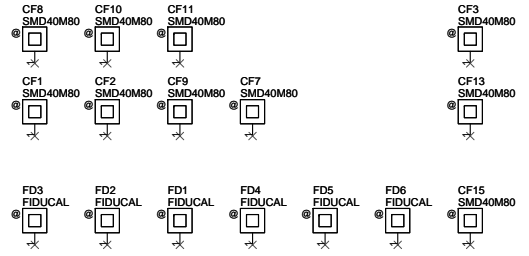
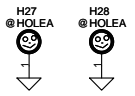
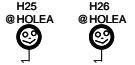
H_C276D165 (CPU)



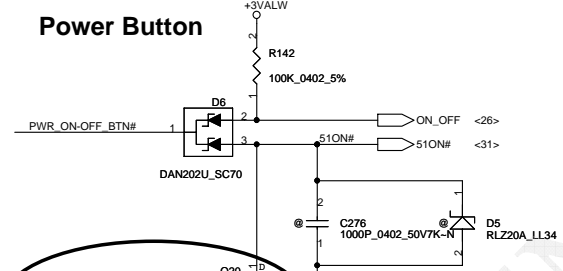
H_S362D118



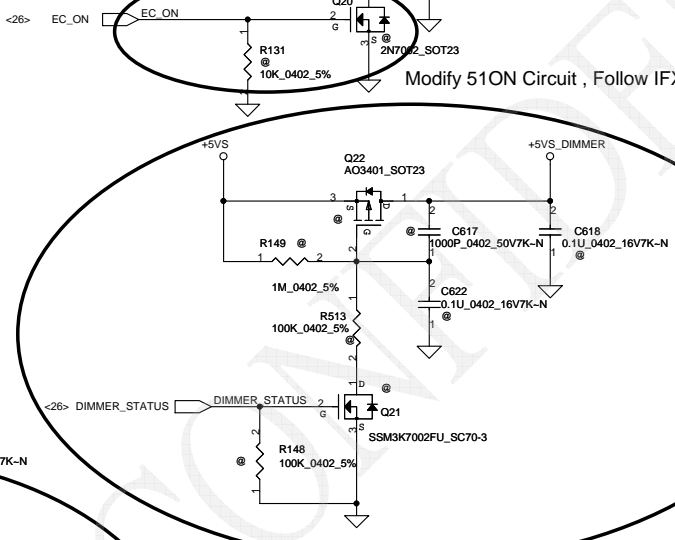
H_C114D114N



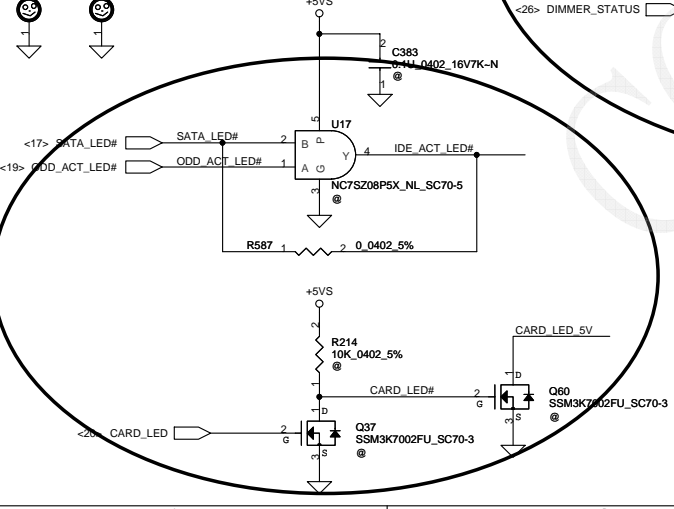
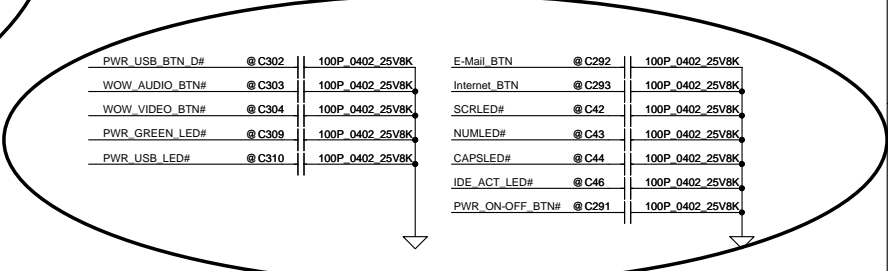
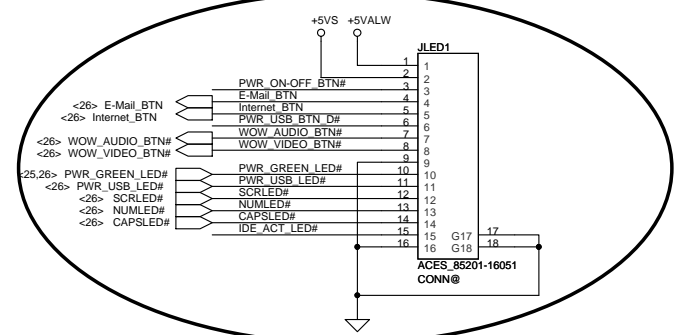
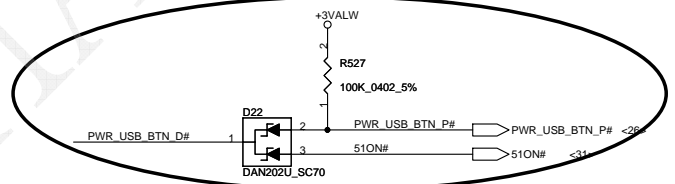
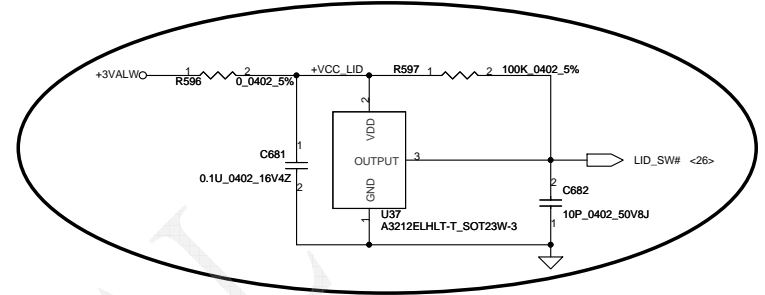
Power Button



Modify 51ON Circuit , Follow IFXXX.



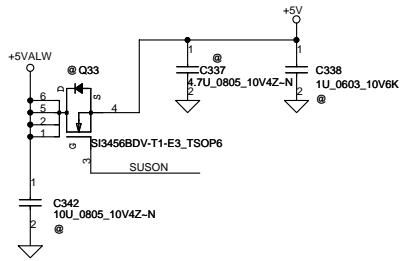
LID Switch



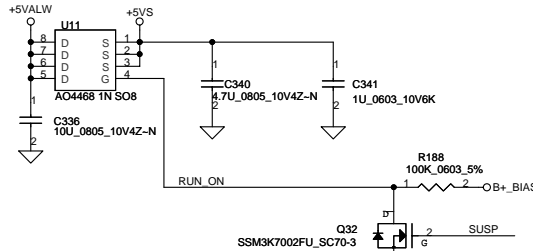
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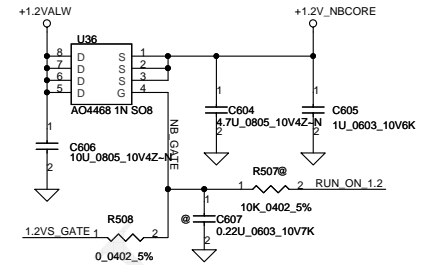
+5VALW TO +5V



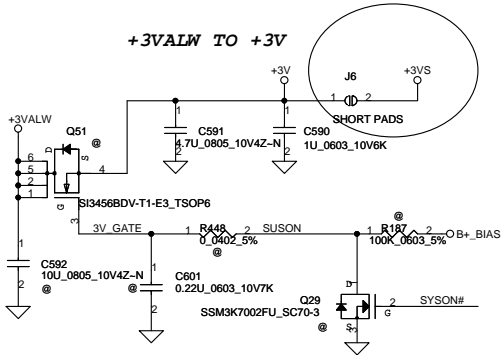
+5VALW TO +5VS



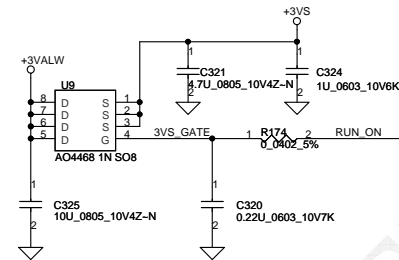
+1.2VALW TO +1.2V_NBCORE



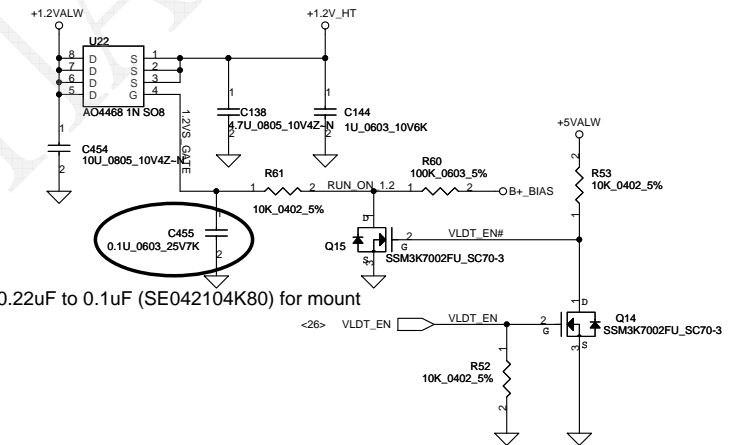
+3VALW TO +3V



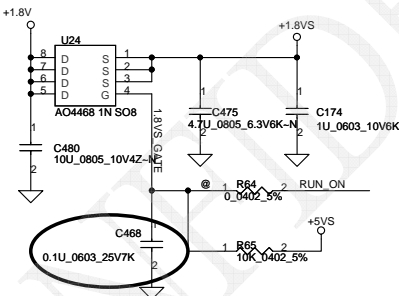
+3VALW TO +3VS



+1.2VALW TO +1.2V_HT

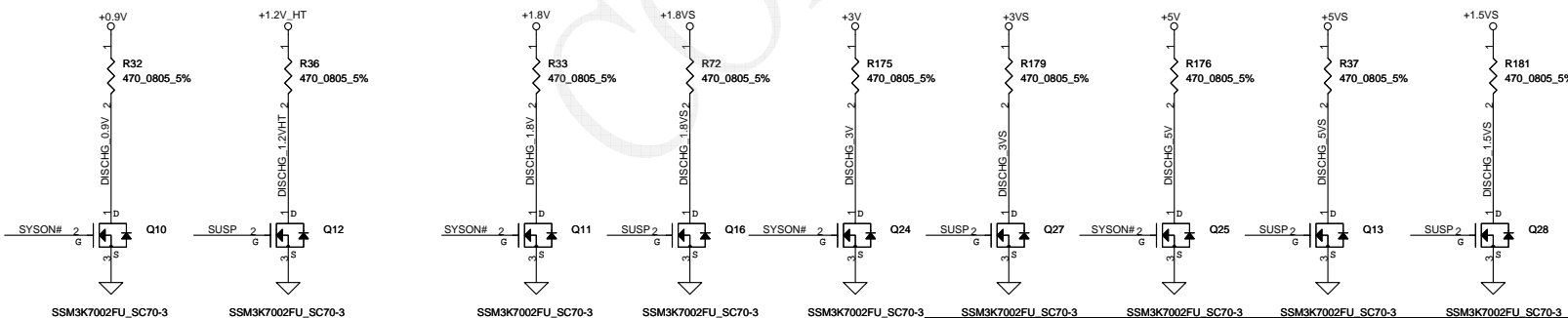
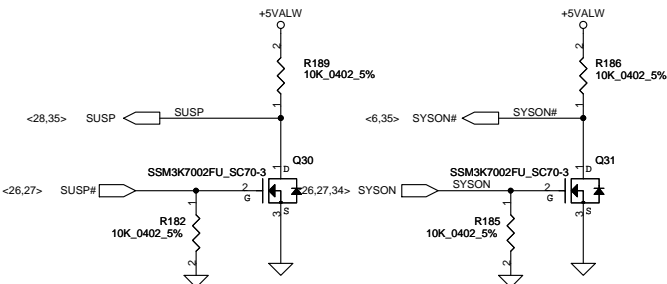


+1.8V TO +1.8VS



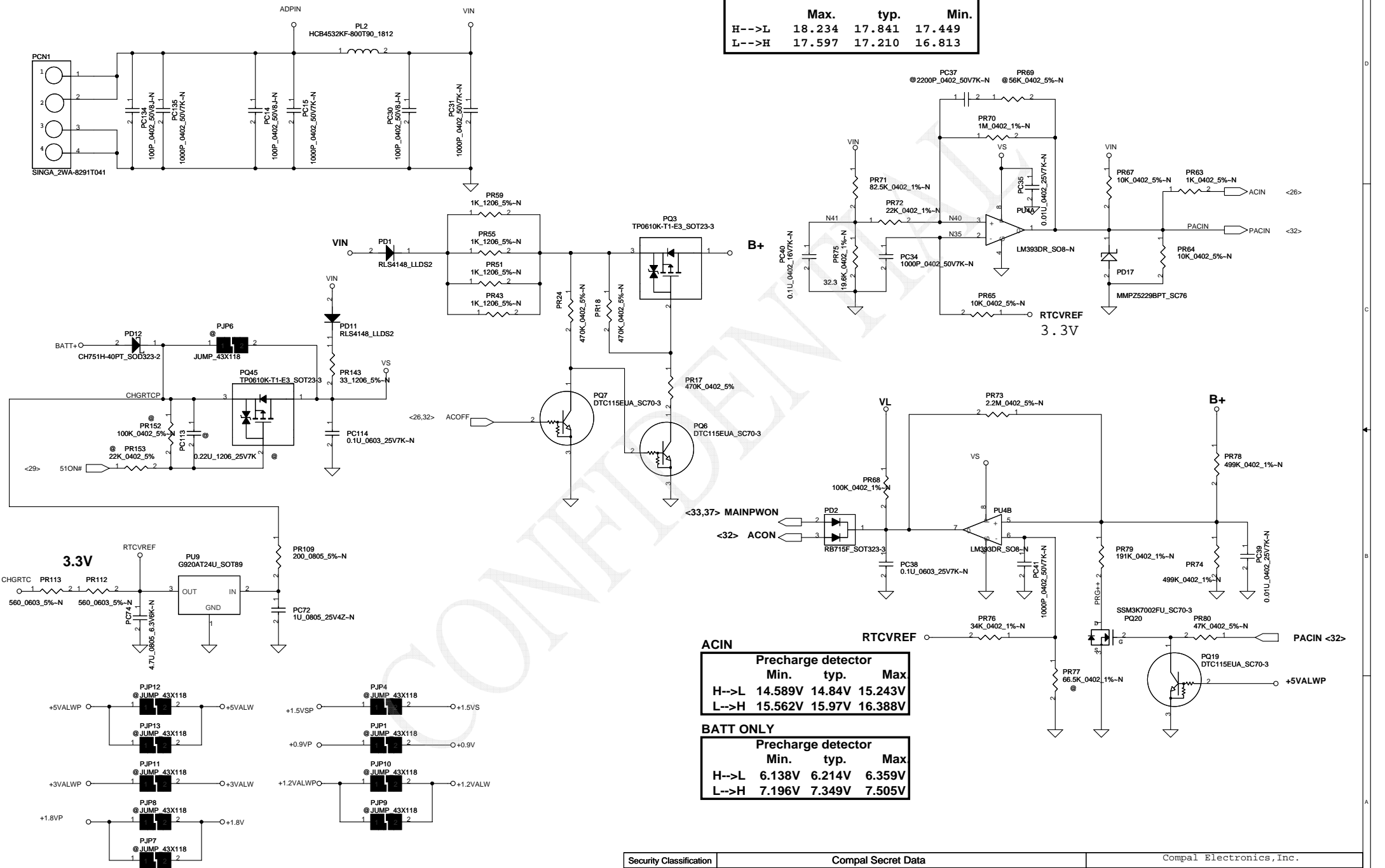
Change C455 from 0.22uF to 0.1uF (SE042104K80) for mount

Change C468 from 0.22uF to 0.1uF (SE042104K80) for mount



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Vin Detector			
	Max.	typ.	Min.
H-->L	18.234	17.841	17.449
L-->H	17.597	17.210	16.813



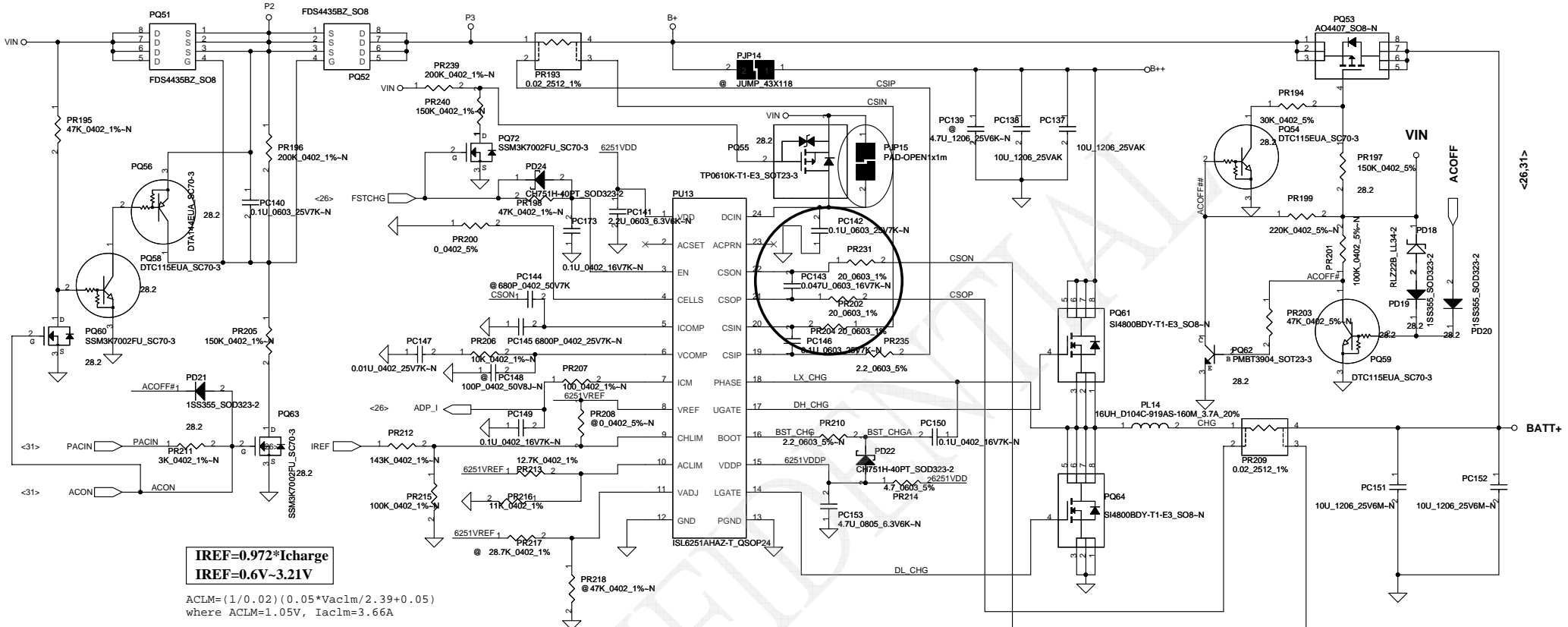
ACIN Precharge detector			
	Min.	typ.	Max
H-->L	14.589V	14.84V	15.243V
L-->H	15.562V	15.97V	16.388V

BATT ONLY Precharge detector			
	Min.	typ.	Max
H-->L	6.138V	6.214V	6.359V
L-->H	7.196V	7.349V	7.505V

Iadp=0~3.95A (75W)

Fosc=14100/Rt=14100/47=300KHz

Charger

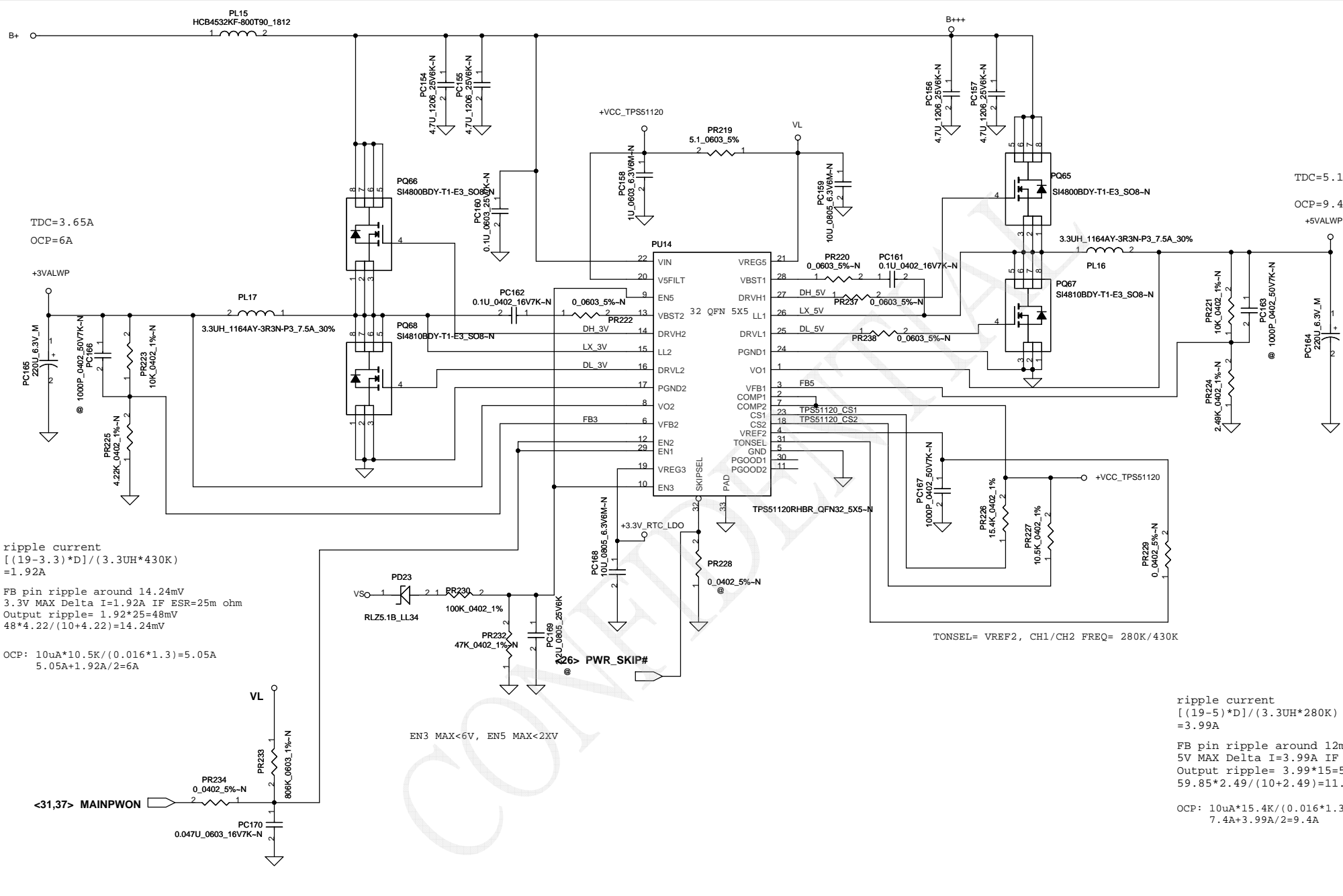


IREF=0.972*Icharge
 IREF=0.6V~3.21V

ACLm=(1/0.02)(0.05*Vaclm/2.39+0.05)
 where ACLm=1.05V, Iaclm=3.66A

LI-3S :1.3.5V---BATT-OVP=1.5V

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TDC=3.65A
OCP=6A

TDC=5.1A
OCP=9.4A
+5VALWP

ripple current
 $[(19-3.3)*D]/(3.3UH*430K)$
 $=1.92A$

FB pin ripple around 14.24mV
 3.3V MAX Delta I=1.92A IF ESR=25m ohm
 Output ripple= $1.92*25=48mV$
 $48*4.22/(10+4.22)=14.24mV$

OCP: $10uA*10.5K/(0.016*1.3)=5.05A$
 $5.05A+1.92A/2=6A$

EN3 MAX<6V, EN5 MAX<2XV

TONSEL= VREF2, CH1/CH2 FREQ= 280K/430K

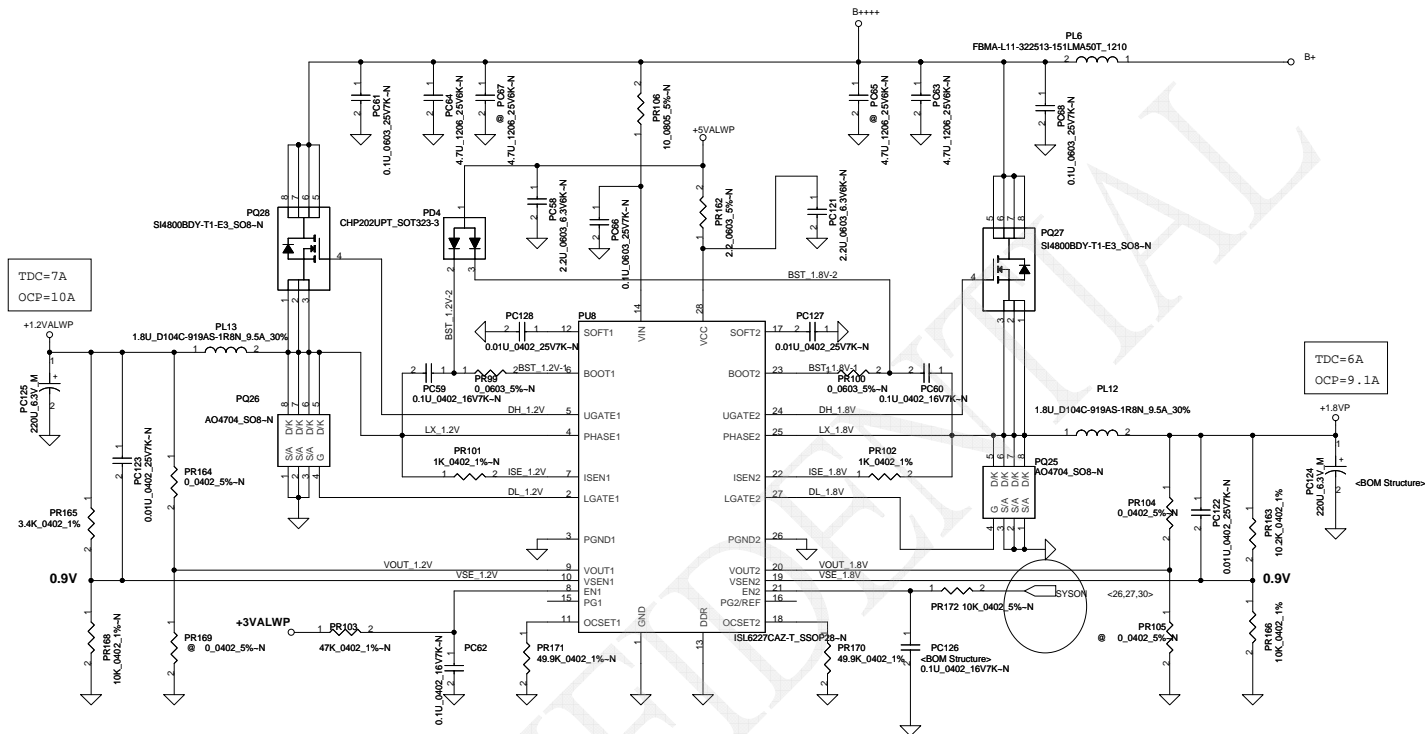
ripple current
 $[(19-5)*D]/(3.3UH*280K)$
 $=3.99A$

FB pin ripple around 12mV
 5V MAX Delta I=3.99A IF ESR=15m ohm
 Output ripple= $3.99*15=59.85mV$
 $59.85*2.49/(10+2.49)=11.93mV$

OCP: $10uA*15.4K/(0.016*1.3)=7.4A$
 $7.4A+3.99A/2=9.4A$

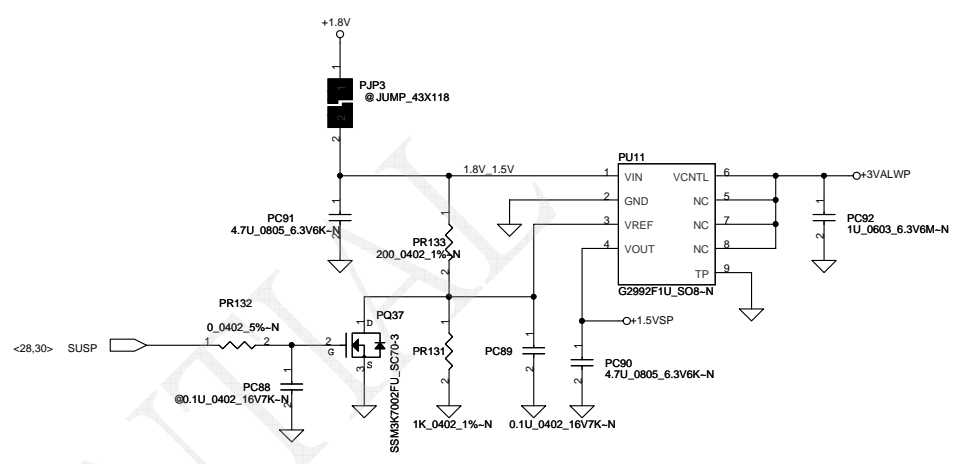
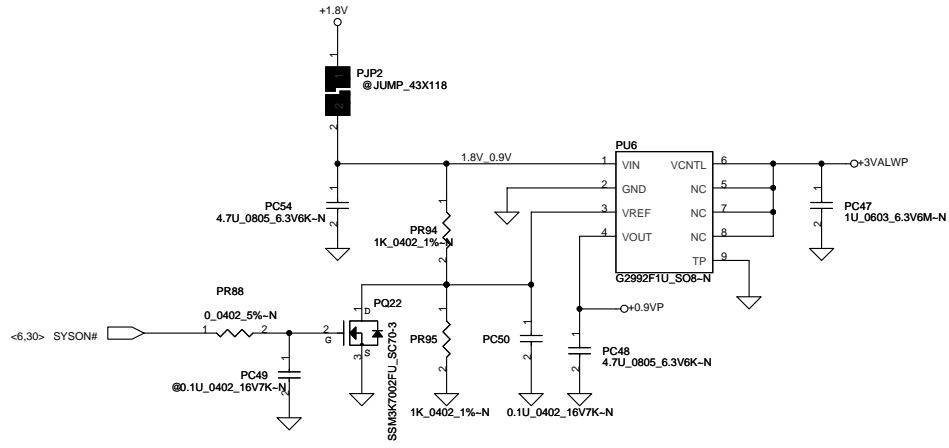
Because EN1 and EN2 have 2uA source current,
 the RC time will be change.

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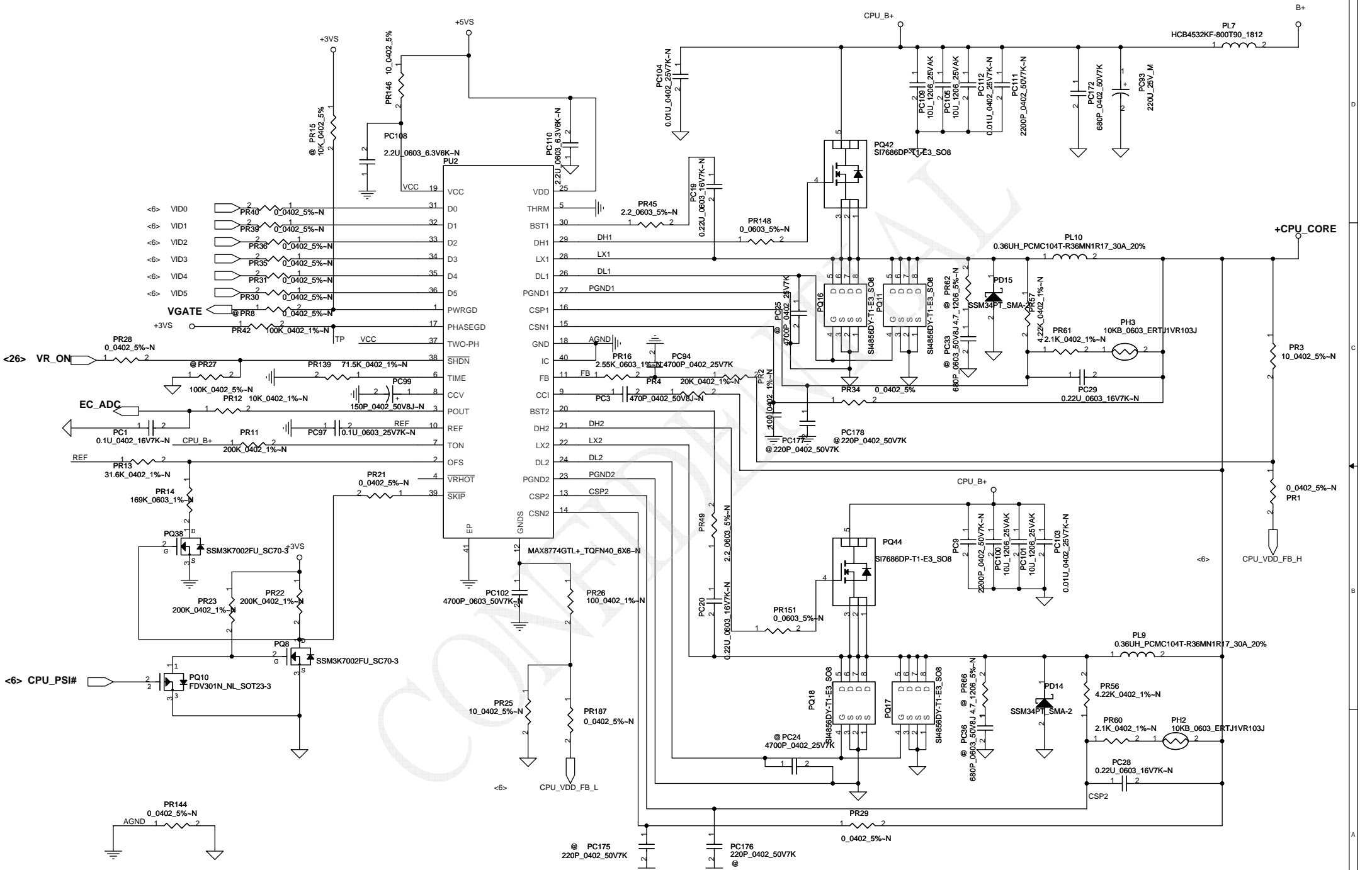
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+1.5VSP/+0.9VSP



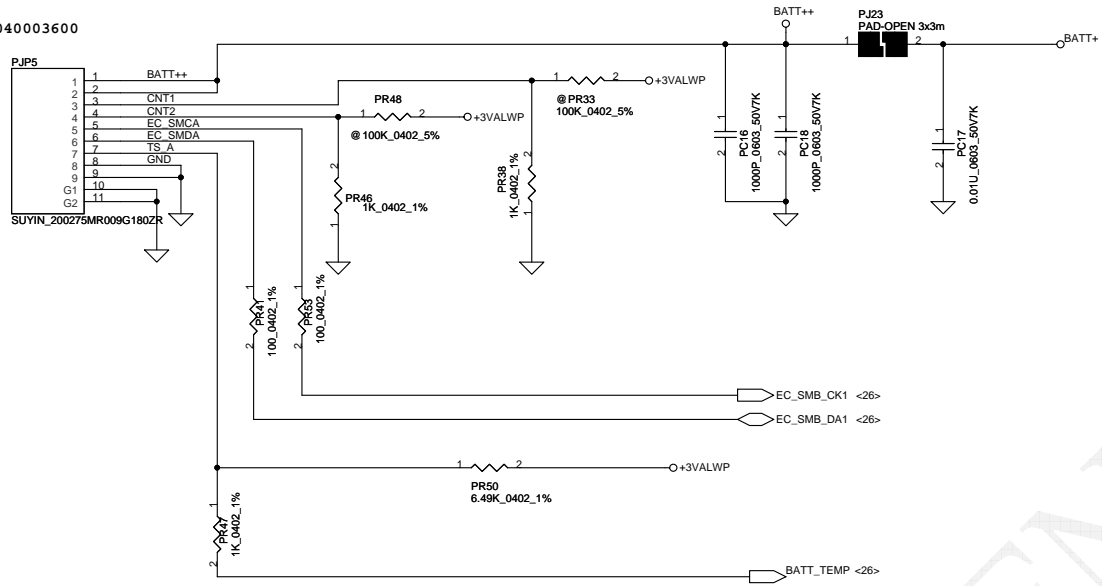
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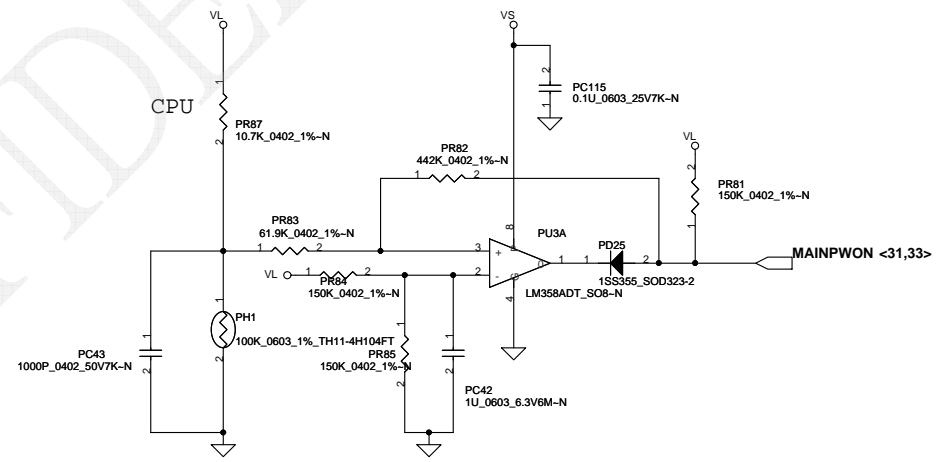
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DC040003600



Battery Connect/OTP

CPU
PH1 under CPU bottom side :
 CPU thermal protection at 85 degree C
 Recovery at 70 degree C



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page	Reason for change	Modify list
DVT	Modify 1.2 OCP to 12A	PR171 to 73.2K, PR101 to 1K
PVT	Modify 1.2 OCP to 14A	PR171 to 49.9K,
	Modify 1.8 OCP to 14A	PR170 to 49.9K,
Pre-MP	Change 1.8UH source to TOKO for nosie issue	PL12,PL13
	Change 3.3UH source to TOKO for nosie issue	PL16,PL17
	Change 4.7UF to 10uF for nosie issue	PC101,PC100,PC105,PC109,PC137,PC138
	DEL 4.7UF for nosie issue	PC139

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