

PART for BOM only

Temp Modify

1012: Delete EC23,31,32,34,39 already have in location

Delete EC35,36 No space to add

Delete GC114,115,120,121,122,123,116,117,118,119 No space to add

RTC BTY

BATT

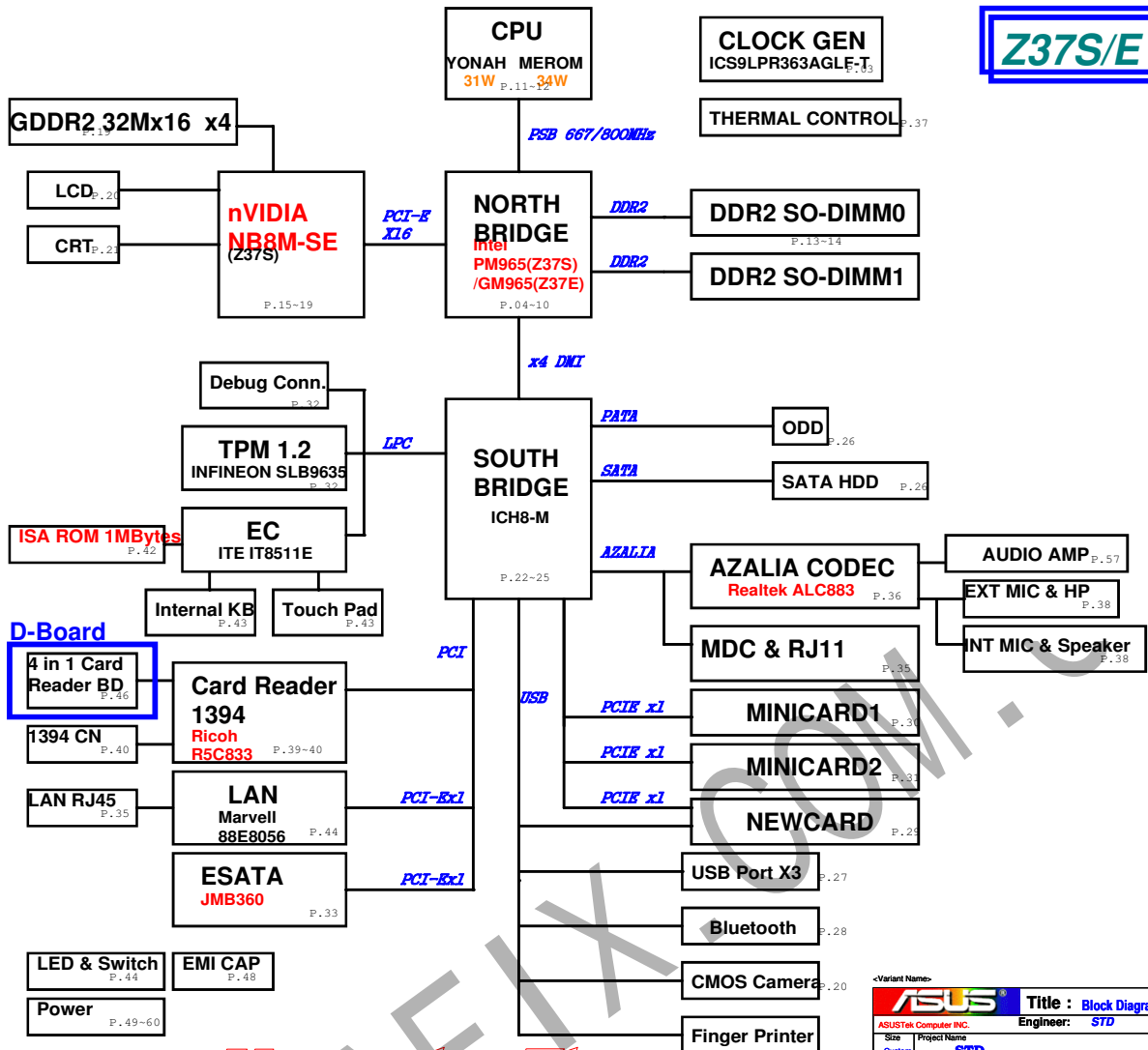
Function	ASUS Partnumber	LOCATION
RTC BTY	07G016412032	
GPU NB8M	02G190012900	

<Variant Name>

Title	Revision
A3	STD
Size	Document Number
A3	STD
Date	Rev
日期: 2007.03.08	2.00
Sheet 1 of 60	

<< Kennedy\_Zhang >>

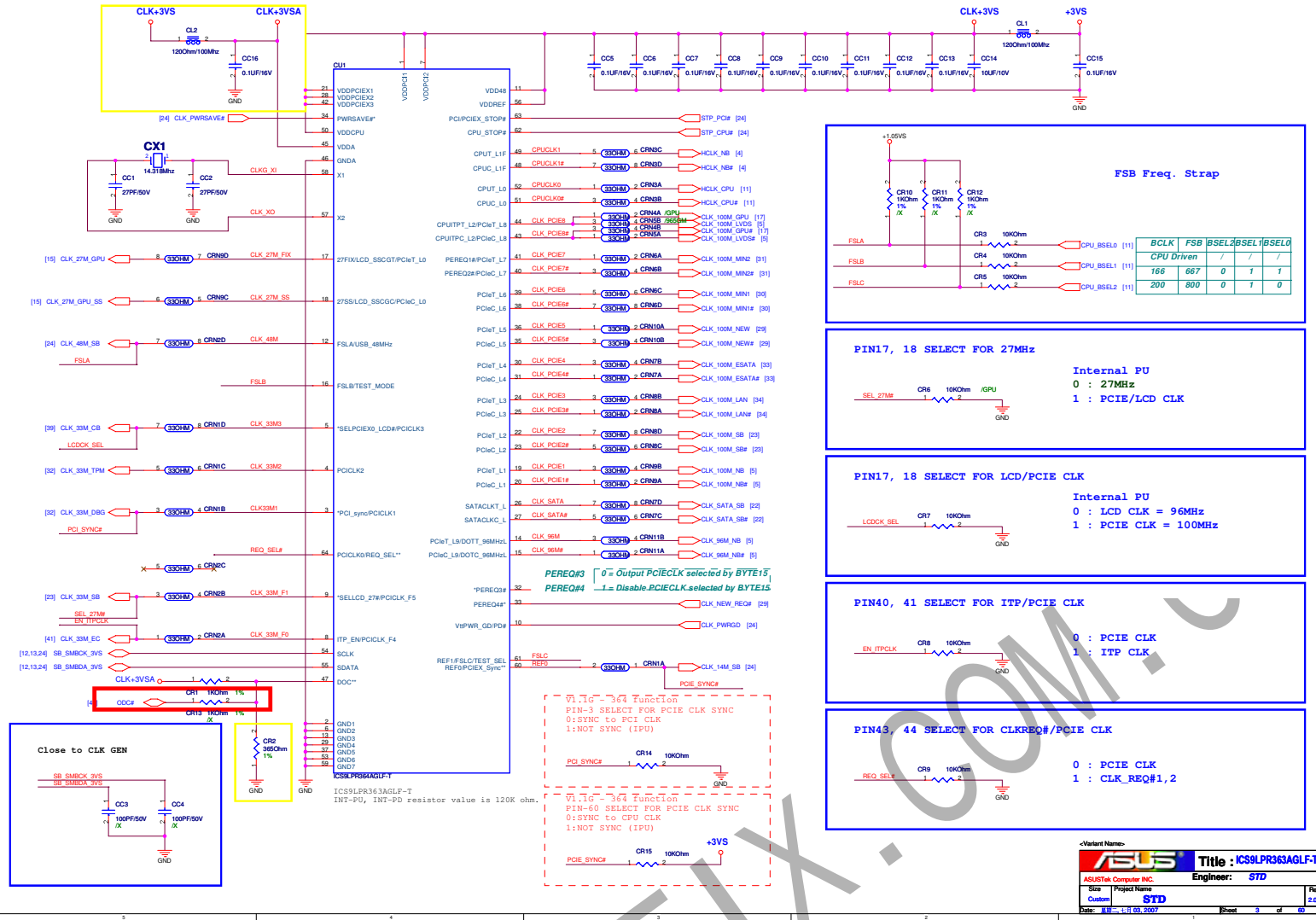
WWW.CHINAPELX.COM.CN



« Kennedy Zhang »

WWW.CHINAFOX.COM

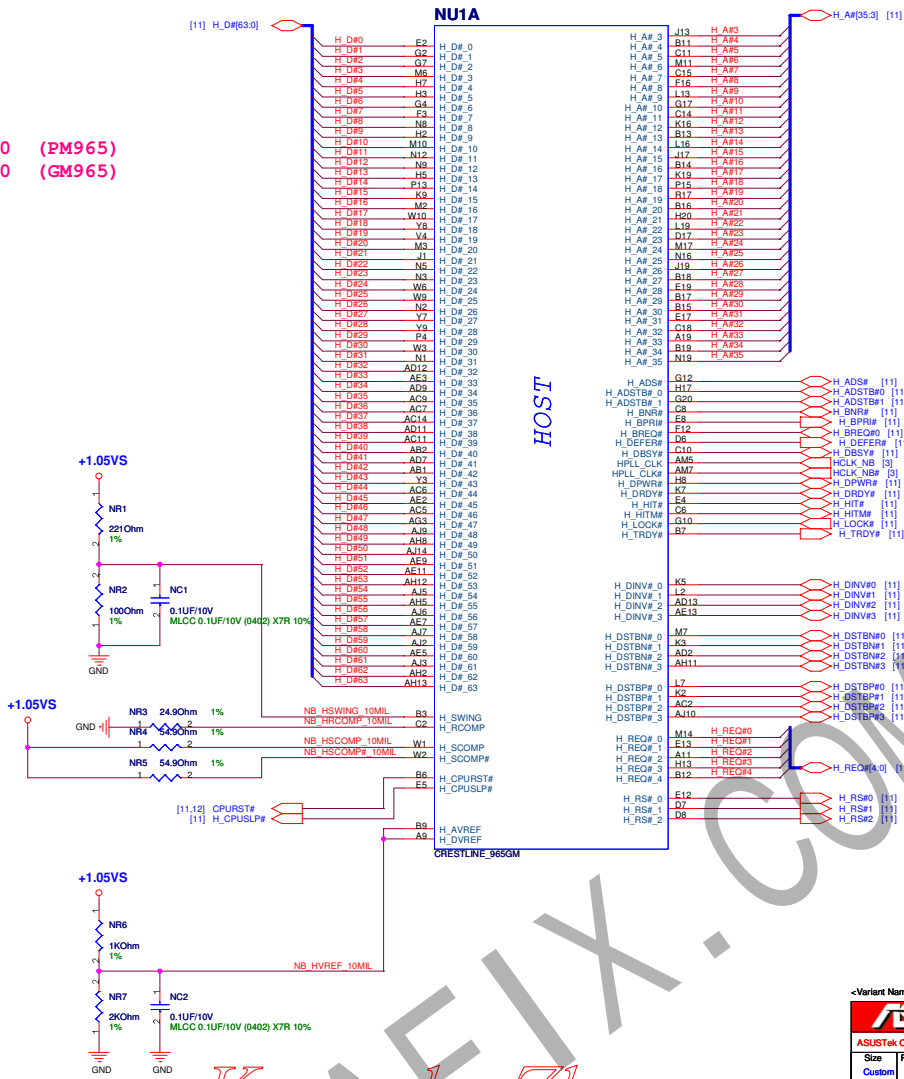
ASUS		Title : Block Diagram	
ASUSTek Computer Inc.		Engineer: STD	
Site: Custom	Project Name: STD	Rev: 2.0G	
Date: 11/14/03	2007	Sheet 2 of 80	



<< Kennedy\_Zhang >>

WWW.CHINAFFIX.COM

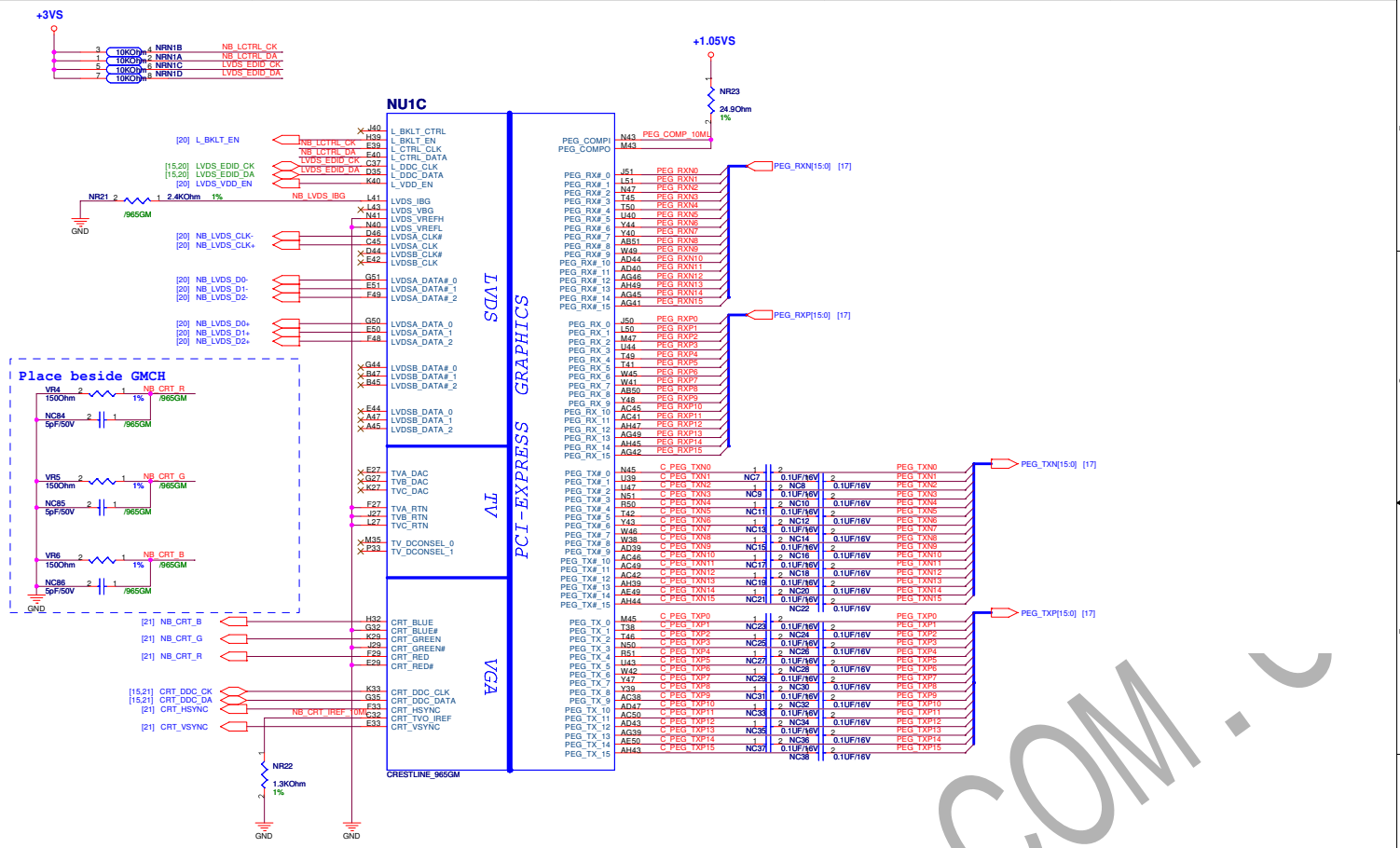
Z37S: 02G010017200 (PM965)  
Z37E: 02G010017300 (GM965)  
V2.0G



« Kennedy\_Zhang »

ASUS		Title : NB-965GM1-FSB
ASUSTek Computer INC.		Engineer: STD
Size	Project Name	Rev
Customer	STD	2.0G
Date: 8/8/2007	1:11:03, 2007	Sheet 4 of 60



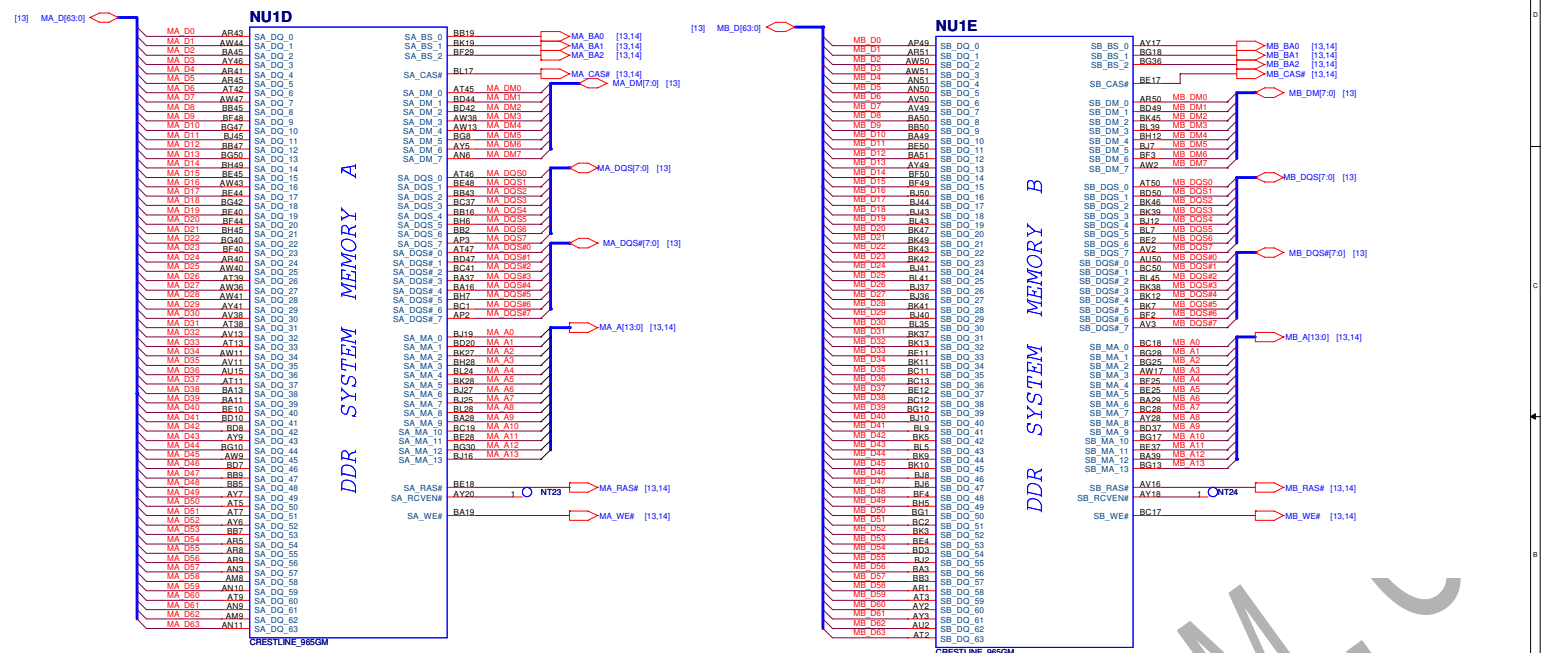


<Variant Name>

ASUSTek Computer INC.		Title NB-965GM3-PEG/VGA	
Site STD		Engineer: STD	
Size	Project Name	Rev	2.0G
Date: 11/11/08, 2007	Sheet	6	of 80

<< Kennedy\_Zhang >>

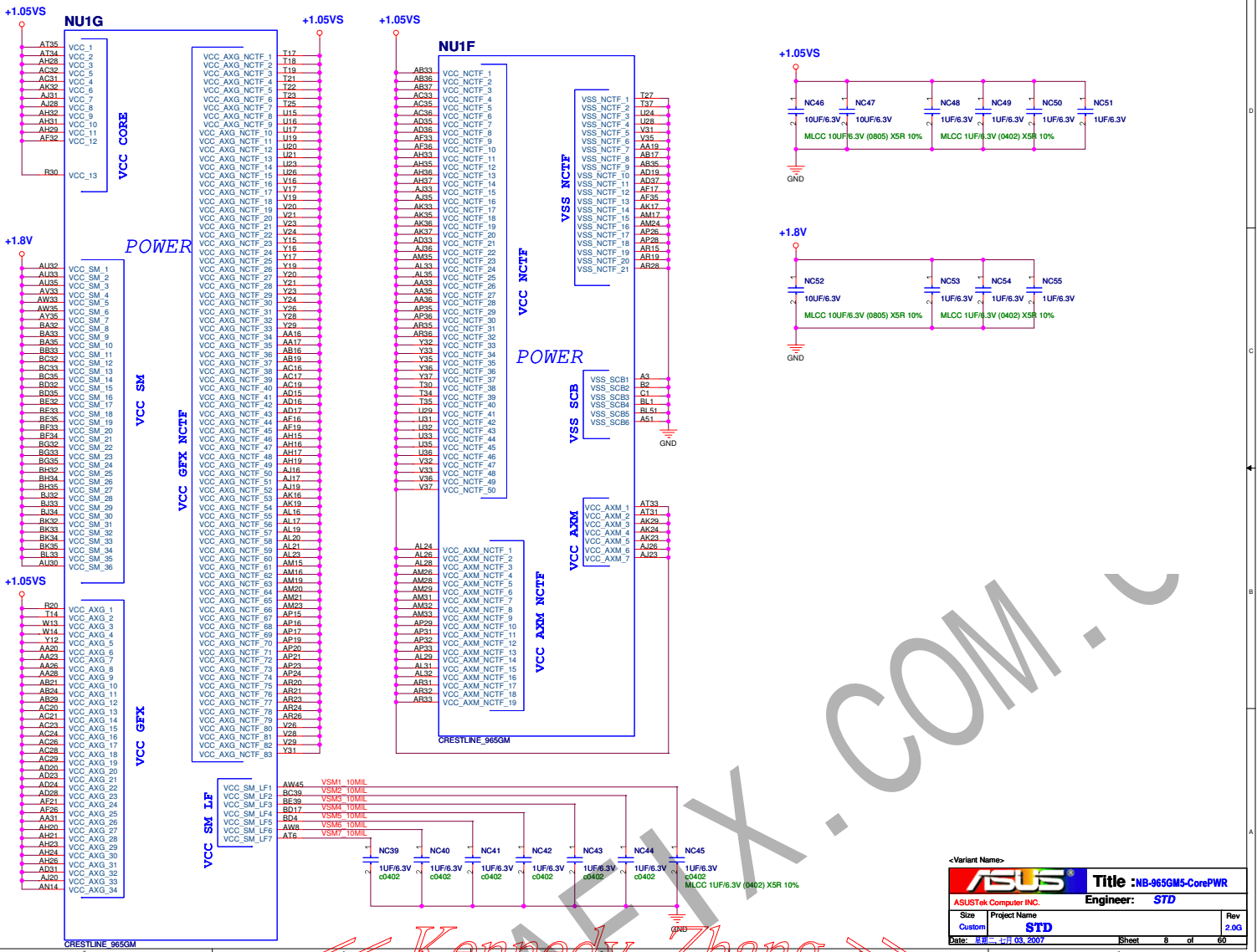
WWW.CHINAPEAK.COM.CN



ASUS		Title : NB-965GM4-DDR2	
ASUSTek Computer Inc.		Engineer: STD	
Site	Project Name	Rev	2.0G
AS	STD		
Date: 11/18/2007	Sheet	7	of 80

<< Kennedy\_Zhang >>

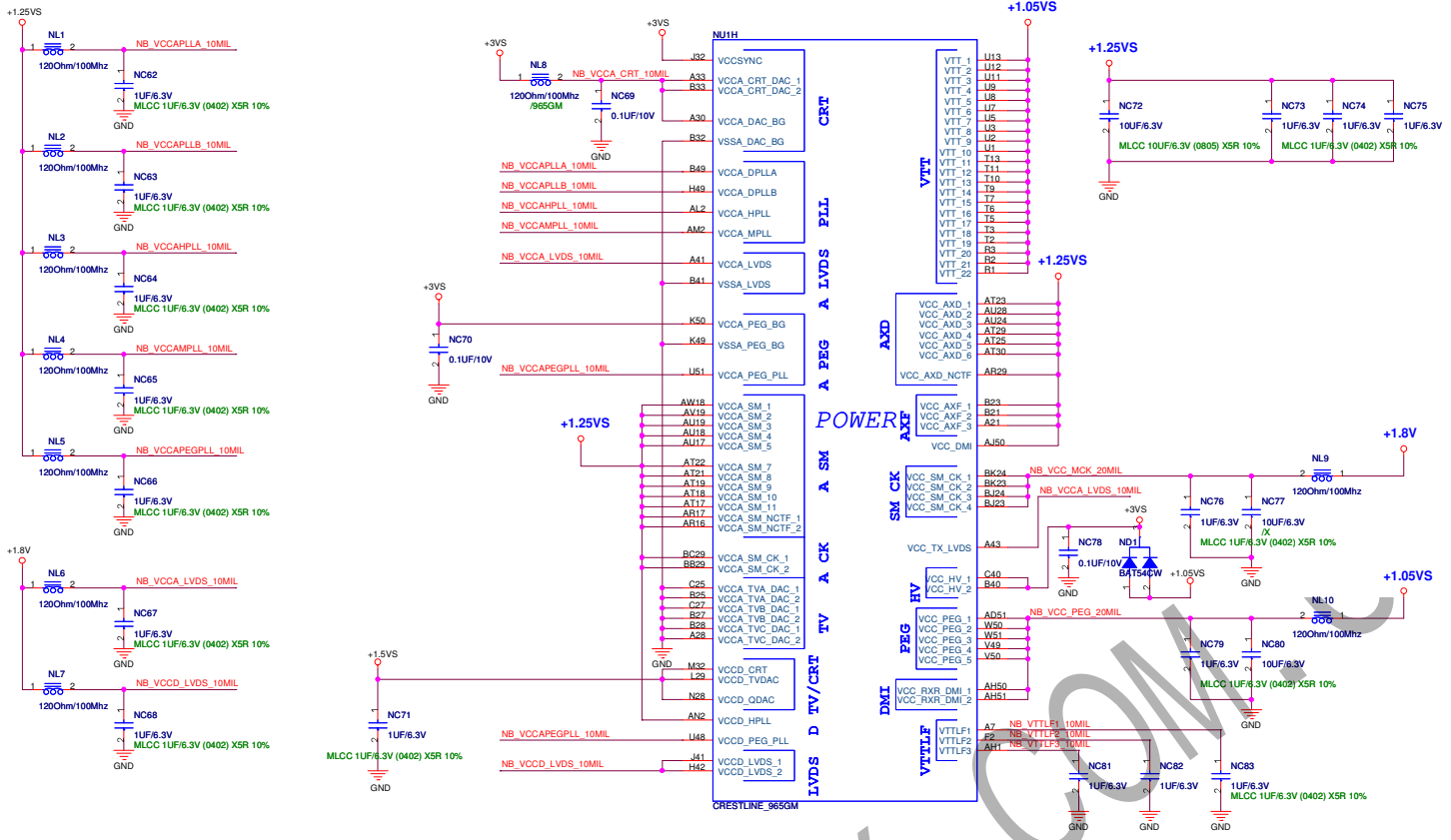
WWW.CHINAFEL.COM.CN



Kennedy\_Zhang

ASUS		Title : NB-965GM5-CorePWR	
ASUSTek Computer INC.		Engineer: STD	
Size	Project Name	Rev	2.0G
Custom	STD	Date: 03/03/2007	Sheet 8 of 60





Kennedy Zhang

HIGH LANES REVERSED

ASUS		<b>Title : NB-965GM6-IOPWR</b>	
ASUSTek Computer INC.		Engineer: <b>STD</b>	
Size	Project Name	Date	Rev
	<b>STD</b>	2007.03.11	2.00
Sheet	9	of	60

**NU1I**

A13	VSS_1	VSS_100	AW24
A15	VSS_2	VSS_101	AW29
A17	VSS_3	VSS_102	AW32
A24	VSS_4	VSS_103	AW5
AA21	VSS_5	VSS_104	AW7
AA24	VSS_6	VSS_105	AY10
AA29	VSS_7	VSS_106	AY24
AB29	VSS_8	VSS_107	AY37
AB29	VSS_9	VSS_108	AY42
AB29	VSS_10	VSS_109	AY43
AB29	VSS_11	VSS_110	AY45
AB29	VSS_12	VSS_111	AY47
AC10	VSS_13	VSS_112	AY50
AC13	VSS_14	VSS_113	AY10
AC3	VSS_15	VSS_114	B20
AC39	VSS_16	VSS_115	B24
AC41	VSS_17	VSS_116	B29
AC47	VSS_18	VSS_117	B30
AD2	VSS_19	VSS_118	B33
AD2	VSS_20	VSS_119	B38
AD26	VSS_21	VSS_120	B43
AD29	VSS_22	VSS_121	B45
AD3	VSS_23	VSS_122	B5
AD41	VSS_24	VSS_123	B8
AD45	VSS_25	VSS_124	BA1
AD49	VSS_26	VSS_125	BA17
AD5	VSS_27	VSS_126	BA18
AD59	VSS_28	VSS_127	BA5
ADR	VSS_29	VSS_128	BA24
AE10	VSS_30	VSS_129	BB25
AE14	VSS_31	VSS_130	BB40
AER	VSS_32	VSS_131	BB49
AF20	VSS_33	VSS_132	BB8
AF23	VSS_34	VSS_133	BB88
AF24	VSS_35	VSS_134	BB9
AF31	VSS_36	VSS_135	BC16
AG2	VSS_37	VSS_136	BC24
AG39	VSS_38	VSS_137	BC25
AG42	VSS_39	VSS_138	BC26
AG47	VSS_40	VSS_139	BC40
AG59	VSS_41	VSS_140	BD11
AH3	VSS_42	VSS_141	BD2
AH40	VSS_43	VSS_142	BD28
AH41	VSS_44	VSS_143	BD45
AH7	VSS_45	VSS_144	BD48
AH9	VSS_46	VSS_145	BE5
AH11	VSS_47	VSS_146	BE1
AJ13	VSS_48	VSS_147	BE19
AJ24	VSS_49	VSS_148	BE19
AJ29	VSS_50	VSS_149	BE29
AJ32	VSS_51	VSS_150	BE30
AJ43	VSS_52	VSS_151	BE42
AJ49	VSS_53	VSS_152	BE51
AJ49	VSS_54	VSS_153	BE9
AK20	VSS_55	VSS_154	BF12
AK21	VSS_56	VSS_155	BF16
AK21	VSS_57	VSS_156	BF36
AK29	VSS_58	VSS_157	BG19
AK29	VSS_59	VSS_158	BG2
AK31	VSS_60	VSS_159	BG34
AK31	VSS_61	VSS_160	BG39
AL1	VSS_62	VSS_161	BG48
AM13	VSS_63	VSS_162	BG5
AM1	VSS_64	VSS_163	BG51
AM4	VSS_65	VSS_164	BH17
AM41	VSS_66	VSS_165	BH30
AM45	VSS_67	VSS_166	BH44
AM4	VSS_68	VSS_167	BH49
AN1	VSS_69	VSS_168	BH6
AN39	VSS_70	VSS_169	BH9
AN43	VSS_71	VSS_170	BJ11
AN5	VSS_72	VSS_171	BJ38
AN7	VSS_73	VSS_172	BJ4
AP4	VSS_74	VSS_173	BJ42
AP49	VSS_75	VSS_174	BJ46
AP49	VSS_76	VSS_175	BK15
AR11	VSS_77	VSS_176	BK17
AR2	VSS_78	VSS_177	BK25
AR29	VSS_79	VSS_178	BK29
AR41	VSS_80	VSS_179	BK36
AR47	VSS_81	VSS_180	BK40
AR7	VSS_82	VSS_181	BK44
AT10	VSS_83	VSS_182	BK6
AT14	VSS_84	VSS_183	BK8
AT19	VSS_85	VSS_184	BL11
AU1	VSS_86	VSS_185	BL13
AU1	VSS_87	VSS_186	BL19
AU23	VSS_88	VSS_187	BL22
AU29	VSS_89	VSS_188	BL27
AU3	VSS_90	VSS_189	BL47
AU39	VSS_91	VSS_190	C12
AU51	VSS_92	VSS_191	C19
AU59	VSS_93	VSS_192	C28
AV39	VSS_94	VSS_193	C29
AW1	VSS_95	VSS_194	C33
AW16	VSS_96	VSS_195	C38
AW17	VSS_97	VSS_196	C41
AW17	VSS_98	VSS_197	C41
AW16	VSS_99	VSS_198	C41

GND

**NU1J**

C46	VSS_199	VSS_287	W11
C50	VSS_200	VSS_288	W39
C7	VSS_201	VSS_289	W43
D12	VSS_202	VSS_290	W47
AW5	VSS_203	VSS_291	W5
D3	VSS_204	VSS_292	W7
D39	VSS_205	VSS_293	Y13
AY24	VSS_206	VSS_294	Y2
D45	VSS_207	VSS_295	Y41
D49	VSS_208	VSS_296	Y45
E10	VSS_209	VSS_297	Y49
E16	VSS_210	VSS_298	Y5
E24	VSS_211	VSS_299	Y50
E28	VSS_212	VSS_300	Y11
E32	VSS_213	VSS_301	F29
E47	VSS_214	VSS_302	F29
F19	VSS_215	VSS_303	T29
F36	VSS_216	VSS_304	T33
F4	VSS_217	VSS_305	R28
F40	VSS_218		
F50	VSS_219		
F50	VSS_220		
G1	VSS_221		
G13	VSS_222	VSS_308	AK30
G16	VSS_223	VSS_307	AK32
G19	VSS_224	VSS_308	AD32
G24	VSS_225	VSS_309	AF28
G28	VSS_226	VSS_310	AF29
G33	VSS_227	VSS_311	AF29
G42	VSS_228	VSS_312	AV25
G45	VSS_229	VSS_313	H50
G48	VSS_230		
H24	VSS_231		
H28	VSS_232		
H4	VSS_233		
H4	VSS_234		
H24	VSS_235		
J11	VSS_236		
J16	VSS_237		
J2	VSS_238		
J24	VSS_239		
J33	VSS_240		
J35	VSS_241		
J39	VSS_242		
J39	VSS_243		
K12	VSS_245		
K47	VSS_246		
K8	VSS_247		
L7	VSS_248		
L7	VSS_249		
L20	VSS_250		
L24	VSS_251		
L28	VSS_252		
L3	VSS_253		
L33	VSS_254		
L49	VSS_255		
M28	VSS_256		
M42	VSS_257		
M45	VSS_258		
M49	VSS_259		
M5	VSS_260		
M50	VSS_261		
N10	VSS_262		
N11	VSS_263		
N14	VSS_264		
N17	VSS_265		
N29	VSS_266		
N32	VSS_267		
N36	VSS_268		
N39	VSS_269		
N44	VSS_270		
N49	VSS_271		
N7	VSS_272		
P19	VSS_273		
P2	VSS_274		
P23	VSS_275		
P3	VSS_276		
P50	VSS_277		
R49	VSS_278		
T39	VSS_279		
T43	VSS_280		
T47	VSS_281		
U41	VSS_282		
U45	VSS_283		
U50	VSS_284		
V2	VSS_285		
V3	VSS_286		

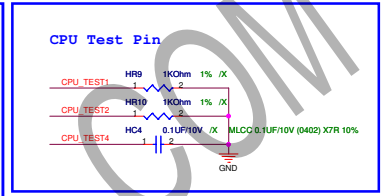
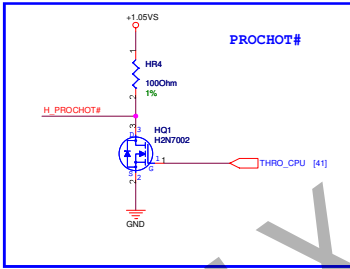
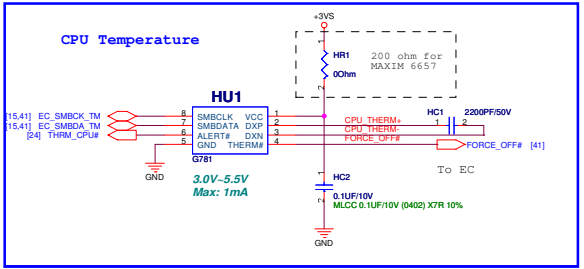
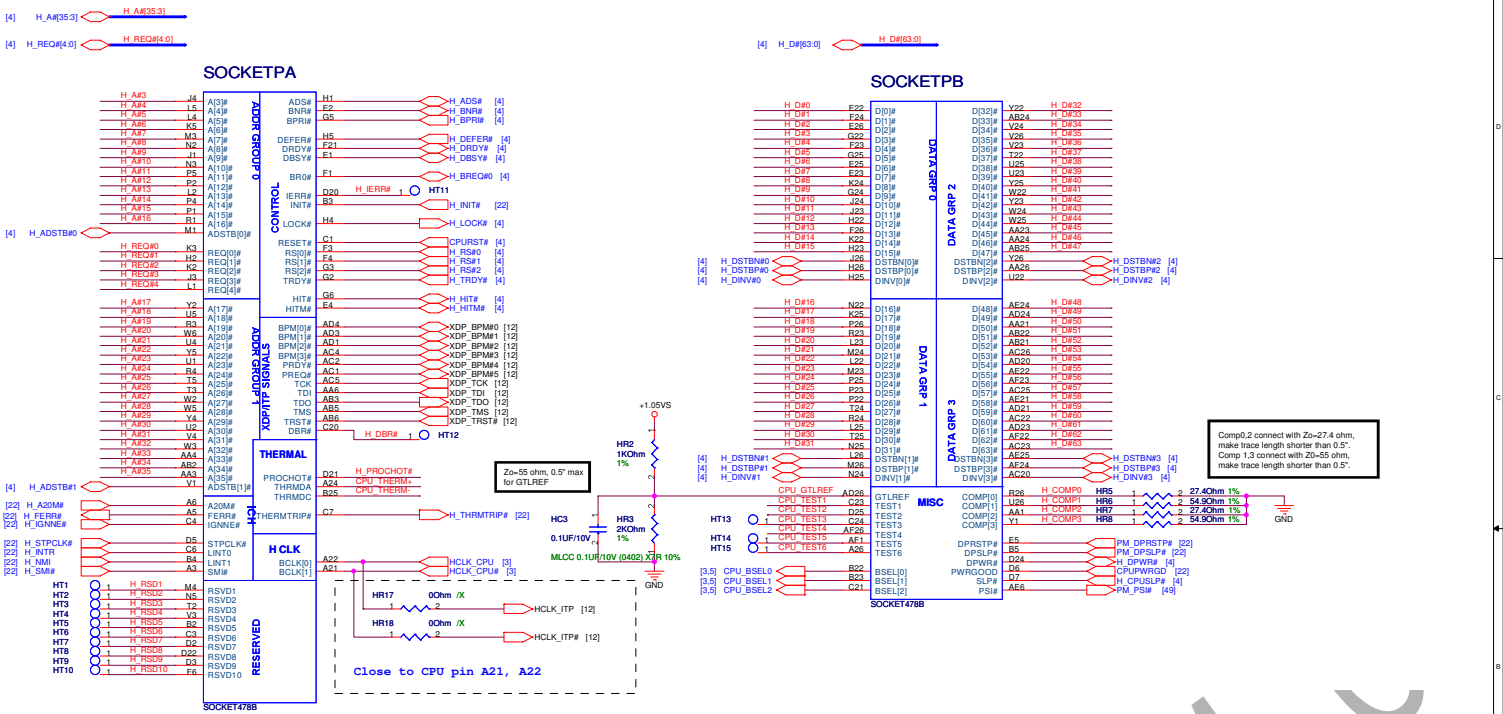
GND

CRESTLINE\_965GM

<Variant Name>

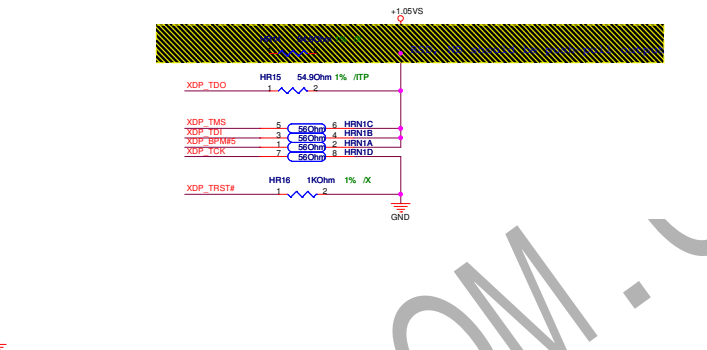
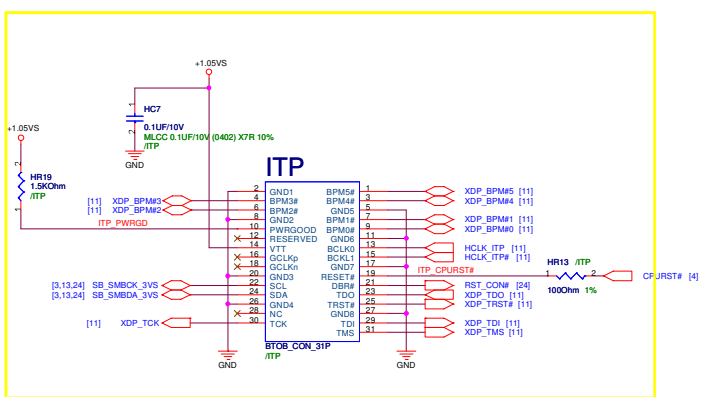
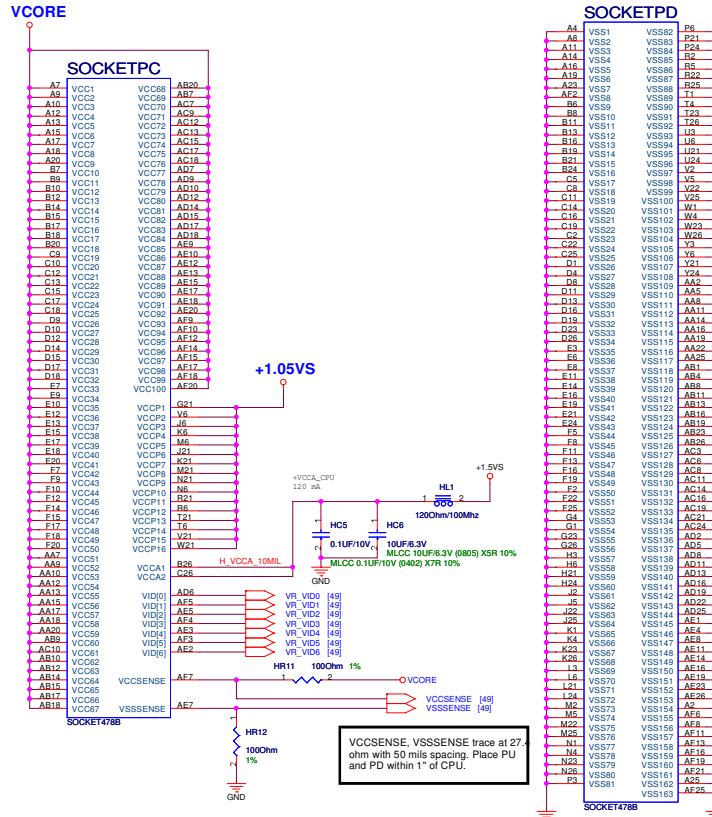
<b>ASUS</b>		<b>Title :</b> NB-965GM7-GND
ASUSTek Computer INC.		Engineer: STD
Size	Project Name	Rev
Custom	STD	2.00
Date: 8/11/2007	Sheet	10 of 60

<< Kennedy\_Zhang >>



ASUS  
ASUSTek Computer Inc.  
Title: MEROM CPU REV(1)  
Engineer: STD  
Date: 11/08/2007  
Sheet: 11 of 80

<< Kennedy\_Zhang >>



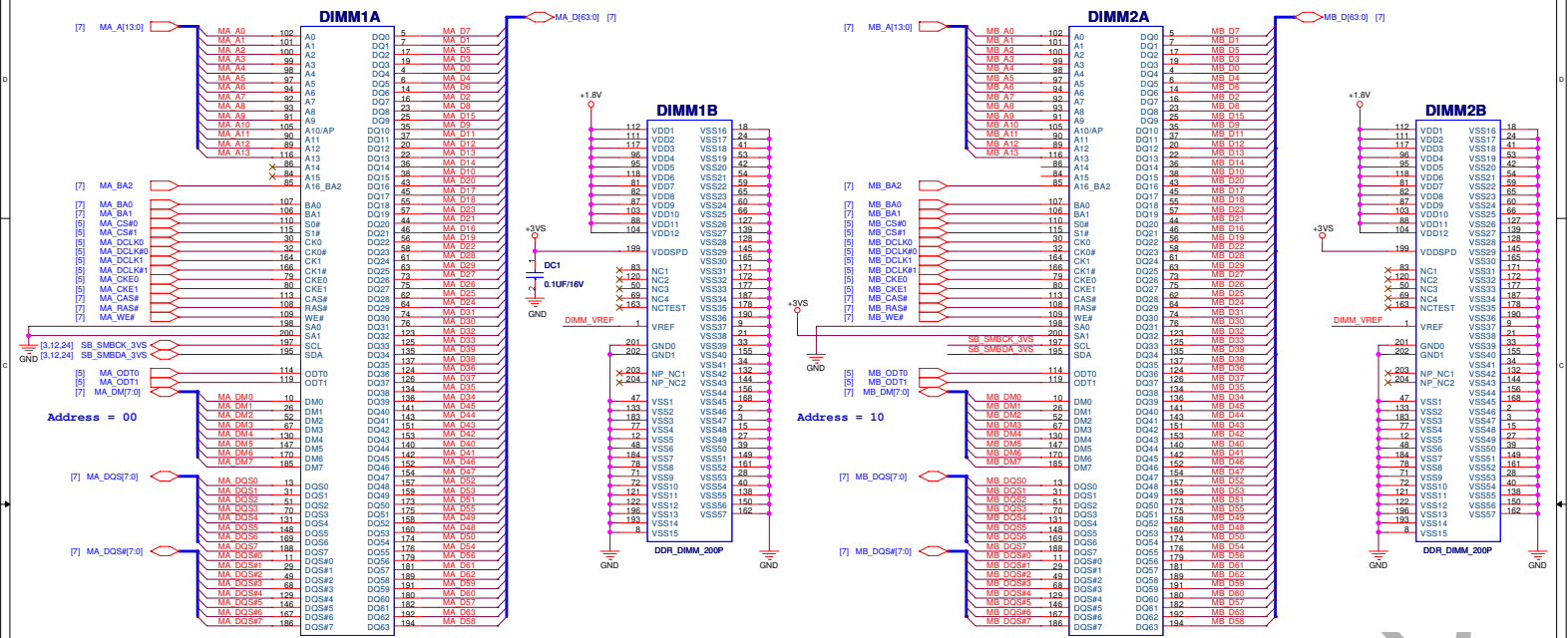
ASUS Logo

ASUSTek Computer INC. Title: MEROM CPU REV(2)  
Engineer: STD

Size: A3 Project Name: STD  
Date: #18-1#108.2007 Rev: 2.0G

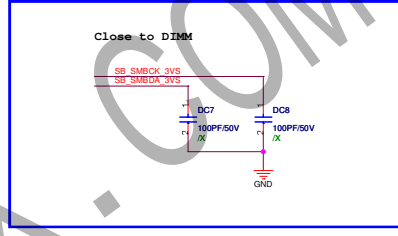
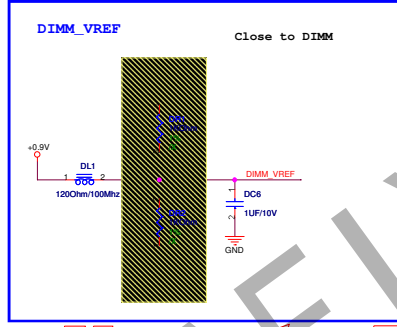
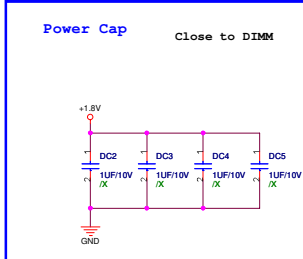
Sheet: 12 of 80

<< Kennedy\_Zhang >>



DDR\_DIMM\_200P  
12G025122007

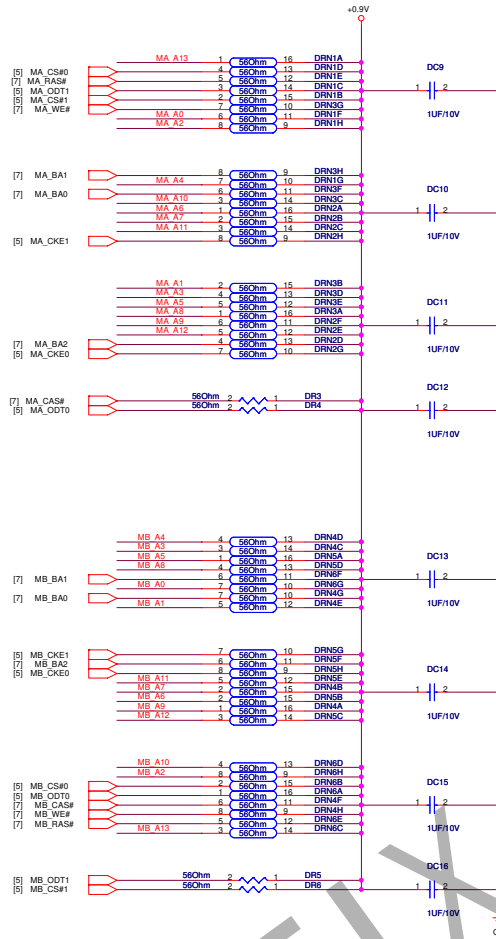
DDR\_DIMM\_200P  
12G025C22000



ASUS  
ASUSTek Computer Inc. Title: DDR2 SO-DIMMs  
Engineer: STD  
Date: #11-14-03-2007 Sheet: 13 of 80

« Kennedy Zhang »

[7] MA\_A[13:0]  
[7] MB\_A[13:0]



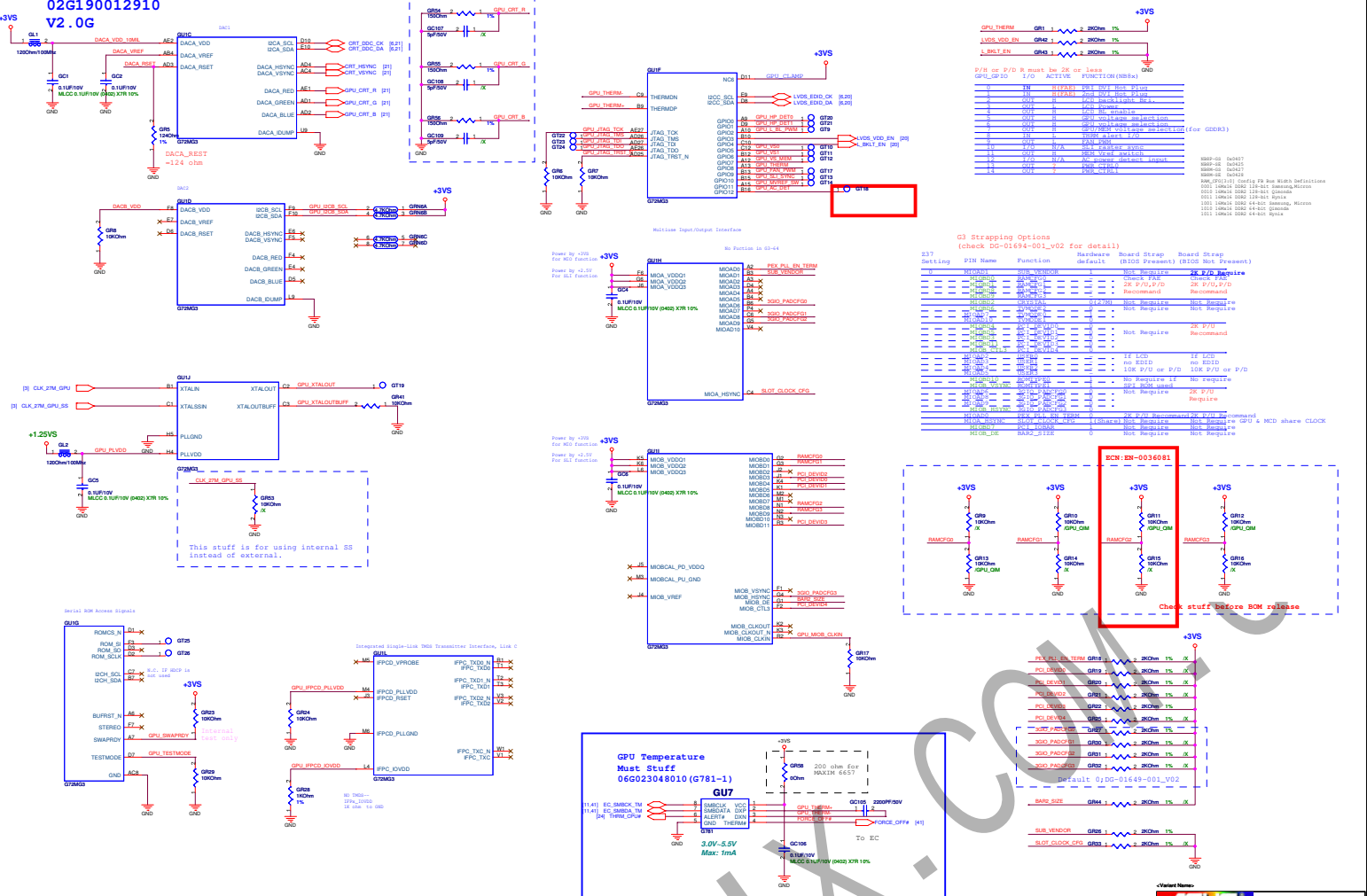
<Variant Name>

<b>ASUS</b>		<b>Title : DDR2 TERMINATION</b>
ASUSTek Computer INC.		Engineer: <b>STD</b>
Site	Project Name	Rev
Custom	<b>STD</b>	2.0G
Date: 11/11/03 2007	Sheet 14	of 80

« Kennedy\_Zhang »

WWW.CHINAFFIX.COM

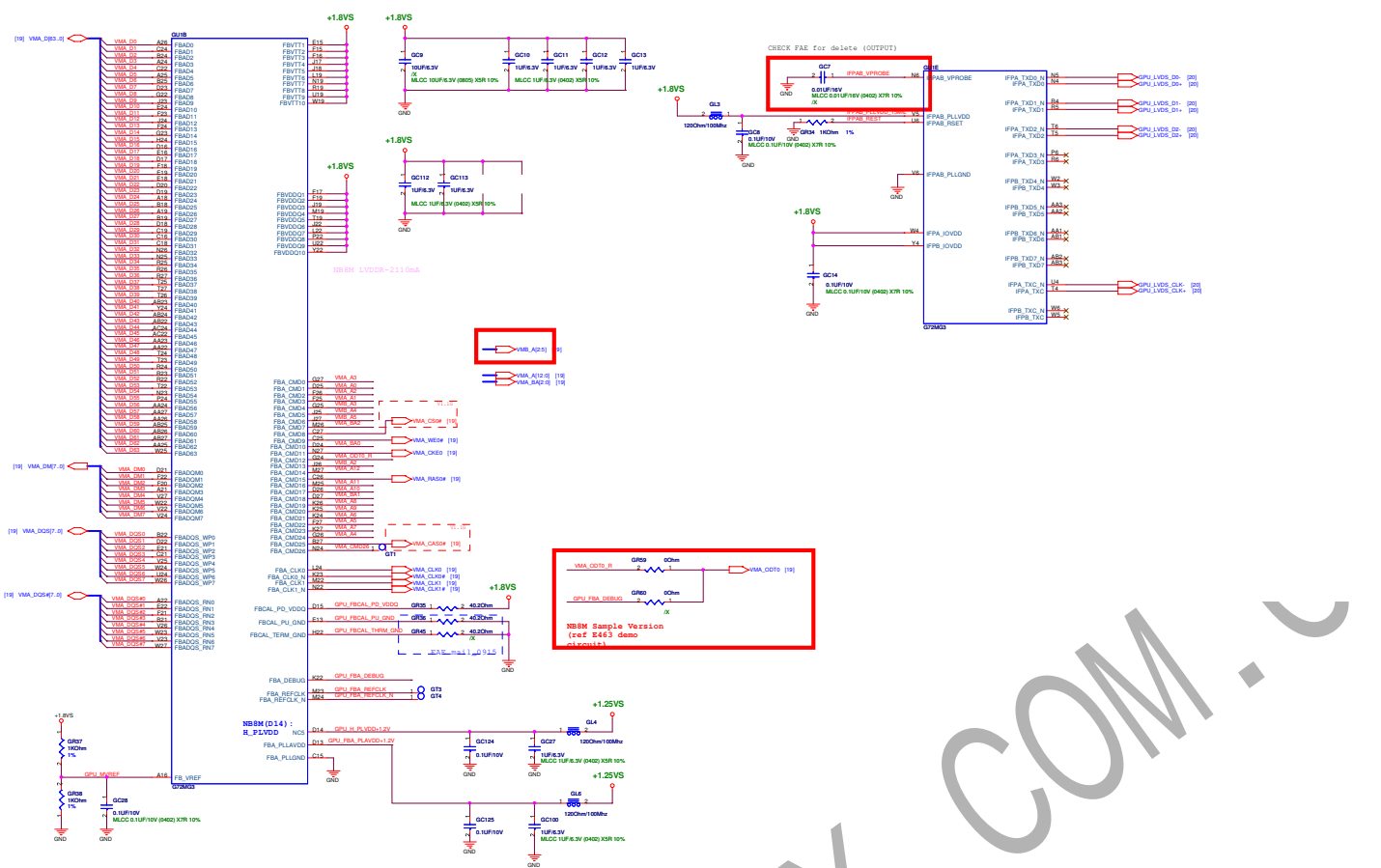
**GPU NB8M :  
02G190012910  
V2.0G**



<< Kennedy\_Zhang >>

WWW.CHINAFERK.COM

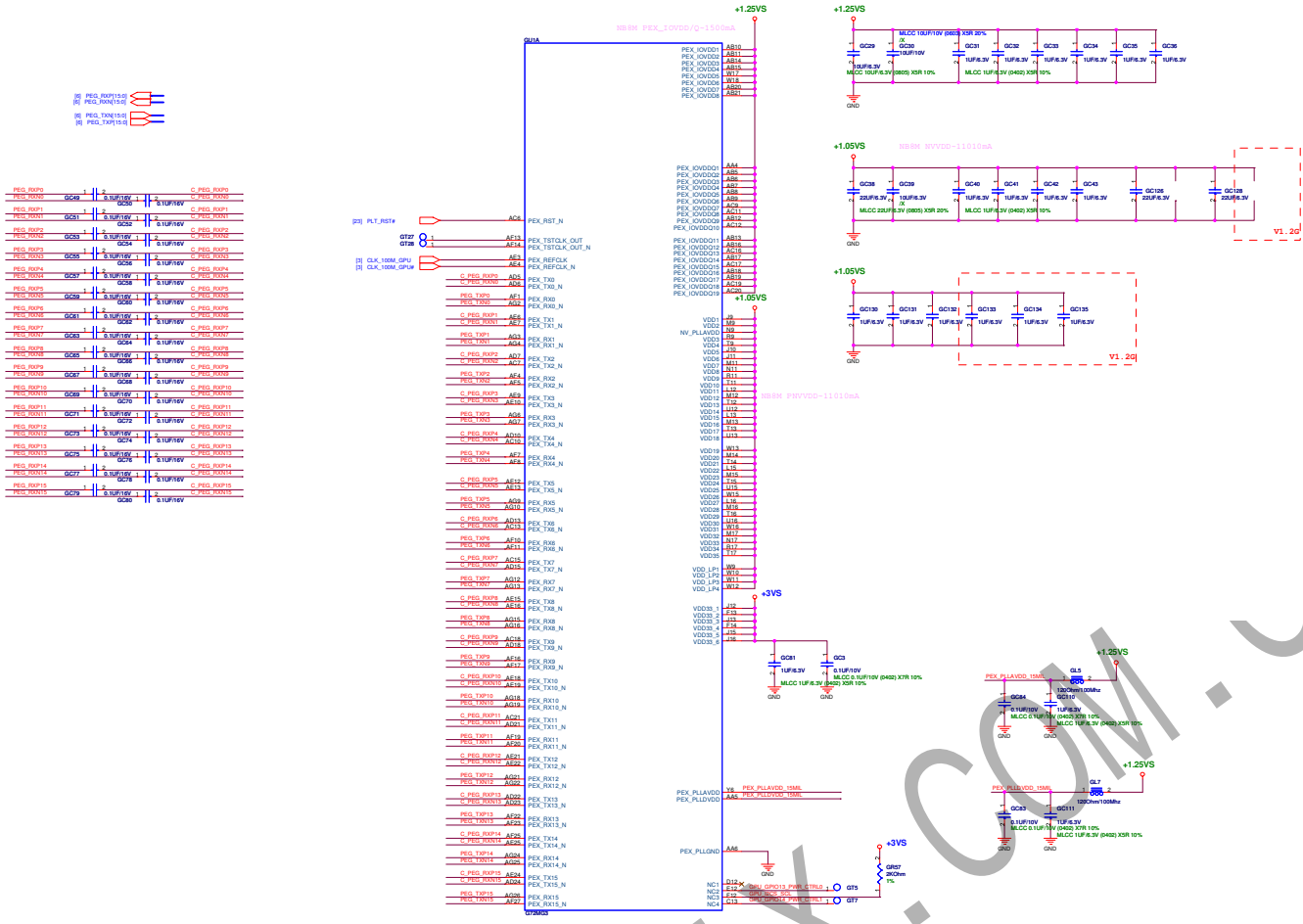
ASUS		Title : GPU MAM(I)	
ASUSTeK COMPUTER INC		Engineer: STD	
Size	Project Name	Rev	Date
A2	STD	1.001	2/2/2023
Scale	1:1 (3.00)	Sheet	1 of 1



<< Kennedy\_Zhang >>

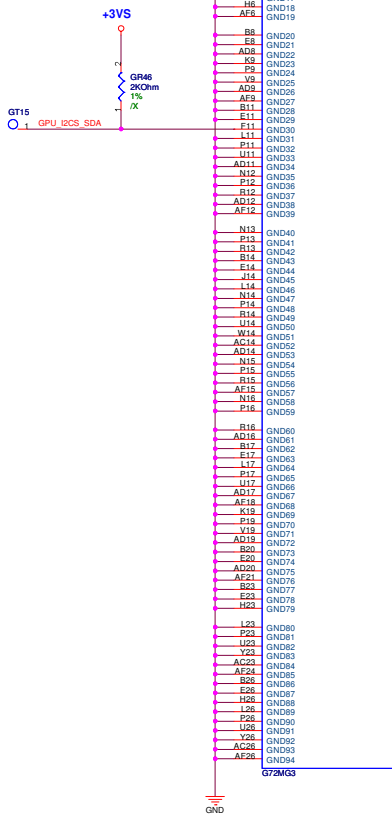
WWW.CHINAFFIX.COM





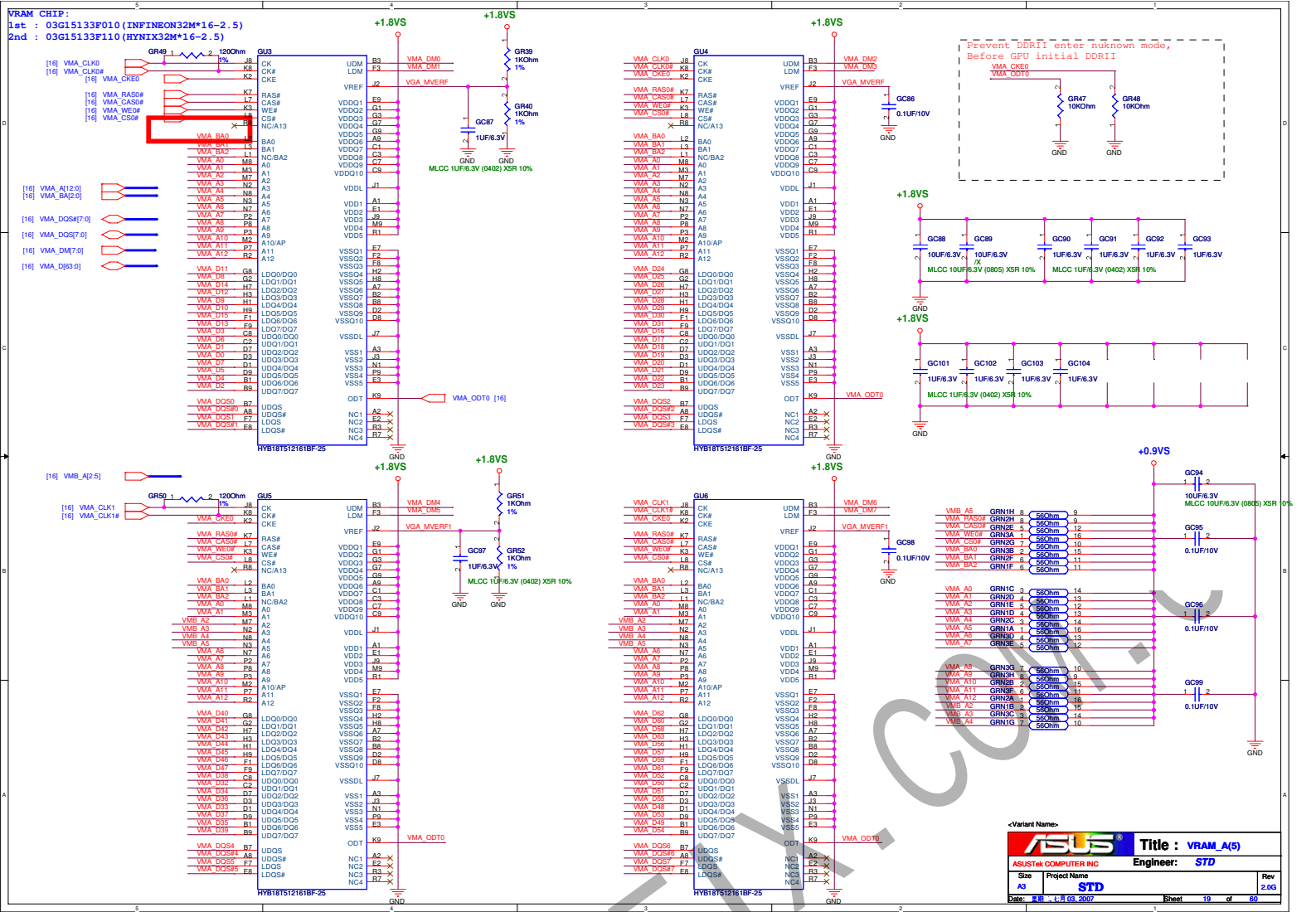
ASUS		Title : GPU_PCI-E)	
ASUSTeK COMPUTER INC	Engineer :	George_Kang	
Doc No :	Project Name :	STD	
Date :	2.8.03.2007	Sheet :	17 of 18

<< Kennedy\_Zhang >>



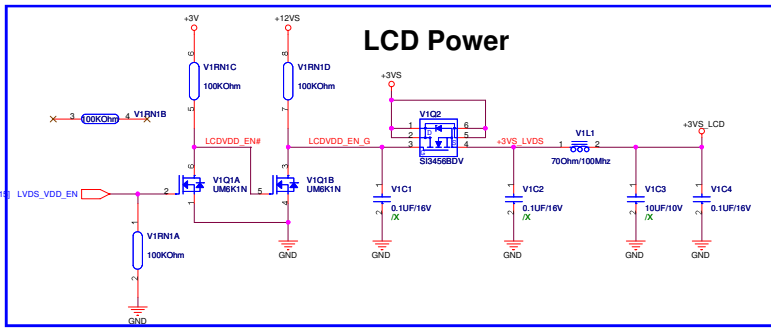
ASUS		Title : GPU_POWER_GND(4)
ASUSTek COMPUTER INC		Engineer: STD
Site	Project Name	Rev
A3	STD	2.0G
Date: 11/11/03, 2007	Sheet	18 of 80

<< Kennedy\_Zhang >>

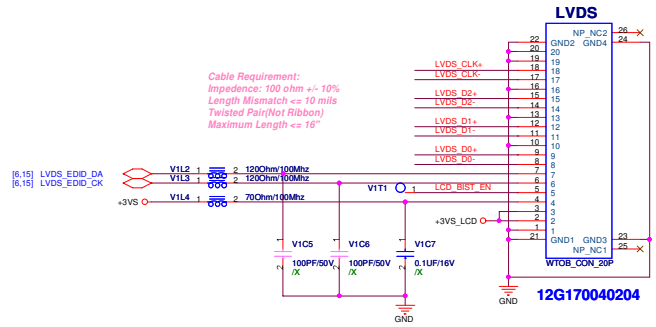


<< Kennedy\_Zhang >>

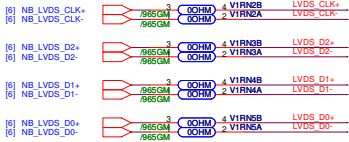
WWW.CHINAFORUM.COM



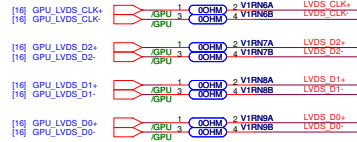
### LCD LVDS Connector



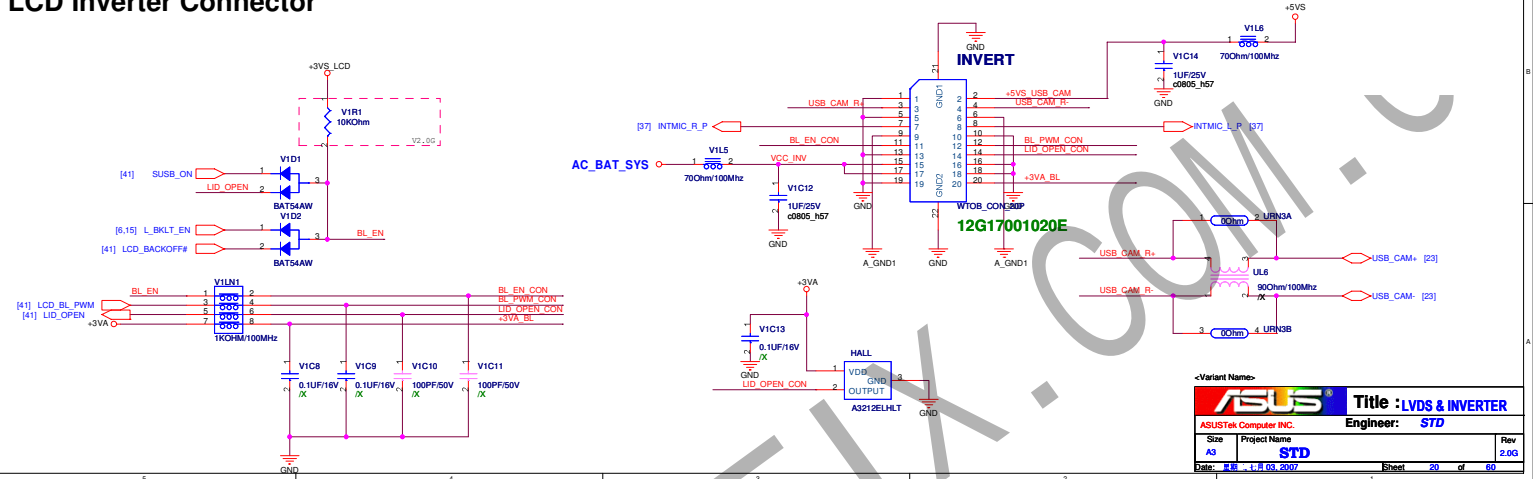
#### LVDS from 965GM



#### LVDS from GPU-NB8M

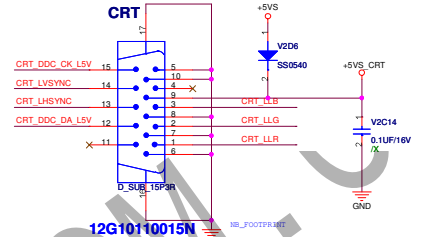
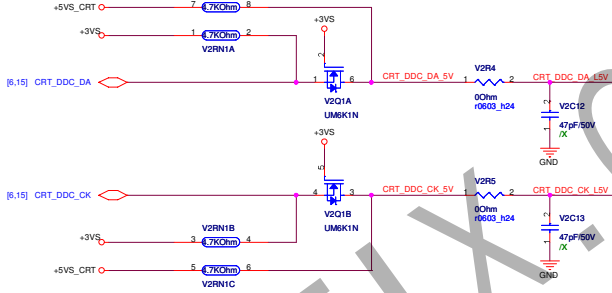
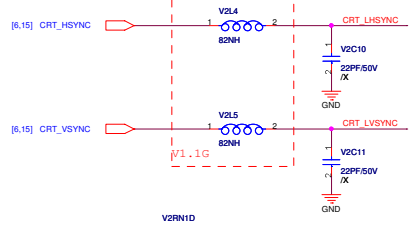
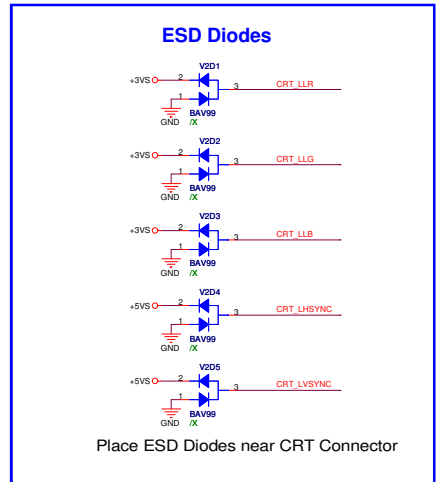
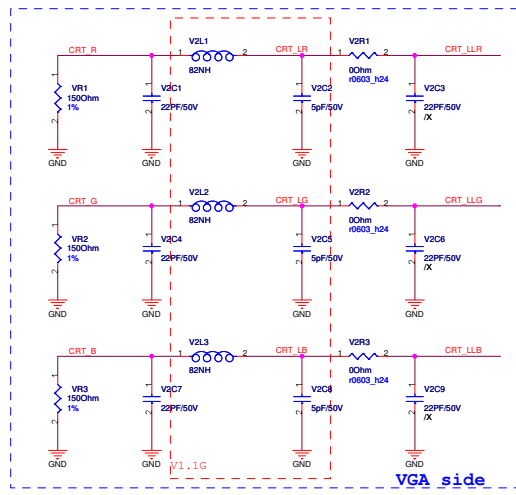
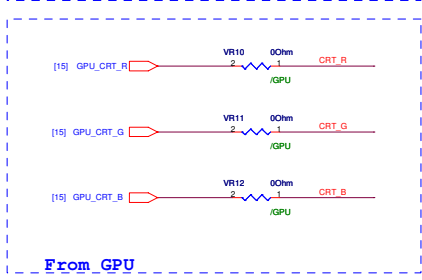
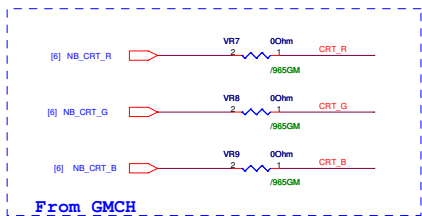


### LCD Inverter Connector



<< Kennedy\_Zhang >>

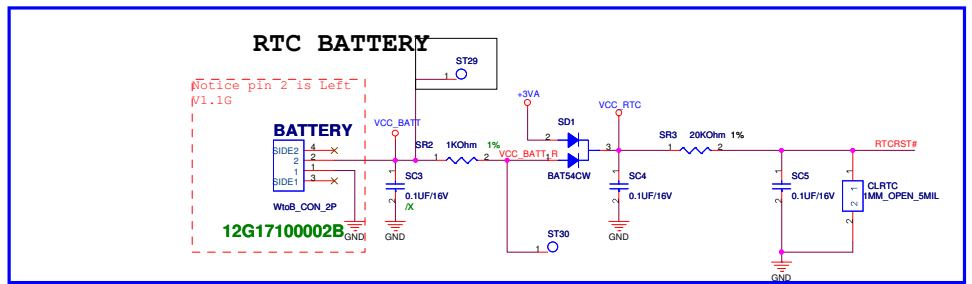
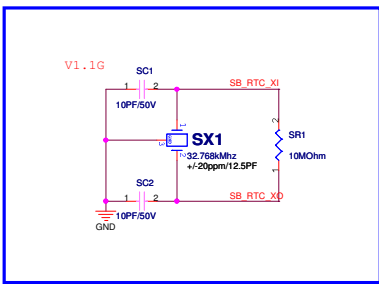
WWW.CHINAFFIX.COM



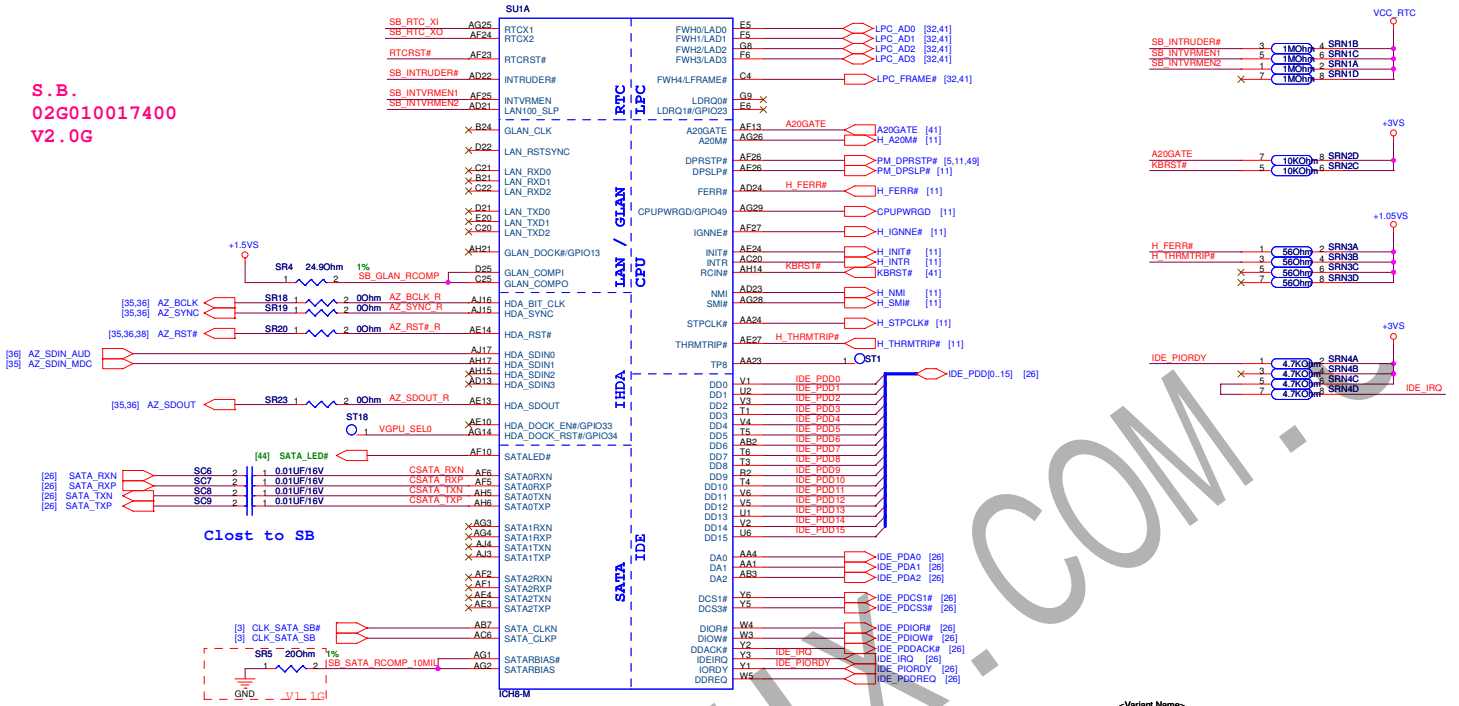
ASUS		Title : CRT
ASUSTek Computer INC.		Engineer: STD
Size	Project Name	Rev
A3	STD	2.0G
Date: 8/10/2007	Sheet	21 of 60

<< Kennedy\_Zhang >>

WWW.CHINAFIX.COM



S.B.  
02G010017400  
V2.0G

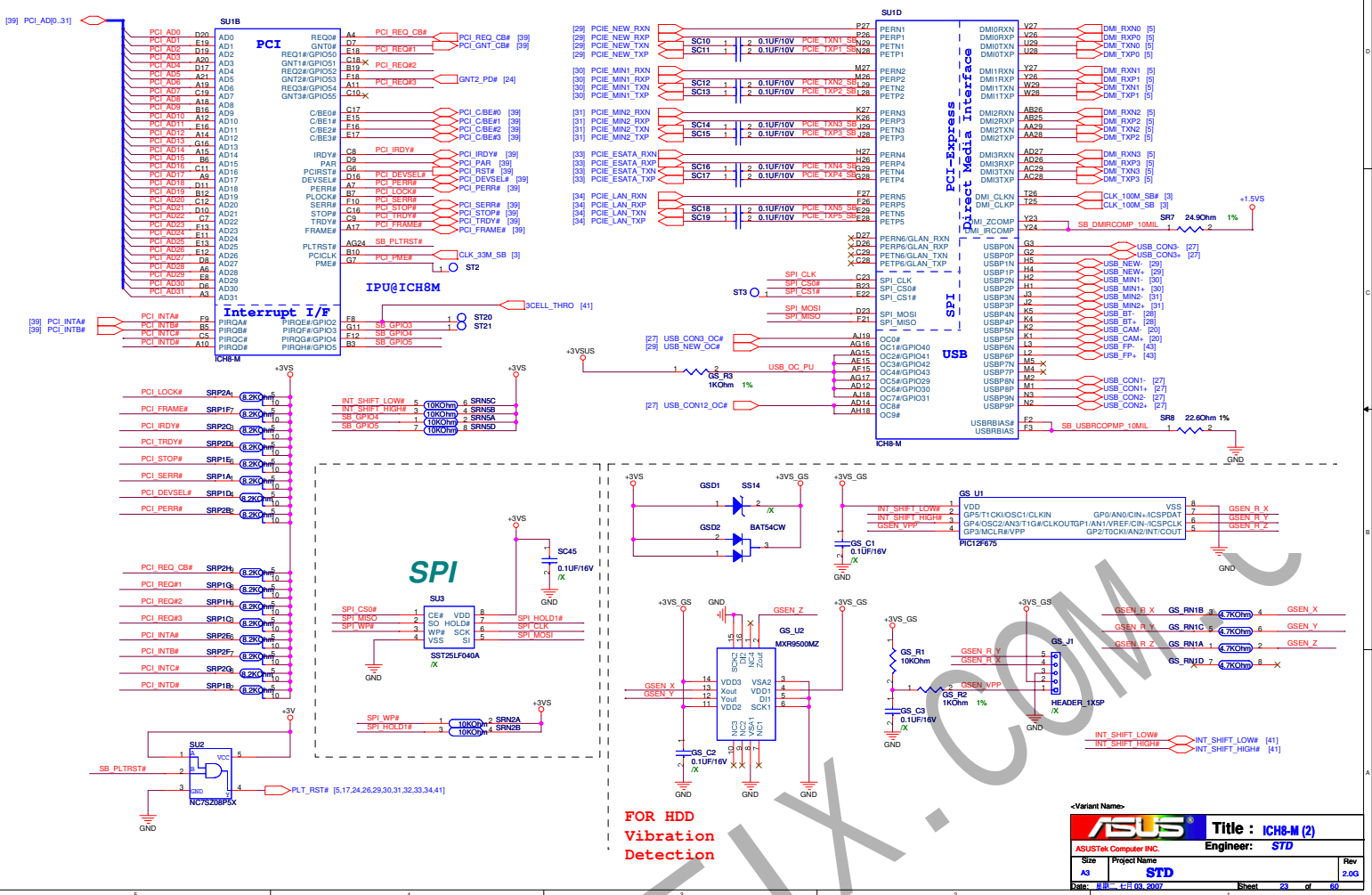


<Variant Name>

<b>ASUS</b>		<b>Title : ICH8-M (1)</b>	
ASUSTek Computer INC.		Engineer: <b>STD</b>	
Size	Project Name	Rev	2.2G
Custom	<b>STD</b>	Date: 11/03/2007	Sheet 22 of 60

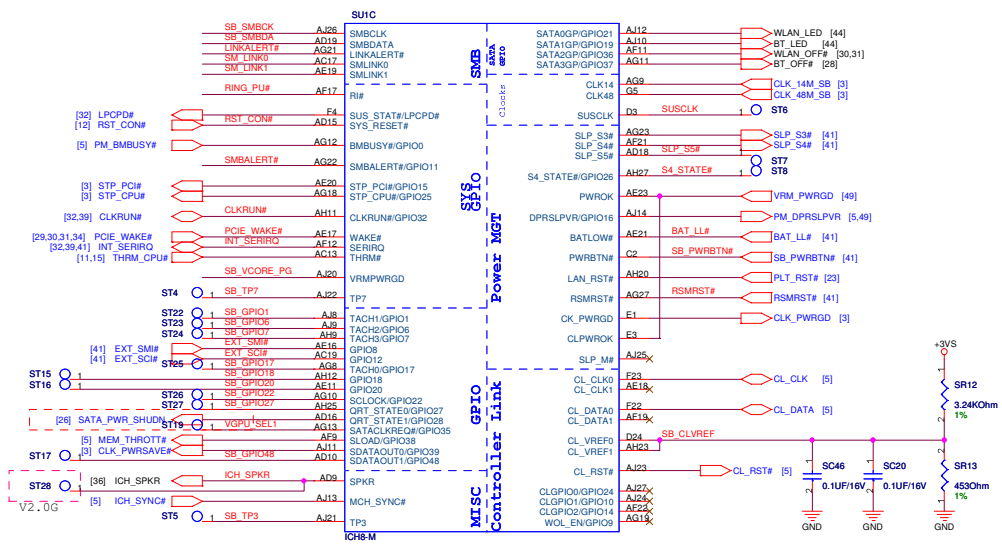
<< Kennedy\_Zhang >>

WWW.CHINAFAIR.COM



<< Kennedy\_Zhang >>

WWW.CHINAFLIX.COM



(ICH\_TP3)  
 1 = PCIE Strap (\*) default internal PU  
 0 = XOR chan test mode  
 (HDA\_SDOUT, HDA\_SYNC)  
 (11) = LANE1-4 as a PCIe4 port  
 (00) = LANE1-4 as 4 PCIe1 ports (\*) default internal PD  
 others = reserved

(GNT2#)  
 1 = LANE5-6 reserved ( internal PU )  
 0 = LANE5-6 as 2 PCIe1 ports (\*)

[23] GNT2\_PD# SR14 1KOhm 1% GND

(SB\_GPIO20)  
 1 = reserved  
 0 = Intel recommend ( \* internal PD )

(GNT1#)  
 1 = Destop/Mobile ( \* internal PU )  
 0 = Server

(GNT3#)  
 1 = Normal ( \* internal PU )  
 0 = Top-block swap for FW

(SPI\_CS1# : GNT0#) = 11 ( \* internal PU )

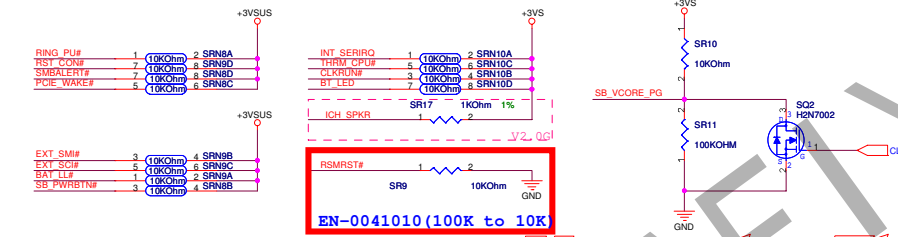
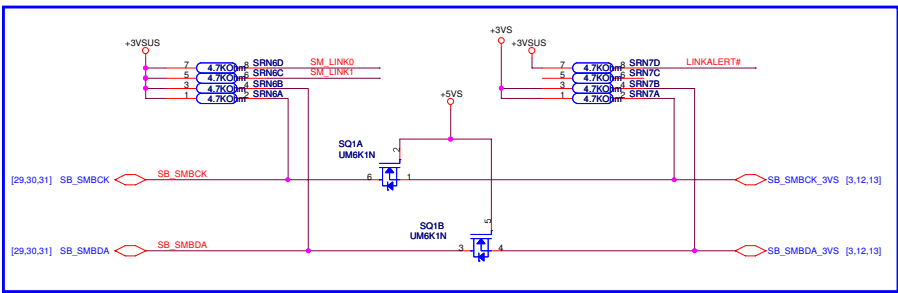
	GNT0#	SPI_CS1#
SPI	0	1
PCI	1	0
LPC	1	1

(INTVRMEN, LAN100\_SLP)  
 1 = Enabled internal VRMs for VccSus1\_05, 1\_5 and VccCl1\_5, 1\_05 and VccLAN1\_05 (\*)  
 0 = Disabled internal VRMs

(ICH\_SATALED#)  
 1 = Enabled internal normal ( \* internal PU )  
 0 = LANE1-4 reverse

(SPKR)  
 1 = Disabled TCO Reboot  
 0 = Enabled TCO Reboot ( \* internal PD )

(TP\_GPIO33) : Not used for ICH8M  
 1 = Enabled Descriptor Security ( \* internal PU )  
 0 = Disabled Descriptor Security



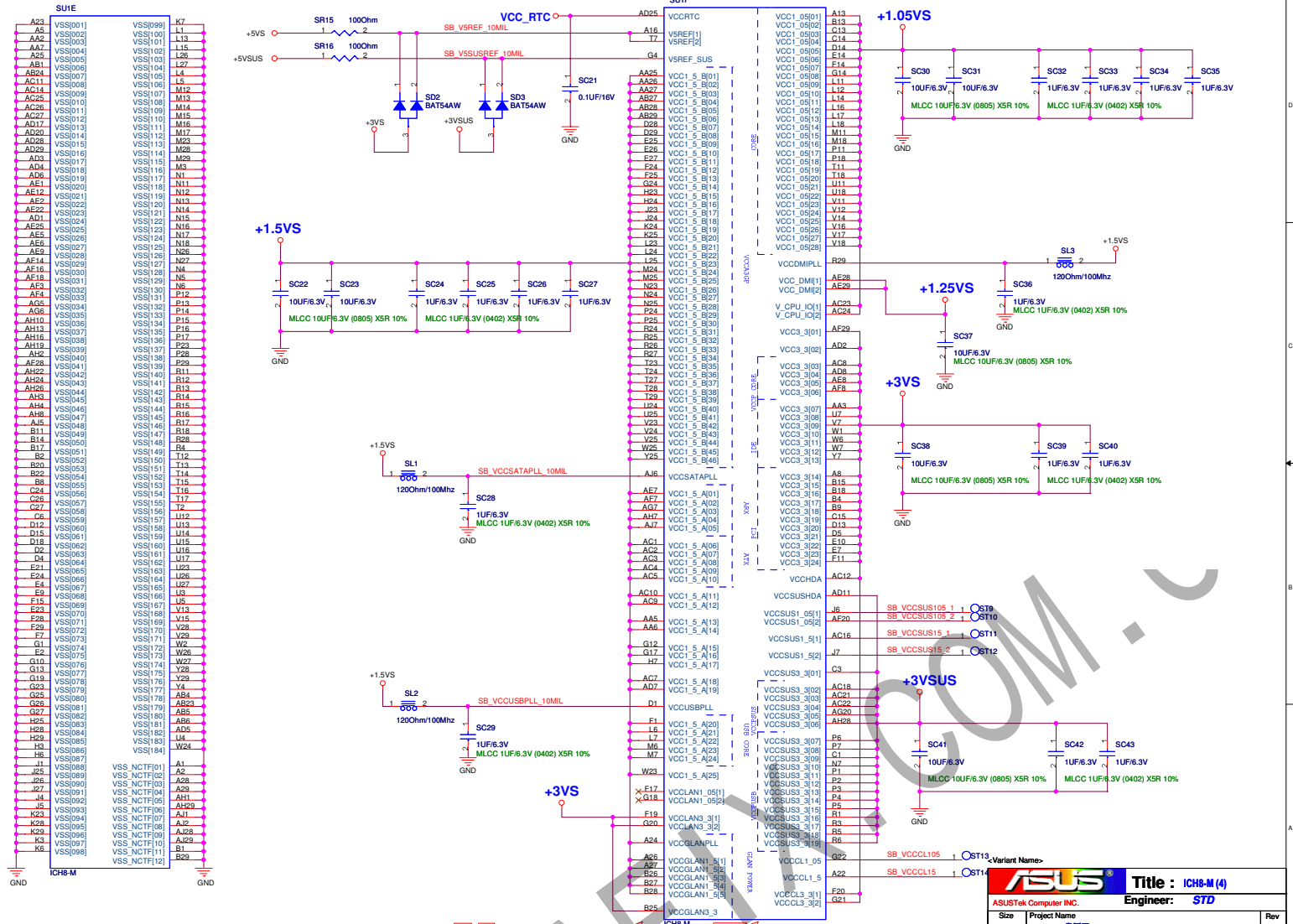
<Variant Name>

<b>ASUS</b>		<b>Title : ICH8-M (3)</b>
ASUSTek Computer INC.	Engineer: <b>STD</b>	
Size	Project Name	Rev
Custom	<b>STD</b>	2.00
Date: 2007.03.03	Sheet	24 of 50

« Kennedy\_Zhang »

WWW.CHINAFIX.COM.CN

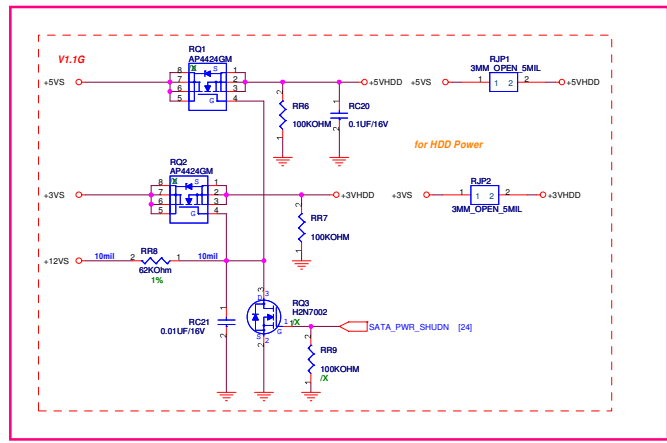
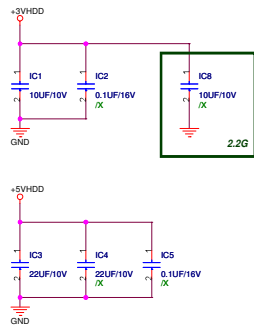
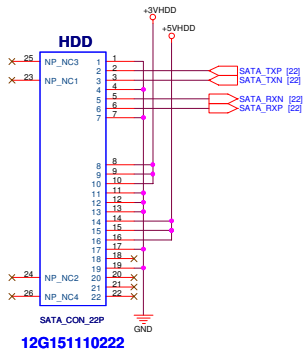




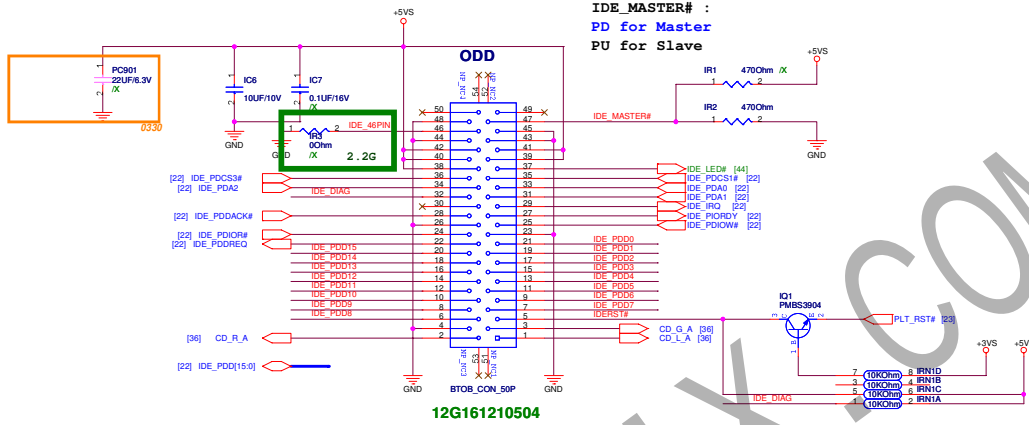
« Kennedy\_Zhang »

		<b>Title : ICH8-M (4)</b>	
		ASUSTek Computer INC.	Engineer: <b>STD</b>
Size	Project Name	Rev	2.00
Custon	<b>STD</b>	Date: 11-11-03, 2007	Sheet 25 of 60

# SATA HDD



# ODD

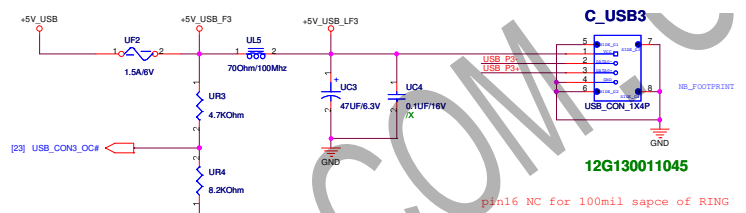
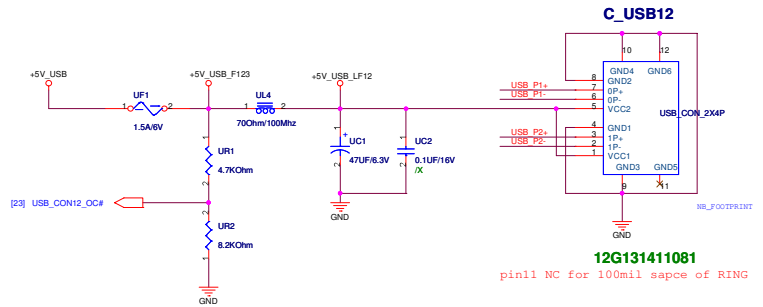
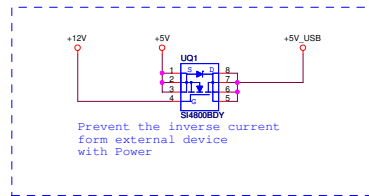
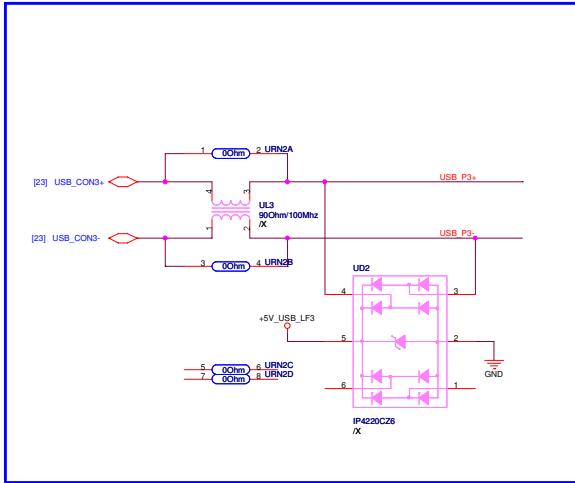
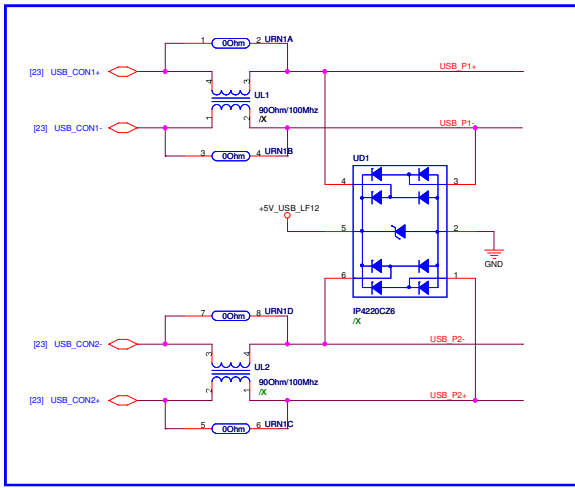


ASUS		Title : SATA-HDD & ODD
ASUSTek Computer INC.		Engineer: STD
Site	Project Name	Rev
Custom	STD	2.2G
Date: 11/14/03/2007	Sheet	26 of 80

« Kennedy Zhang »

WWW.CHINAELK.COM

All close to USB connectors



<Variant Name>

ASUS		Title : USB CONN
ASUSTek Computer INC.		Engineer: STD
Size A3	Project Name	Rev 2.0G
Date: 11/11/03/2007	Sheet 27	of 60

<< Kennedy\_Zhang >>

WWW.CHINAFIX.COM

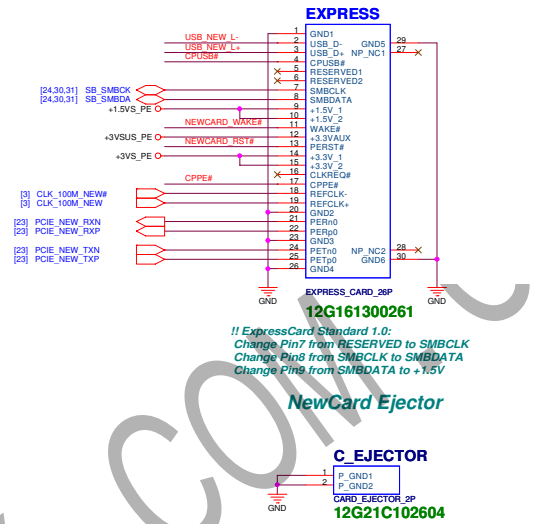
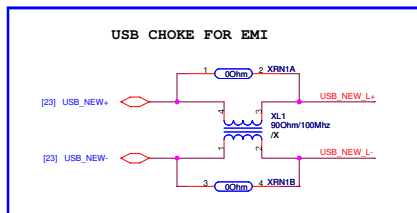
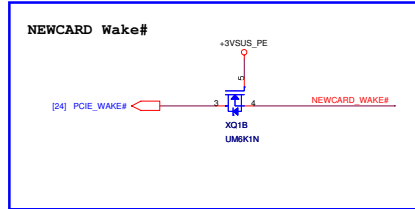
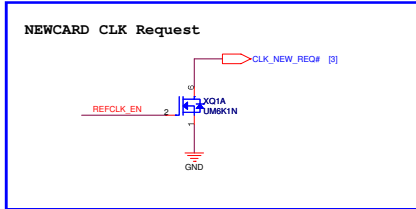
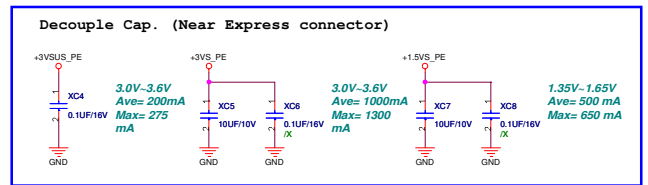
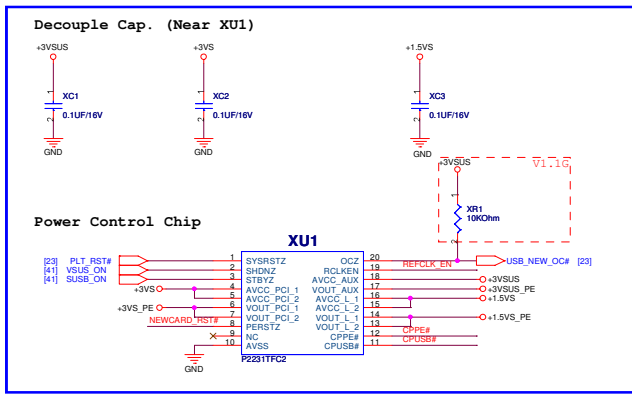
## Bluetooth Connector



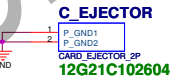
BT\_OFF# : (connect to GPO, push-pull, default High)  
 0 => BT Disabled  
 1 => BT Enabled

<< Kennedy\_Zhang >>  
 WWW.CHINAPEIX.COM.CN

<Variant Name>		
	<b>Title : Blue Tooth</b>	
ASUSTek Computer Inc.		
Engineer: <b>STD</b>		
Site	Project Name	Rev
Custom	<b>STD</b>	2.00
Date: 11/1/03	11/03/2007	Sheet 28 of 80



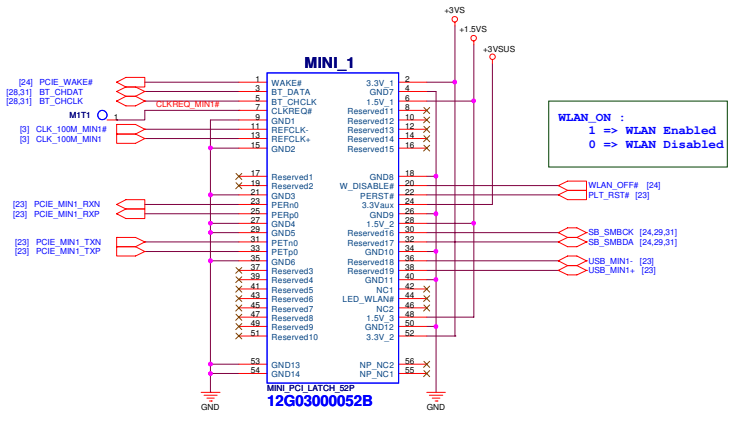
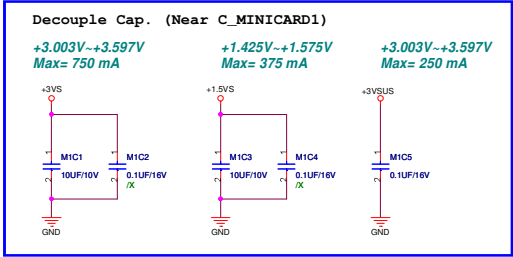
NewCard Ejector



ASUS		Title : NEWCARD
ASUSTek Computer INC		Engineer: STD
Size	Project Name	Rev
Custom	STD	2.00
Date: 11/12/03 2002	Sheet	29 of 80

« Kennedy\_Zhang »

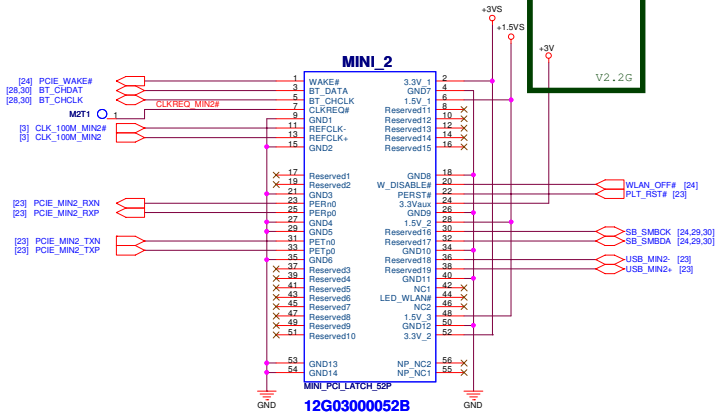
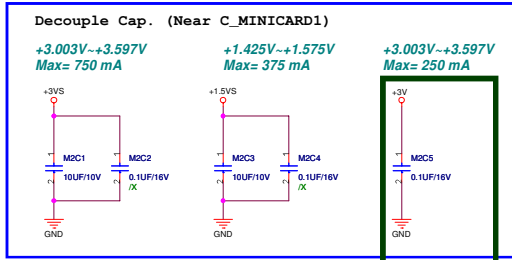
WWW.CHINAELIX.COM



« Kennedy\_Zhang »

<Variant Name>

<b>ASUS</b>		<b>Title : MINI CARD</b>
ASUSTek Computer INC.		Engineer: STD
Size	Project Name	Rev
Custom	STD	2.00
Date: 2007-07-03	Sheet	30 of 60



WLAN\_ON :

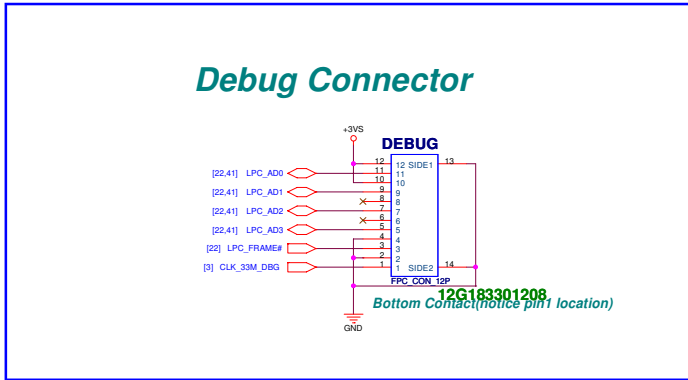
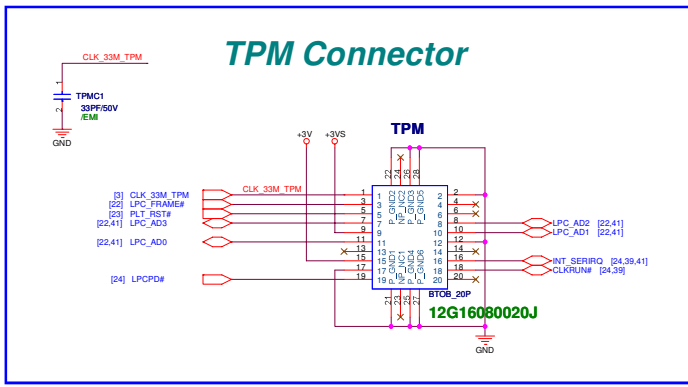
1 => WLAN Enabled

0 => WLAN Disabled

« Kennedy\_Zhang »

<Variant Name>

<b>ASUS</b>		<b>Title : MINI CARD</b>
ASUSTek Computer INC.		Engineer: STD
Size	Project Name	Rev
Custom	STD	2.00
Date: 2007-03-09	Sheet	31 of 60

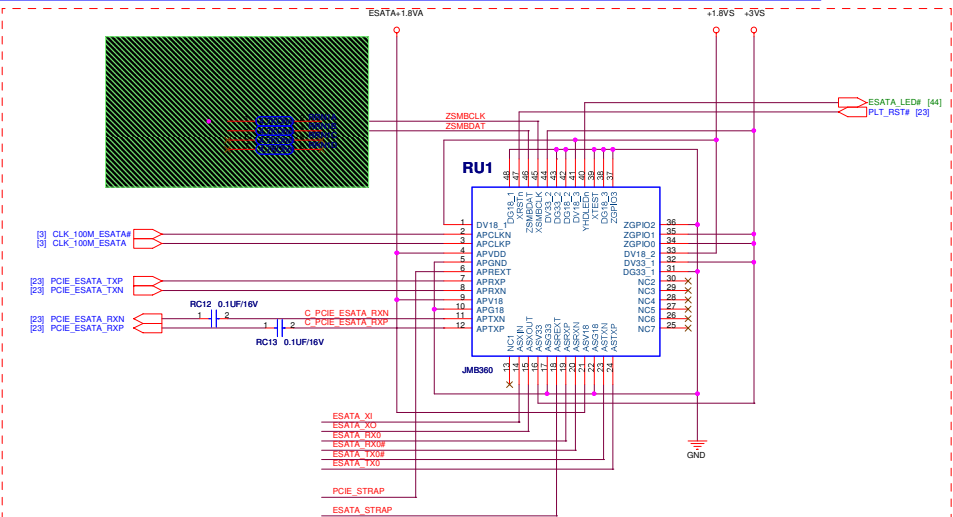
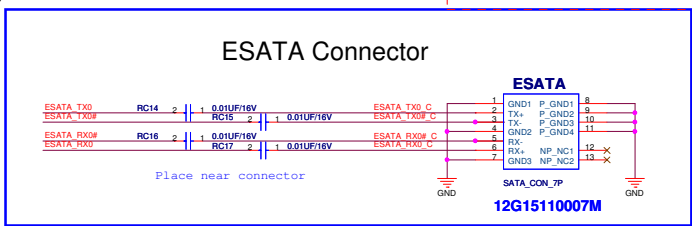
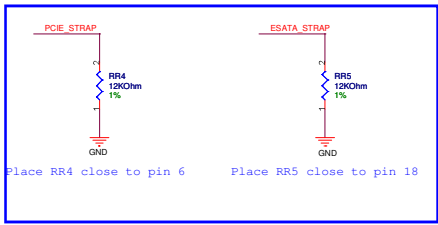
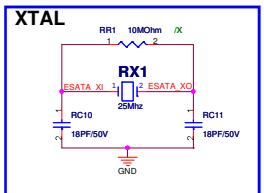
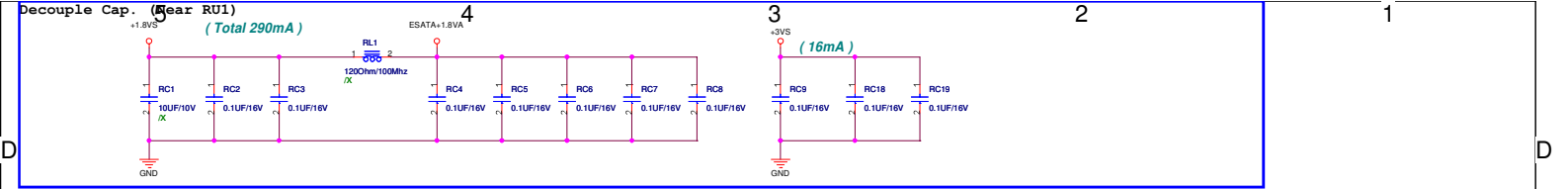


<Variant Name>		
<b>ASUS</b>		<b>Title : TPM</b>
ASUSTek Computer Inc.		Engineer: <b>STD</b>
Size	Project Name	Rev
Custom	<b>STD</b>	2.0G
Date: 11/14/03	2007	Sheet 32 of 80

<< Kennedy\_Zhang >>

WWW.CHINAPELX.COM.CN





ASUS Title: ESATA JMB360  
 Project Name: ESATA JMB360 Engineer: STD  
 Date: 11/18/03, 2007 Sheet: 33 of 60

<< Kennedy\_Zhang >>

WWW.CHINAHELPX.COM

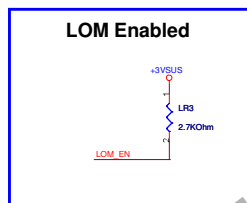
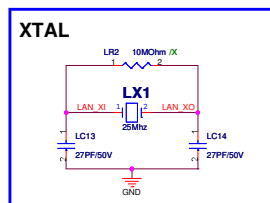
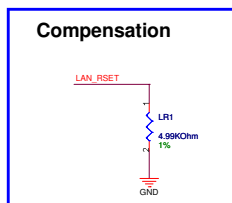
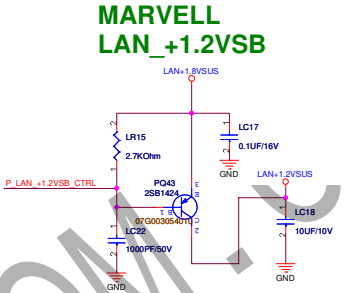
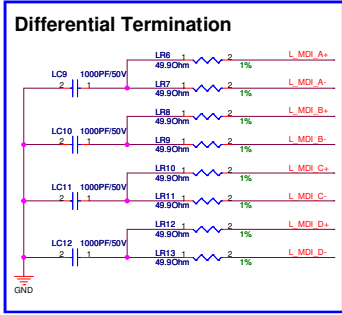
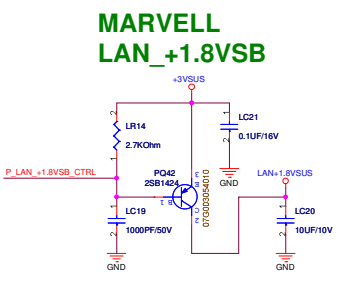
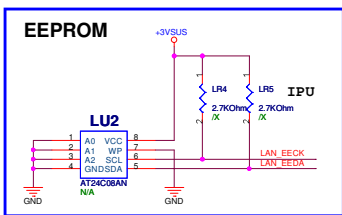
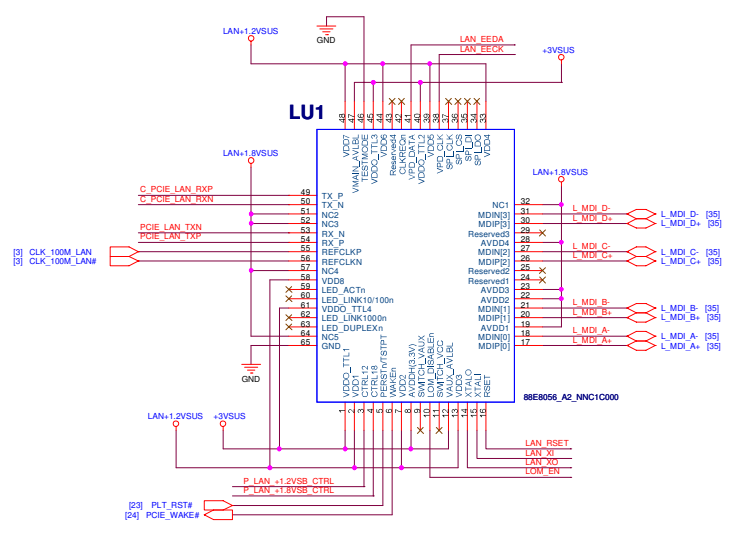
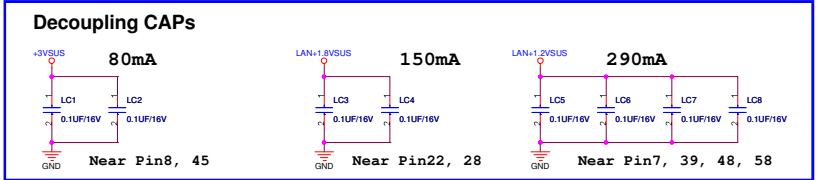
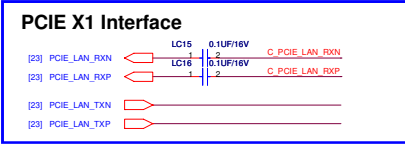


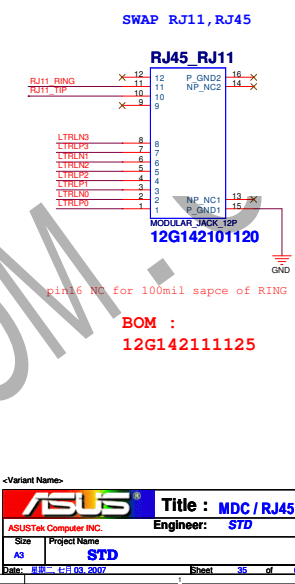
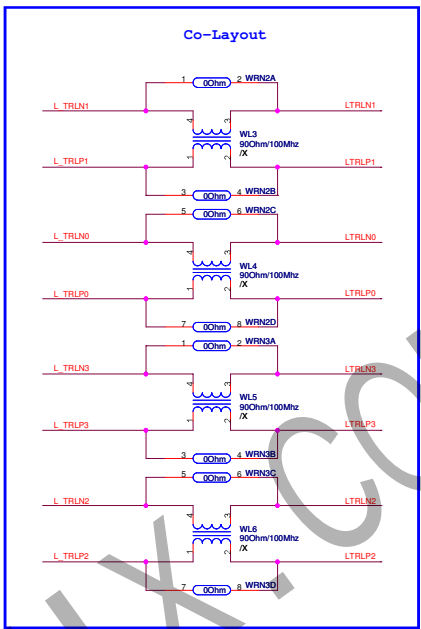
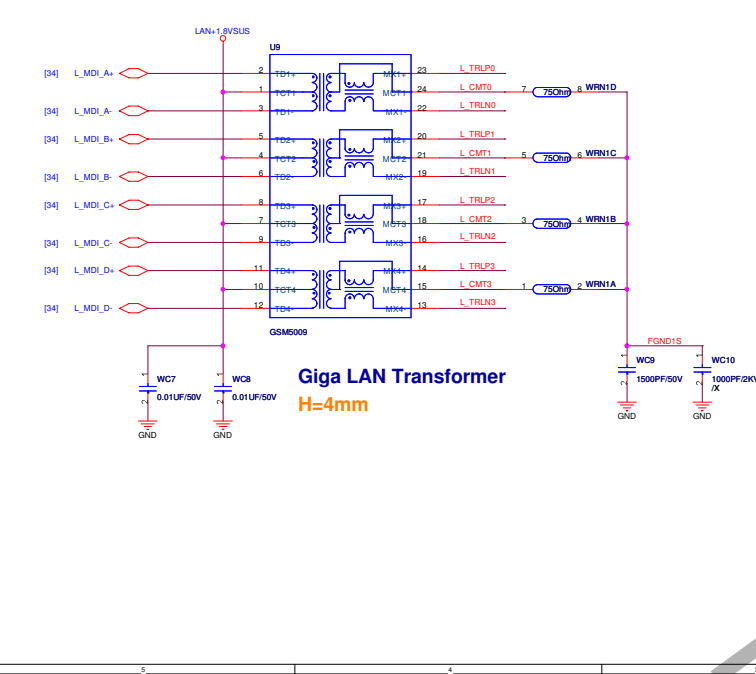
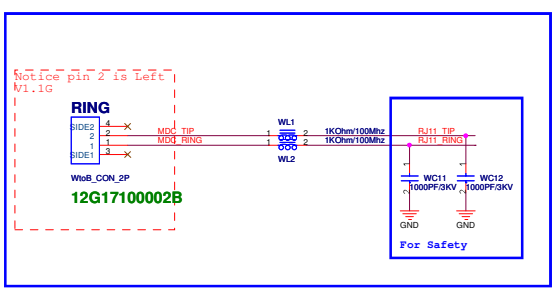
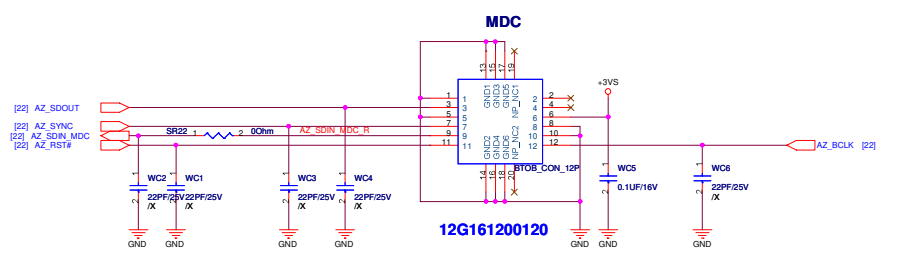
Table 30: Media Dependent Interface Pin Mapping

Pin	MDIX	MDIX	MDIX	MDIX	MDIX
MDIXP0	MDIX0	MDIX1	MDIX2	MDIX3	MDIX4
MDIXN0	MDIX0	MDIX1	MDIX2	MDIX3	MDIX4
MDIXP1	MDIX5	MDIX6	MDIX7	MDIX8	MDIX9
MDIXN1	MDIX5	MDIX6	MDIX7	MDIX8	MDIX9
MDIXP2	MDIX10	MDIX11	MDIX12	MDIX13	MDIX14
MDIXN2	MDIX10	MDIX11	MDIX12	MDIX13	MDIX14

ASUS Title: Marvell 88E8056  
 Engineer: STD  
 Project Name: [Blank]  
 Rev: 2.0G  
 Date: 11/03/2007 Sheet 34 of 60

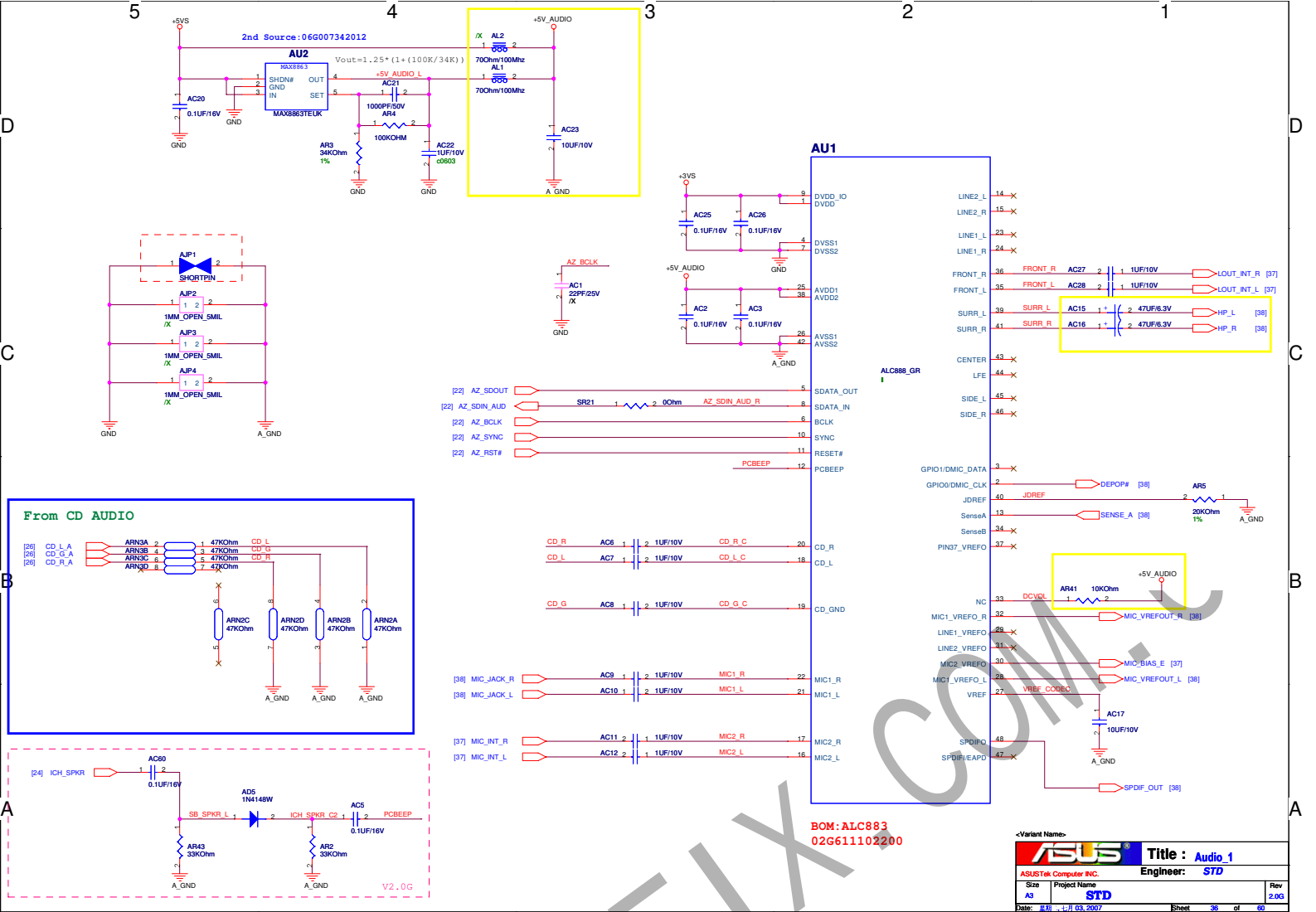
<< Kennedy\_Zhang >>

WWW.CHINAFFIX.COM



<< Kennedy\_Zhang >>

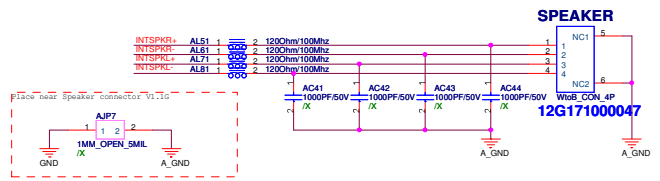
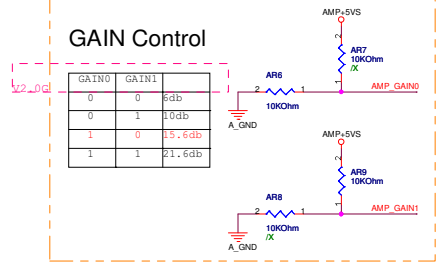
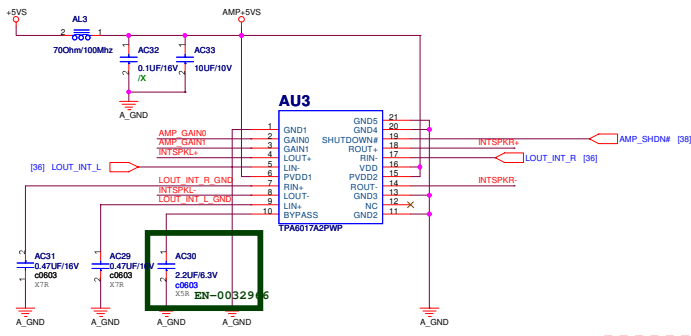
WWW.CHINAFAIR.COM



<< Kennedy\_Zhang >>

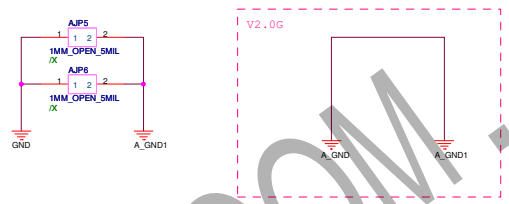
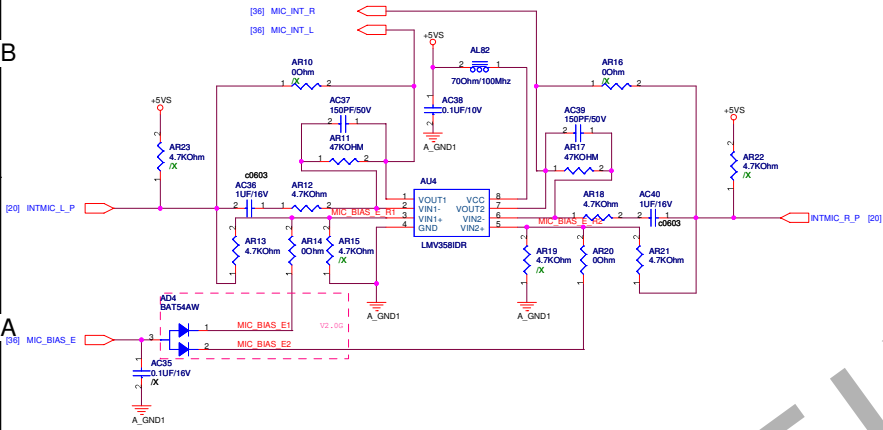
WWW.CHINAFFIX

# Audio Amp.



# Internal MIC Amp.

FL = 33.86kHz, FH = 22.5kHz  
Place Near INTMIC Connector

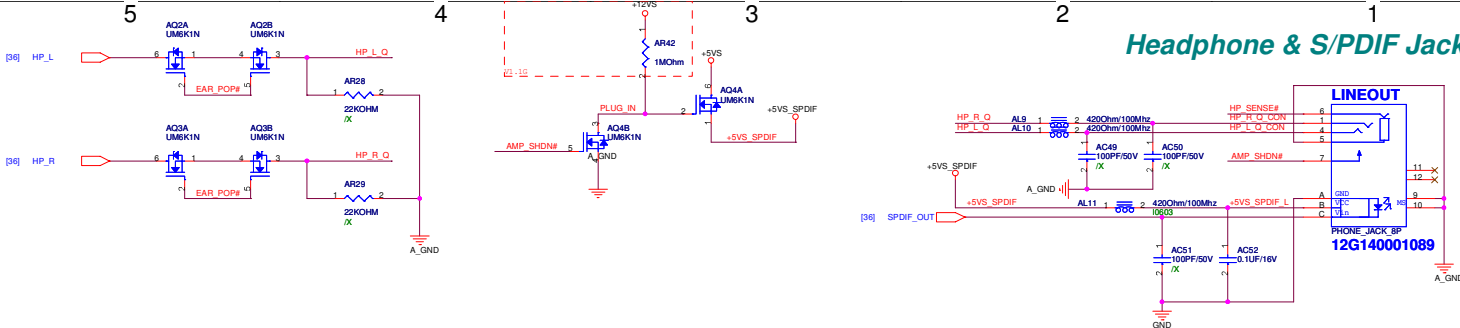


ASUS Title: Audio\_2  
ASUSTek Computer INC. Engineer: STD  
Size A3 Project Name STD  
Date: 11/11/03, 2007 Sheet 37 of 60

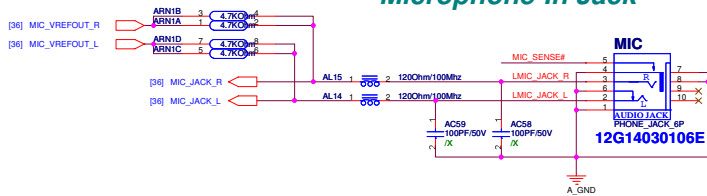
<< Kennedy\_Zhang >>

WWW.CHINAFFIX.COM

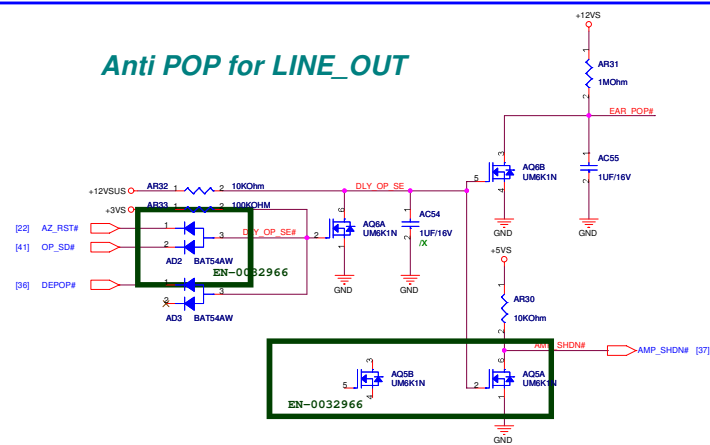
### Headphone & S/PDIF Jack



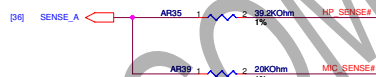
### Microphone-In Jack



### Anti POP for LINE\_OUT



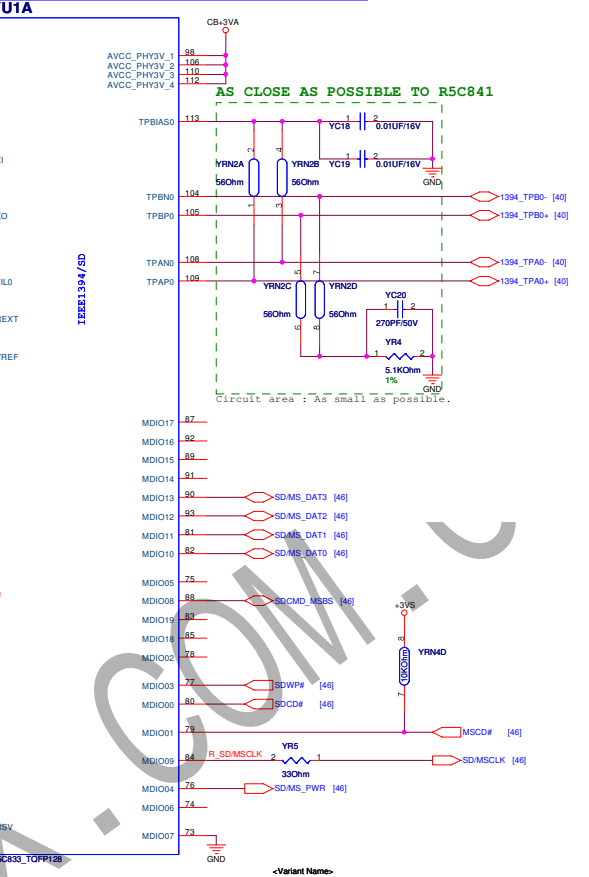
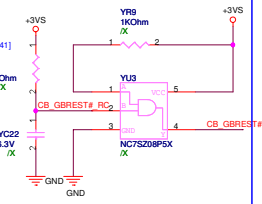
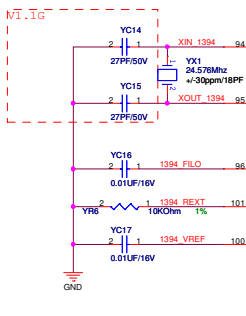
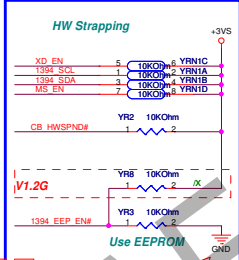
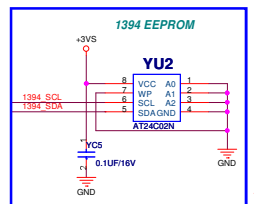
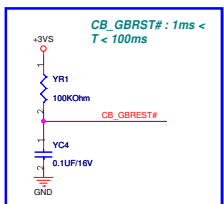
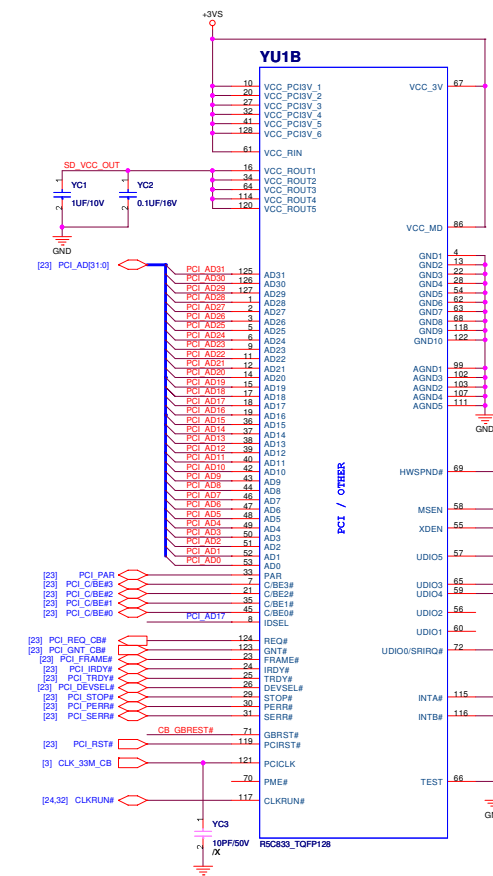
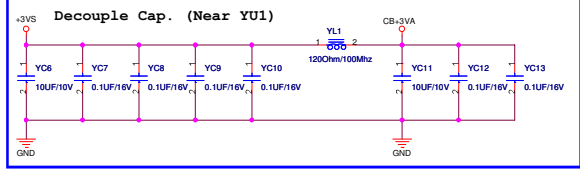
### Jack Plug-in Detection



ASUS		Title: Audio_3	
ASUSTek Computer INC.		Engineer: STD	
Size: A3	Project Name: STD	Date: 03/03/2007	Rev: 2.00
Date: 03/03/2007		Sheet: 38	of: 60

<< Kennedy\_Zhang >>

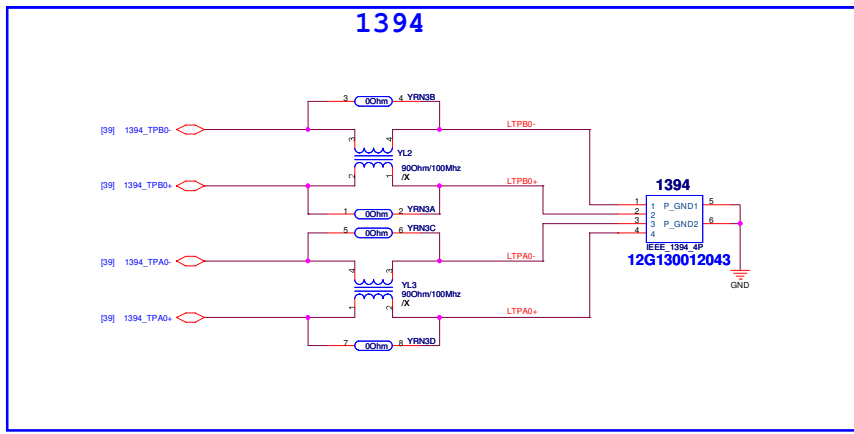
WWW.CHINAHELPX.COM



ASUS		Title : CARD1394-R5C832(1)
ASUSTek Computer INC.		Engineer: STD
Site	Project Name	Rev
Custom	STD	2/26
Date: 11/14/03/2007	Sheet	39 of 80

« Kennedy\_Zhang »

WWW.CHINAAEAS.COM



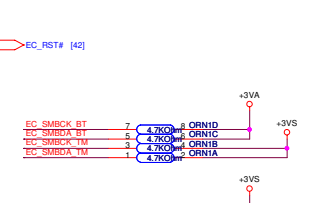
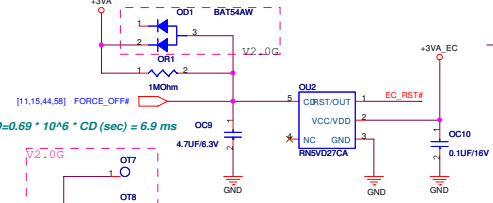
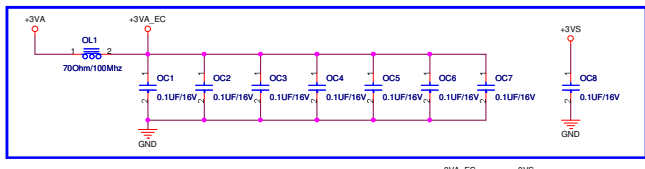
<Variant Name>

<b>ASUS</b>		<b>Title 1394 CON</b>
ASUSTek Computer INC.		Engineer: <b>STD</b>
Size	Project Name	Rev
A3	<b>STD</b>	2.0G
Date: 8/10/07	EH 03, 2007	Sheet 40 of 60

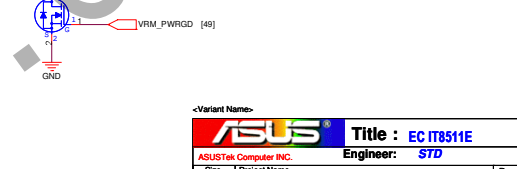
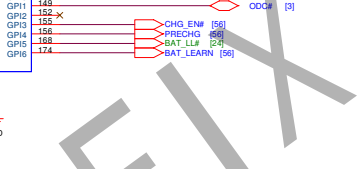
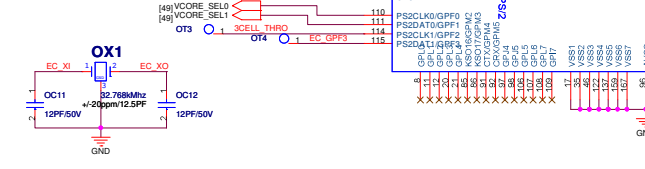
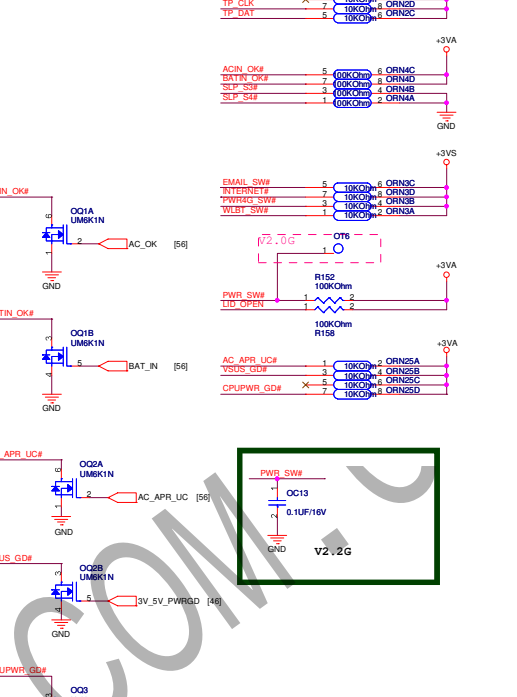
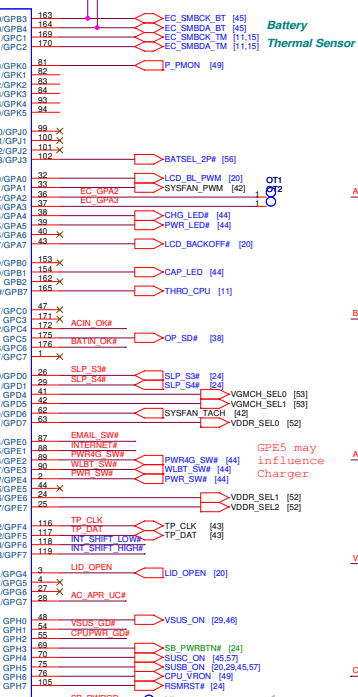
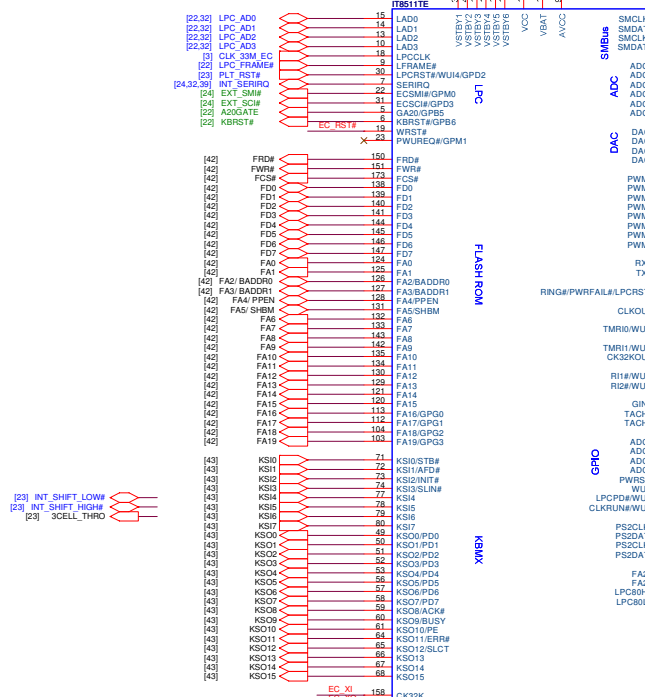
*<< Kennedy\_Zhang >>*

WWW.CHINAPELX.COM.CN





Check LPC PU in SB



ASUS		Title: EC IT8511E	
ASUSTek Computer INC.		Engineer: STD	
Site	Project Name	Rev	
Custom	STD	2.00	
Date: 8/8	1/23/03	Sheet	41 of 80

« Kennedy Zhang »

WWW.CHINAFIX.COM

# ISA ROM

## EC Hardware Strapping

[ FA3/ BADDR1 : FA2/ BADDR0 ]

00: PNPCNG Access Register Pair Are 002Eh and 002Fh  
 01: PNPCNG Access Register Pair Are 004Eh and 004Fh  
 10: PNPCNG Access Register Pair Are Determined by EC Domain Registers SWCBALR and SWCBAHR.  
 11: Reserved

FA4/ PPEN

0: Normal  
 1: K8S Interface Pins Are Switched to Parallel Port Interface for In-System Programming

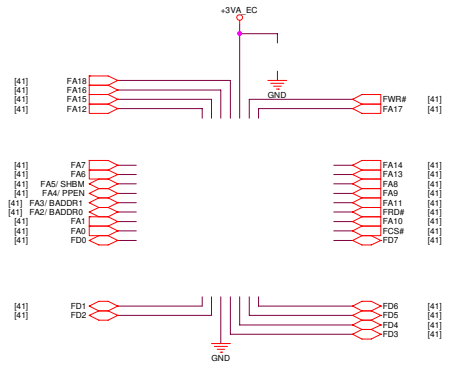


FA5/ SHBM

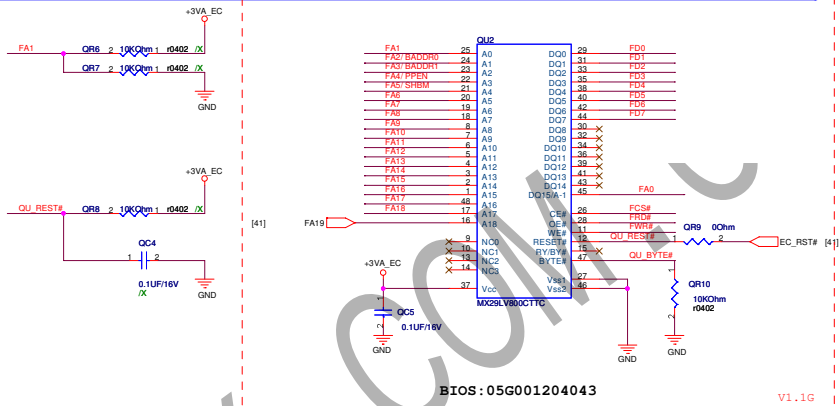
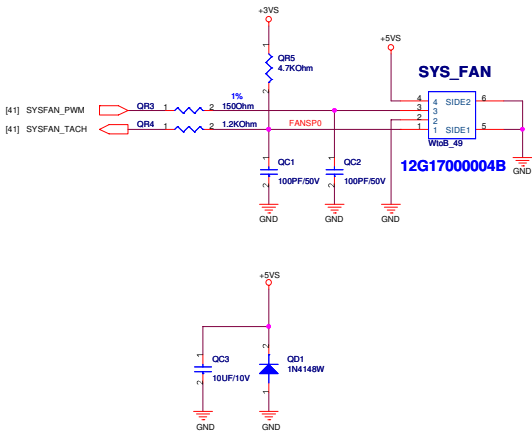
0: Disable Shared Memory with Host BIOS  
 1: Enable Shared Memory with Host BIOS



Note: Sampled at VSTBY Power Up Reset



## System Fan Connector



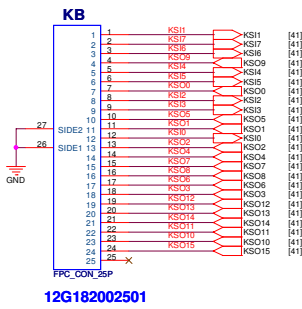
BIOS: 05G001204043

ASUS		Title : ISA FLASH ROM	
ASUSTek Computer INC.		Engineer: STD	
Site	Project Name	Rev	
Custom	STD	2.00	
Date: 8/8/2007		Sheet 42 of 80	

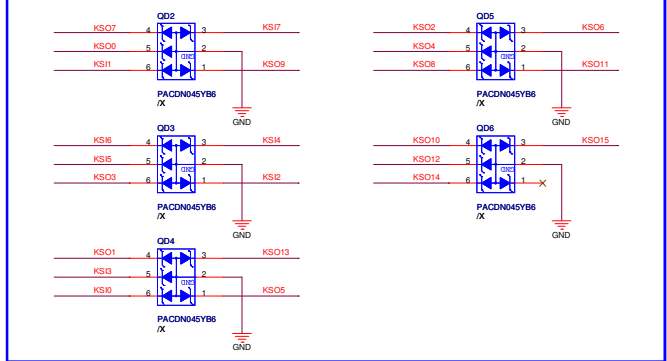
« Kennedy\_Zhang »

WWW.CHINAHELPX.COM

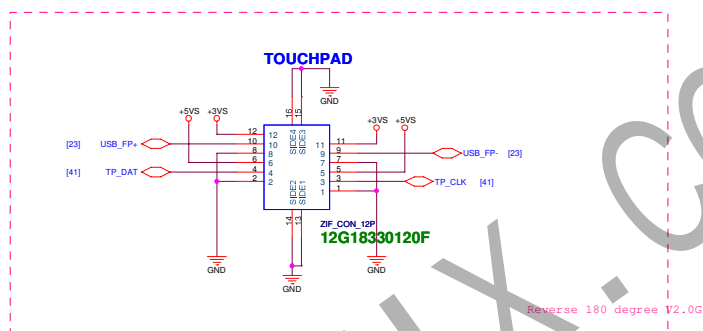
### Keyboard Connector



### FOR EMI/ESD



### Fingerprint & TouchPad Connector

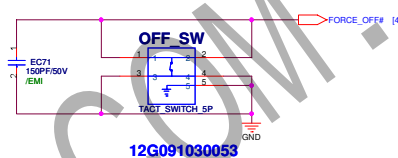
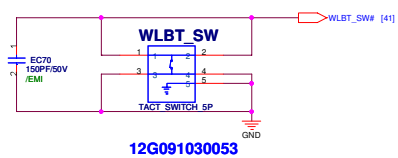
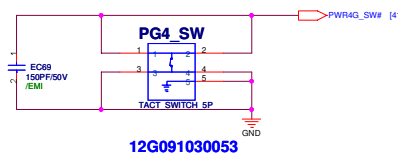
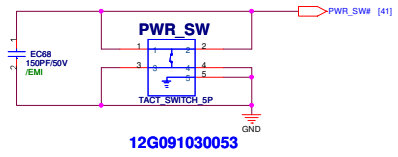
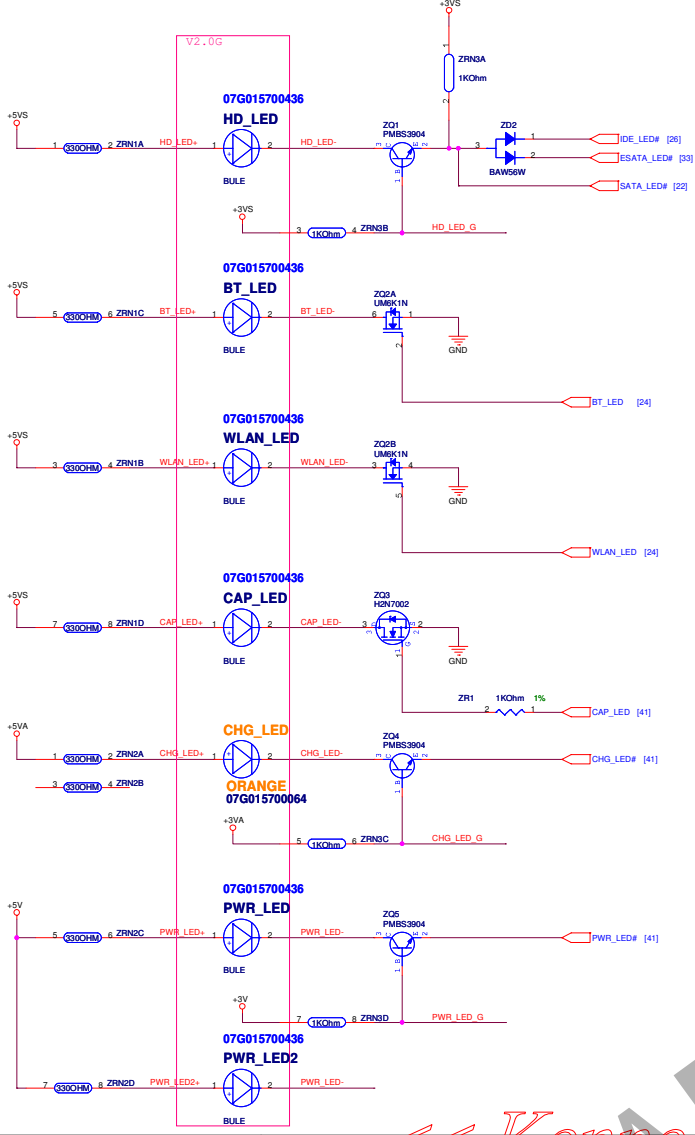


<Variant Name>

<b>ASUS</b>		<b>Title : Touch Pad &amp; KB</b>	
ASUSTek Computer INC.		Engineer: STD	
Size	Project Name	Rev	
Custom	STD	2.0G	
Date: 11/11/08	11/08/2007	Sheet	43 of 60

« Kennedy\_Zhang »

WWW.CHINAFOX.COM



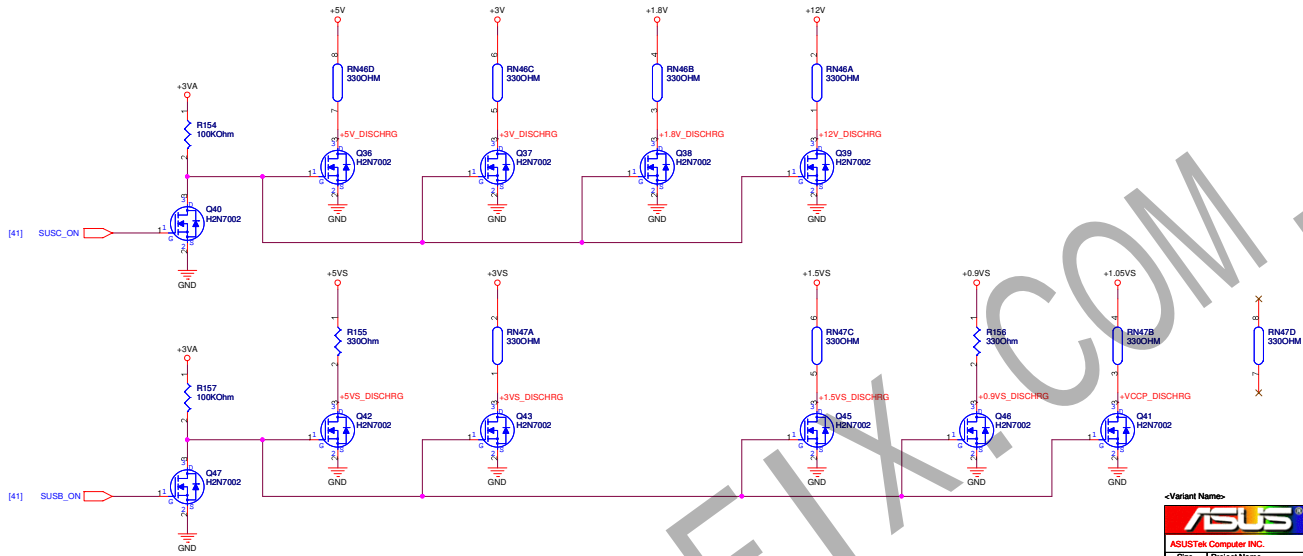
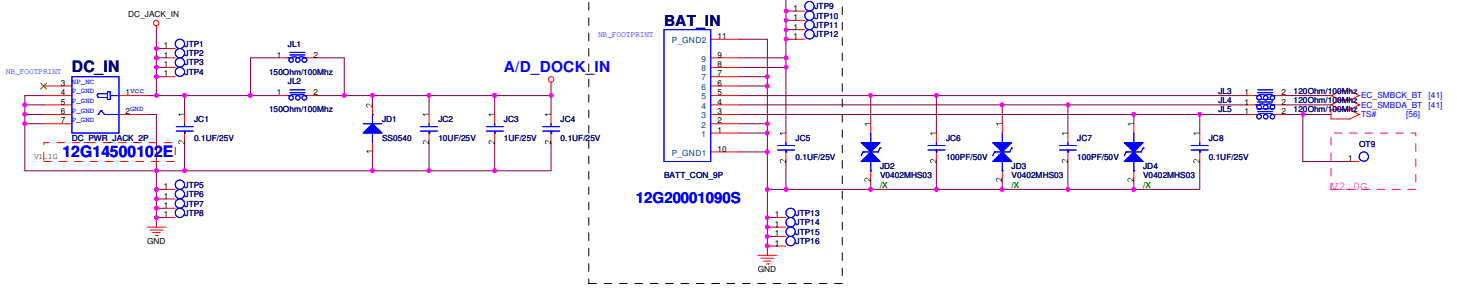
« Kennedy\_Zhang »

<Variant Name>

<b>ASUS</b>		<b>Title : LED &amp; SW</b>
ASUSTek Computer INC.		Engineer: <b>STD</b>
Size	Project Name	Rev
Custom	<b>STD</b>	2.0G
Date: 11/11/08, 2007	Sheet	44 of 60

# BAT-IN Connector

## DC-IN Connector



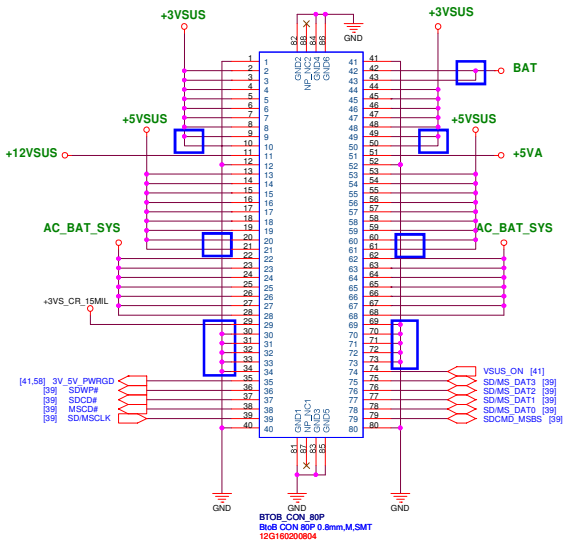
<Variant Name>

<b>ASUS</b>		<b>Title : DC IN &amp; BAT IN</b>	
ASUSTek Computer INC.		Engineer: <b>STD</b>	
Site	Project Name	Rev	
Custom	<b>STD</b>	2.00	
Date: 11/11/2007		Sheet	45 of 80

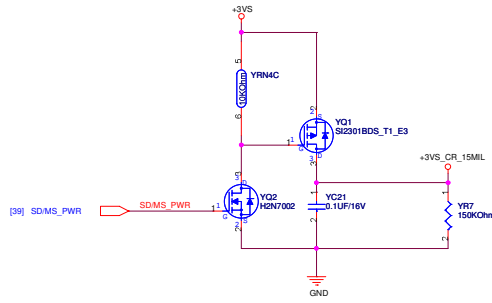
« Kennedy\_Zhang »

WWW.CHINAKEY.COM

CARDBD\_CON



BOM : 12G160200805 --8mmHigh

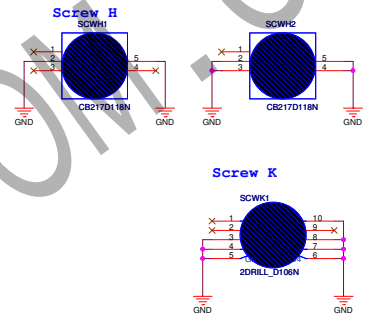
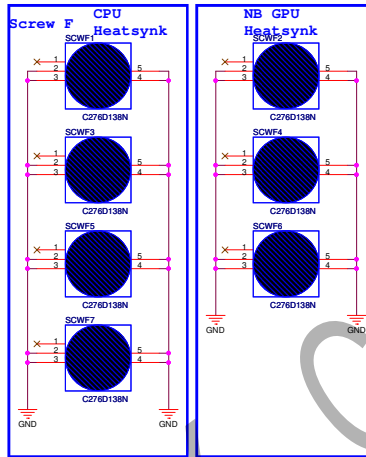
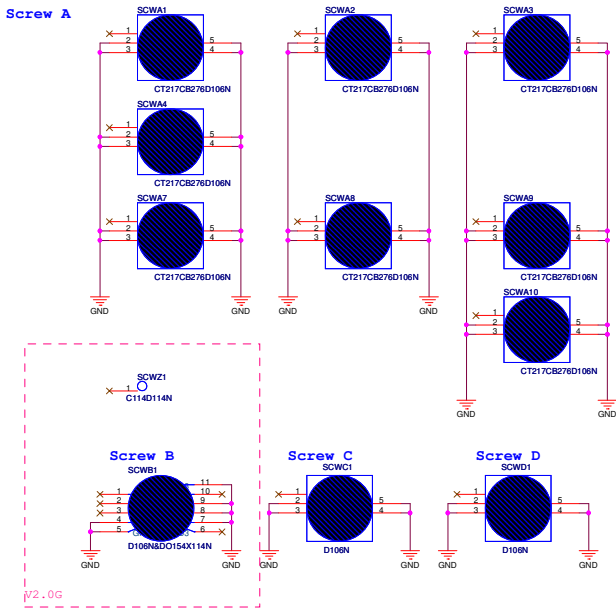
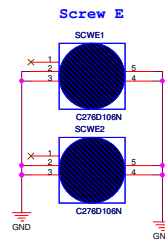
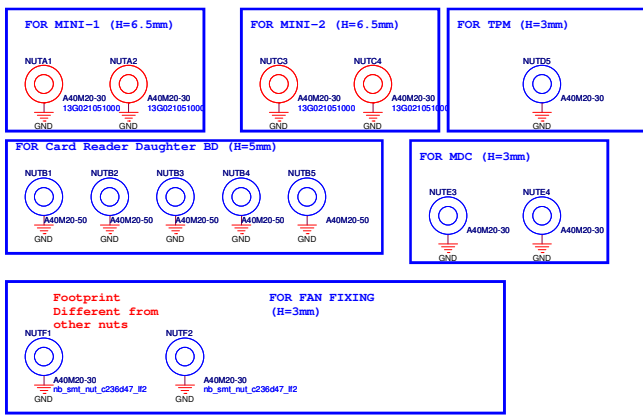


<Variant Name>

<b>ASUS</b>		<b>Title 4 in 1 CARD READER</b>	
ASUSTek Computer INC.		Engineer: <b>STD</b>	
Size	Project Name		Rev
A3	<b>STD</b>		2.0G
Date: 8/10/07	CH 03, 2007	Sheet	48 of 60

<< Kennedy\_Zhang >>

WWW.CHINAPELX.COM.CN



pin9 NC for 100mil sapce of RING

ASUS		Title : Screw Hole	
ASUSTek Computer INC		Engineer: STD	
Size	Project Name	Rev	
Custom	STD	2.00	
Date: 11/18/03 2002	Sheet: 47	of 60	

« Kennedy Zhang »

WWW.CHINAELIX.COM

D

D

C

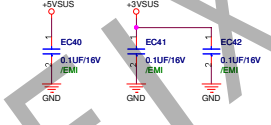
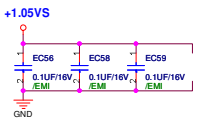
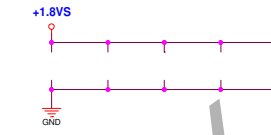
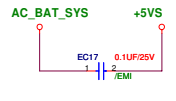
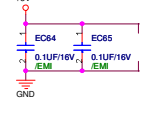
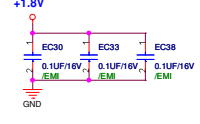
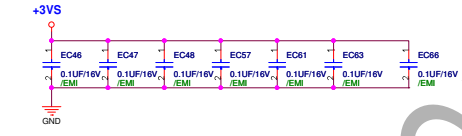
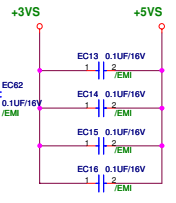
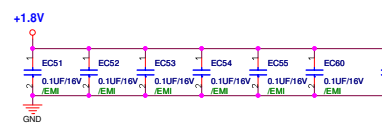
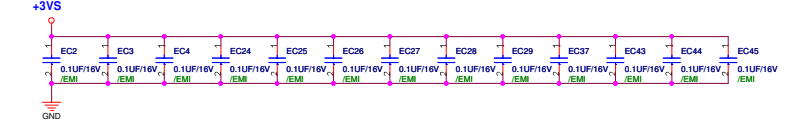
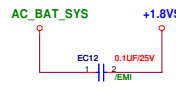
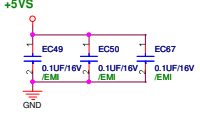
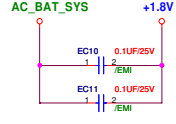
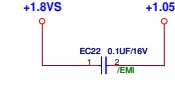
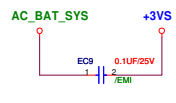
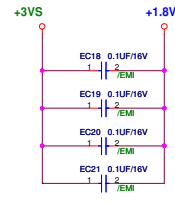
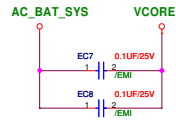
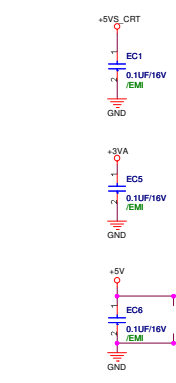
C

B

B

A

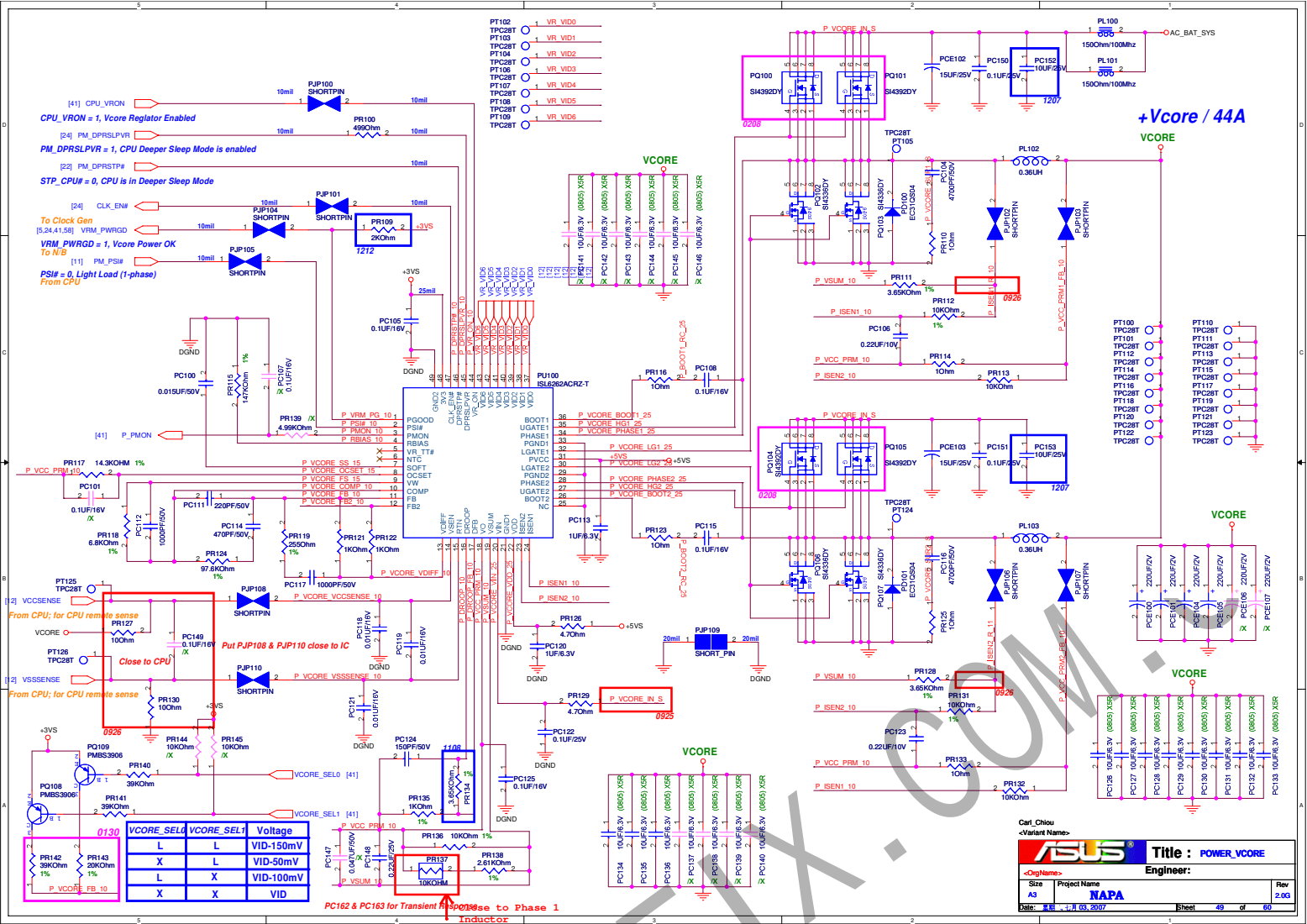
A



« Kennedy\_Zhang »

ASUS		Title: EMI CAP
ASUSTek Computer INC.		Engineer: STD
Size: Custom	Project Name: STD	Rev: 2.03
Date: 11/03/2007	Sheet: 48	of: 60

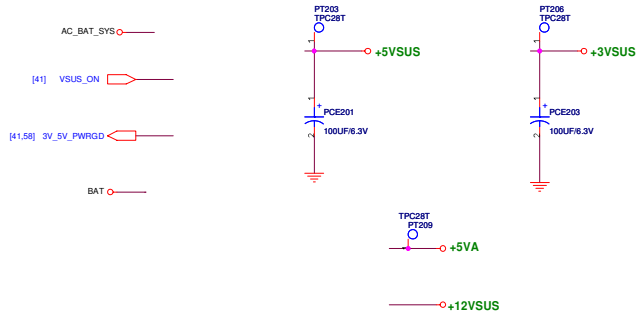




<< Kennedy\_Zhang >>

WWW.CHINAFFIX.COM

Put these elements close to daughter board !!



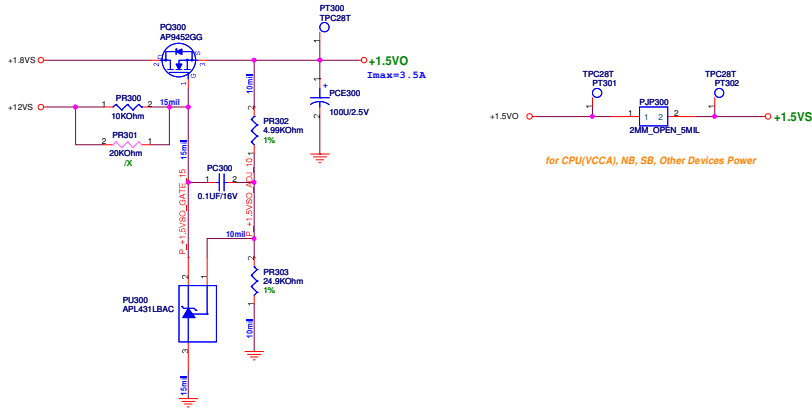
<Variant Name>

		Title : POWER_SYSTEM	
<OrigName>		Engineer:	
Size	Project Name	Rev	
A3	NAPA	2.0G	
Date:	11.03.2007	Sheet	50 of 50

<< Kennedy\_Zhang >>

WWW.CHINAPELX.COM.CN

# +1.5VS / 3.5A



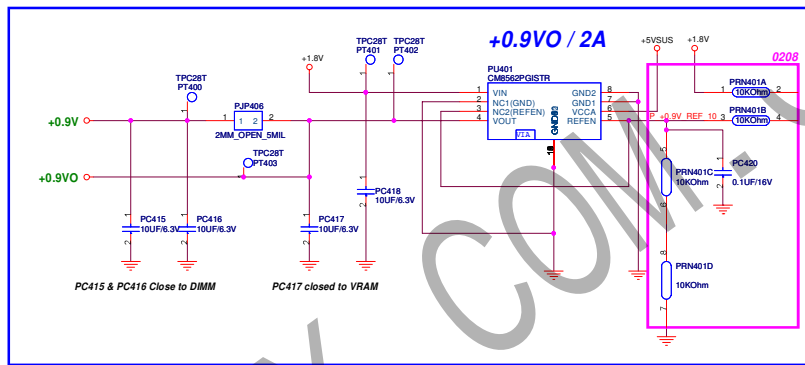
for CPU(VCCA), NB, SB, Other Devices Power

<Variant Name>		ASUS		Title : POWER_ID_1.5VS & 1.8VS	
<OrgName>		Project Name		Engineer:	
Size	AS	NAPA	Rev	2.05	
Date	11/18/2007		Sheet	51	of 60

<< Kennedy\_Zhang >>

**+VDDQ**

VDDR_SEL2	VDDR_SEL1	VDDR_SEL0	Voltage
X	X	X	1.630V
X	X	L	1.656V
X	L	X	1.681V
X	L	L	1.708V
L	X	X	1.728V
L	X	L	1.755V
L	L	X	1.779V
L	L	L	1.806V

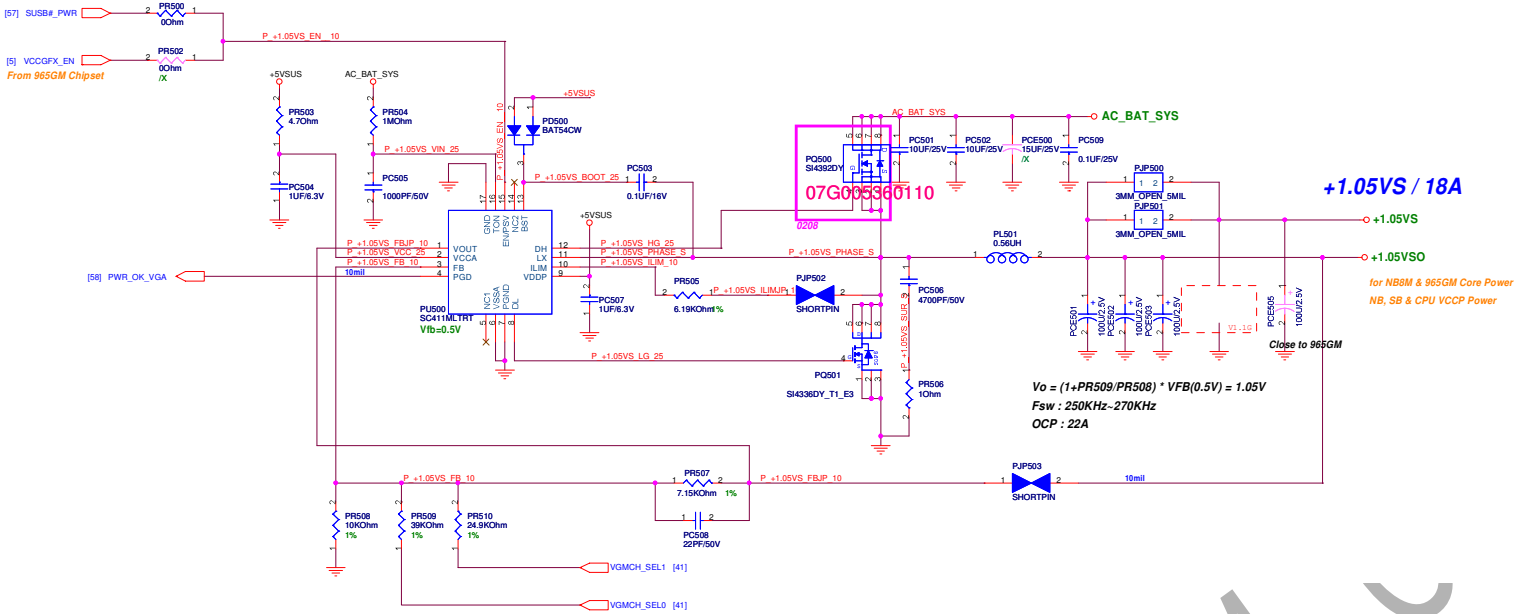


ASUS  
Title : POWER\_IO\_DDR & VTT  
Engineer:  
Date: 11/03/2007

<< Kennedy\_Zhang >>

WWW.CHINAKEY.COM

**Enable Signal :**  
 Implemet Intel 965GM --> PR502  
 Implemet NBBM Chipset --> PR500



**+1.05VS**

VGMCH_SEL0	VGMCH_SEL1	Voltage
L	L	1.093V
X	L	1.001V
L	X	0.949V
X	X	0.857V


<Variant Name>

<b>ASUS</b>		Title : <Title>	
ASUSTek COMPUTER Inc.		Engineer: Carl Chlou	
Size A3	Project Name <Doc>	Rev 2.03	
Date: 11/11/03, 2007	Sheet 53	of 60	

<< Kennedy\_Zhang >>

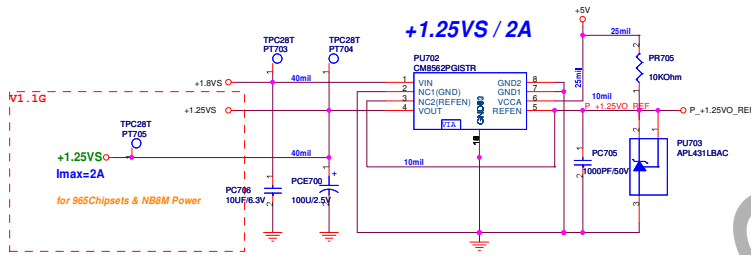
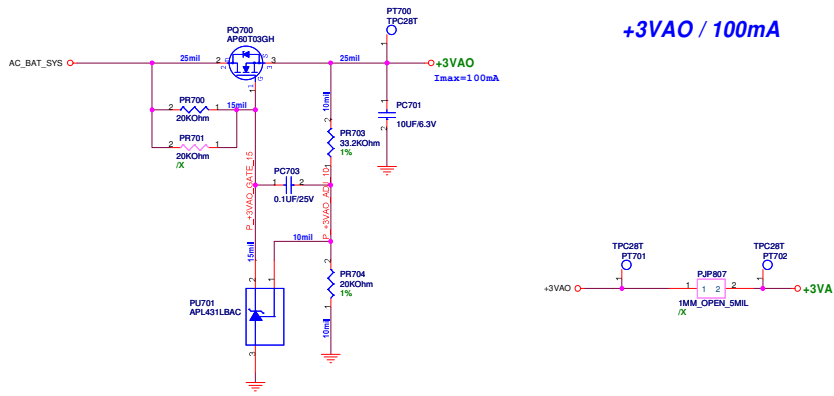
WWW.CHINAHELPX.COM

<Variant Name>

		<b>Title :</b> POWER_VGA_CORE & RAM
<OrigName>		<b>Engineer:</b>
Size	Project Name	Rev
A3	NAPA	2.0G
Date:	11.11.03.2007	Sheet 54 of 60

<< Kennedy\_Zhang >>

WWW.CHINAPEIX.COM.CN

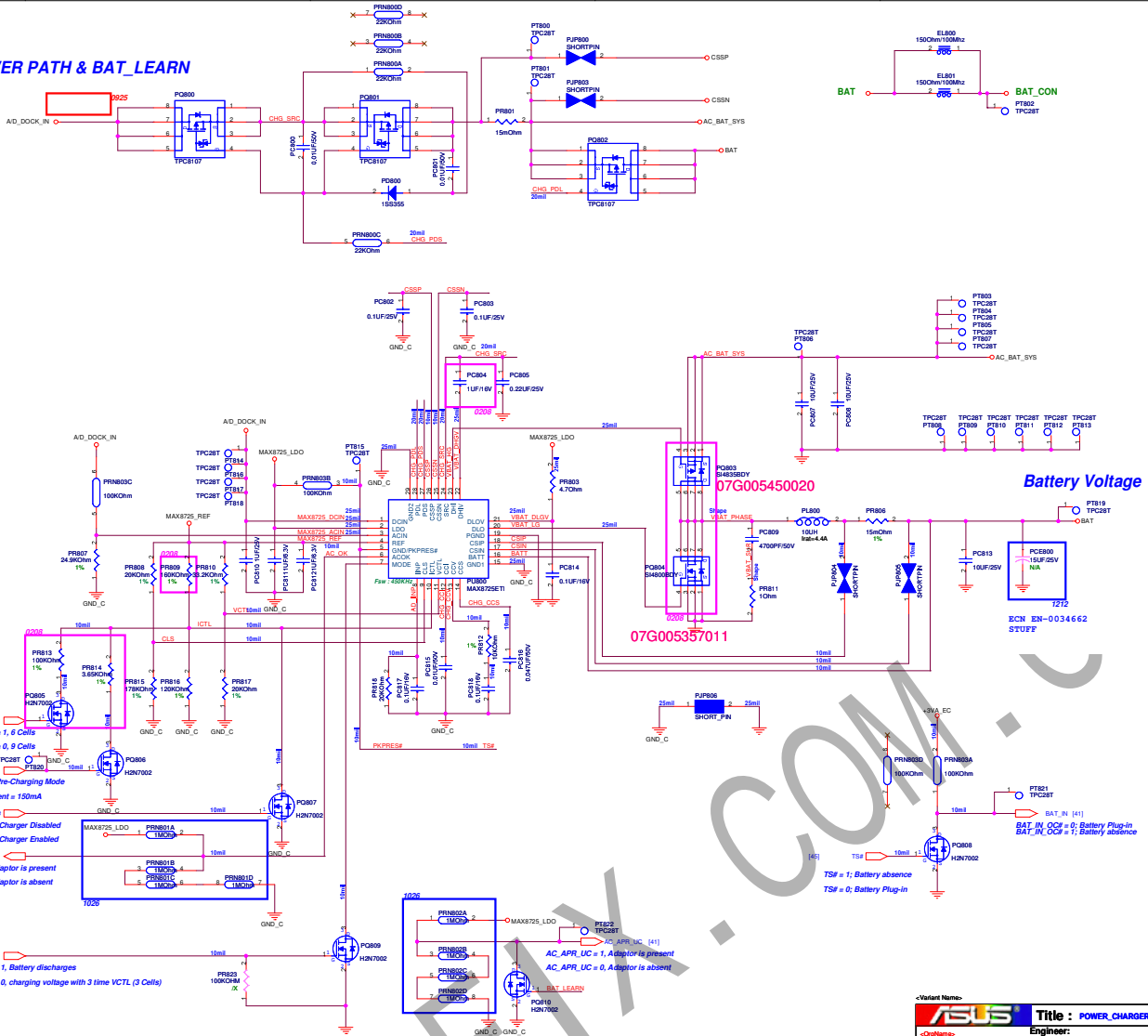


ASUS		Title : POWER_I/O_+3VA & +2.5V	
-<OrigName>		Engineer:	
Size	Project Name	Rev	
A3	NAPA	2.03	
Date:	11/11/03	Sheet	56 of 60

<< Kennedy\_Zhang >>

WWW.CHINAHELPX.COM

# POWER PATH & BAT\_LEARN



AC\_IN\_Threshold 2.048Vmax AD\_DOCK\_IN = 1.415 active  
 VCTL<0.8V or DCIN = 7V ->Charger Disable

PKPRESE ( Pns )	Battery
High (>2.235V)	Absence
Low (<0.8V)	Presence

MODE ( Pin7 )	Battery
High (>2.8V)	4-Cell Battery Learn
Low (<0.8V)	3-Cell
High Impedance (1.6V<Vmode<2V)	3-Cell

MAX8725\_REF : 4.2235V  
 MAX8725\_LDO : 5.4V

**Battery Charging Voltage :**  
 $+V\_BAT = 3 \times [4.2235 + (Vctl - 1.8) / 9.52]$   
**Battery Charging Current :**  
 $I_{charge} = (0.075 / PR806) \times (Vctl / 3.6)$   
**Input Adaptor Max. Current Limit :**  
 $I_{limit\_current} = (0.075 / PR801) \times (VcIs / 4.2235)$

**Pre-Charging Mode :**  
 Precharging current = 127.1mA  
 Vctl = 91.5mV

**Battery Cell Selection :**  
 BATSEL\_2P# = 1, 3 Cells; Vctl = 1.074V  
 $\Rightarrow I_{charge} = 1.491A$   
 BATSEL\_2P# = 0, 6 Cells; Vctl = 1.81V  
 $\Rightarrow I_{charge} = 2.514A$

**Adaptor Max. Current :**  
 PR815 = 178K; Ilimit = 4.49A; 85.4W  
 PR815 = 36K; Ilimit = 3.21A; 61.1W

- [41] BATSEL\_2P# = 1, 3 Cells
- [41] PRECHG = 1, Pre-Charging Mode
- [41] CHG\_EN# = 1, Charger Disabled
- [41] CHG\_EN# = 0, Charger Enabled
- [41] AC\_OK = 1, Adaptor is present
- [41] AC\_OK = 0, Adaptor is absent
- [41] BAT\_LEARN = 1, Battery discharges
- [41] BAT\_LEARN = 0, charging voltage with 3 time VCTL (3 Cells)

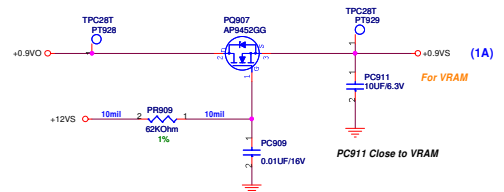
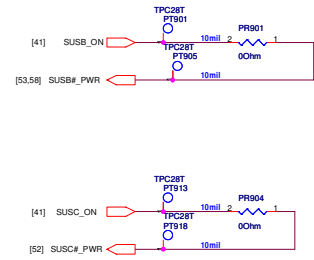
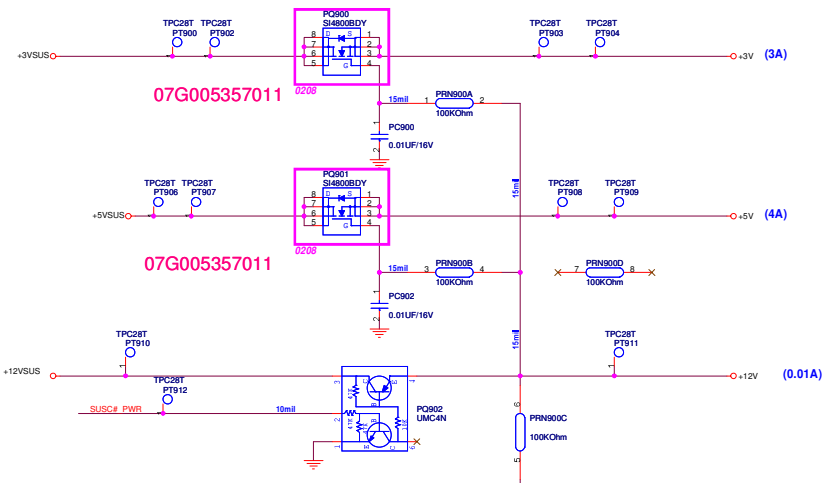
## Battery Voltage

ASUS		Title : POWER_CHARGER	
BSA	Project Name	Rev	
C	NAPA	2.00	
Date:	11-05-2007	Sheet:	56 of 60

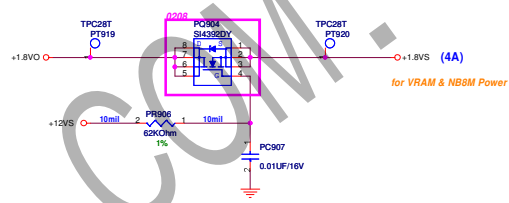
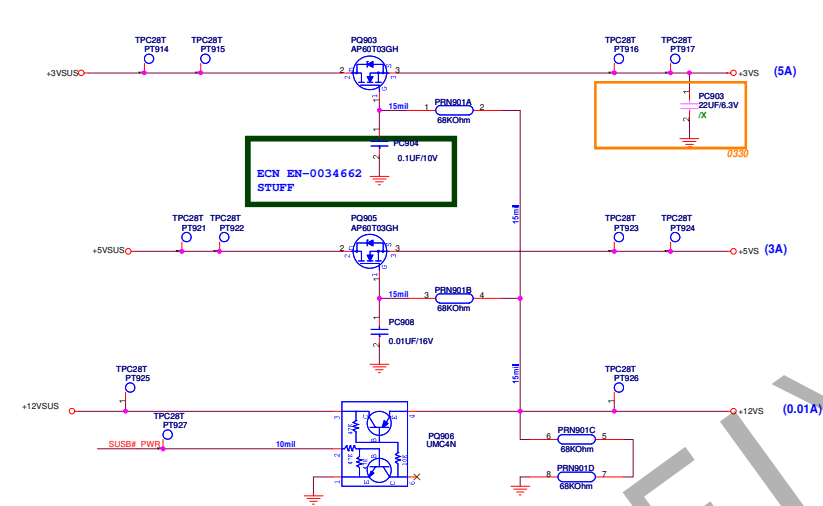
« Kennedy Zhang »



**SUSC#\_PWR POWER**



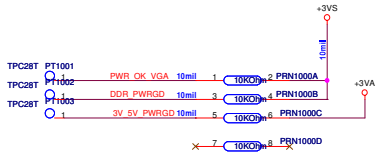
**SUSB#\_PWR POWER**



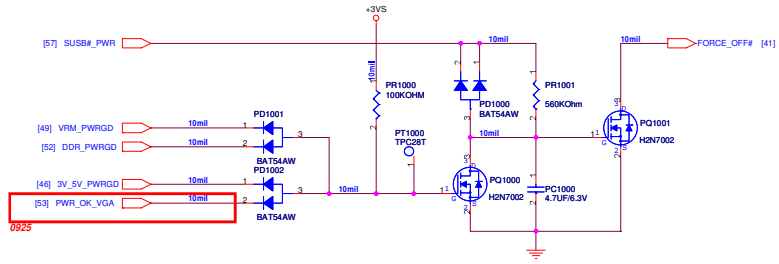
-Variant Name-		<b>ASUS</b>		<b>Title : POWER_LOAD SWITCH</b>
<OrigName>		Project Name		Rev
Size	Custom	NAPA		2.26
Date: 11/13/03	2002	Sheet		57 of 80

« Kennedy\_Zhang »

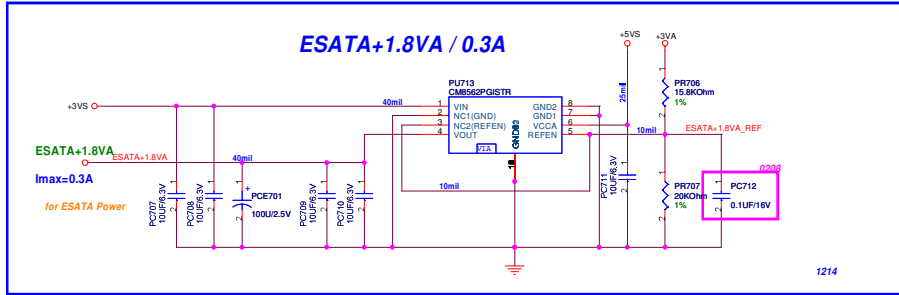
WWW.CHINAFLIX.COM



### Power Good Detector



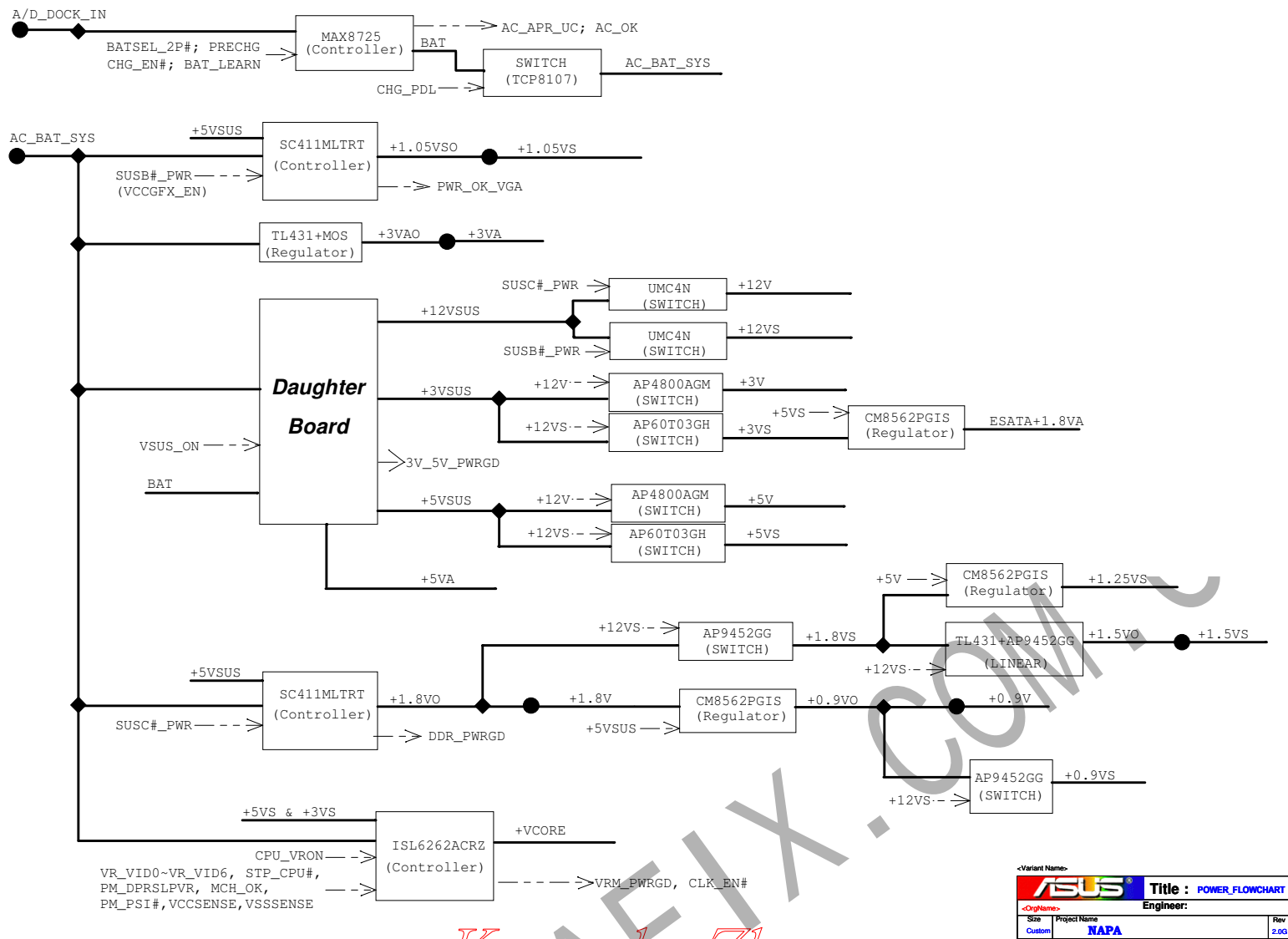
### ESATA+1.8VA / 0.3A



-Variant Name-		<b>ASUS</b> Title : POWER_DETECT	
-OrigName-		Engineer:	
Size	Project Name	Rev	
A3	NAPA	2.0G	
Date:	11/03/2007	Sheet	58 of 60

<< Kennedy\_Zhang >>

WWW.CHINAHELPX.COM




« Kennedy Zhang »

ASUS		Title : POWER_FLOWCHART	
Engineer:			
Size	Project Name	Rev	
Custom	NAPA	2.0G	
Date: 11/11/03 2007		Sheet 69	of 80

WWW.CHINAFILEX.COM

WWW.CHINAPEIX.COM.CN

« Kennedy\_Zhang »

<Variant Name>		
		Title : POWER_SIGNAL
<OrigName>		Engineer: Carl Chou
Site	Project Name	Rev
Custom	NAPA	2.00
Date: 11/11/03 2007	Sheet	80 of 80