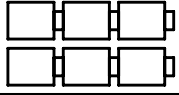


A8N_A8Dc AMD/MCP67MV/MCP67D BLOCK DIAGRAM

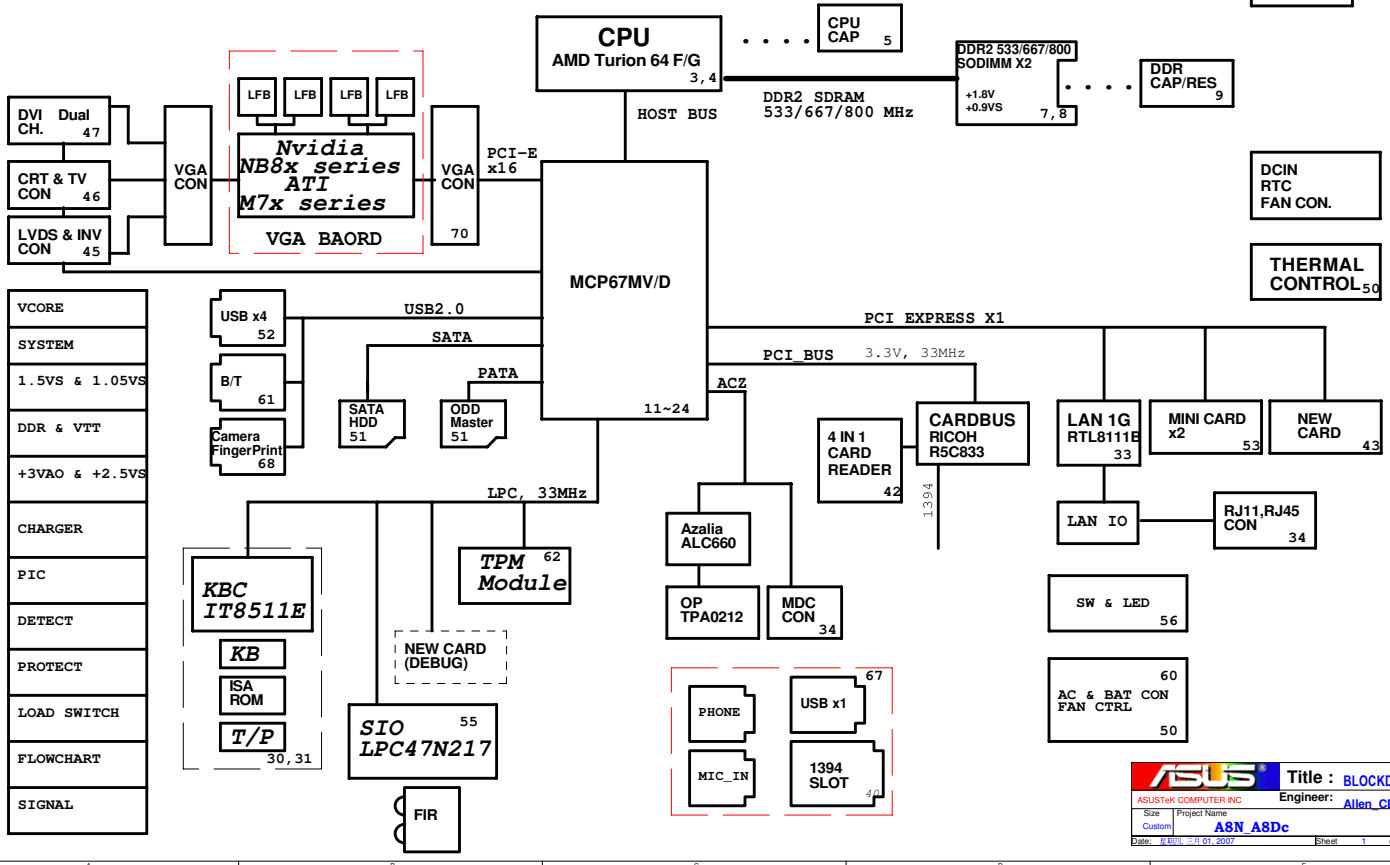
Sub block Diagram / BOM option

64

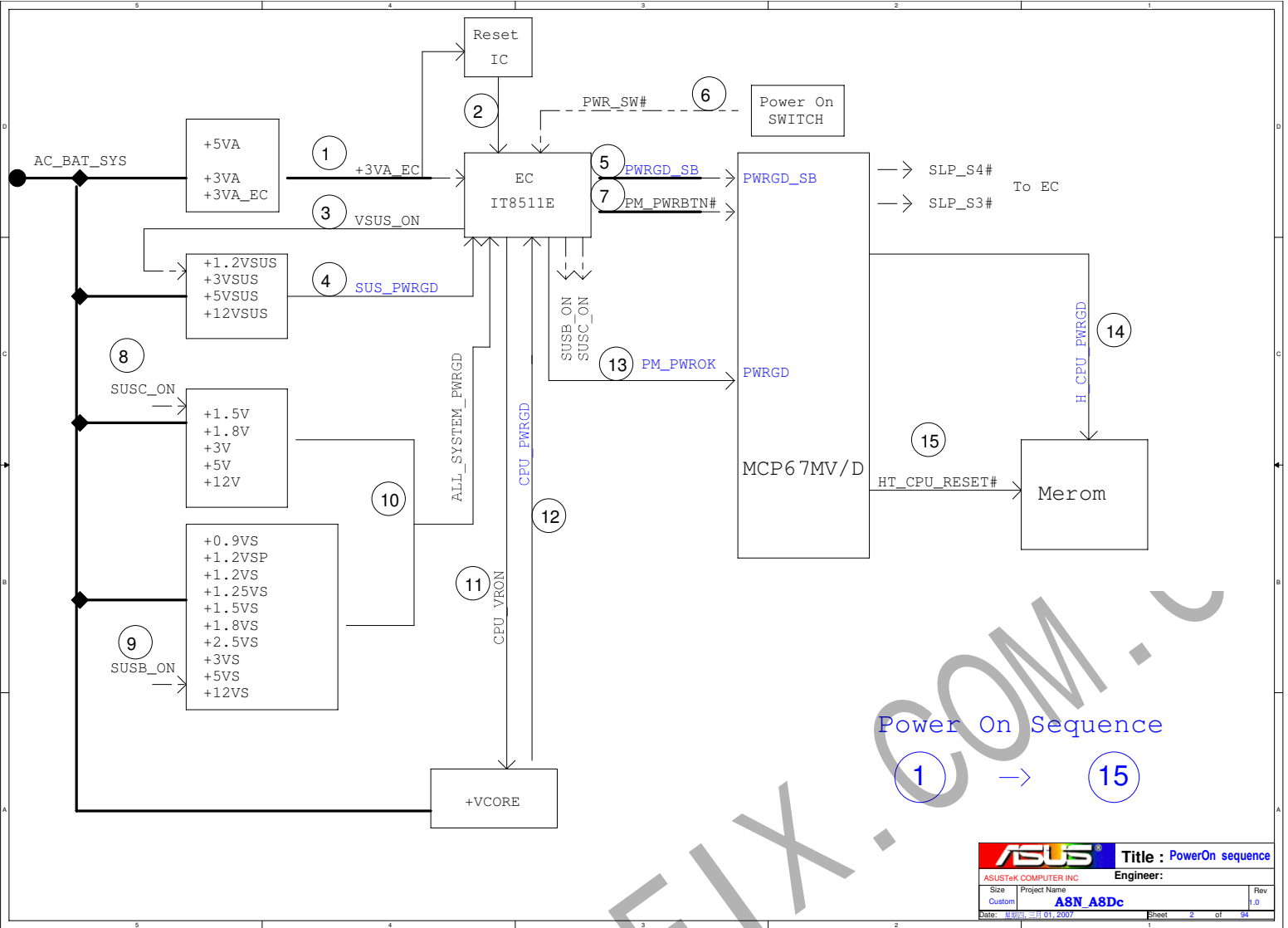
BATTERY TYPE
3S2P



POWER SEQUENCE
2



ASUS Title : BLOCKDIAGRAM
 ASUSTeK COMPUTER INC. Engineer: Allen_CD Wu
 Size: Custom Project Name: A8N_A8Dc Rev: 1.0
 Date: 10/10/2007 Sheet: 1 of 34



ASUS		Title : PowerOn sequence	
ASUSTek COMPUTER INC		Engineer:	
Size	Project Name		Rev
Custom	A8N_A8Dc		1.0
Date: 11/01/2007		Sheet 2	of 94

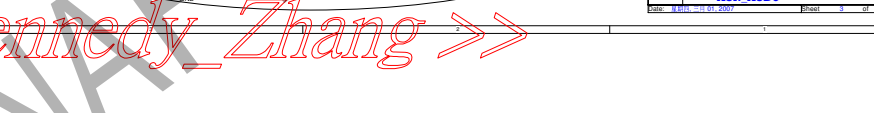
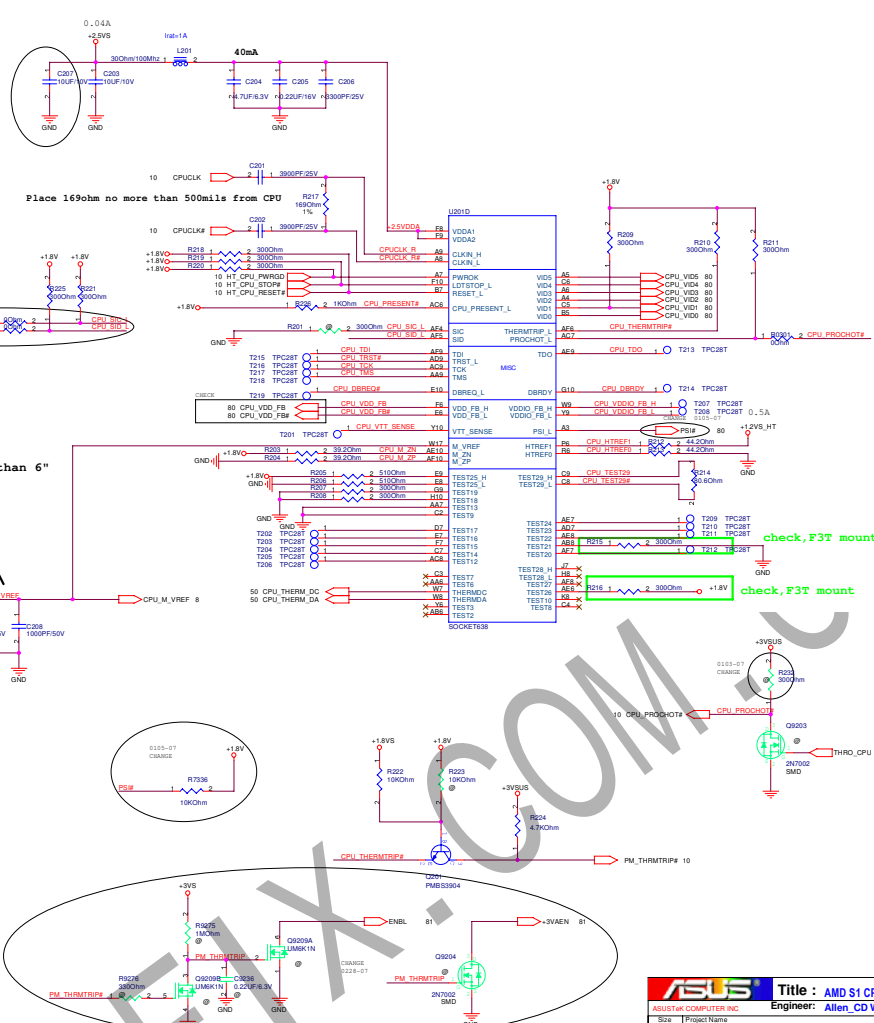
<< Kennedy_Zhang >>

U031A			
10 HT_CPU_RX_CLK1	LS	L0_CLKIN_H1	HT_CPU_TX_CLK1_10
10 HT_CPU_RX_CLK4	MS	L0_CLKIN_L1	HT_CPU_TX_CLK4_10
10 HT_CPU_RX_CLK8	PS	L0_CLKIN_H0	HT_CPU_TX_CLK8_10
10 HT_CPU_RX_CLK8	PS	L0_CLKIN_L0	HT_CPU_TX_CLK8_10

CHARGE 1211-06			
10 HT_CPU_RXCKT10	NS	L0_CLKOUT_H1	HT_CPU_TXCKT10_10
10 HT_CPU_RXCKT14	NS	L0_CLKOUT_L1	HT_CPU_TXCKT14_10
10 HT_CPU_RXCKT18	NS	L0_CLKOUT_H0	HT_CPU_TXCKT18_10
10 HT_CPU_RXCKT18	NS	L0_CLKOUT_L0	HT_CPU_TXCKT18_10

HYPERTRANSPORT			
10 HT_CPU_RXD07	NS	L0_CADOUT_H0	HT_CPU_TXD07_10
10 HT_CPU_RXD07	NS	L0_CADOUT_L0	HT_CPU_TXD07_10
10 HT_CPU_RXD08	NS	L0_CADOUT_H1	HT_CPU_TXD08_10
10 HT_CPU_RXD08	NS	L0_CADOUT_L1	HT_CPU_TXD08_10

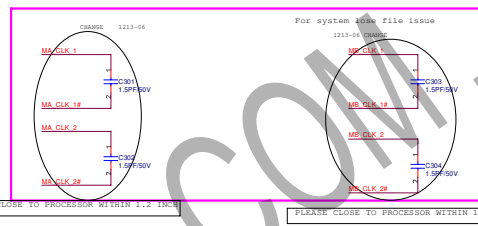
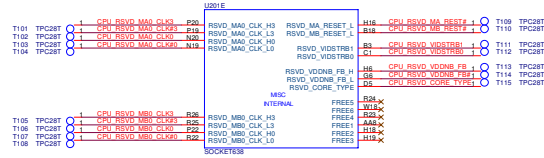
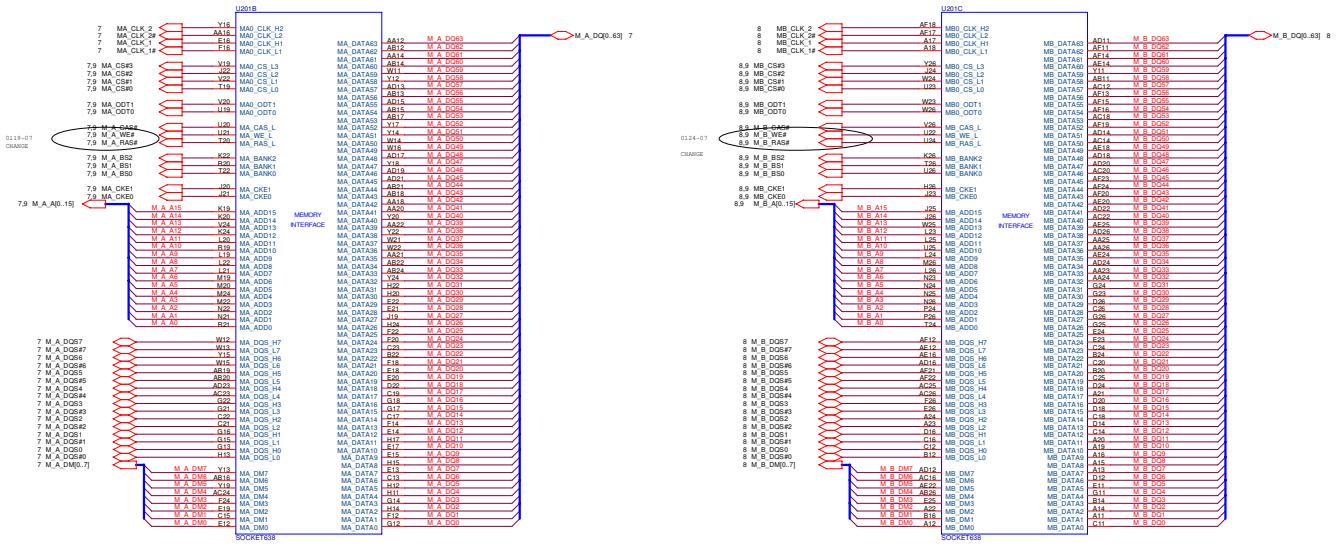
CHARGE 1211-06			
10 HT_CPU_RXD10	NS	L0_CADOUT_H0	HT_CPU_TXD10_10
10 HT_CPU_RXD10	NS	L0_CADOUT_L0	HT_CPU_TXD10_10
10 HT_CPU_RXD11	NS	L0_CADOUT_H1	HT_CPU_TXD11_10
10 HT_CPU_RXD11	NS	L0_CADOUT_L1	HT_CPU_TXD11_10



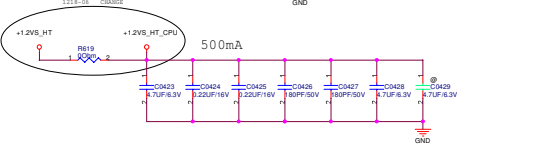
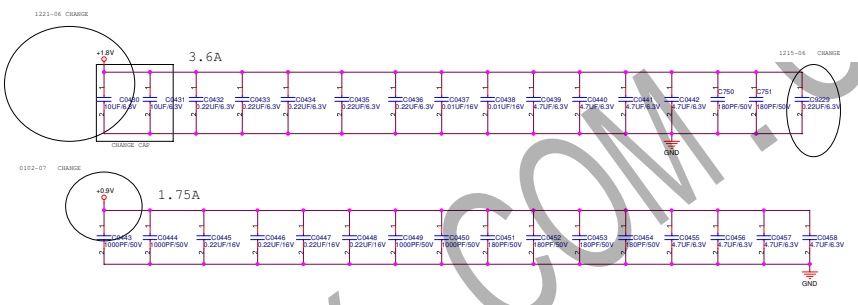
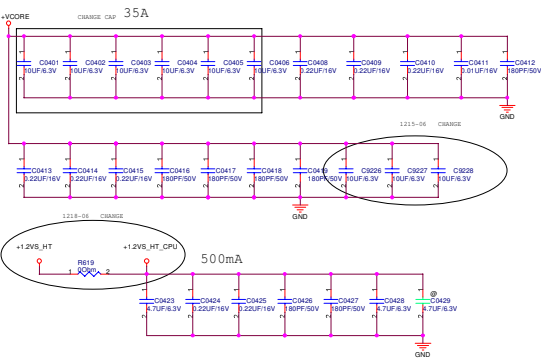
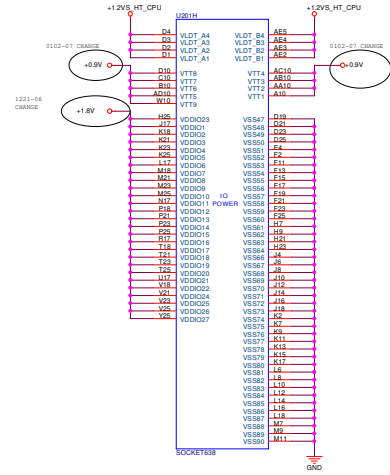
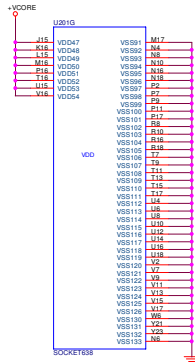
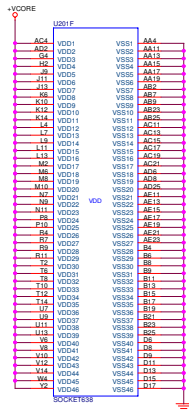
ASUS	Title : AMD S1 CPU1(t)
ASUSTeK COMPUTER INC.	Engineer: Allen_CD Wu
Custom	Project Name:
ASUS A8DC	Rev 1
DATE: 11/11/2007	DRAWN: 3 of 4

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ASUS		Title : AMD S1 CPU3	
ASUSTEK COMPUTER INC		Engineer: Allen_CD Wu	
BS24	Project Name	Rev	
Custom	A8N A8Dc	1	
Date: 2007.05.23	Sheet: 6	of	6

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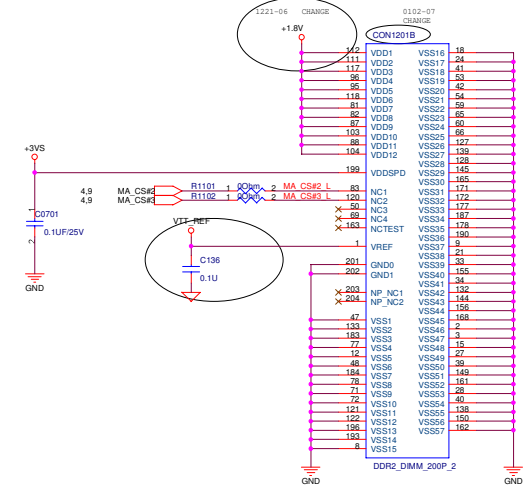
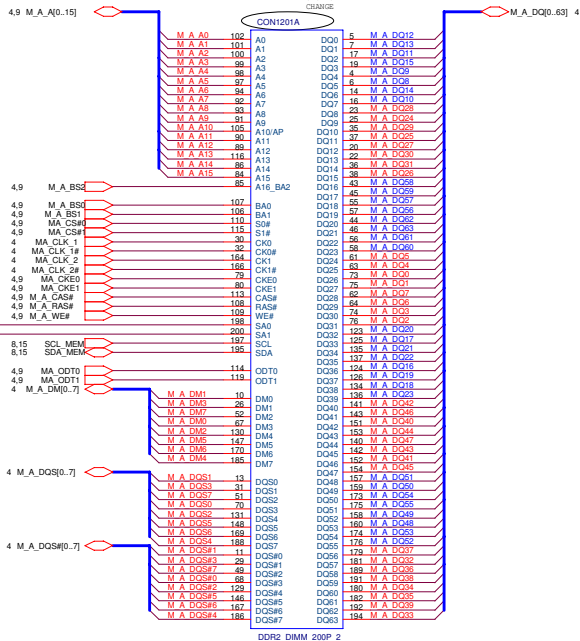
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		Title : BLANK
ASUSTeK COMPUTER INC		Engineer:
Size	Project Name	Rev
A	A8N_A8Dc	0
Date: 星期四, 三月 01, 2007		Sheet 6 of 94

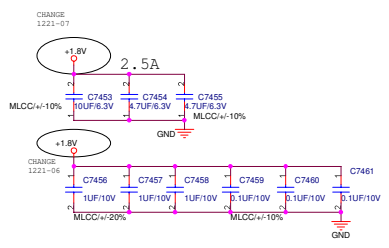
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SMBus Slave Address:A0H



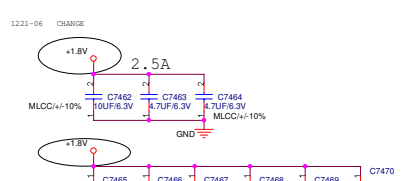
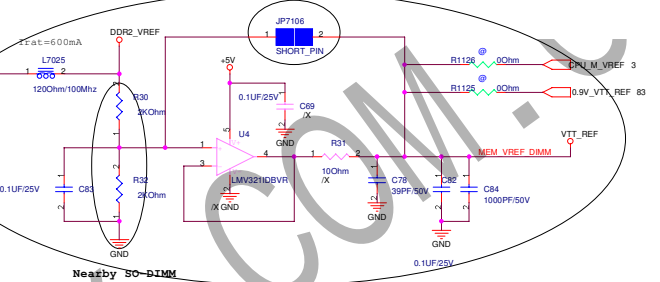
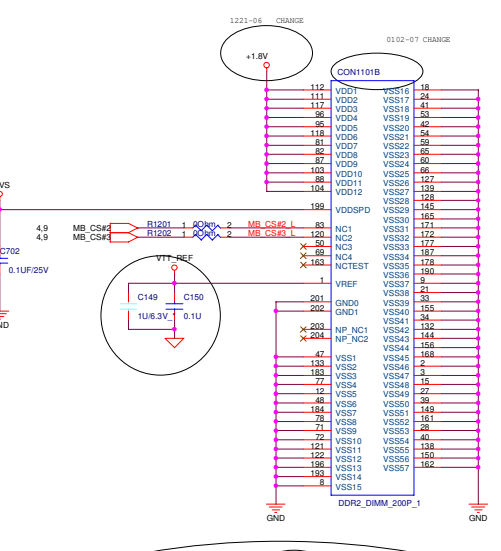
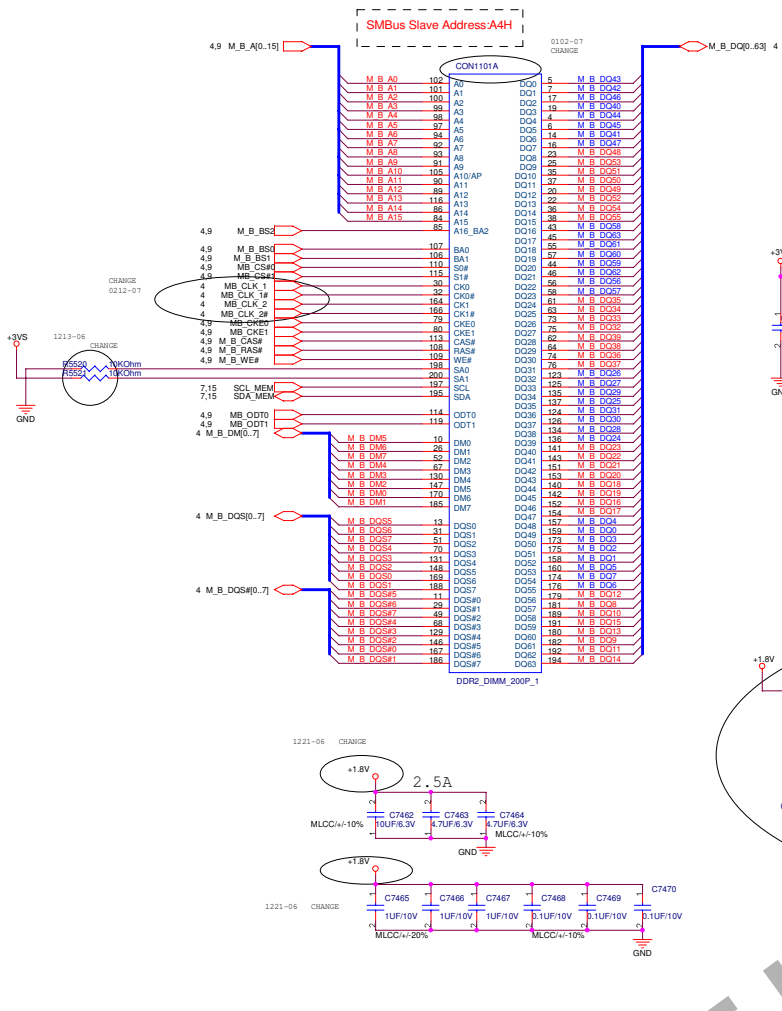
Hight: 4.0 mm



ASUS Title : DDR2 SO-DIMM_0
ASUSTeK COMPUTER INC. Engineer: Allen_CD Wu

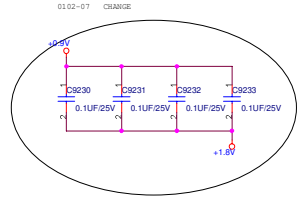
Size	Project Name	Rev
Custom	A8N_A8Dc	1
Date: 11/01/2007	Sheet 7 of 94	

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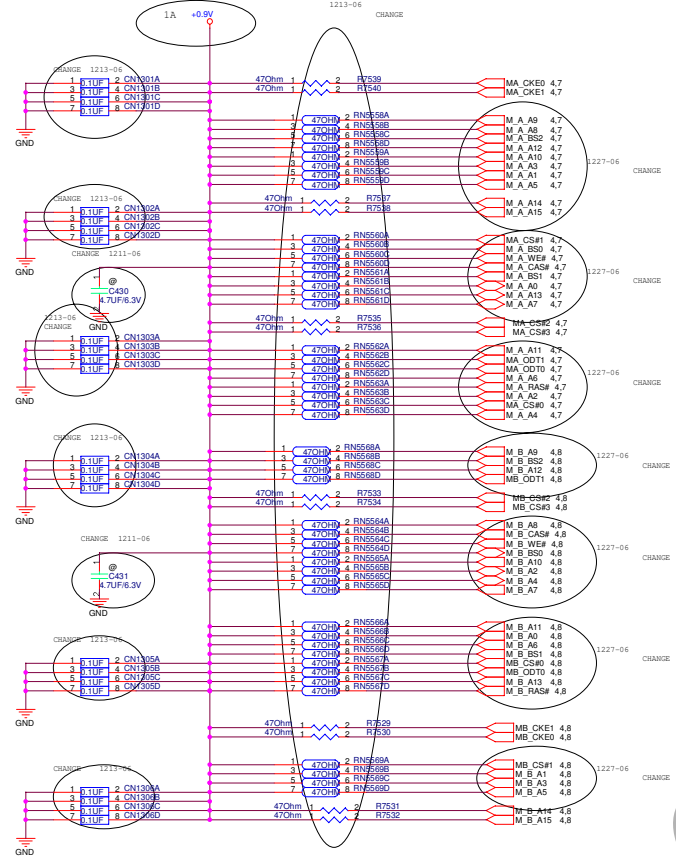


ASUS Title : **DDR2 SO-DIMM_1**
 ASUSTeK COMPUTER INC. Engineer : **Allen_CD Wu**
 Size: Custom Project Name: **A8N A8Dc** Rev: 1
 Date: 01/01/2007 Sheet: 8 of 94

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For Address/Command close to the area of plane switched

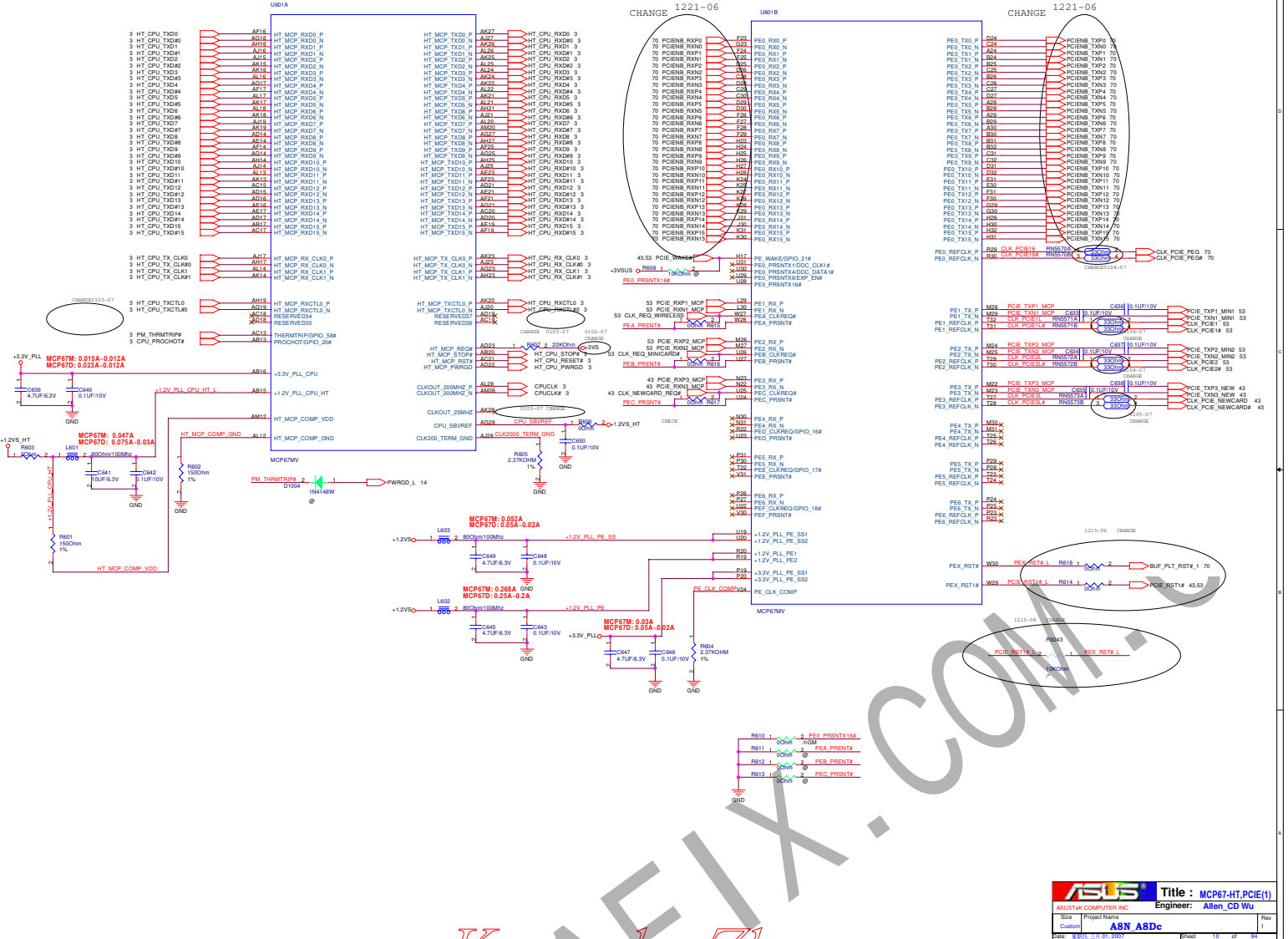


Layout note: Place array cap close to each pullup resistor terminated to +0.9V

ASUS Title :DDR2 TERMINATION
 ASUSTeK COMPUTER INC. Engineer: Alien_CD Wu
 Size Project Name
 Custom **A8N_A8Dc**
 Date: 11/01/07 01:2007 Sheet 6 of 94

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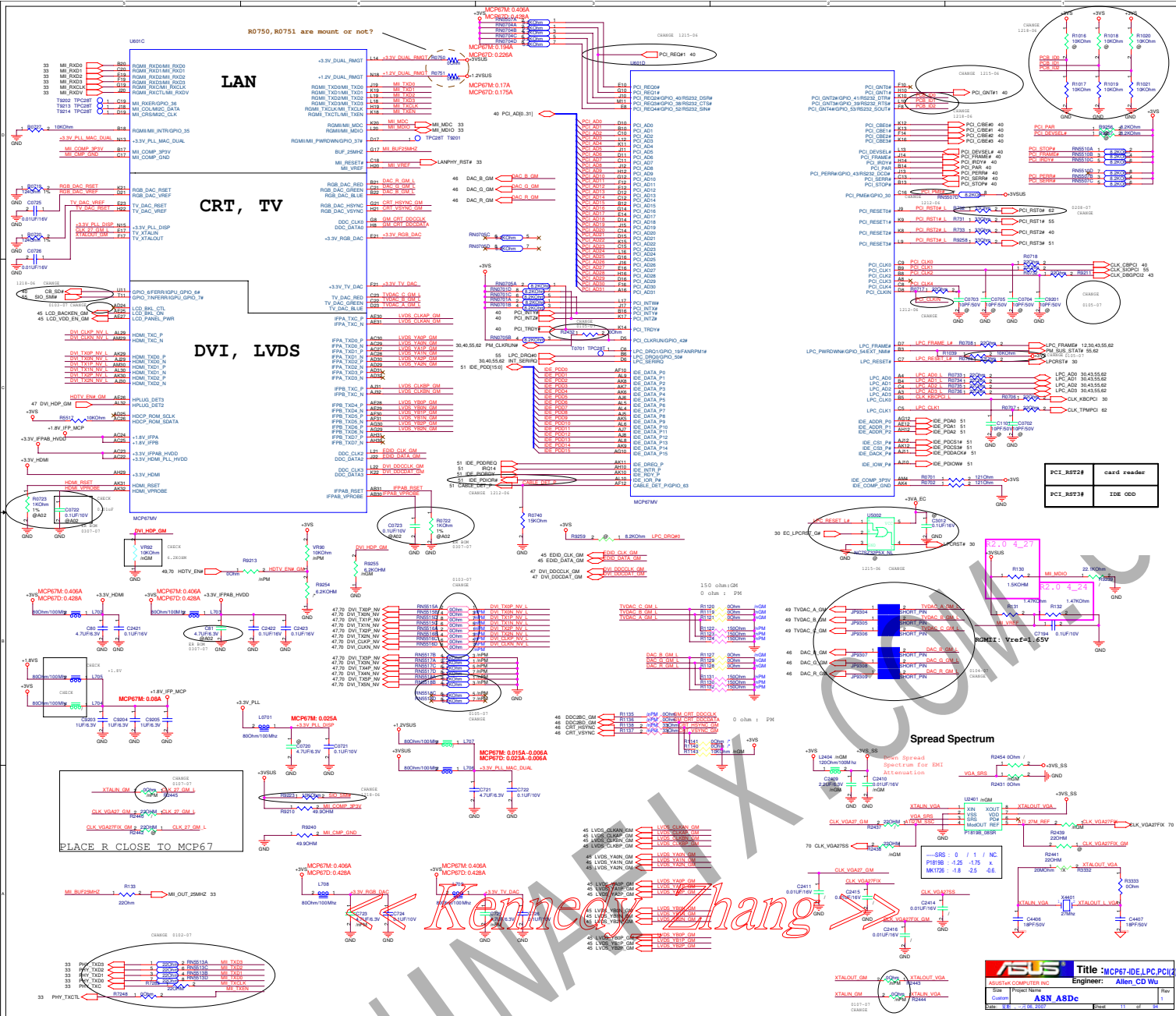
WWW.CHINAPELIX.COM



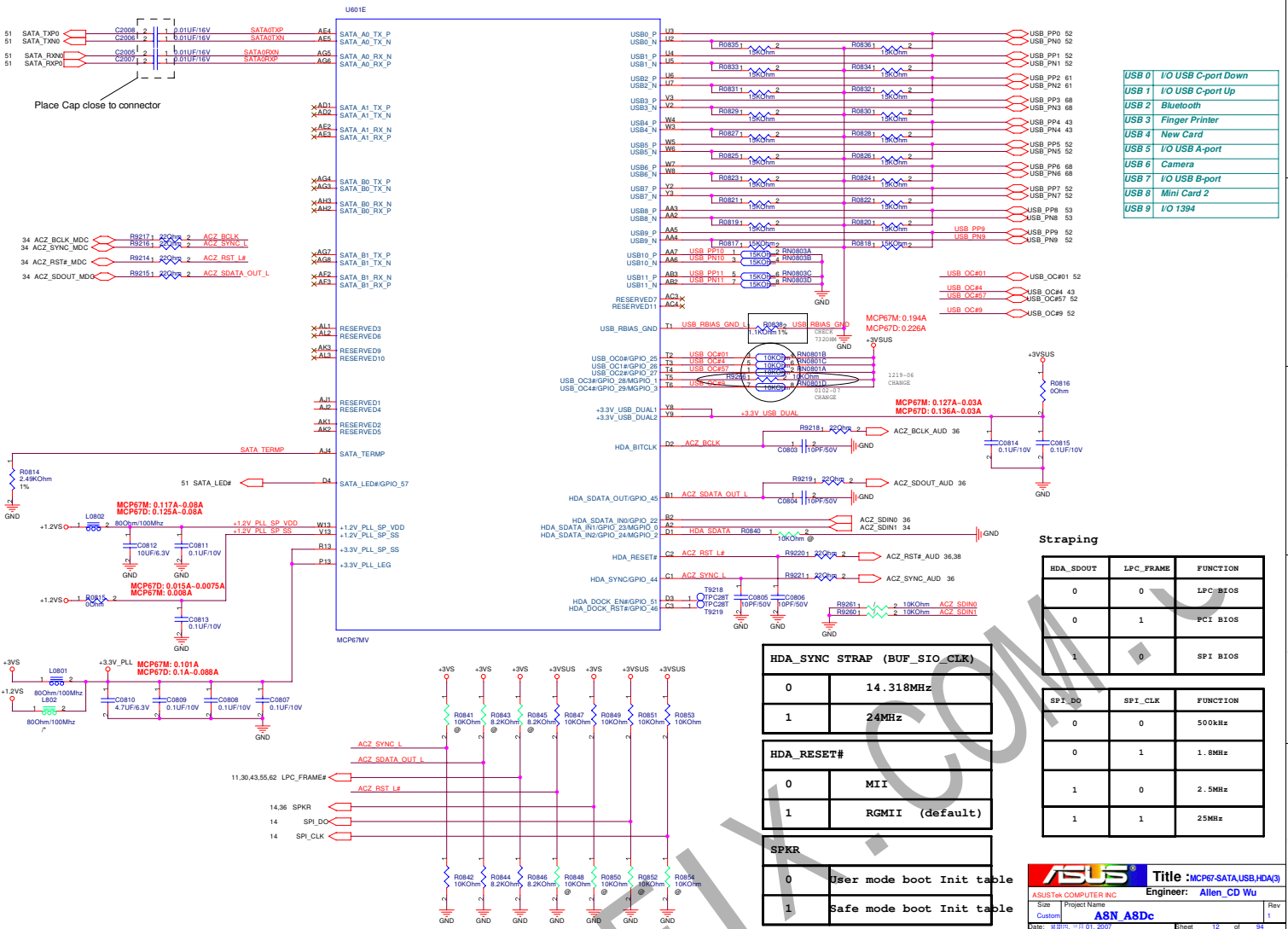
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ASUS		Title : MCP67-HT-PCIE(I)	
ASUS Computer Inc.		Engineer: Allen_CD VU	
Sra	Project Name	Rev	Date
Custom	A8N A8dc	1	2007
Doc	10 of 34	E5ad	10 of 34



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- USB 0 I/O USB C-port Down
- USB 1 I/O USB C-port Up
- USB 2 Bluetooth
- USB 3 Finger Printer
- USB 4 New Card
- USB 5 I/O USB A-port
- USB 6 Camera
- USB 7 I/O USB B-port
- USB 8 Mini Card 2
- USB 9 I/O 1394

Strapping

HDA_SDOUT	LPC_FRAME	FUNCTION
0	0	LPC BIOS
0	1	PCI BIOS
1	0	SPT BIOS

SPT_DO	SPT_CLK	FUNCTION
0	0	500kHz
1	0	2.5MHz
1	1	25MHz

HDA_SYNC STRAP (BUF_SIO_CLK)	
0	14.318MHz
1	24MHz

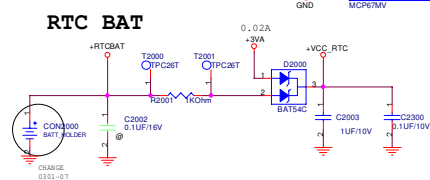
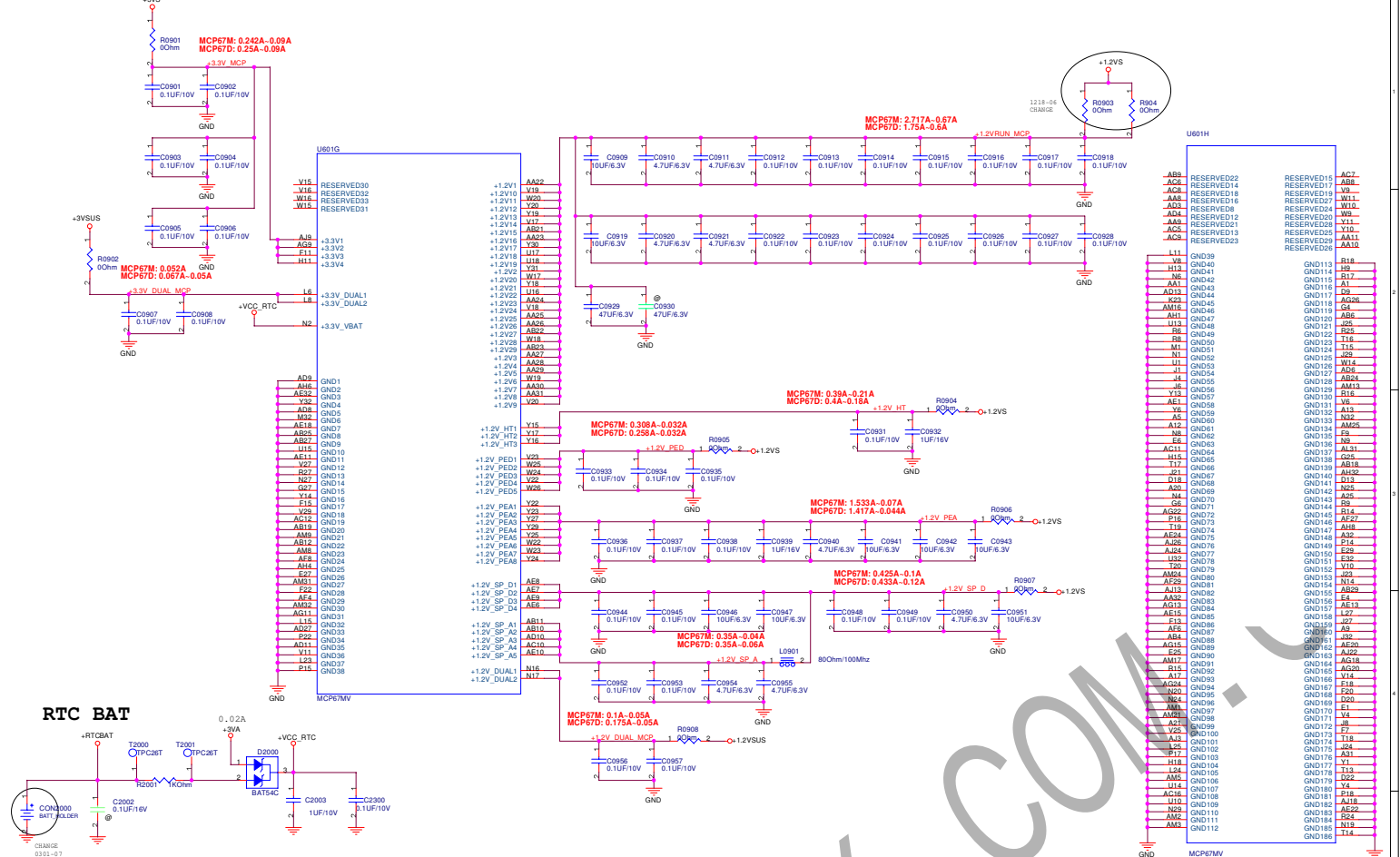
HDA_RESET#	
0	MII
1	RGMI (default)

SPKR	
0	User mode boot Init table
1	Safe mode boot Init table

ASUS Title: MCP67-SATA,USB,HDA(3)
 ASUSTek COMPUTER INC. Engineer: Allen_CD Wu
 Size: Project Name: **ASBN ASDC** Rev: 1
 Date: 11/01/2007 Sheet 12 of 34

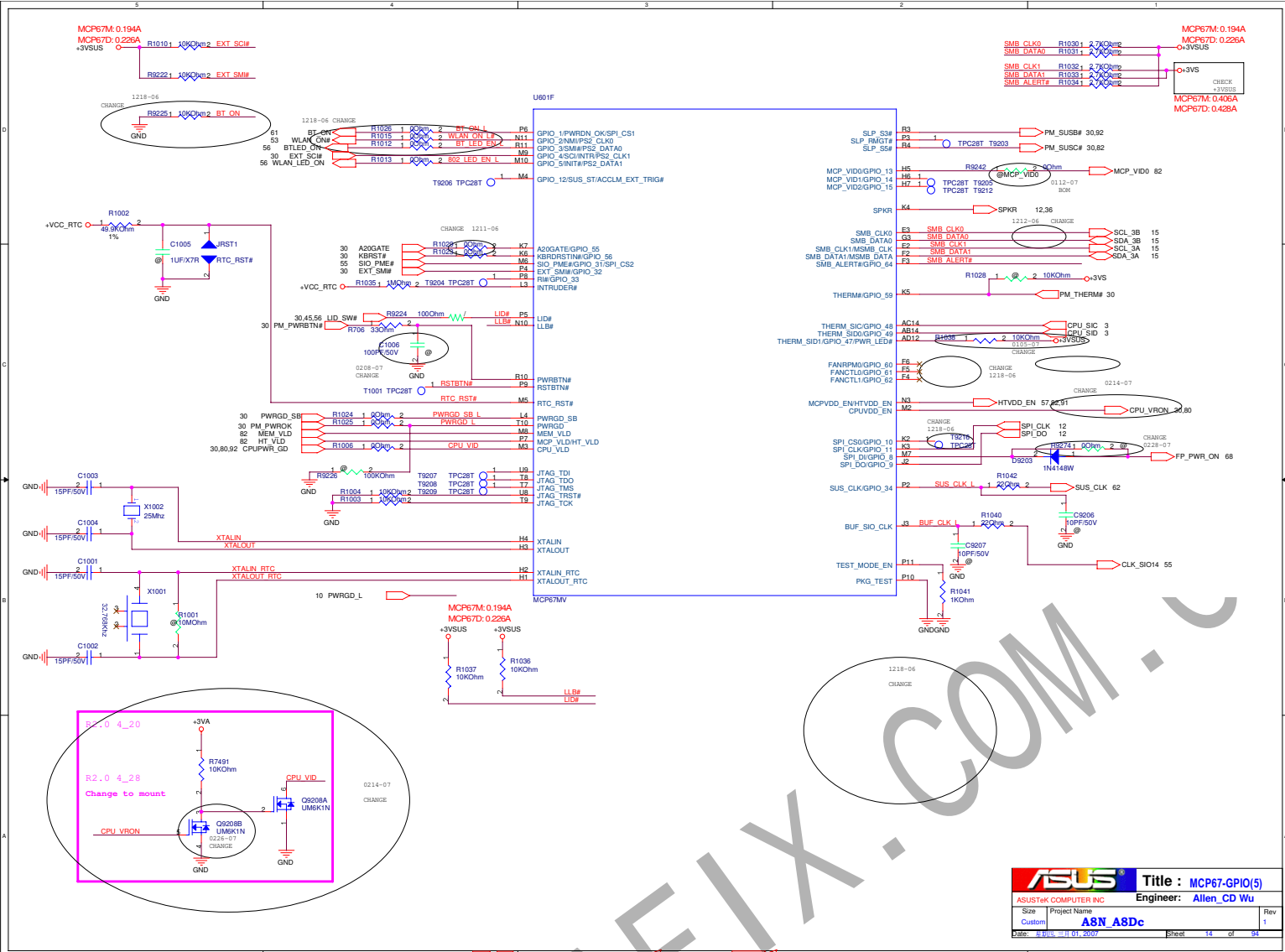
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		Title : MCP67-GPIO(5)
		Engineer : Allen_CD Wu
Size	Project Name	Rev
Custom	ABN_A8Dc	1
Date: 2007-01-01	Sheet: 14	of 84

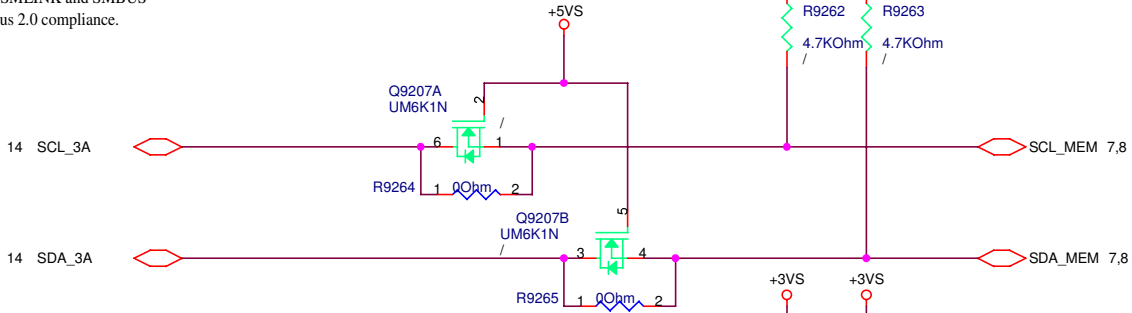
<< Kennedy_Zhang >>

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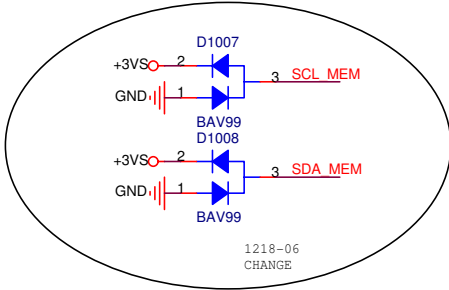
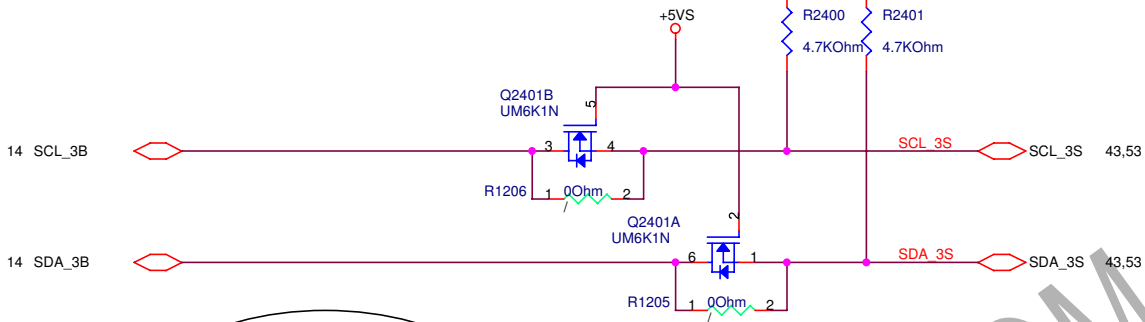
Check

Connect SMLINK and SMBUS for SMBus 2.0 compliance.

ICH8-M



ICH8-M




1218-06
CHANGE

		Title : LEVEL SHIFTER
ASUSTeK COMPUTER INC		Engineer: Allen_CD Wu
Size A	Project Name A8N_A8Dc	Rev 1.0
Date: 星期四, 三月 01, 2007		Sheet 15 of 94


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		Title : BLANK	
ASUSTeK COMPUTER INC		Engineer:	
Size	Project Name		Rev
A	A8N_A8Dc		0
Date: 星期四, 三月 01, 2007		Sheet	16 of 94


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		Title : BLANK
ASUSTeK COMPUTER INC		Engineer:
Size	Project Name	Rev
A	A8N_A8Dc	0
Date: 星期四, 三月 01, 2007		Sheet 17 of 94


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		Title : BLANK	
ASUSTeK COMPUTER INC		Engineer:	
Size A	Project Name A8N_A8Dc		Rev 0
Date: 星期四, 三月 01, 2007		Sheet	18 of 94


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ASUSTeK COMPUTER INC		Engineer:	
Size	Project Name	Rev	
A	A8N_A8Dc	0	
Date: 星期四, 三月 01, 2007		Sheet	19 of 94


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		Title : BLANK	
ASUSTek COMPUTER INC		Engineer:	
Size	Project Name		Rev
Custom	A8N A8Dc		0
Date: 01/01/2007	Sheet 20 of 94		


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		Title : BLANK	
ASUSTeK COMPUTER INC		Engineer:	
Size	Project Name		Rev
Custom	A8N_A8Dc		0
Date: 2007.01.01		Sheet 21 of 34	


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		Title : BLANK	
ASUSTeK COMPUTER INC		Engineer:	
Size	Project Name	Rev	
Custom	A8N_A8Dc	0	
Date: 08/01/2007		Sheet 22 of 94	


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		Title : BLANK	
ASUSTek COMPUTER INC		Engineer:	
Size	Project Name	Rev	
Custom	A8N_A8Dc	0	
Date: 星期日 01, 2007		Sheet	23 of 94


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		Title : BLANK
ASUSTeK COMPUTER INC		Engineer:
Size	Project Name	Rev
A	A8N_A8Dc	0
Date: 星期四, 三月 01, 2007		Sheet 24 of 94


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		Title : BLANK	
ASUSTeK COMPUTER INC		Engineer:	
Size A	Project Name A8N_A8Dc	Rev 0	
Date: 星期四, 三月 01, 2007		Sheet	25 of 94


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		Title : BLANK
ASUSTeK COMPUTER INC		Engineer:
Size A	Project Name A8N_A8Dc	Rev 0
Date: 星期四, 三月 01, 2007		Sheet 26 of 94


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		Title : BLANK	
ASUSTeK COMPUTER INC		Engineer:	
Size	Project Name		Rev
A	A8N_A8Dc		0
Date: 星期四, 三月 01, 2007		Sheet	27 of 94


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		Title : BLANK	
ASUSTeK COMPUTER INC		Engineer:	
Size	Project Name	Rev	
A	A8N_A8Dc	0	
Date: 星期四, 三月 01, 2007		Sheet	28 of 94

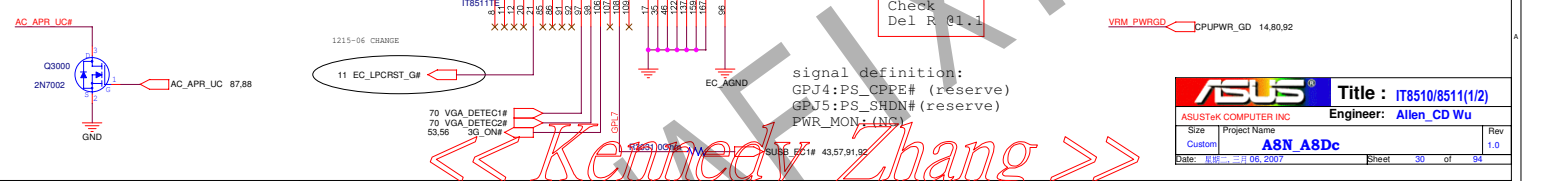
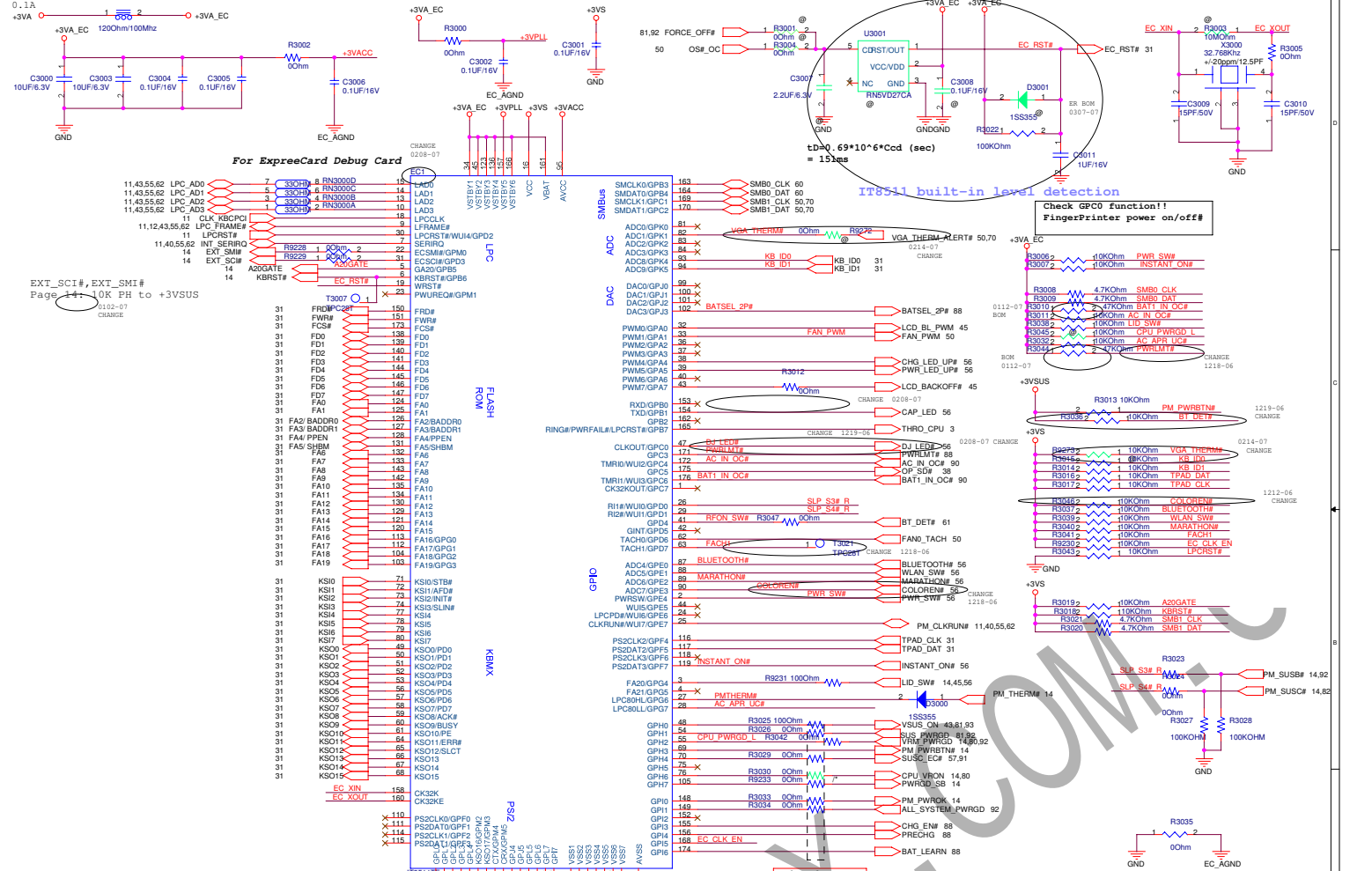
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		Title : BLANK
ASUSTek COMPUTER INC. Engineer:		
Size	Project Name	Rev
Custom	A8N_A8Dc	1.0
Date: 8/20/2007	Sheet	29 of 84

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signal definition:
 GPJ4:PS_CPPE# (reserve)
 GPJ5:PS_SHDN# (reserve)
 PWR_MON: (NG)

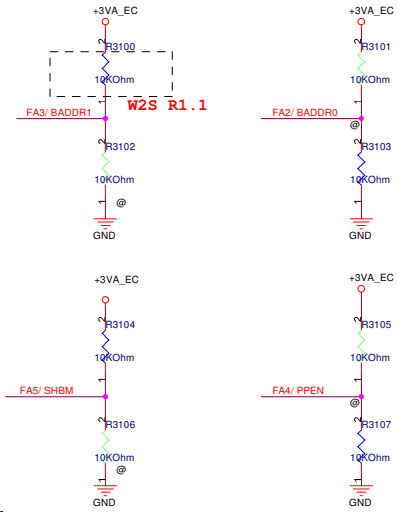
ASUS		Title : IT8510/8511(1/2)
ASUSTek COMPUTER INC		Engineer: Allen_CD Wu
Subj	Project Name	Rev
Custom	ASN_A8Dc	1.0
Date: 11/11/2007	Sheet	30 of 30

DVDDCO_ON#
 DVDDCO_ON#

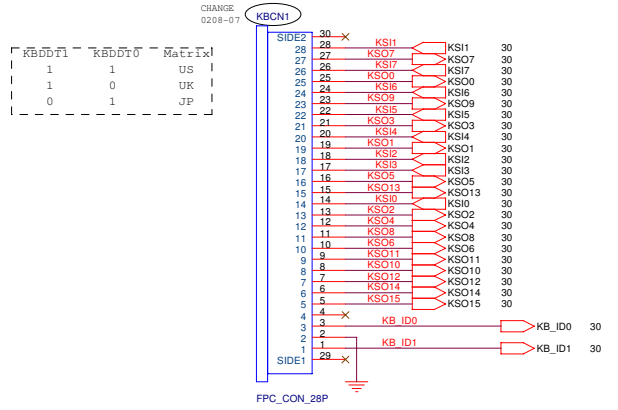


Kennedy Zhang

10:Determined by EC

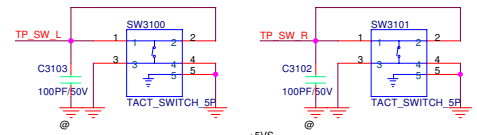


BADDR[1:0]
No pull up:
 The register pair to access PNPCFG is 002Eh and 002Fh.
Ext 10K up on BADDR0.
 The register pair to access PNPCFG is 004Eh and 004Fh.
Ext 10K up on BADDR1:
 The register pair to access PNPCFG is SWCBALR and SWCBAHR.

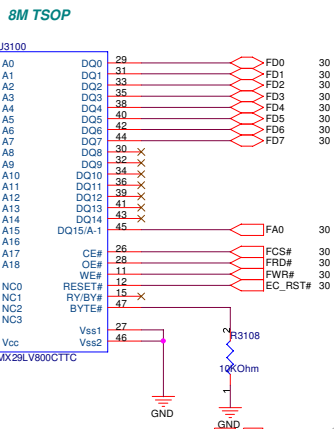
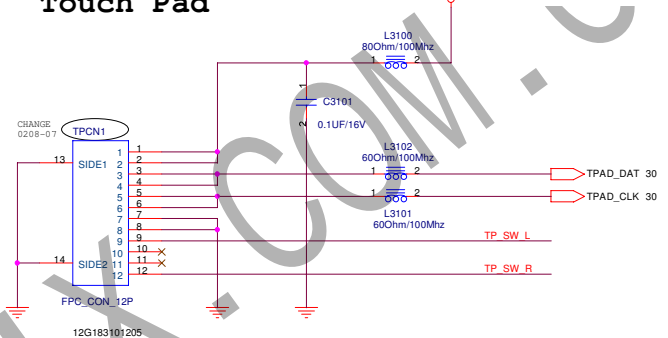


SHBM
No pull up:
 disable shared memory with host BIOS
Ext 10K up:
 enable shared memory with host BIOS

PPEN
No pull up:
 Normal
Ext 10K up:
 KBS interface pins are switched to parallel port interface for in-system programming.




Touch Pad



ASUS		Title : IT8510/8511(2/2)
ASUSTeK COMPUTER INC		Engineer: Allen_CD Wu
Size	Project Name	Rev
Custom	A8N A8Dc	1.0
Date:	2007.01.01	Sheet 31 of 94

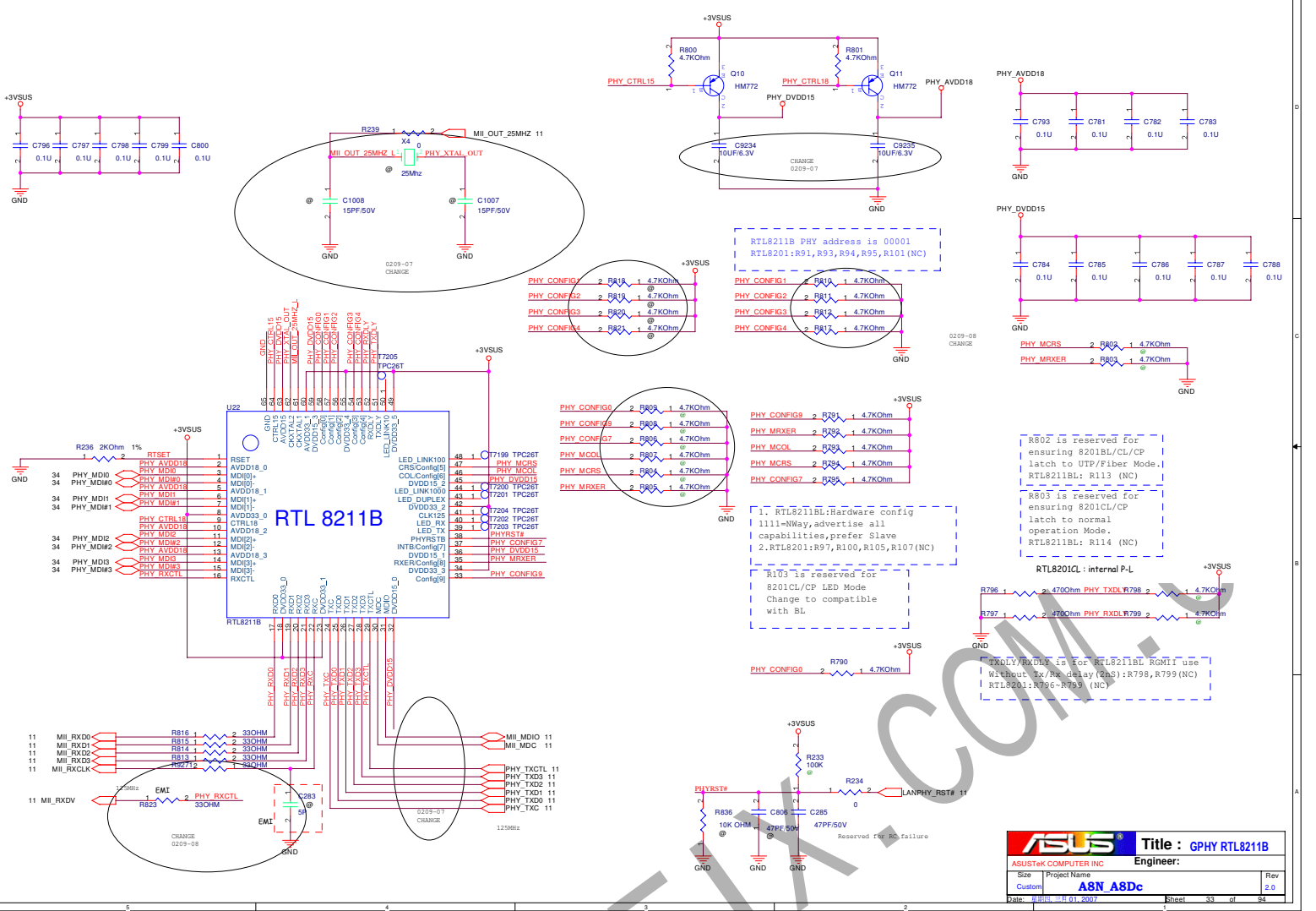
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		Title : BLANK	
ASUSTeK COMPUTER INC		Engineer:	
Size	Project Name	Rev	
A	A8N_A8Dc	0	
Date: 星期四, 三月 01, 2007		Sheet	32 of 94

<< Kennedy_Zhang >>

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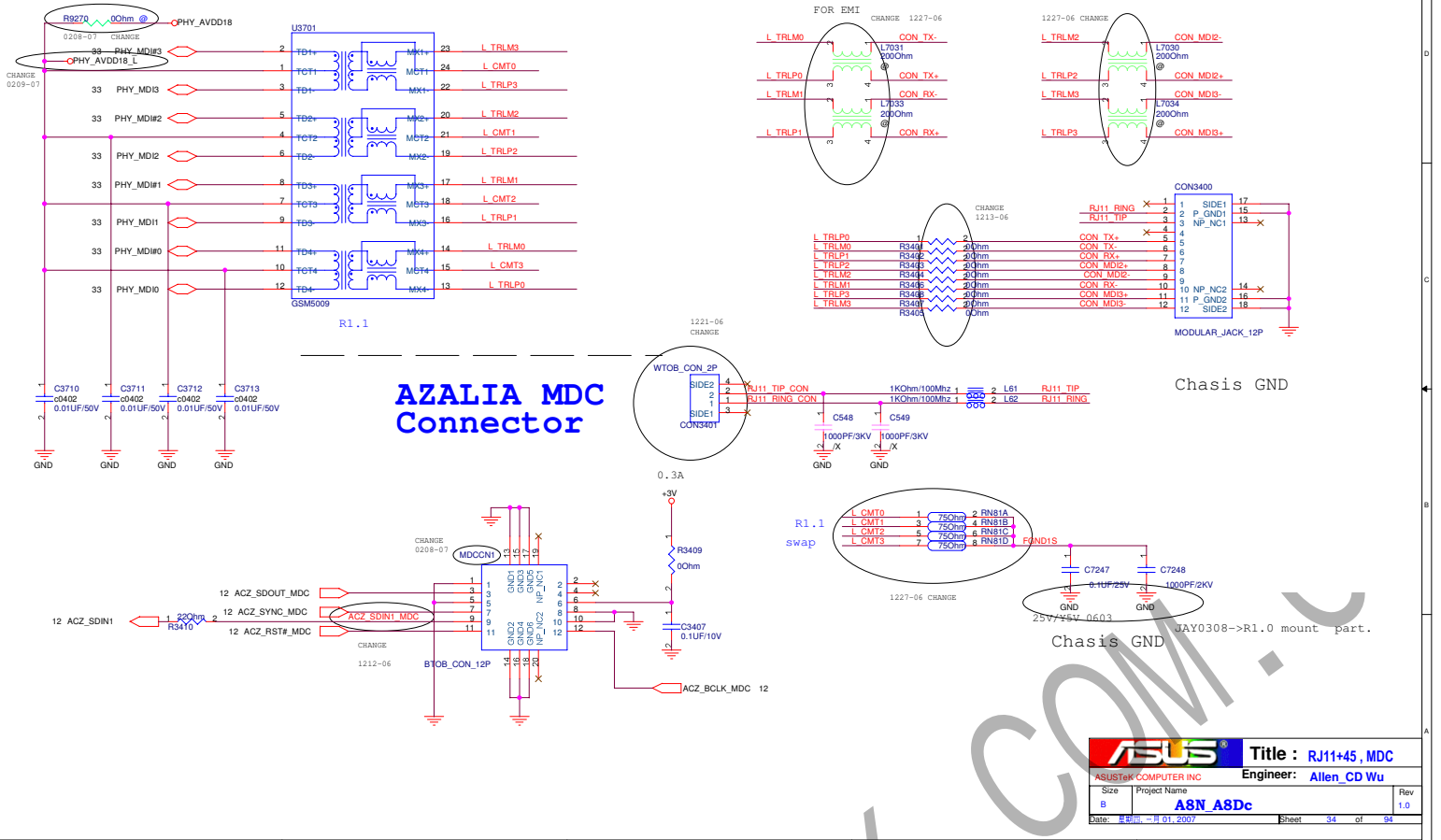


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		Title : GPHY RTL8211B	
ASUSTeK COMPUTER INC.		Engineer:	
Size:	Project Name:	Rev:	2.0
Custom:	A8N A8dc	Date:	01/01/2007
Sheet:	33	of:	34

LAN PORT




AZALIA MDC Connector

Size	Project Name	Rev
B	A8N_A8Dc	1.0
Date: #B#	-#F# 01.2007	Sheet 34 of 84

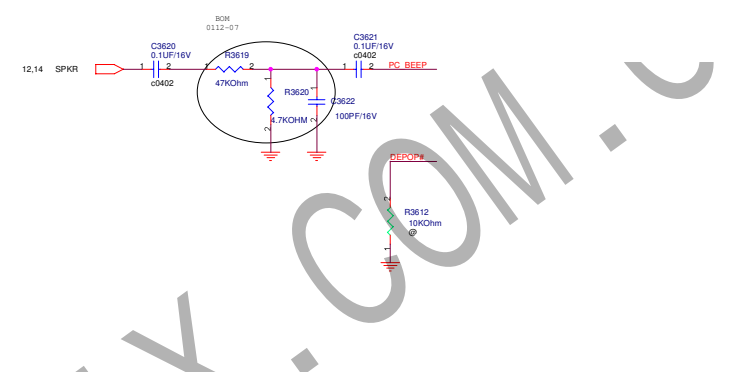
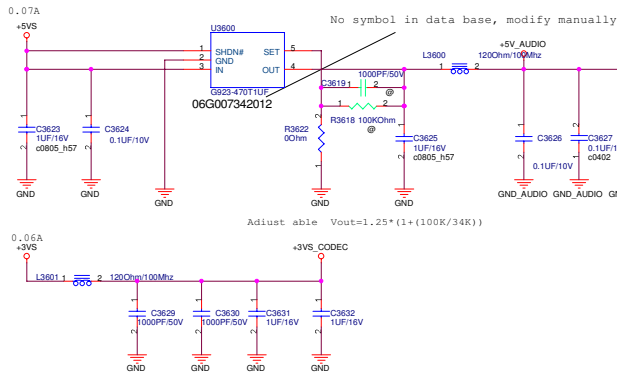
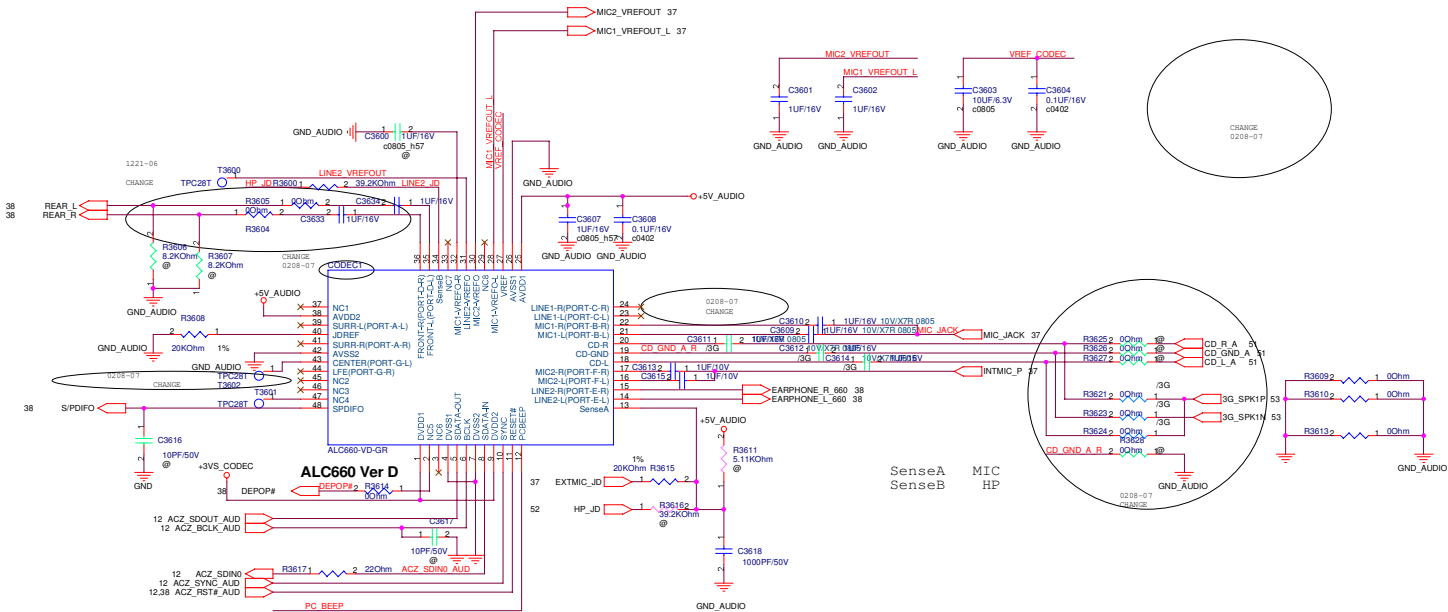
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		Title : BLANK	
ASUSTeK COMPUTER INC		Engineer:	
Size A	Project Name A8N_A8Dc	Rev 0	
Date: 星期四, 三月 01, 2007		Sheet 35	of 94

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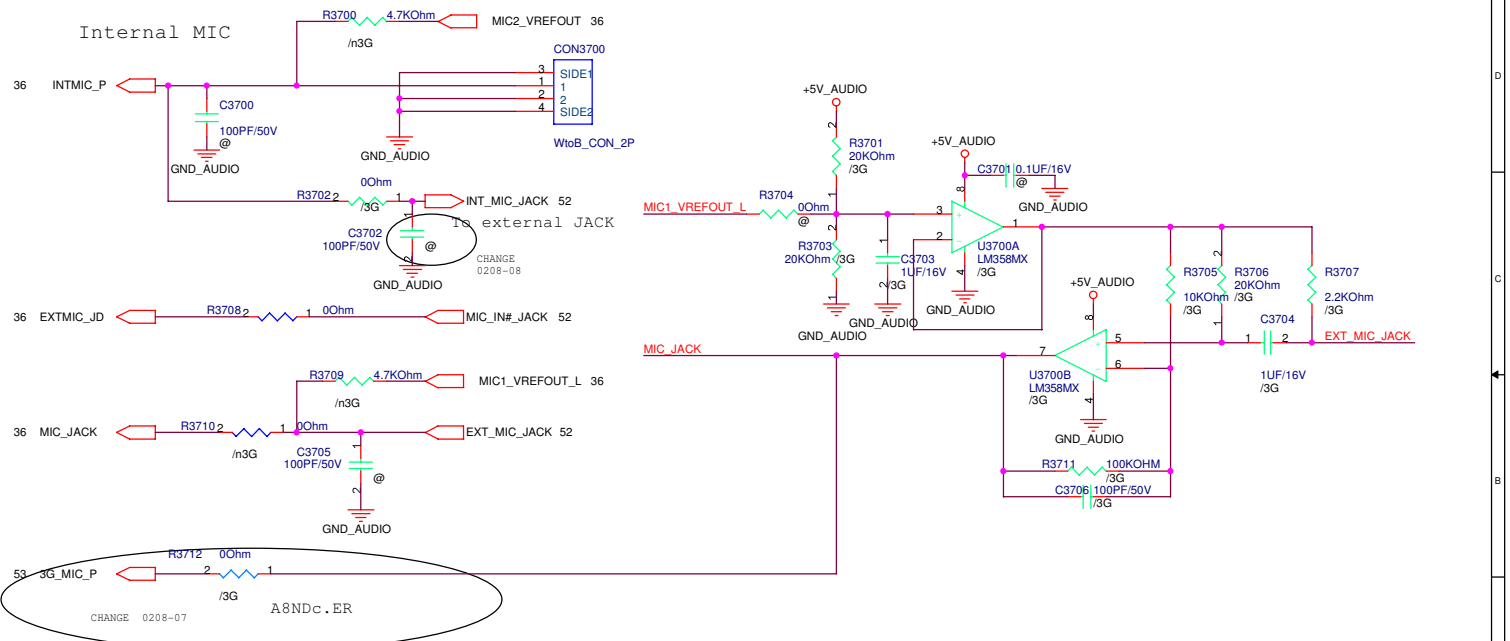
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ASUS		Title : CODEC-ALC660	
ASUSTek COMPUTER INC		Engineer: Allen_CD Wu	
Size	Project Name	Rev	
Custom	A8N_A8Dc	1.0	
Date:	2007-01-01, 2007	Sheet	38 of 84

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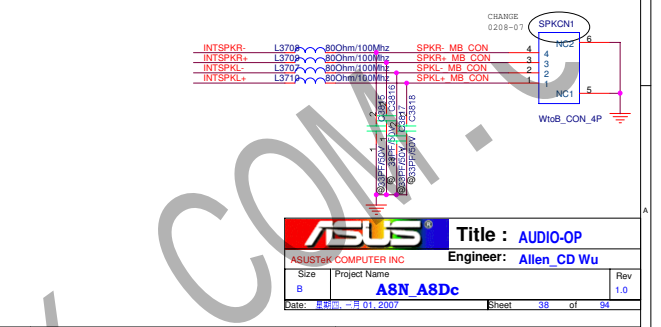
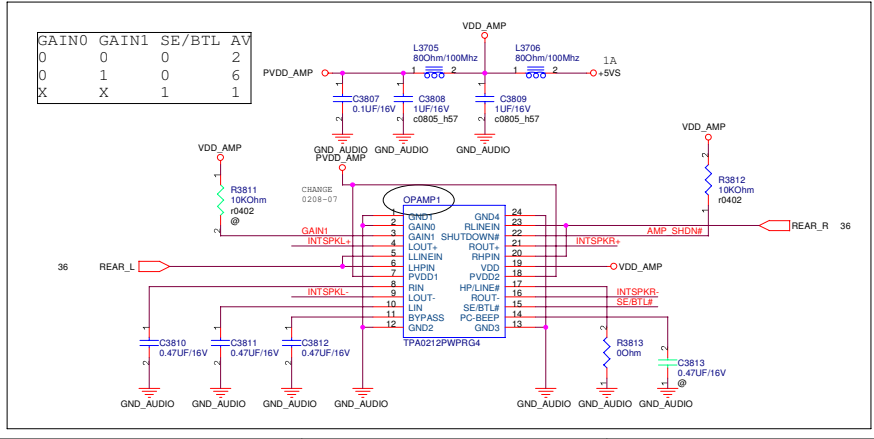
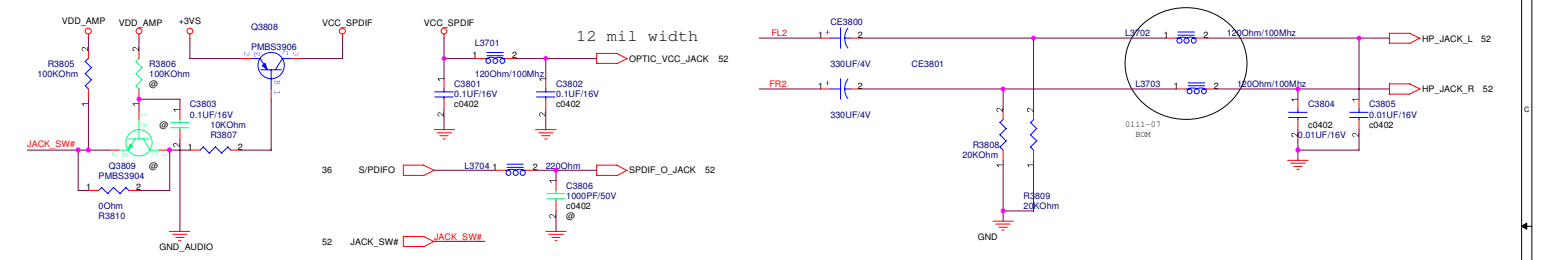
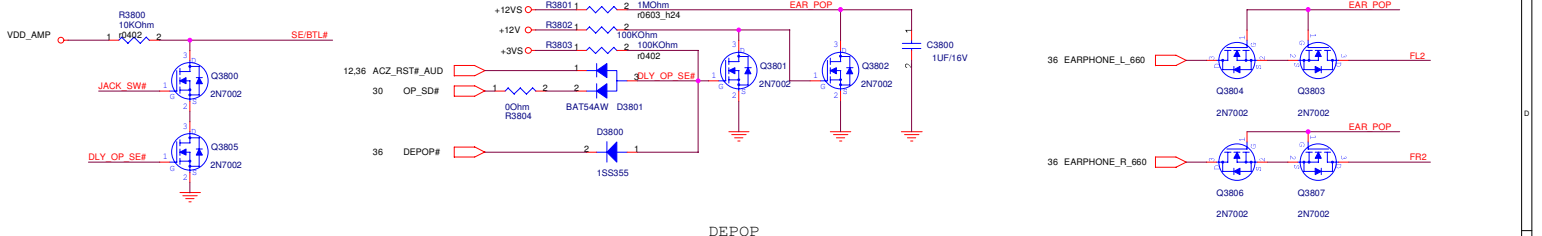
36 INTMIC_P
 36 EXTMIC_ID
 36 MIC_JACK
 53 3G_MIC_P

CHANGE 0208-07 A8NDc.ER

ASUS		Title : AUDIO-MIC
ASUSTeK COMPUTER INC		Engineer: Allen_CD Wu
Size Custom	Project Name A8N_A8Dc	Rev 1.0
Date: 星期三, 三月 01, 2007		Sheet 37 of 94

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ASUS		Title : AUDIO-OP	
ASUSTek COMPUTER INC		Engineer: Allen_CD Wu	
Size	Project Name		Rev
B	A8N_A8Dc		1.0
Date: # B100 - 11 01 2007	Sheet		38 of 84

<< Kennedy_Zhang >>

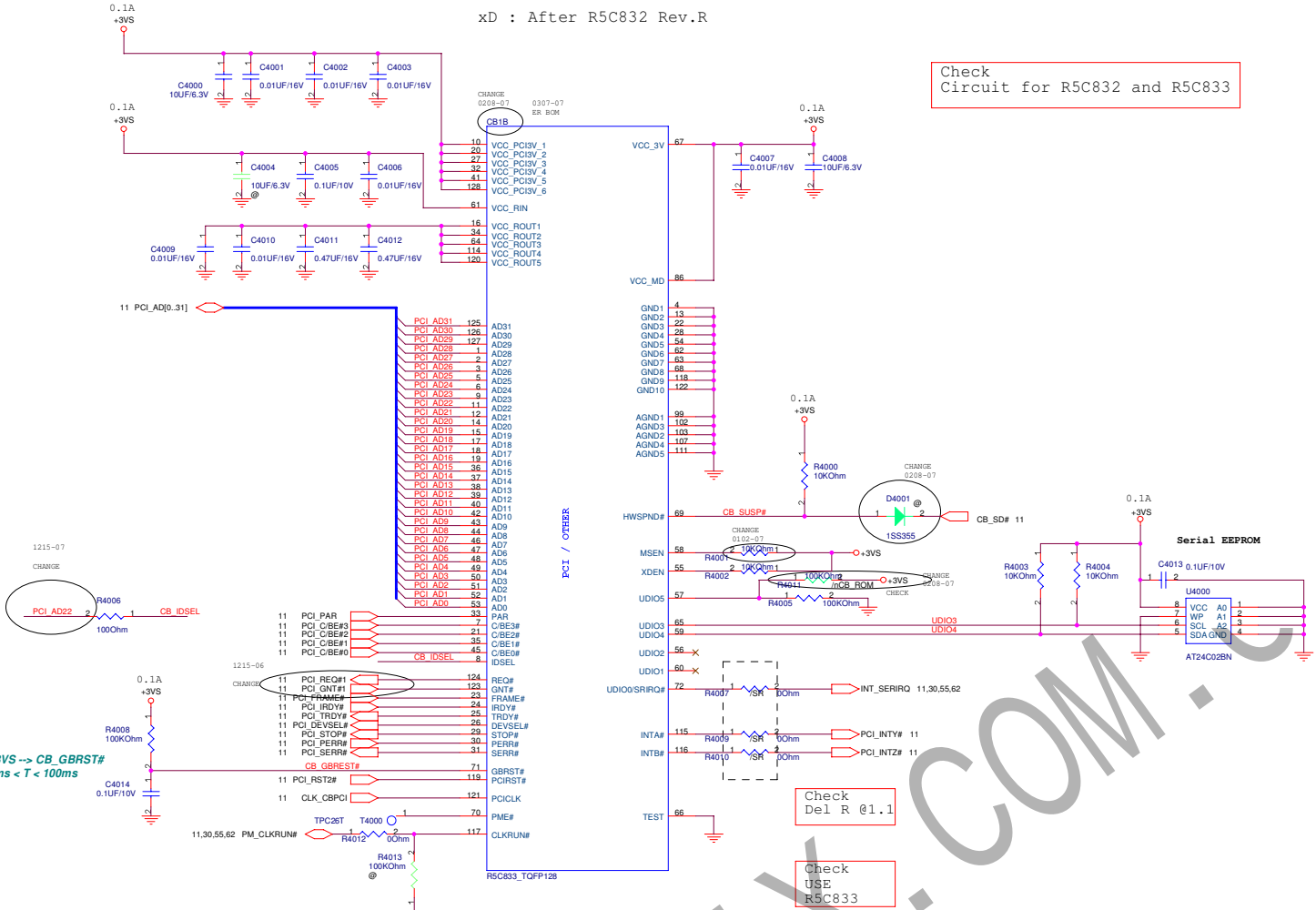
WWW.CHINAELIX.COM

		Title : BLANK	
ASUSTeK COMPUTER INC		Engineer:	
Size A	Project Name A8N_A8Dc	Rev 0	
Date: 星期四, 三月 01, 2007		Sheet	39 of 94

<< Kennedy_Zhang >>

WWW.CHINAHELIX.COM.CN

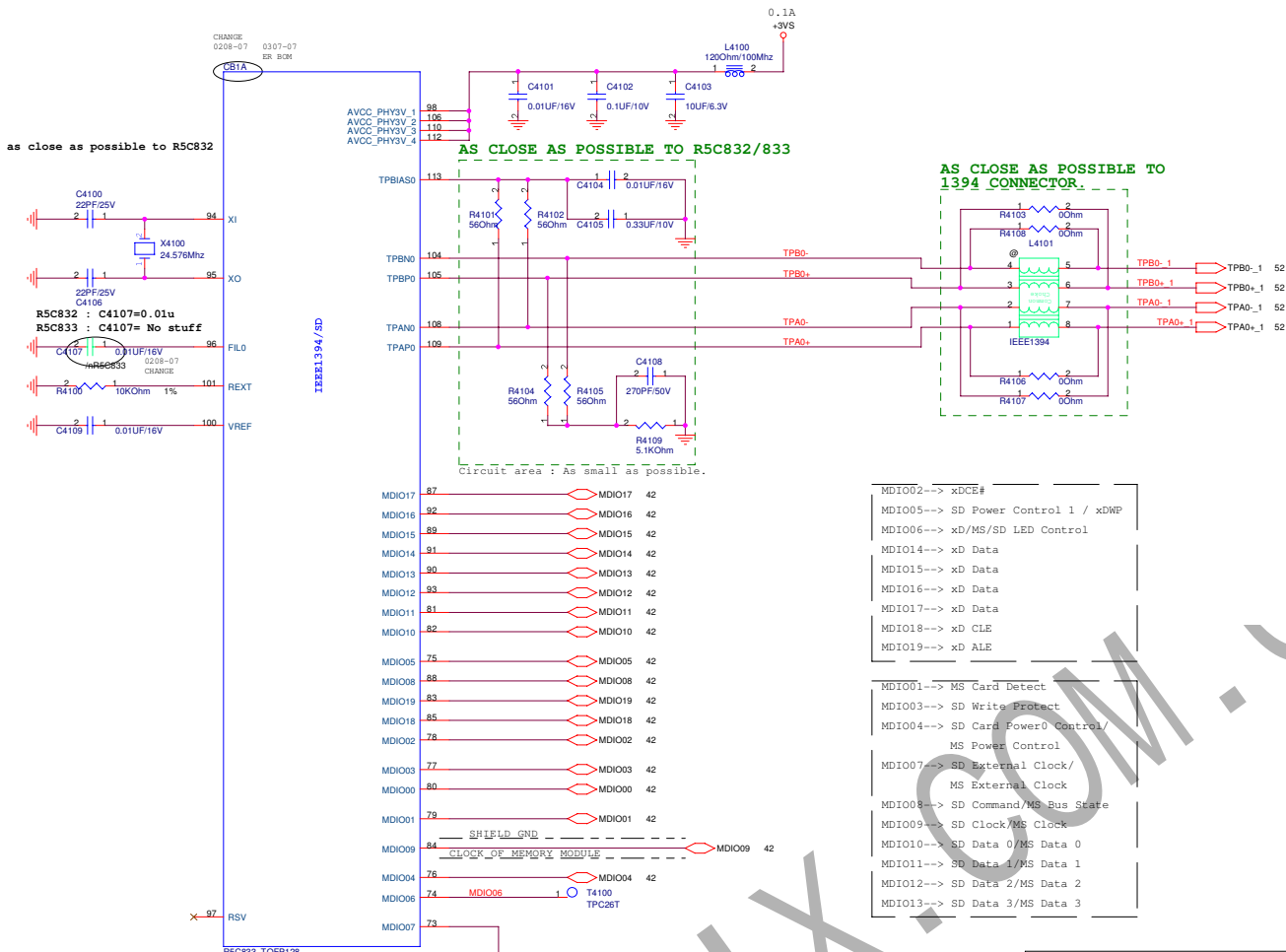
Check Circuit for R5C832 and R5C833



ASUS		Title : RICOH R5C832/PCI B	
ASUSTeK COMPUTER INC		Engineer: Allen_CD Wu	
Size	Project Name		Rev
Custom	A8N A8Dc		1.0
Date: 星期日 05.2007		Sheet	40 of 94

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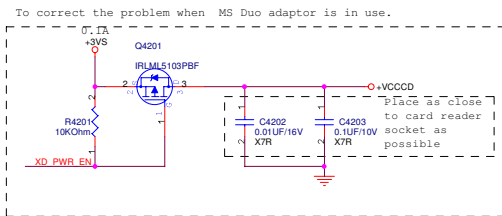
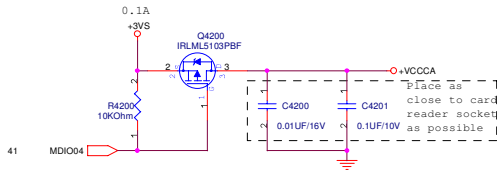
- MDIO17 --- MDIO17 42
- MDIO16 --- MDIO16 42
- MDIO15 --- MDIO15 42
- MDIO14 --- MDIO14 42
- MDIO13 --- MDIO13 42
- MDIO12 --- MDIO12 42
- MDIO11 --- MDIO11 42
- MDIO10 --- MDIO10 42
- MDIO09 --- MDIO09 42
- MDIO08 --- MDIO08 42
- MDIO07 --- MDIO07 42
- MDIO06 --- MDIO06 42
- MDIO05 --- MDIO05 42
- MDIO04 --- MDIO04 42
- MDIO03 --- MDIO03 42
- MDIO02 --- MDIO02 42
- MDIO01 --- MDIO01 42
- MDIO00 --- MDIO00 42
- MDIO09 --- MDIO09 42
- MDIO04 --- MDIO04 42
- MDIO06 --- MDIO06 42
- MDIO07 --- MDIO07 42

- MDIO02--> xDCE#
- MDIO05--> SD Power Control 1 / xDWP
- MDIO06--> xD/MS/SD LED Control
- MDIO14--> xD Data
- MDIO15--> xD Data
- MDIO16--> xD Data
- MDIO17--> xD Data
- MDIO18--> xD CLE
- MDIO19--> xD ALE
- MDIO01--> MS Card Detect
- MDIO03--> SD Write Protect
- MDIO04--> SD Card Power0 Control/MS Power Control
- MDIO07--> SD External Clock/MS External Clock
- MDIO08--> SD Command/MS Bus State
- MDIO09--> SD Clock/MS Clock
- MDIO10--> SD Data 0/MS Data 0
- MDIO11--> SD Data 1/MS Data 1
- MDIO12--> SD Data 2/MS Data 2
- MDIO13--> SD Data 3/MS Data 3

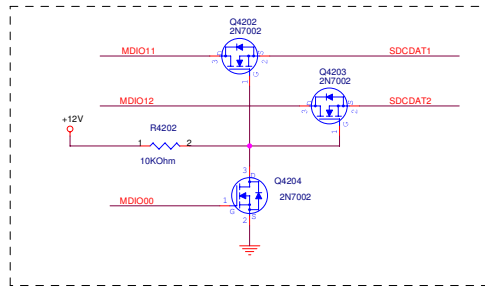
ASUS		Title : RICOH R5C832/PCI A	
ASUSTeK COMPUTER INC		Engineer: Allen_CD Wu	
Size	Project Name		Rev
Custom	A8N A8Dc		1.0
Date: 星期日 05/20/2007		Sheet: 41	of 94

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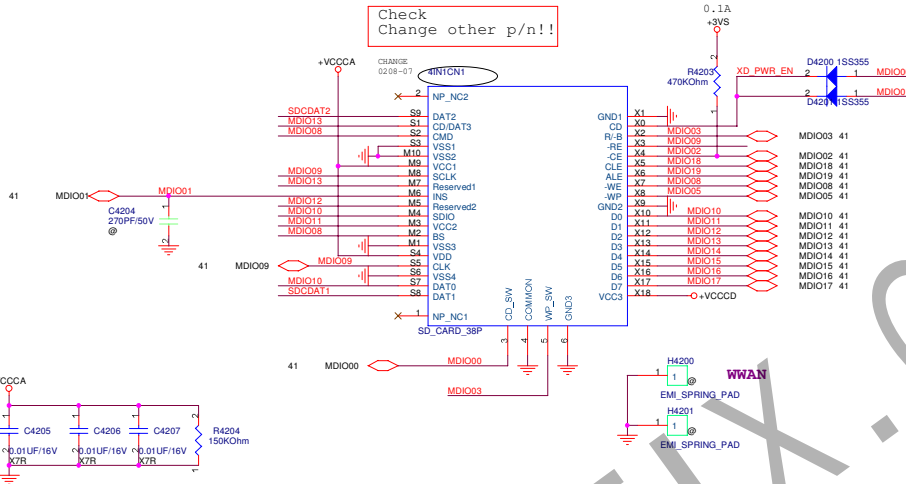


Solve MS Duo Adaptor short issue.



MDIO00-->	SD Card Detect
MDIO01-->	MS Card Detect
MDIO03-->	SD Write Protect
MDIO04-->	SD Card Power0 Control/MS Power Control
MDIO08-->	SD Command/MS Bus State
MDIO09-->	SD Clock/MS Clock
MDIO10-->	SD Data 0/MS Data 0
MDIO11-->	SD Data 1/MS Data 1
MDIO12-->	SD Data 2/MS Data 2
MDIO13-->	SD Data 3/MS Data 3

Check other p/n!!
Change other p/n!!



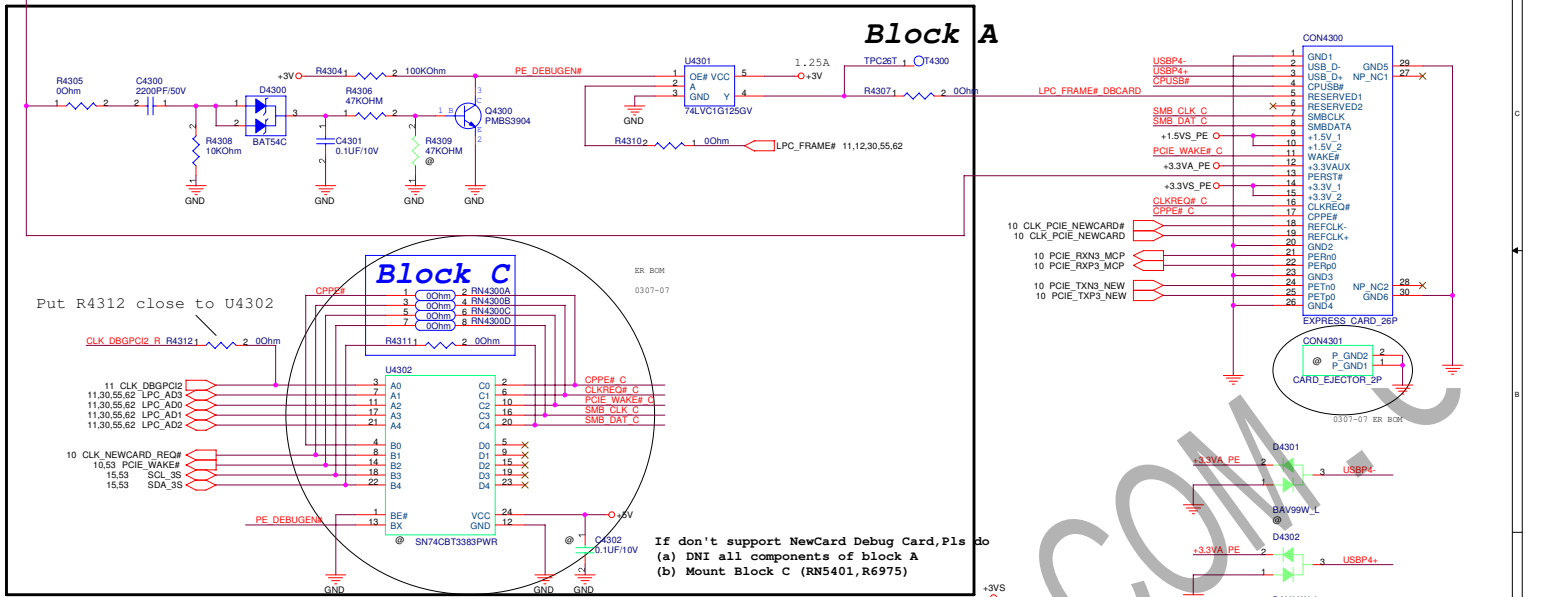
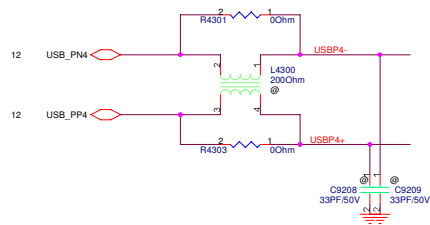
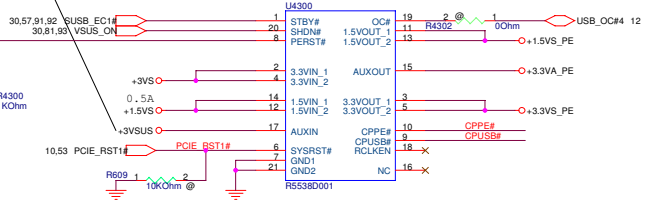
MDIO02-->	xDCE#
MDIO05-->	SD Power Control 1 / xDWP
MDIO06-->	xD/MS/SD LED Control
MDIO14-->	xD Data
MDIO15-->	xD Data
MDIO16-->	xD Data
MDIO17-->	xD Data
MDIO18-->	xD CLE
MDIO19-->	xD ALE

ASUS		Title : CardReader
ASUSTek COMPUTER INC		Engineer : Allen_CD Wu
Size	Project Name	Rev
Custom	A8N A8Dc	1.0
Date: 2007.01.01		Sheet 42 of 94

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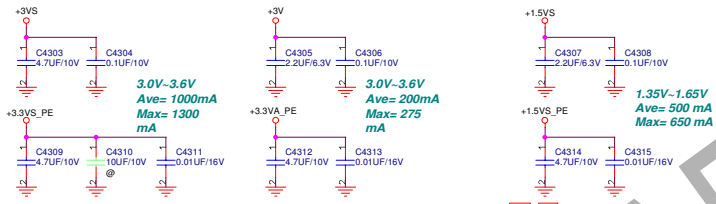
WWW.CHINAFIX.COM

Check +3V



Put R4312 close to U4302

If don't support NewCard Debug Card, Pls do
 (a) DNI all components of block A
 (b) Mount Block C (RN5401, R6975)



ASUS Title : Express Card
 ASUSTeK COMPUTER INC. Engineer: Allen_CD Wu
 Size Project Name
 Custom A8N A8Dc Rev 1.0
 Date: 01/05/2007 Sheet 43 of 94


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5 4 3 2 1

D
C
B
A

D
C
B
A

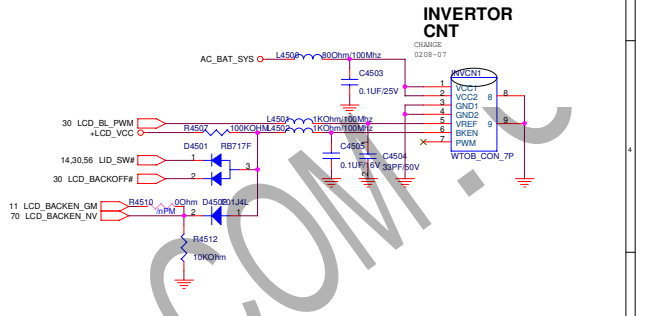
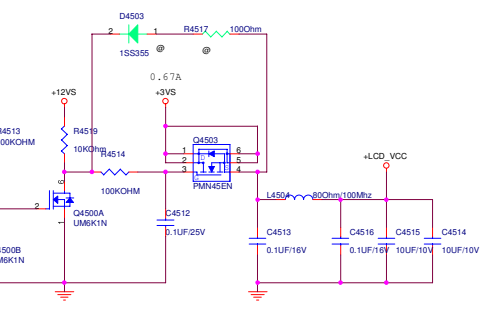
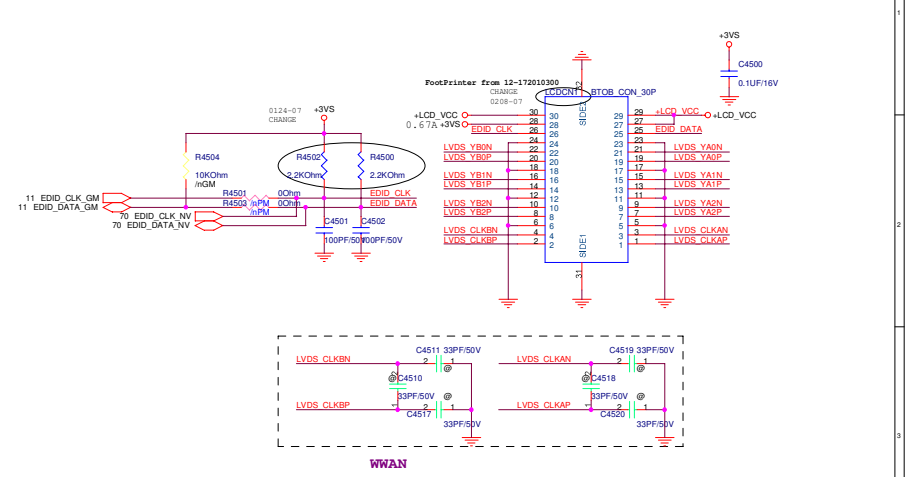
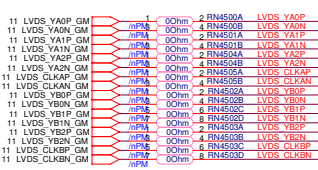
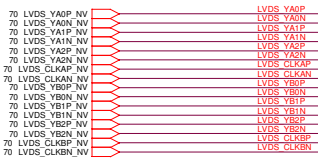
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ASUSTeK COMPUTER INC		Engineer:
Size	Project Name	Rev
A	A8N A8Dc	1.0
Date: 星期四, 三月 01, 2007		Sheet 44 of 94

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Check stub

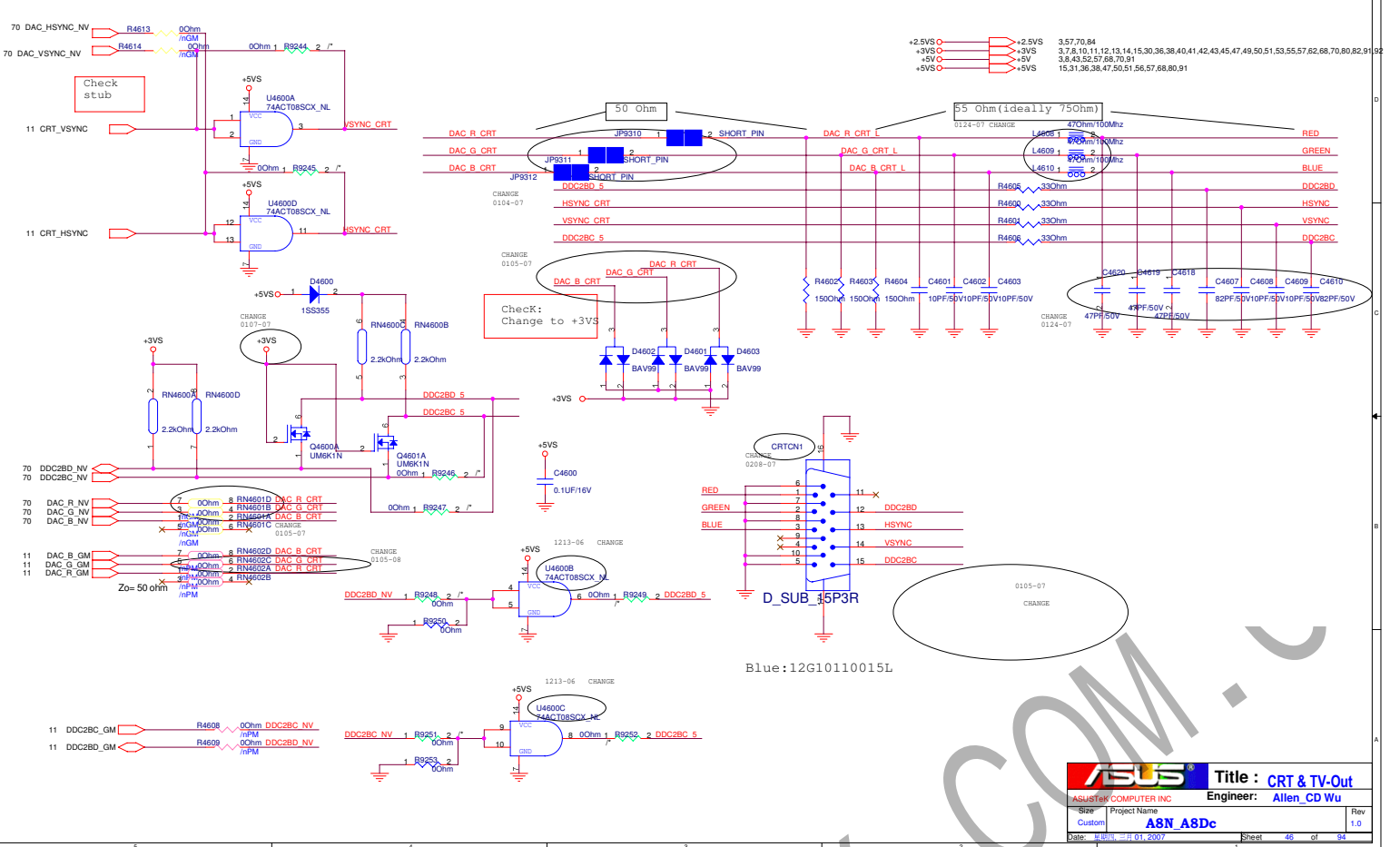
+3VS 3,7,8,10,11,12,13,14,15,30,36,38,40,41,42,43,46,47,49,50,51,53,55,57,62,68,70,80,82,84,85
 +3VSUS 3,10,11,12,13,14,30,33,43,53,57,81,82
 +12VS 86,87,70,91



ASUS		Title : LVDS & Inverter
ASUSTeK COMPUTER INC		Engineer: Allen_CD Wu
Size	Project Name	Rev
Custom	A8N_A8Dc	1.0
Date: 08/08/2007	Sheet	45 of 94

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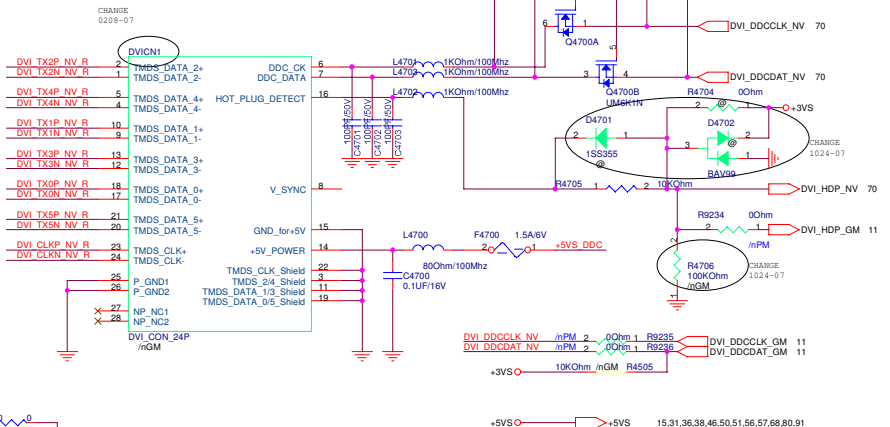
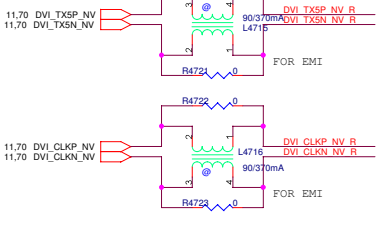
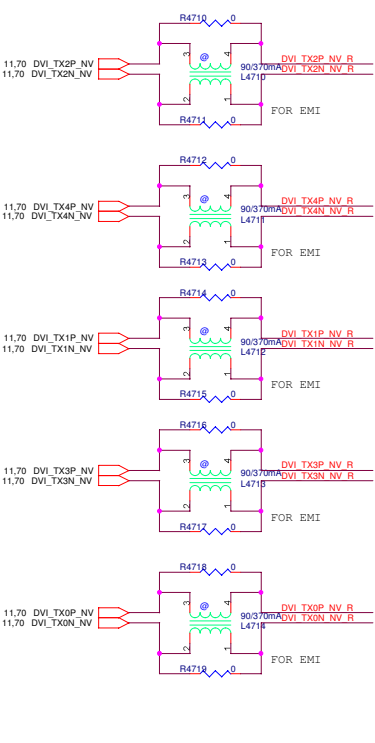


Blue:12G10110015L

ASUS		Title : CRT & TV-Out
ASUSTek COMPUTER INC	Engineer: Allen_CD Wu	
Size: Custom	Project Name: A8N_A8Dc	Rev: 1.0
Date: 2007.01.01.2007	Sheet: 46	of 94

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ASUS		Title : DVI	
ASUSTeK COMPUTER INC		Engineer: Allen_CD Wu	
Size	Project Name	Rev	
B	A8N_A8Dc	1.0	
Date: 11/01/2007	Sheet 47	of 94	

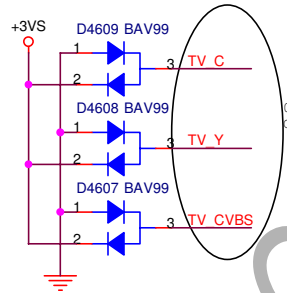
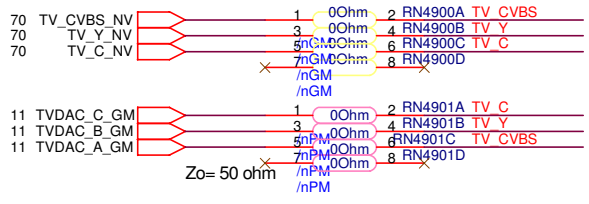
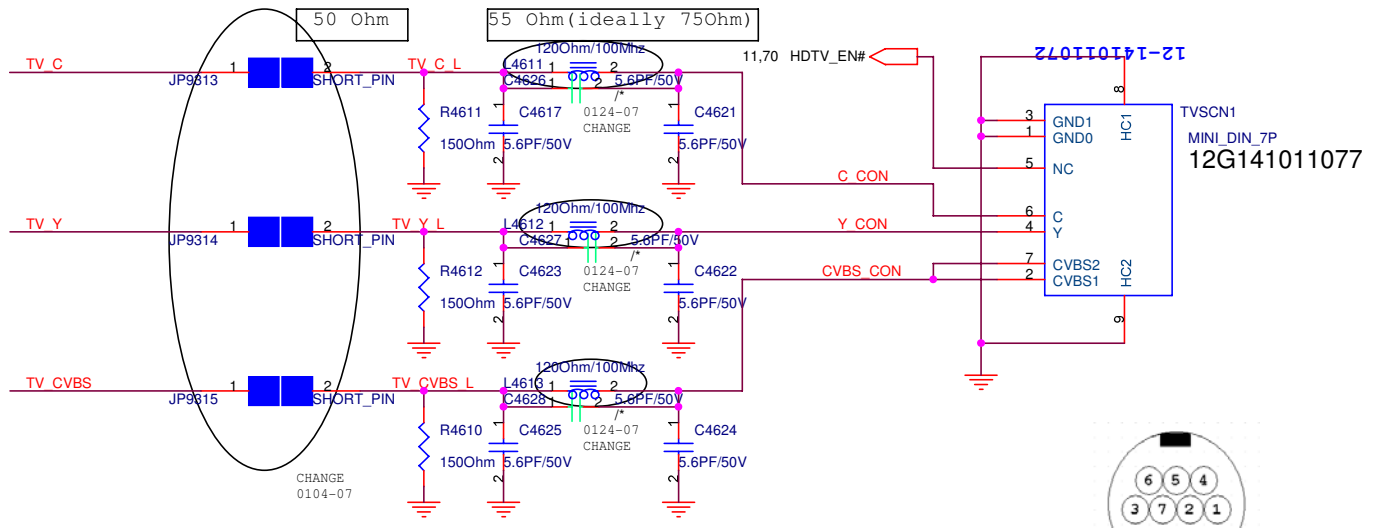
<< Kennedy_Zhang >>

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		Title : BLANK	
ASUSTeK COMPUTER INC		Engineer:	
Size	Project Name		Rev
A	A8N_A8Dc		0
Date: 星期四, 三月 01, 2007		Sheet	48 of 94

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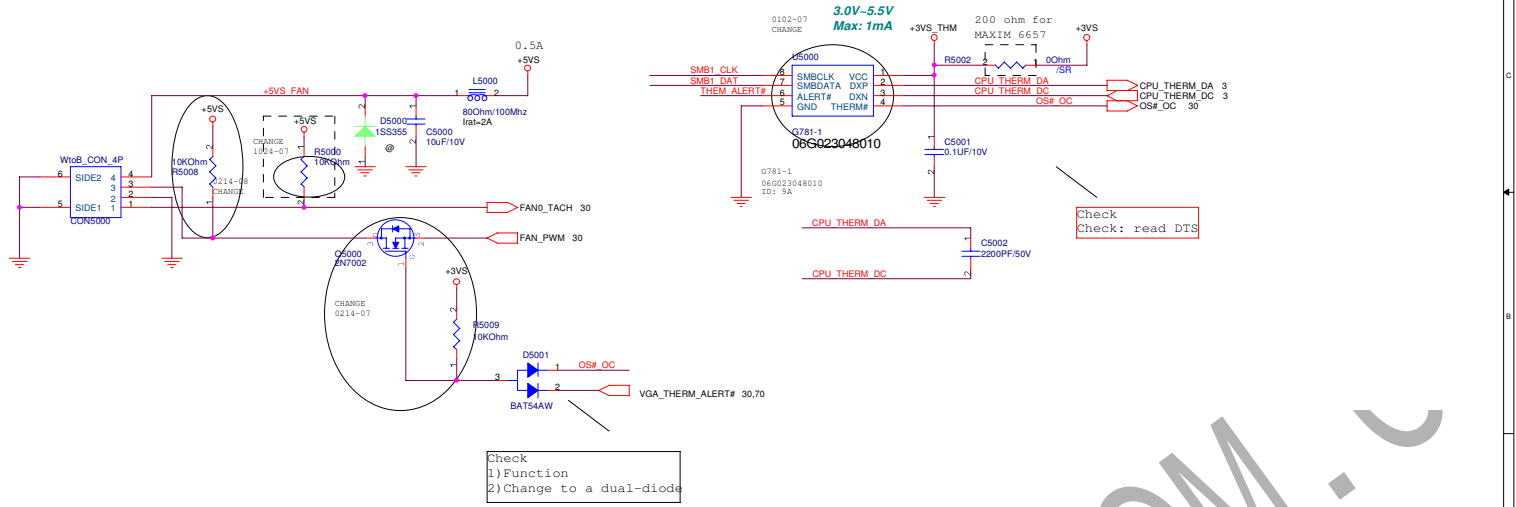


ASUS		Title : TV	
ASUSTeK COMPUTER INC		Engineer: Allen_CD Wu	
Size A	Project Name A8N_A8Dc	Date: 星期四, 三月 01, 2007	Rev 1.0
Date: 星期四, 三月 01, 2007		Sheet 49 of 94	

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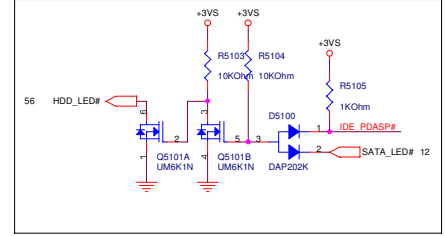
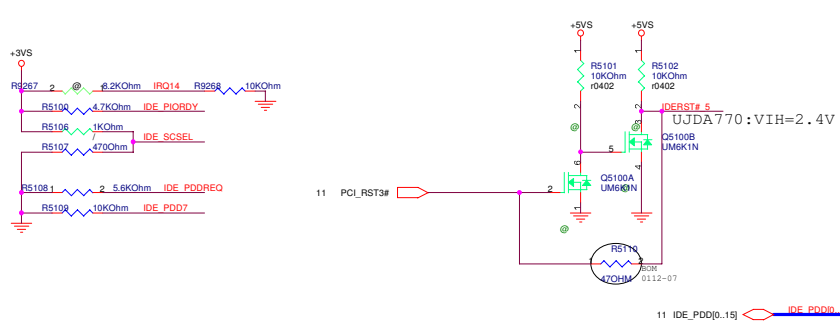
FAN & THERMAL CONTROLLER



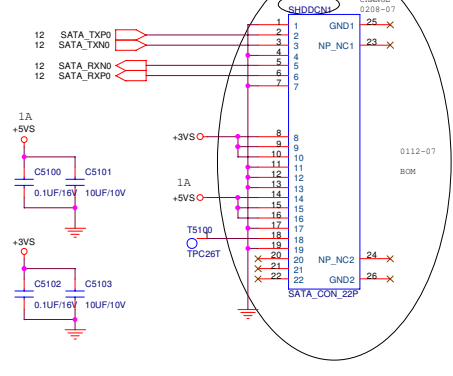
ASUS		Title : FAN & THERMAL	
ASUSTek COMPUTER INC		Engineer: Allen_CD Wu	
Size	Project Name	Rev	
B	A8N_A8Dc	1.0	
Date: 11/01/2007	Sheet 50	of 94	

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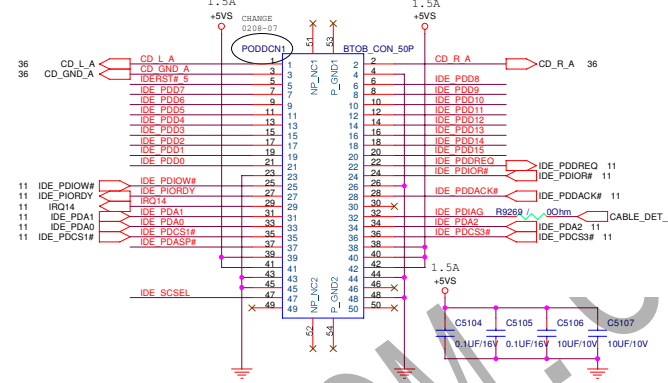
WWW.CHINAPEIX.COM



SATA HDD CON



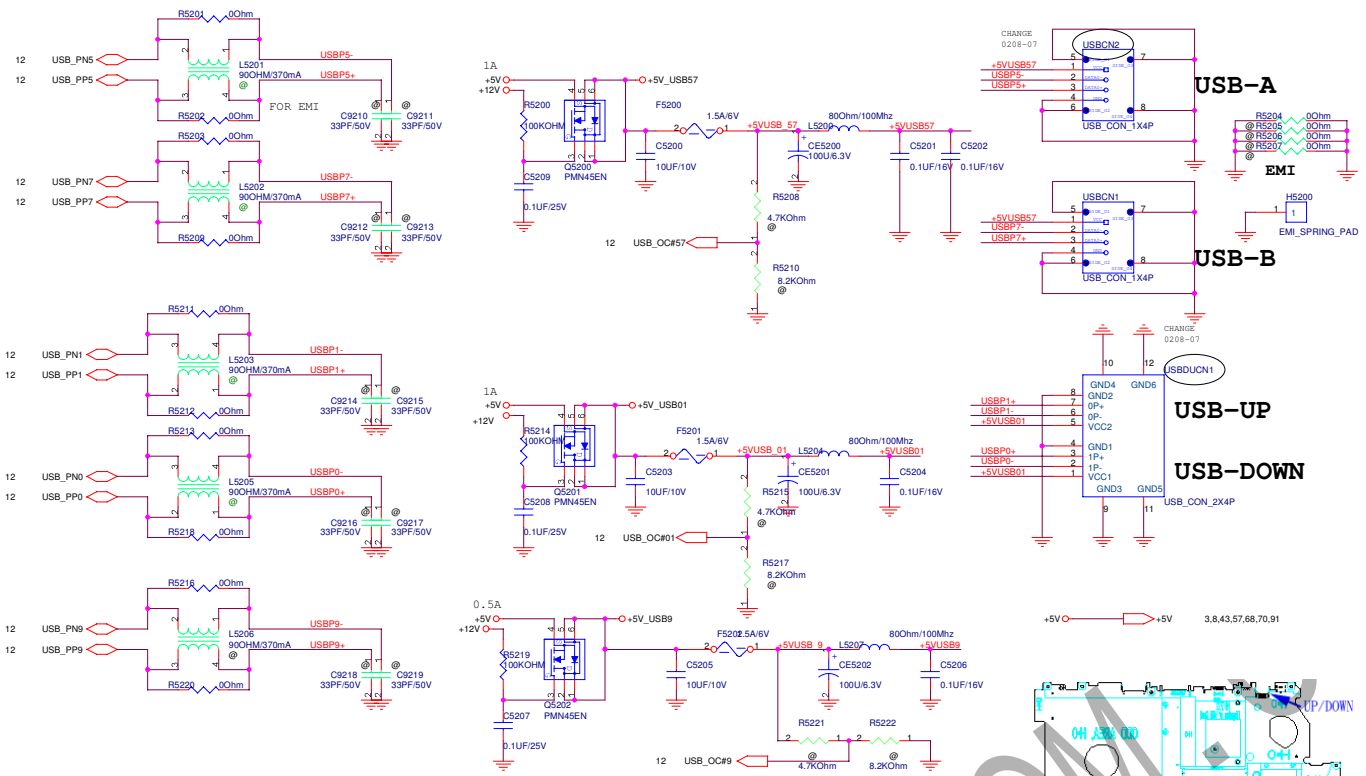
PATA CD-ROM CON



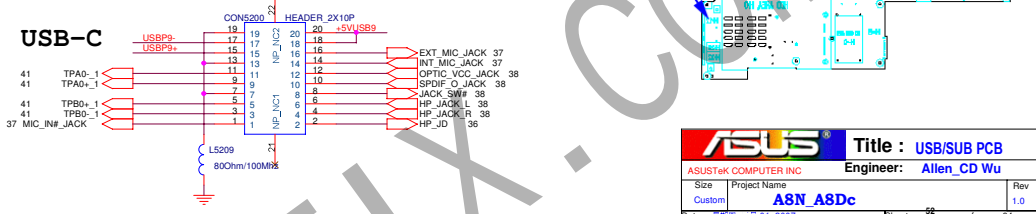
+3VS ○ → +3VS 3,7,8,10,11,12,13,14,15,30,36,38,40,41,42,43,45,46,47,49,50,53,55,57,62,68,70,80,82,91,92
+5VS ○ → +5VS 15,31,36,38,46,47,50,56,57,68,80,91
+5V ○ → +5V 3,8,43,52,57,68,70,91

ASUS		Title : HDD & ODD	
ASUSTeK COMPUTER INC		Engineer: Allen_CD Wu	
Size	Project Name		Rev
B	A8N_A8Dc		1.0
Date: # 01/01/2007		Sheet #1 of 94	

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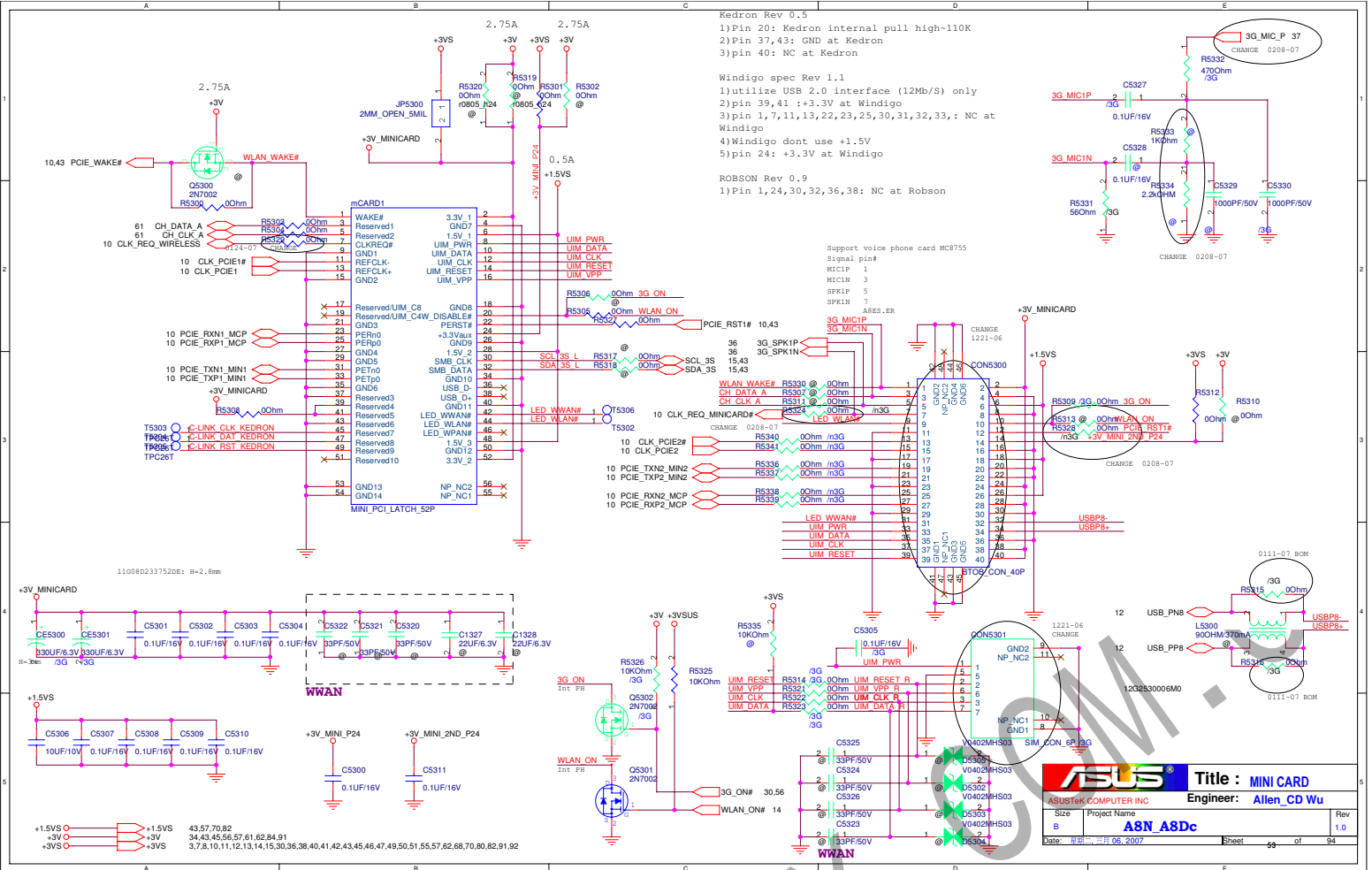


SUB-PCB: USB/1394/MIC/EARPHONE



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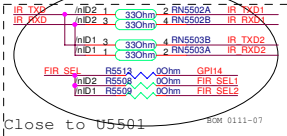
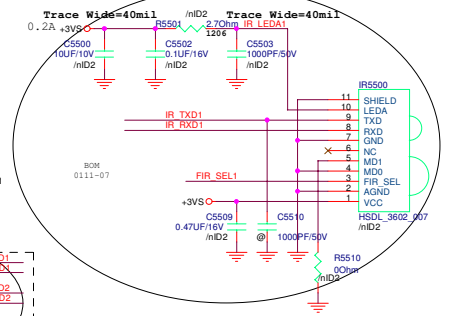
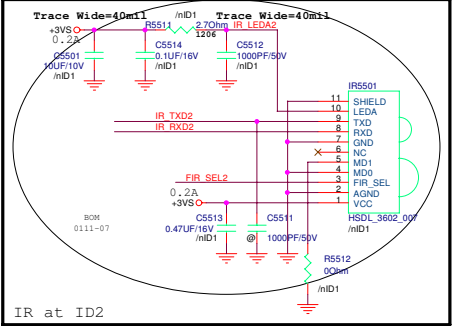
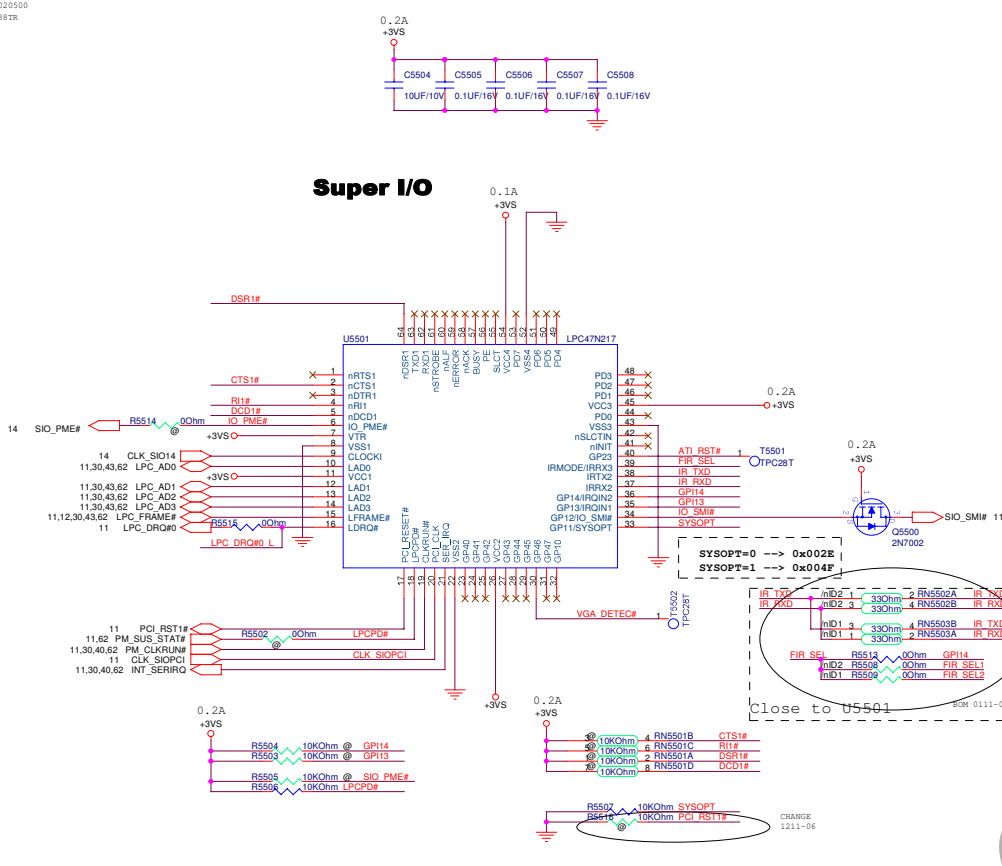
ASUS Title: **MINI CARD**
 ASUSTek COMPUTER INC Engineer: **Allen_CD Wu**
 Project Name: **A8N A8Dc**
 Date: 06/06/2007 Sheet 59 of 94

		Title : BLANK	
ASUSTek COMPUTER INC		Engineer:	
Size	Project Name	Rev	
Custom	A8N_A8Dc	0	
Date: 11/21/2007		Sheet 54 of 54	

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Super I/O



Transceiver Control Truth Table

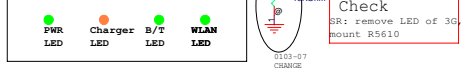
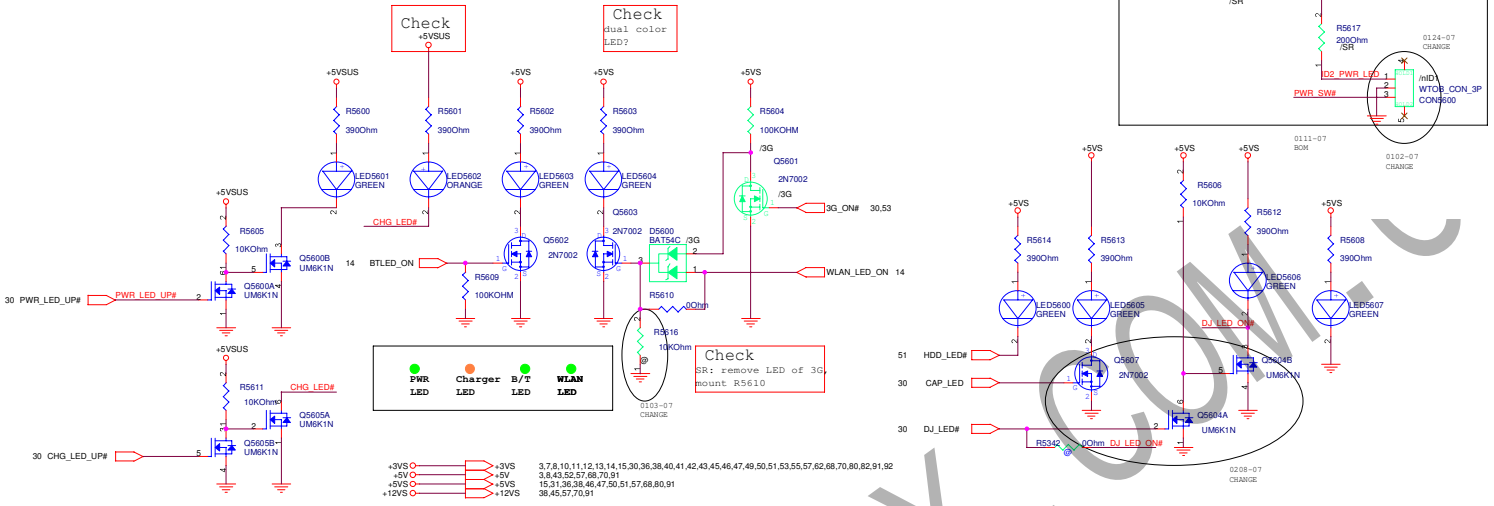
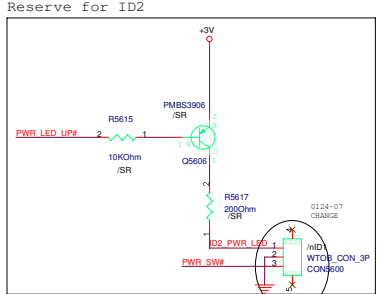
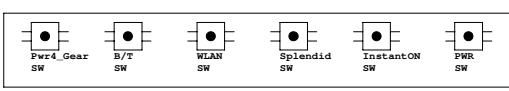
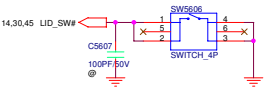
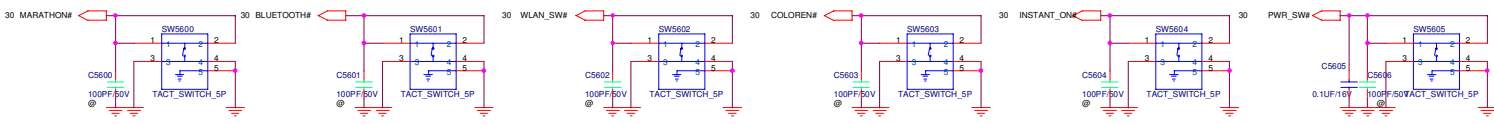
Mode 0	Mode 1	FIR_SEL	RX Function	TX Function
1	0	X	Shutdown	Shutdown
0	0	0	SIR	Full Distance Power
0	1	0	SIR	2/3 Distance Power
1	1	0	SIR	1/3 Distance Power
0	0	1	MIR/FIR	Full Distance Power
0	1	1	MIR/FIR	2/3 Distance Power
1	1	1	MIR/FIR	1/3 Distance Power

ASUS Title : IrDA
ASUSTeK COMPUTER INC Engineer: Allen_CD Wu

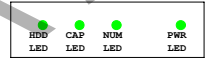
Size B	Project Name A8N_A8Dc	Rev 1.0
Date: 11/01/2007	Sheet 55 of 94	

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+3VS	3,7,8,10,11,12,13,14,15,30,36,38,40,41,42,43,45,46,47,49,50,51,53,55,57,62,68,70,80,82,91,92
+5V	3,8,43,52,57,68,70,91
+9VS	15,31,36,38,40,47,50,51,57,68,80,91
+12VS	38,45,57,70,91

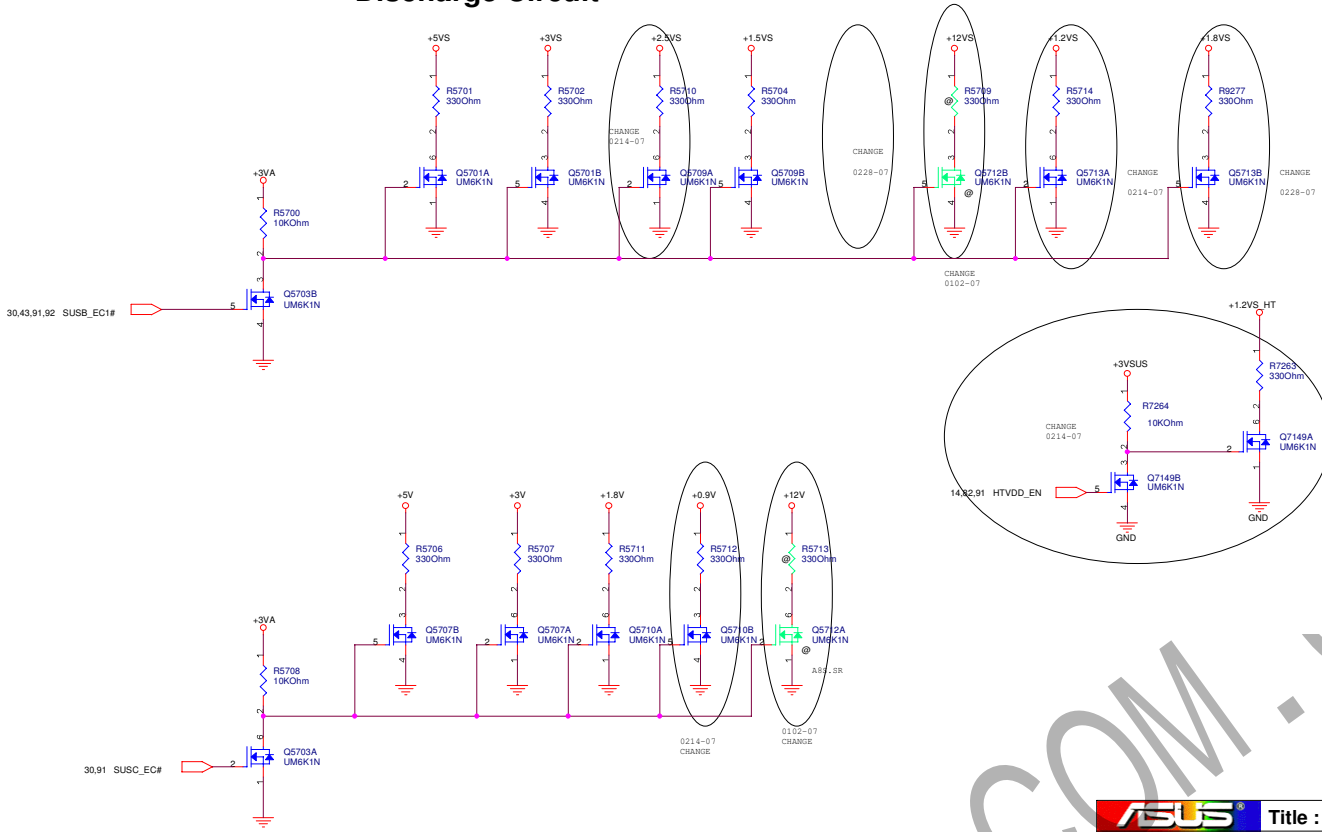


ASUS Title : LED/Instant key
 ASUSTeK COMPUTER INC Engineer: Allen_CD Wu
 Size Project Name
 Custom A8N A8Dc
 Date: 11/01/2007 Sheet 56 of 84

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Discharge Circuit




		Title : Discharge	
ASUSTeK COMPUTER INC		Engineer: Allen_CD Wu	
Size	Project Name	Rev	
B	ASN_A8Dc	1.0	
Date: # 11/01/2007	Sheet	67	of 94

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<Variant Name>

		Title :	
ASUSTeK COMPUTER INC		Engineer:	
Size A	Project Name A8N_A8Dc	Rev 1.0	
Date: 星期四, 三月 01, 2007		Sheet	of 94

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5

4

3

2

1

D

D

C

C

B

B

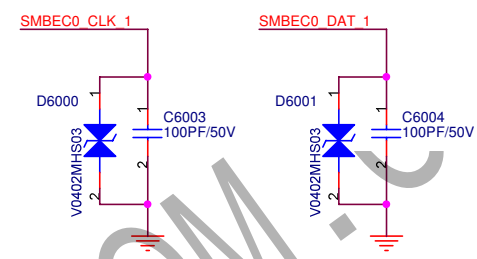
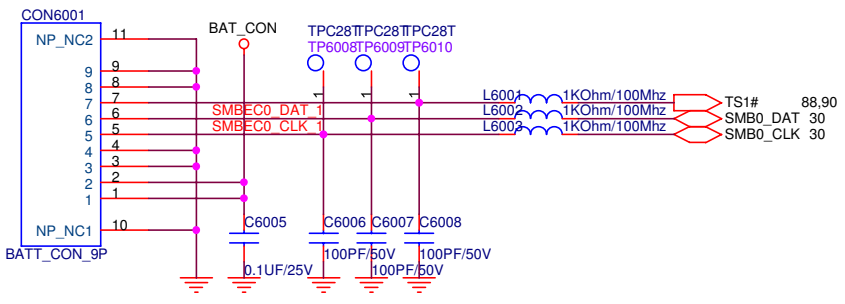
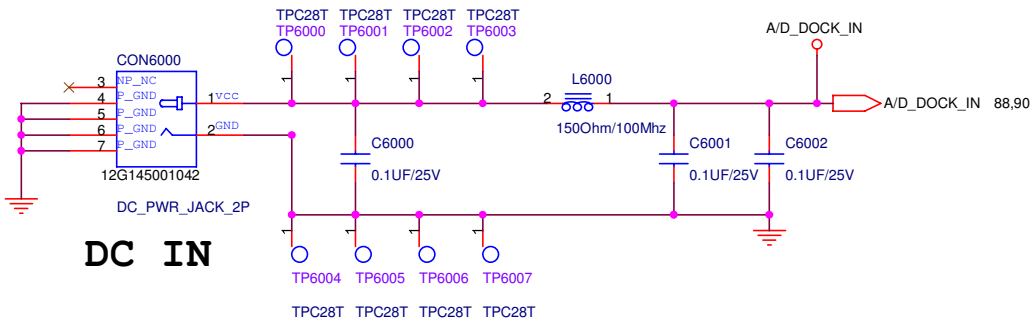
A

A

Title		
<Title>		
Size	Document Number	Rev
A	A8N_A8Dc	1.1
Date: 星期四, 三月 01, 2007		Sheet 59 of 94

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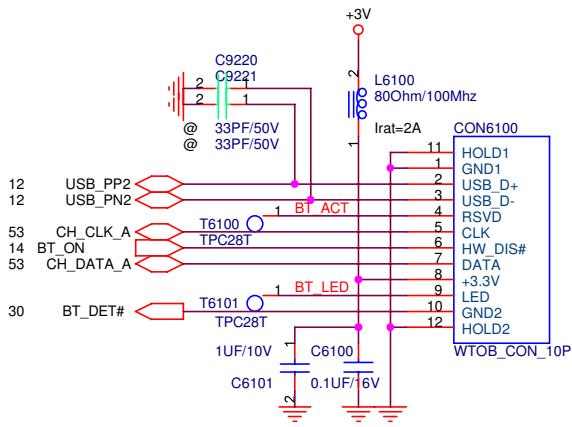
WWW.CHINAPELTY.COM.CN



		Title : DC IN / BAT	
ASUSTeK COMPUTER INC		Engineer: Allen_CD Wu	
Size	Project Name		Rev
A	A8N_A8Dc		1.0
Date: 星期四, 三月 01, 2007		Sheet 60	of 94

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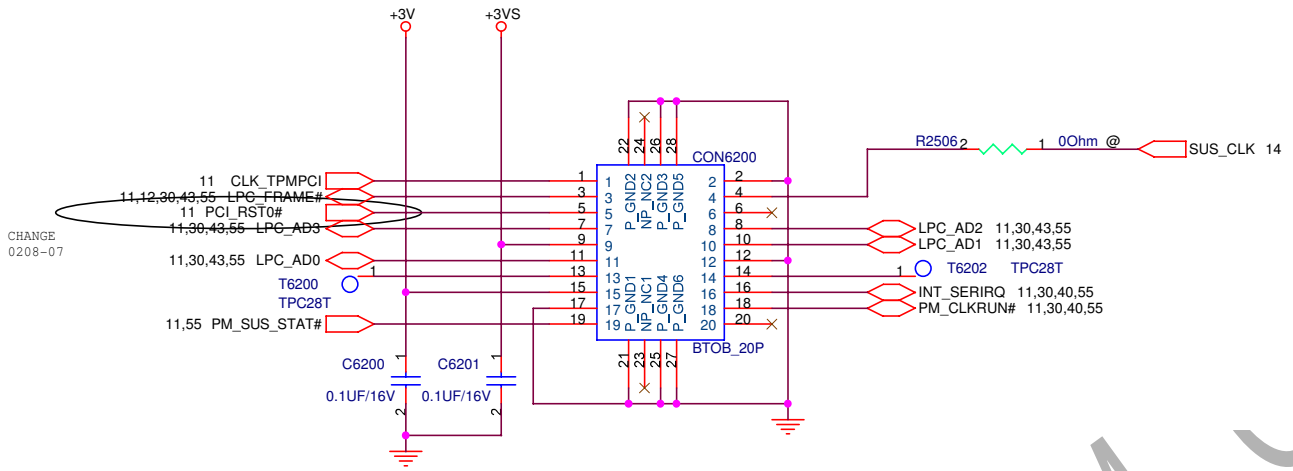
Bluetooth Module CON

		Title :BT/CAMERA	
ASUSTek COMPUTER INC		Engineer: Allen_CD Wu	
Size	Project Name		Rev
A	A8N_A8Dc		1.0
Date: 星期四, 三月 01, 2007		Sheet	61 of 94

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TPM 1.2 Module



		Title :TPM	
ASUSTeK COMPUTER INC		Engineer: Allen_CD Wu	
Size A	Project Name A8N_A8Dc	Rev 1.0	
Date: 星期四, 三月 01, 2007		Sheet 62 of 94	

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		Title : BLANK	
ASUSTeK COMPUTER INC		Engineer:	
Size	Project Name	Rev	
A	A8N_A8Dc	0	
Date: 星期四, 三月 01, 2007		Sheet	63 of 94

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PCI Device	IDSEL#	REQ/GNT#	Interrupts
CARDBUS	AD22 <small>CHANGE 1215-06</small>	1	Y, Z

BOM option

nGM:yellow
nPM:light red
=====
nGM :no stuff for A8N
nPM :no stuff for A8Dc
nGM1:no stuff for A8N, A8N/SR mount for debugging A8S in advance
3G :for Windigo, SR mount for debugging
SR :for SR debugging, should be removed or no stuff @ ER

SM-Bus Device	SM-Bus Address
SO-DIMM 0 (low)	
SPD	A0
SO-DIMM 1(high)	
SPD	A4
G781-1	9A
G781(VGA board)	98

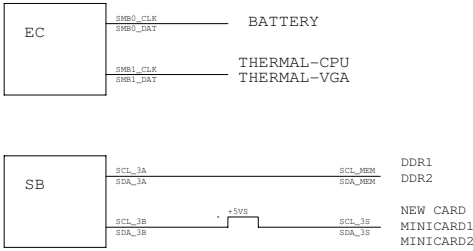
Support ID2:

Support ID2:
page 68, Finger print
page 55, IR
Page 56, pwr switch and LED

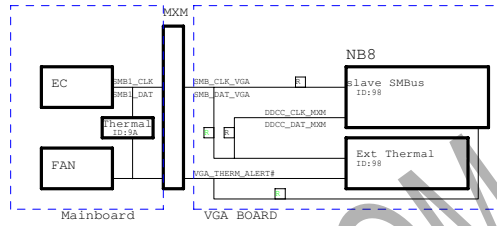
CHANGE 0102-07

Thermal Sensor (CPU)

SM-Bus Mapping 1001100



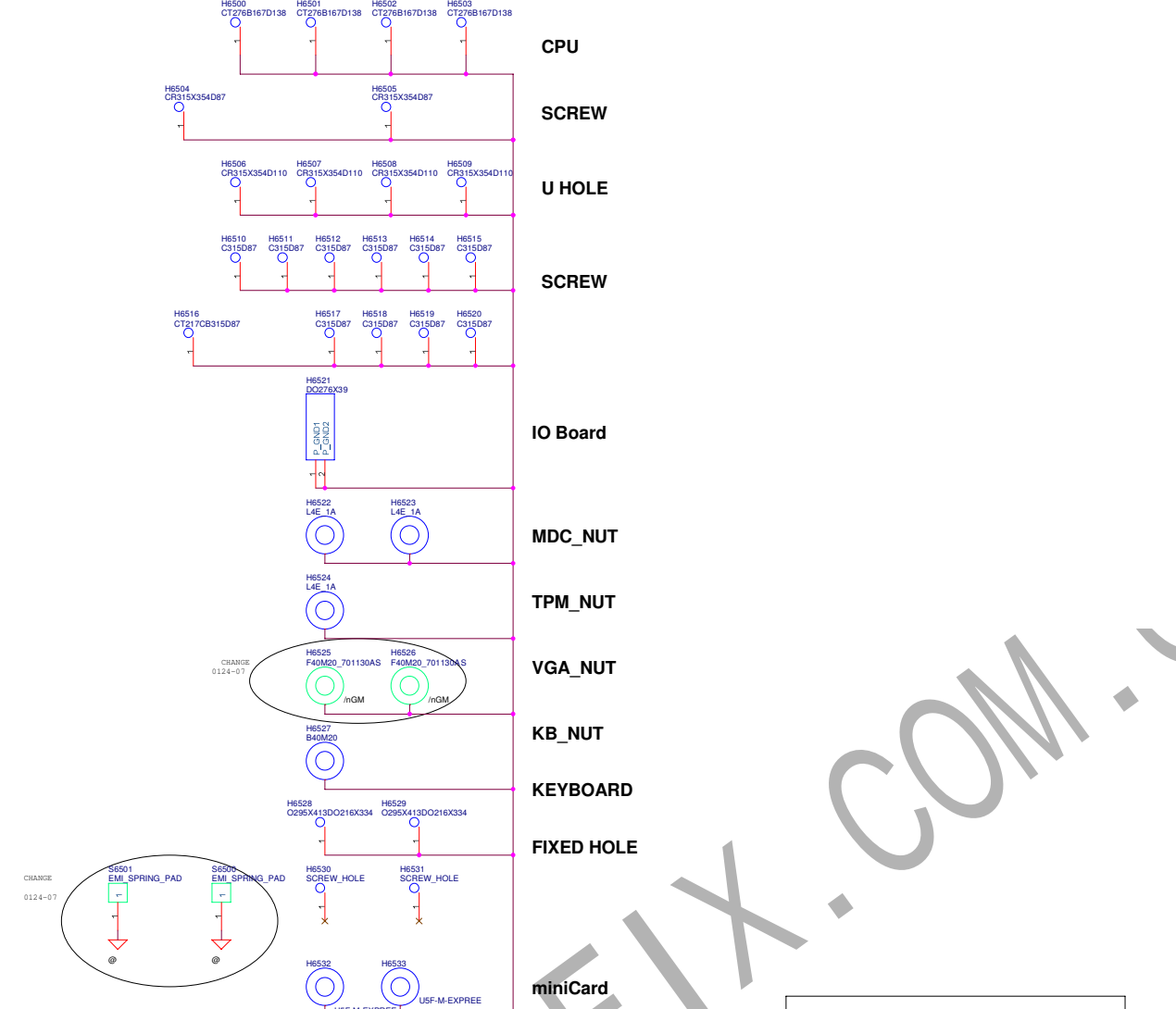
Thermal block diagram



ASUS Title :	
ASUSTeK COMPUTER INC	Engineer: Allen_CD Wu
Size B	Project Name A8N_A8Dc
Date: 11/01/2007	Sheet 64 of 94

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CHANGE 0124-07

CHANGE 0124-07

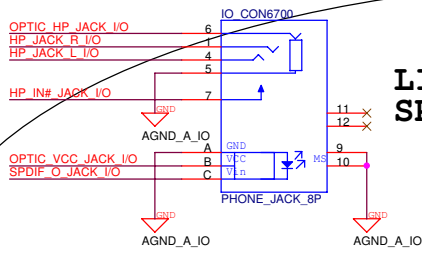
« Kennedy Zhang »

File		<Title>
Size	Document Number	Rev
Sum	MABN_A8Dc	1.1
Date	8/11/2007	Sheet 65 of 94

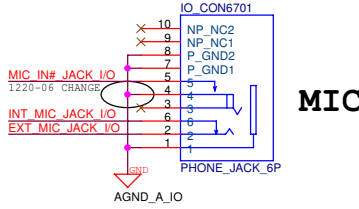
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ASUSTek COMPUTER INC		Engineer: Allen_CD Wu
Size	Project Name	Rev
B	A8N A8Dc	0
Date: 8/28/07 - 8/01/2007		Sheet 66 of 94

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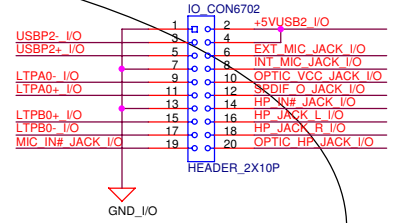
WWW.CHINAPEIX.COM.CN



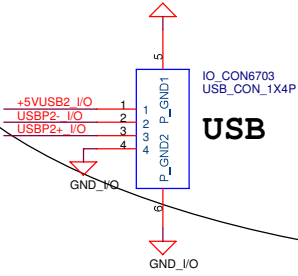
**LINE_OUT
SPDIF**



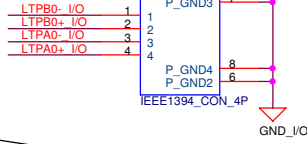
MIC



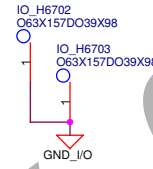
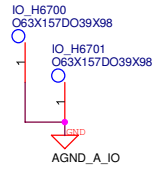
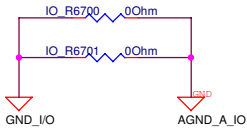
1394A



USB



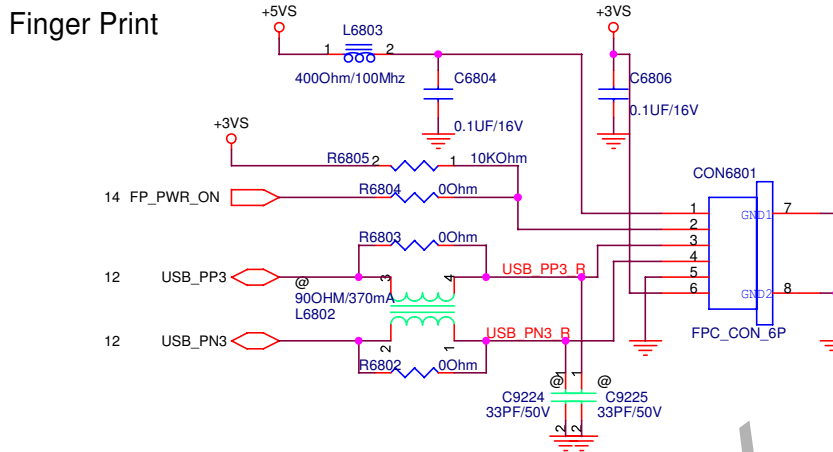
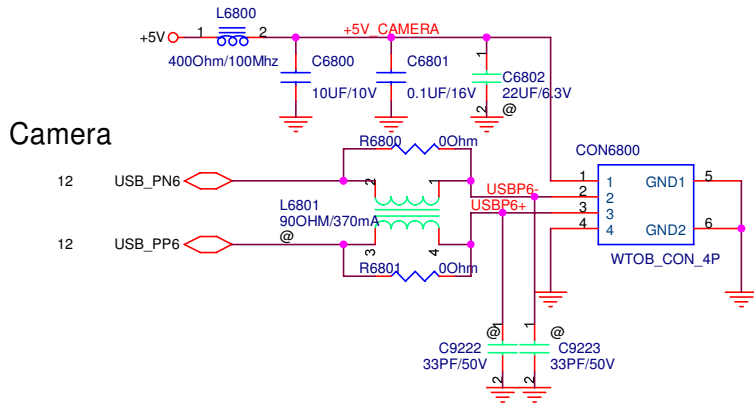
BOM
0112-07



		Title :SUB PCB	
ASUSTeK COMPUTER INC		Engineer: Allen_CD Wu	
Size A4	Project Name A8N_A8Dc	Rev 1.0	
Date: 星期四, 三月 01, 2007		Sheet 67 of 94	

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ASUS		Title : CAMERA/FingerPrinter	
ASUSTeK COMPUTER INC		Engineer: Allen_CD Wu	
Size A	Project Name A8N_A8Dc	Rev 1.0	
Date: 星期四, 三月 01, 2007		Sheet	60 of 94

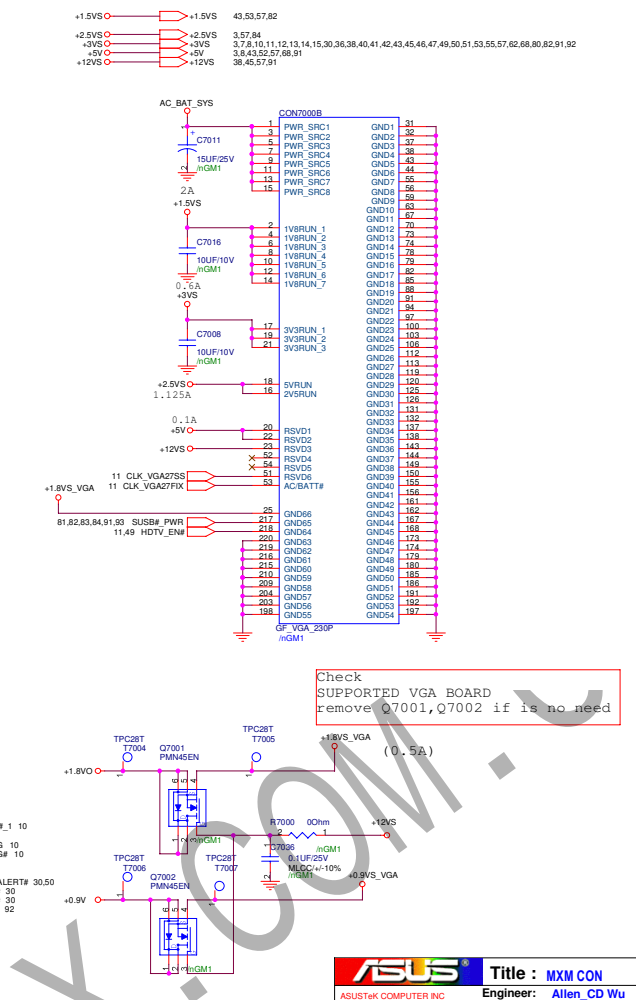
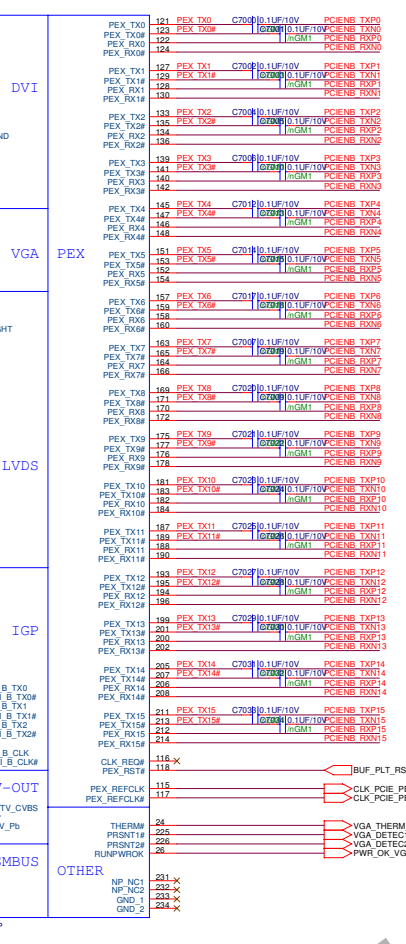
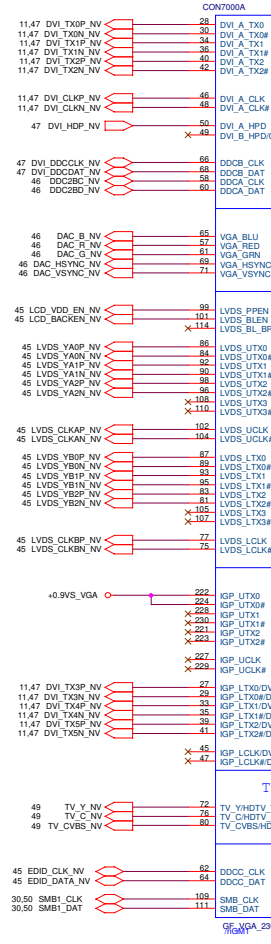
<< Kennedy_Zhang >>

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		Title : BLANK
ASUSTeK COMPUTER INC		Engineer:
Size A	Project Name A8N_A8Dc	Rev 0
Date: 星期四, 三月 01, 2007		Sheet 69 of 94

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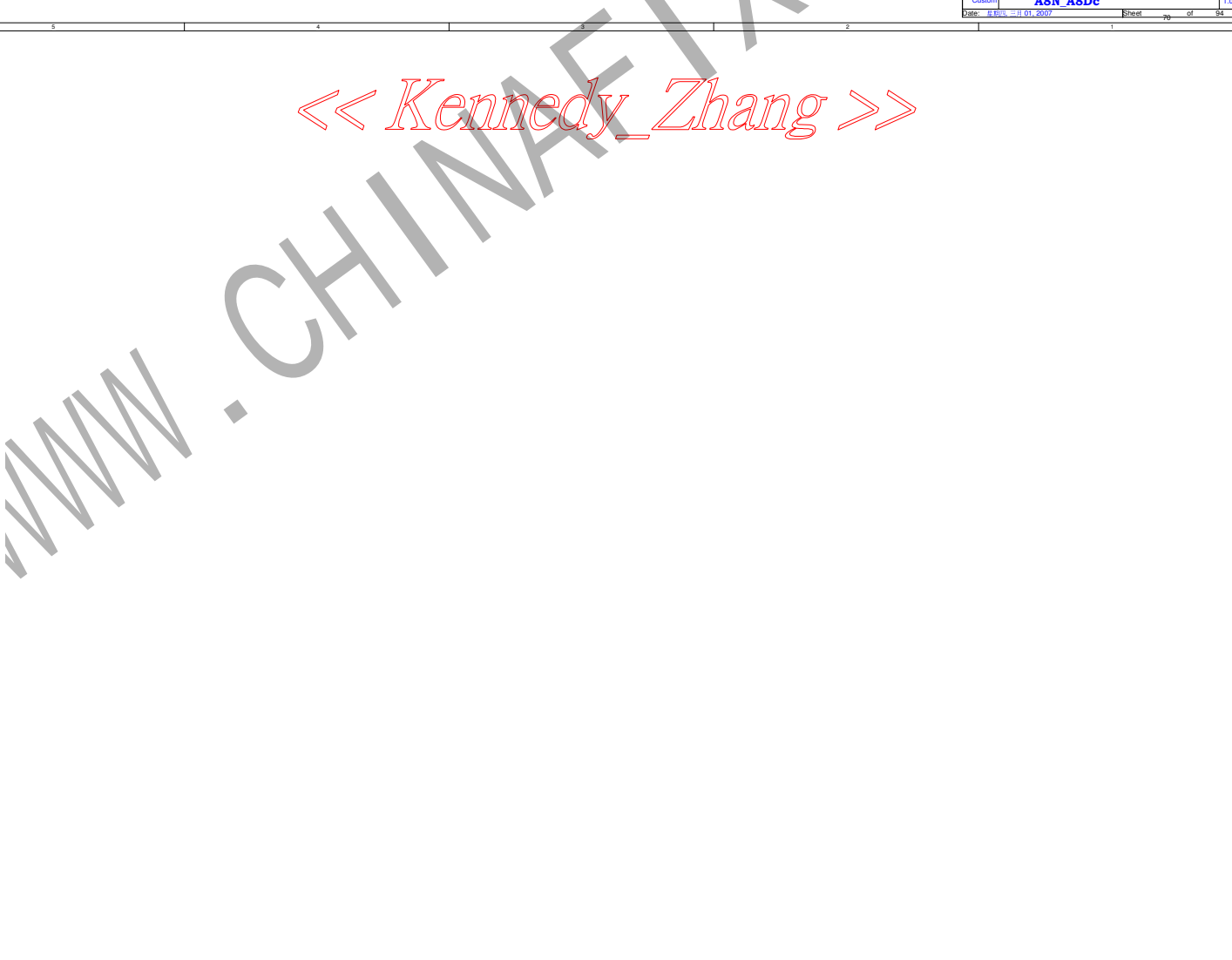
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Check SUPPORTED VGA BOARD removed Q7001,Q7002 if is no need

ASUS Title : **MXM CON**
 ASUSTek COMPUTER INC Engineer: **Allen_CD Wu**
 Size Project Name
 Custom **A8N A8dc** Rev 1.0
 Date: 2007.01.01 Sheet 76 of 84

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Title		
TITLE		
Size	Document Number	Rev
A3	ASR_AB0c	1.0
Date	11/01/2007	Sheet 71 of 94

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Title		
TITLE		
Size	Document Number	Rev
A3	ADR_ABS0c	1.0
Date	Issue	Sheet
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Title		
TITLE		
Size	Document Number	Rev
A3	ADR_ABS0c	1.0
Date	Issue	Sheet
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Title		
TITLE		
Size	Document Number	Rev
A3	ADR_ABS0c	1.0
Date	Issue	Sheet
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Title		
TITLE		
Size	Document Number	Rev
A3	ADR_ABS0c	1.0
Date	Issue	Sheet
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Title		
TITLE		
Size	Document Number	Rev
A3	APN_A3Dc	1.0
Date	Issue	Sheet
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<< Kennedy_Zhang >>

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Title		
TITLE		
Size	Document Number	Rev
A3	ADR_ABS0c	1.0
Date	Issue	Sheet
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		Title : BLANK	
ASUSTEK COMPUTER INC		Engineer:	
Size	Project Name	Rev	
B	A8N_A8Dc	0	
Date: 01/01/2007		Sheet 78 of 94	

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Change Note:

EE :


1. Reference name without 0 tail
2. Synchronize component size and type
3. Remove 0 ohm serial resistor
- 4.
- 5.
- 6.
- 7.
- 8.

Layout :

1. PCI MOAT
2. Equalize Crystal and PCI Clock Length
3. VGA Board
4. PCI-E Thread

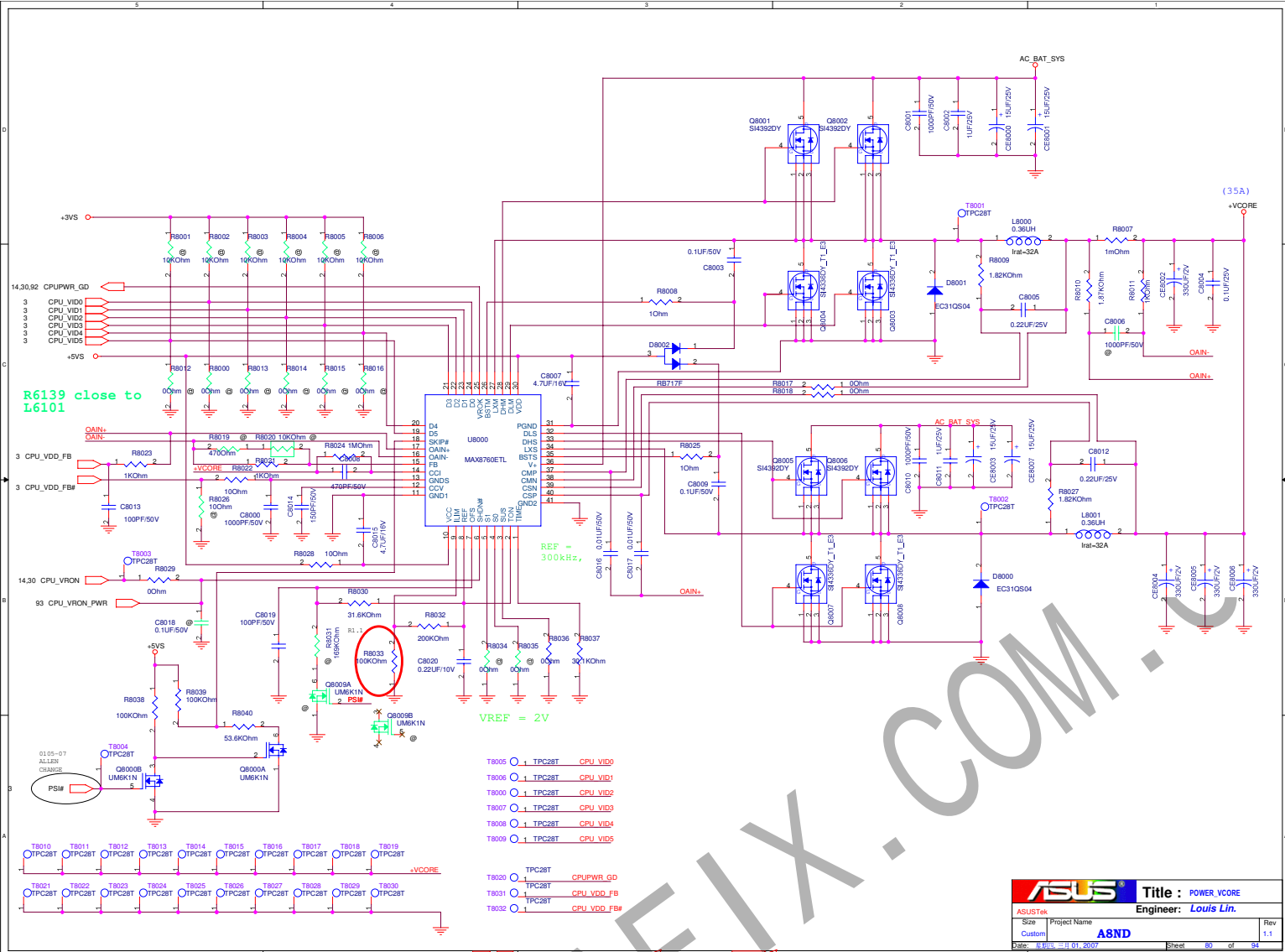
SMT :

- 1.

		Title : History	
ASUSTeK COMPUTER INC		Engineer: Allen_CD Wu	
Size	Project Name		Rev
Custom	A8N A8Dc		1.0
Date: 星期日, 11 01, 2007		Sheet 79	of 84

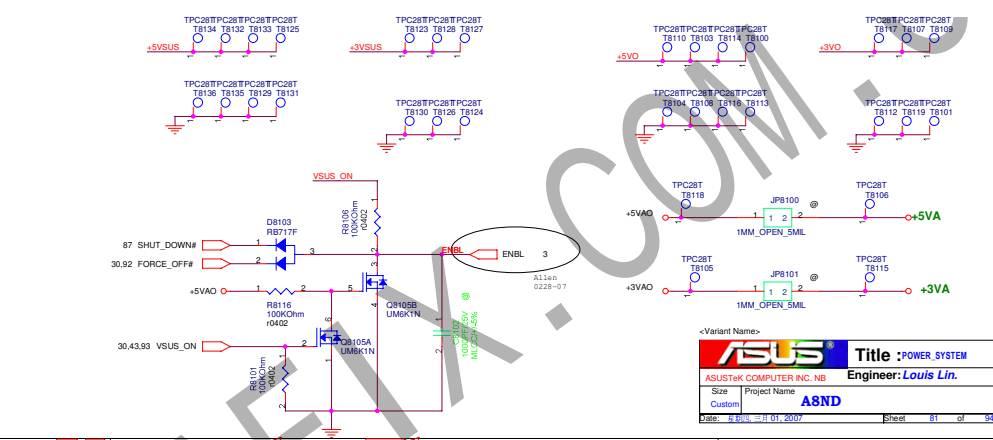
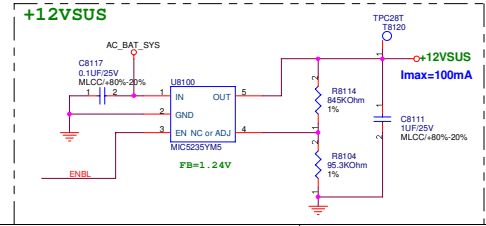
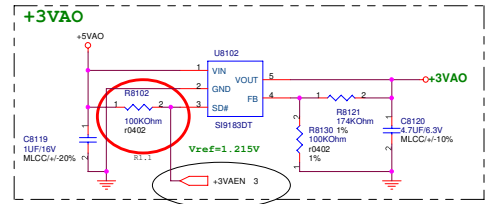
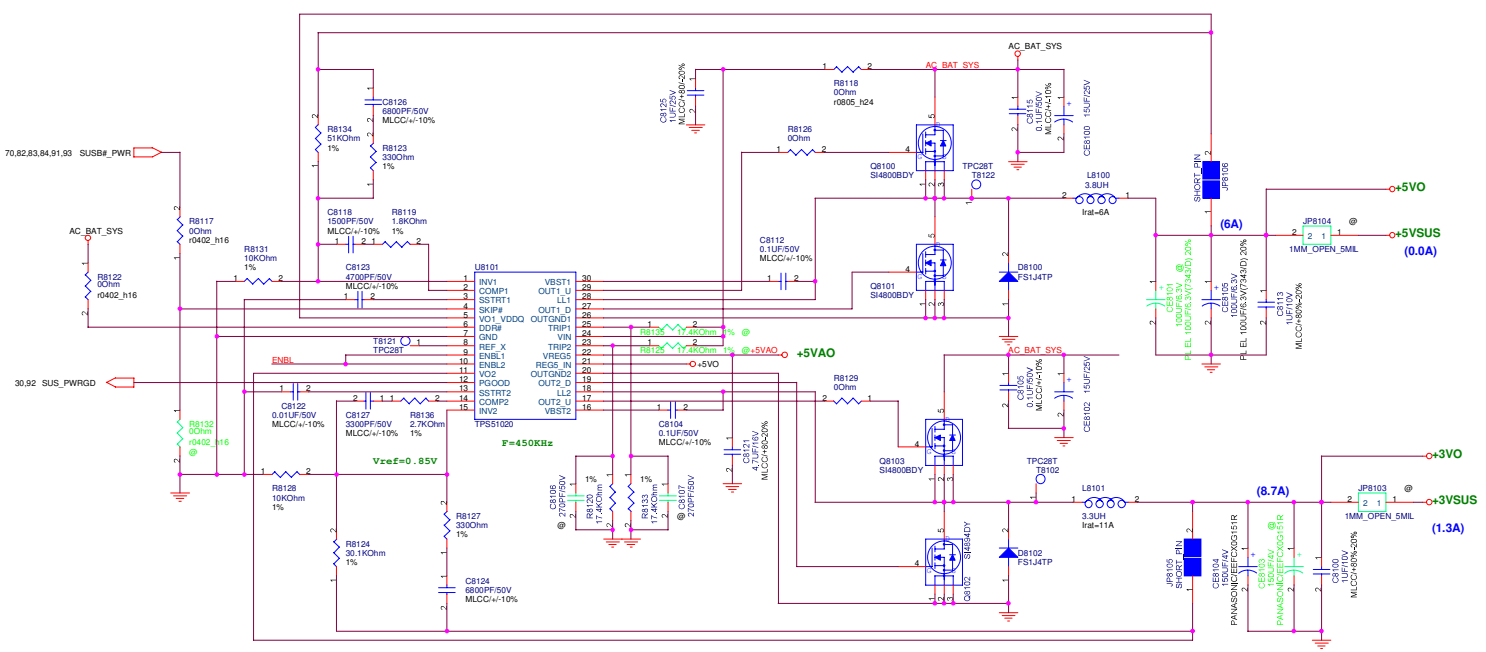
«« Kennedy_Zhang »»

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		Title : POWER_VCORE	
ASUSTek	Project Name	Engineer: Louis Lin	
Size	Custom	A8ND	Rev 1.1
Date: 11/04/2007	Sheet	80	of 94

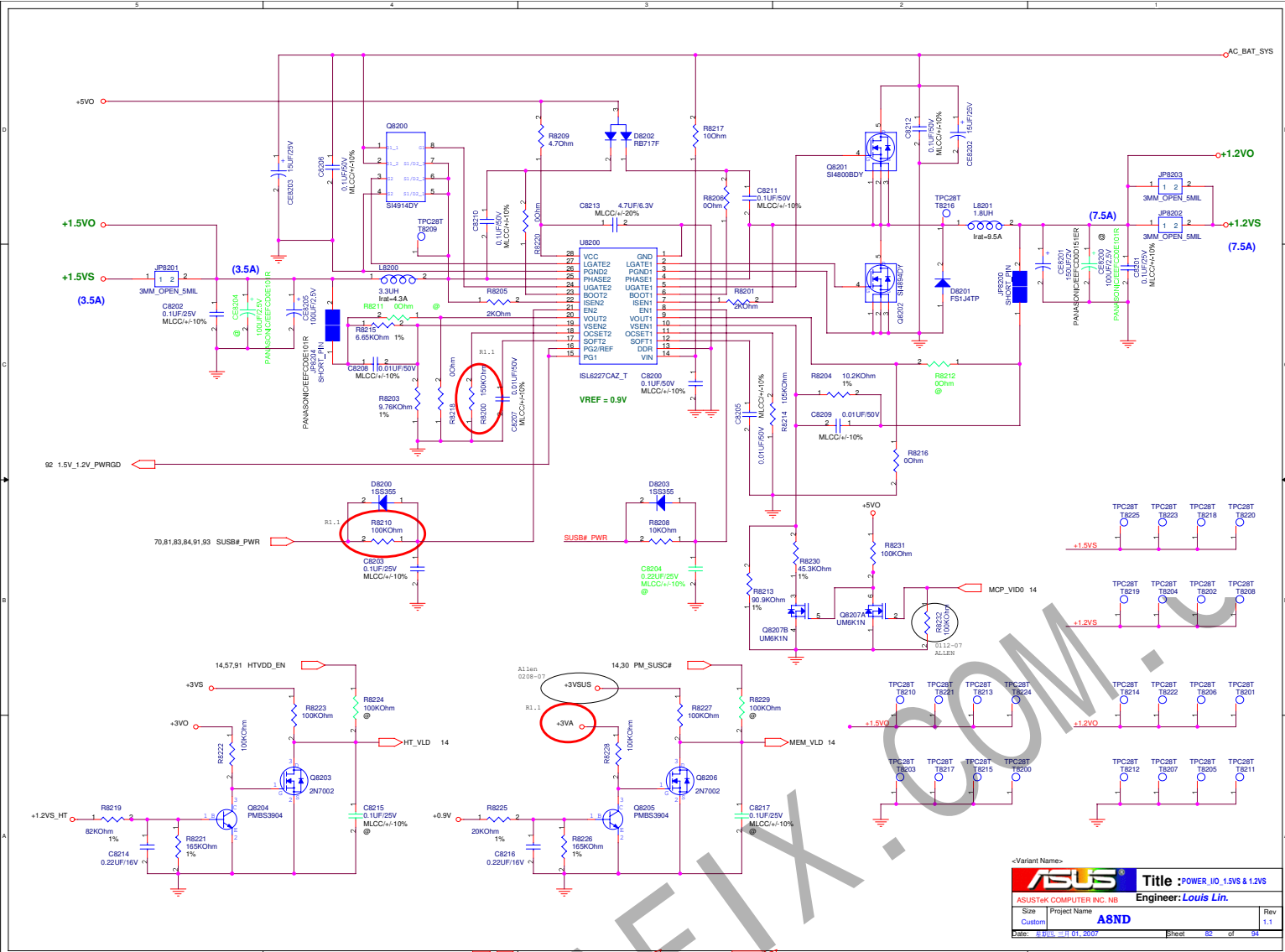


<Variant Names>

ASUS		Title: POWER SYSTEM
ASUSTeK COMPUTER INC. NB		Engineer: Louis Lin.
Size: Custom	Project Name: A8ND	Rev: 1.1
Date: 2007-01-01	Sheet: 81	of 94

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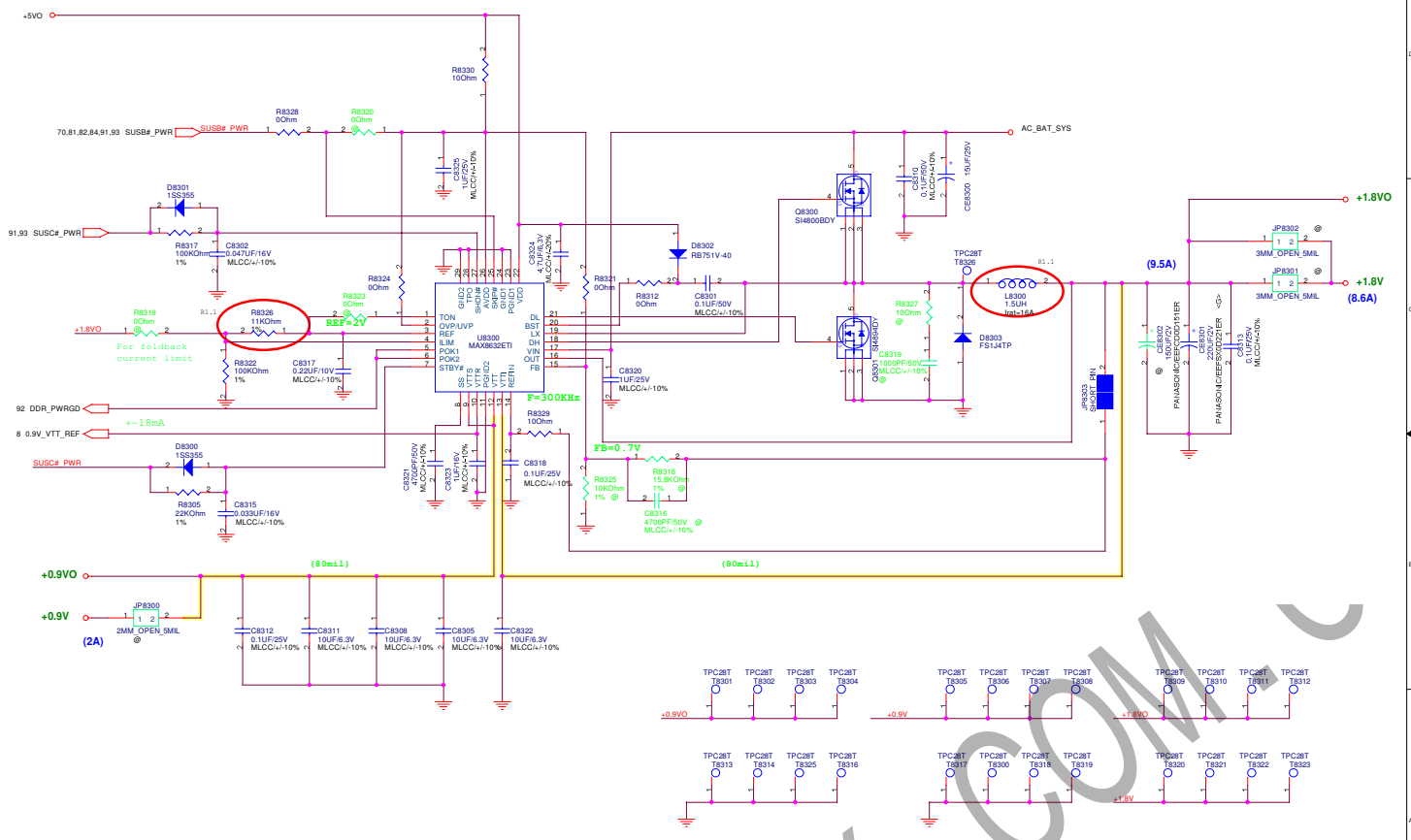


<Variant Name>

ASUS		Title : POWER_I/O_1.5VS & 1.2VS
ASUSTeK COMPUTER INC. NB		Engineer: Louis Lin.
Size	Project Name	Rev
Custom	A8ND	1.1
Date: 2007.01.01	Sheet	82 of 94

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<Variant Name>

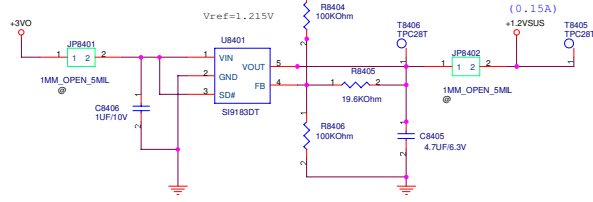
ASUS		Title : POWER_IO_DDR & VTT	
ASUS Tek COMPUTER INC. NB		Engineer: Louis Lin.	
Size	Project Name		Rev
Custom	ASND		1.1
Date: 3/30/07	01/01/2007	Sheet	83 of 94

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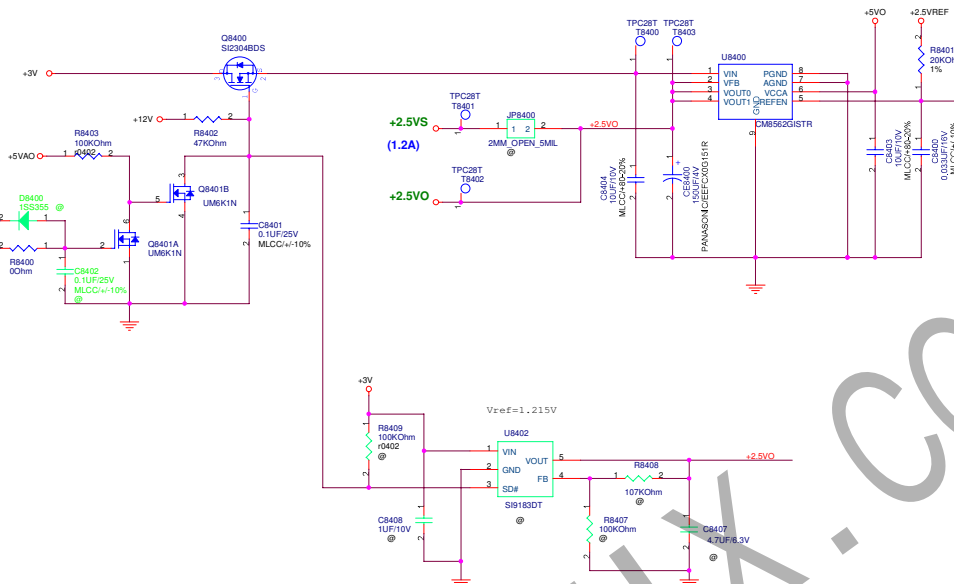
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+1.2VSUS

+2.5VREF



+2.5VS



<Variant Name>

ASUS		Title : POWER_IQ_+1.2VSUS & +2.5V	
-<OrigName>		Engineer: Louis Lin.	
Size	Project Name		Rev
Custom	A8ND		1.1
Date: 01/01/2007	Sheet 04	of 04	

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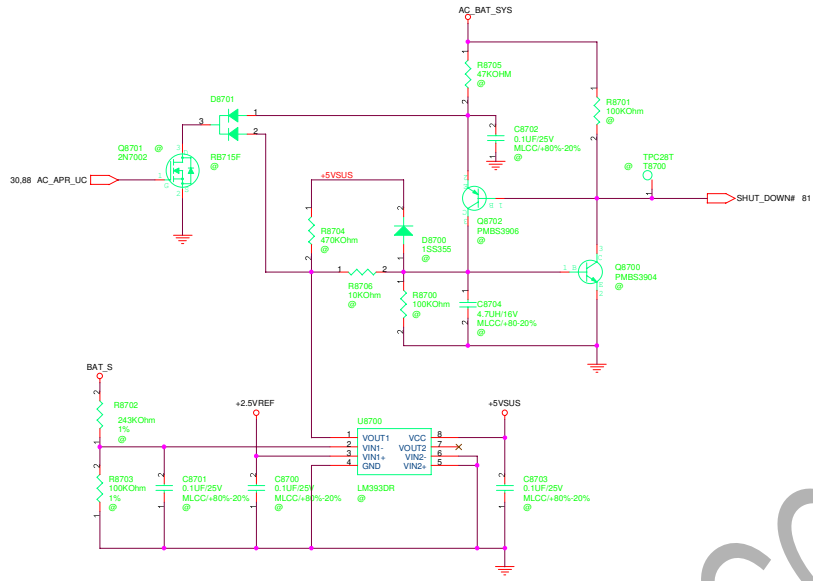
ASUS		Title: POWER_VGA_CORE & H.264
Engineer: Louis Lin		
Size	Project Name	Rev
C	ARND	1.1
DATE: 11/07/2007	Sheet	85 of 84

<Variant Name>		ASUS®		Title : NA
<OrgName>		Engineer:		
Size	Project Name			Rev
B	A8ND			1.1
Date: # B101	-- # 01, 2007		Sheet	86 of 84

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PIC only



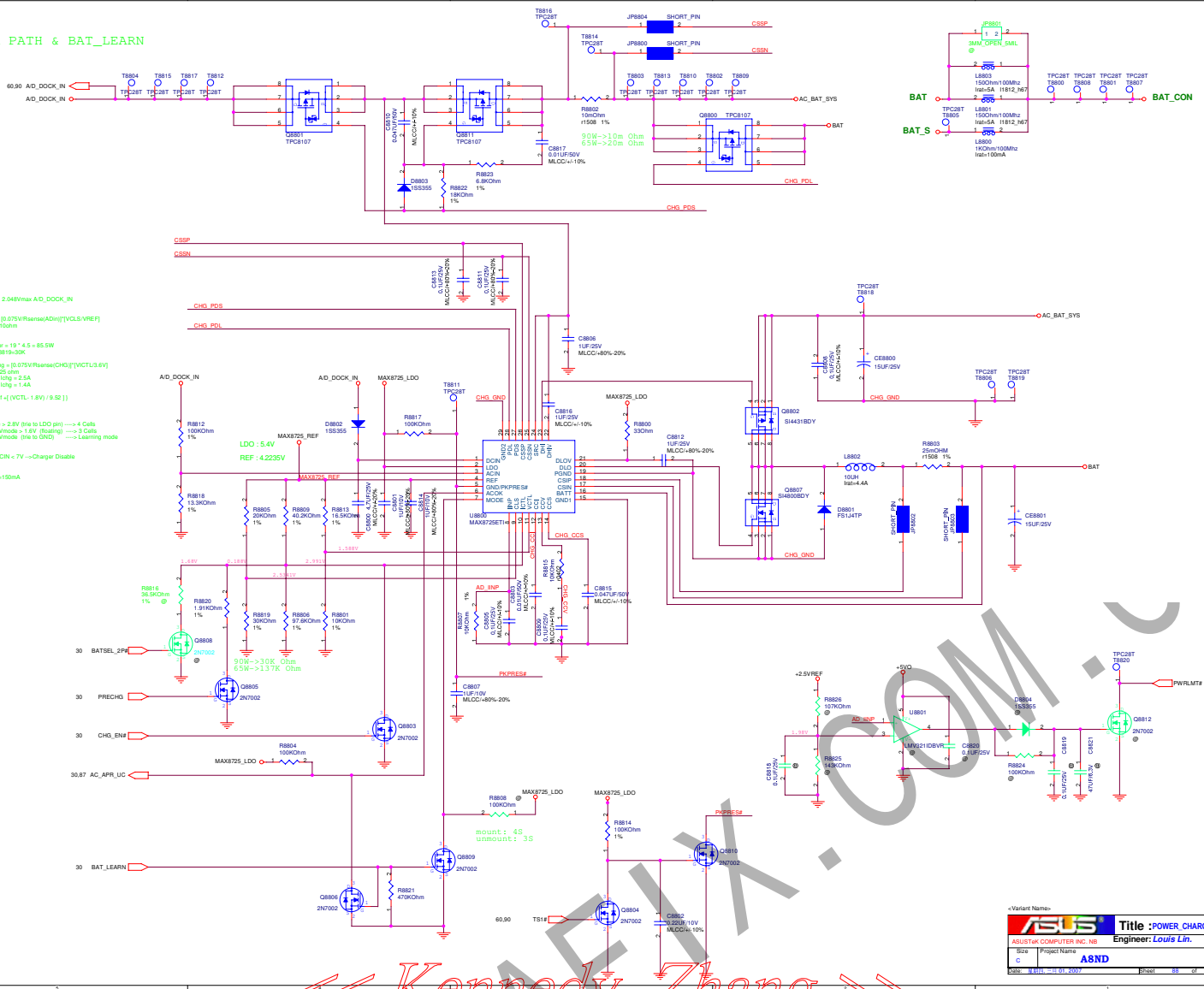
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		Title : POWER_SHUTDOWN#
Engineer: <i>Louis Lin.</i>		
<OrigName>	Project Name	Rev
Custom	A8ND	1.1
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POWER PATH & BAT_LEARN



● AC_IN Threshold: 2.048Vmax AD_DOCK_IN
 => 17.4W active
 Adapter I(max) = (0.075V/RsenseADm)/(VCL5.VREF)
 ResenseADm=0.010ohm
 VCL5=5.544V
 => I(max)=4A
 => Constant Power = 19 * 4.5 = 85.5W
 => R8805=20K,R8819=30K
 Charge Current Idm = (0.075V/RsenseCHG)/(VCTL3.6V)
 ResenseCHG=0.025ohm
 VCTL3.6V => Idm=2.5A
 => Learning mode
 Vbat = Cell * [Vref - (VCTL - 1.8V) / 9.52]
 VCTL = 5.88V
 => Vbat = 4.2V
 Mode pin : Vmode > 2.8V (Pin to LDO pin) => 4 Cells
 2.5 * Vmode > 1.8V (Booster) => 3 Cells
 0.8 * Vmode (Pin to GND) => Learning mode
 VCTL=0.6V or DCIN = 7V => Charge Disable
 Precharge current=150mA

ASUS		Title : POWER CHARGER
ASUSTEK COMPUTERS INC. NE		Engineer: LOUIS LIN.
Srca	Project Name	Rev
C	A8ND	1.1
Doc. No.:	1-10-01-2007	ESch: 88 of 84

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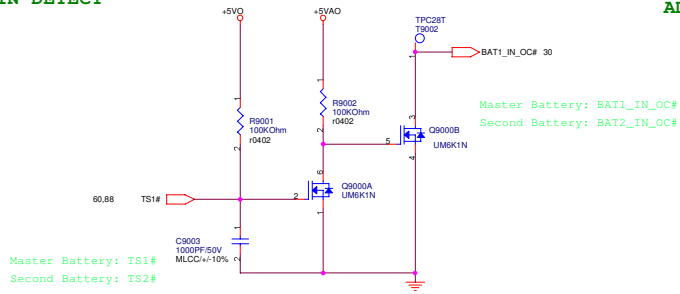
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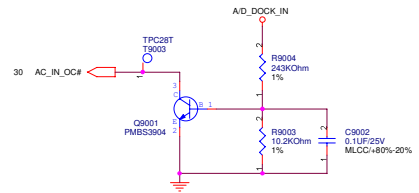
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<Variant Name>		ASUS		Title : NA	
<OrigName>		Engineer:			
Size	Project Name			Rev	
Custom	A8ND			1.1	
Date: 2007-01-01		Sheet		89	of 94

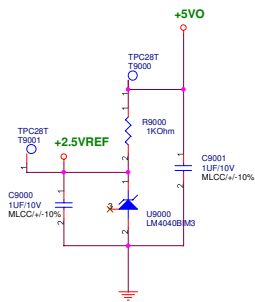
BATTERY IN DETECT



ADAPTER IN DETECT



+2.5VREF

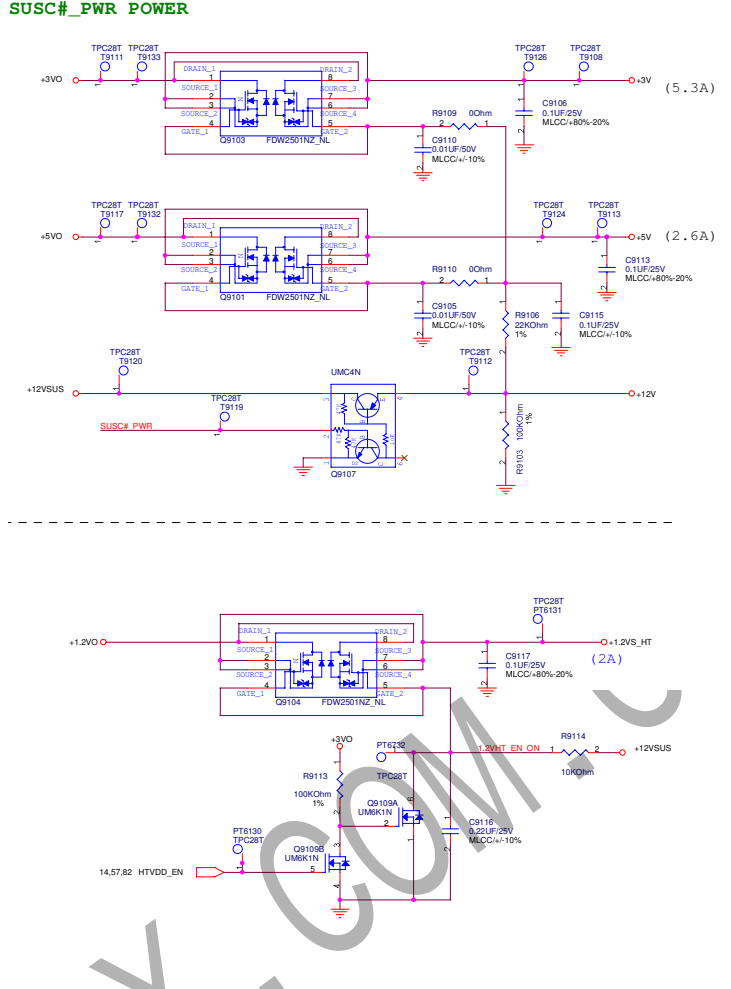
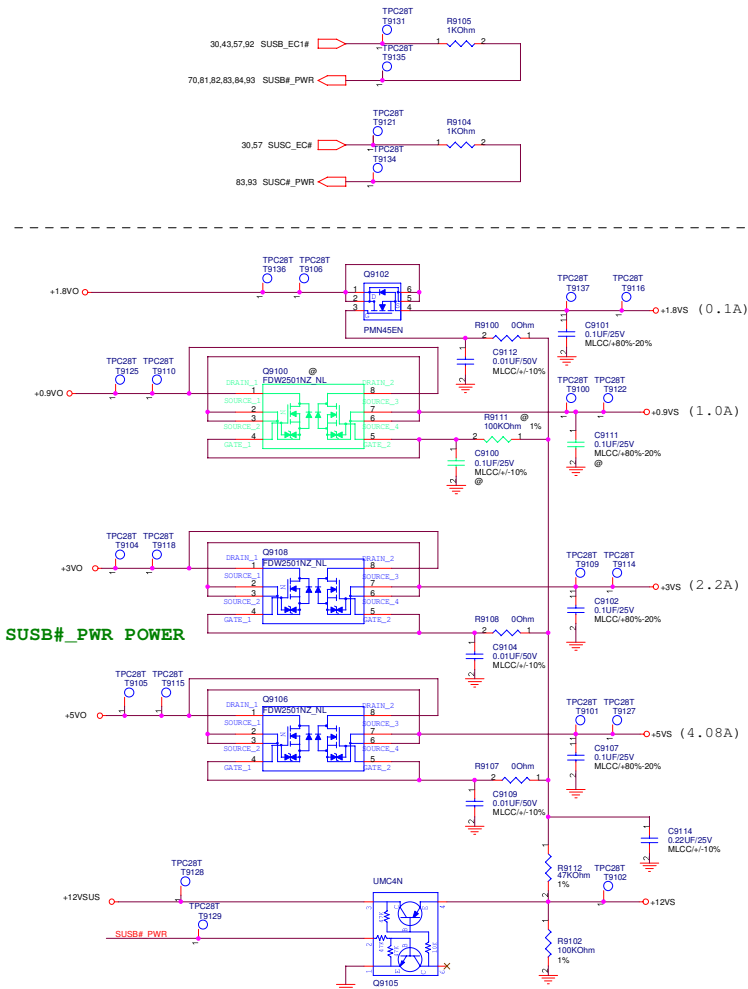


<Variant Name>

ASUS		Title :POWER_DETECT
ASUSTeK COMPUTER INC. NB		Engineer: Louis Lin.
Size	Project Name	Rev
Custom	A8ND	1.1
Date: 2007.01.01	Sheet	90 of 94

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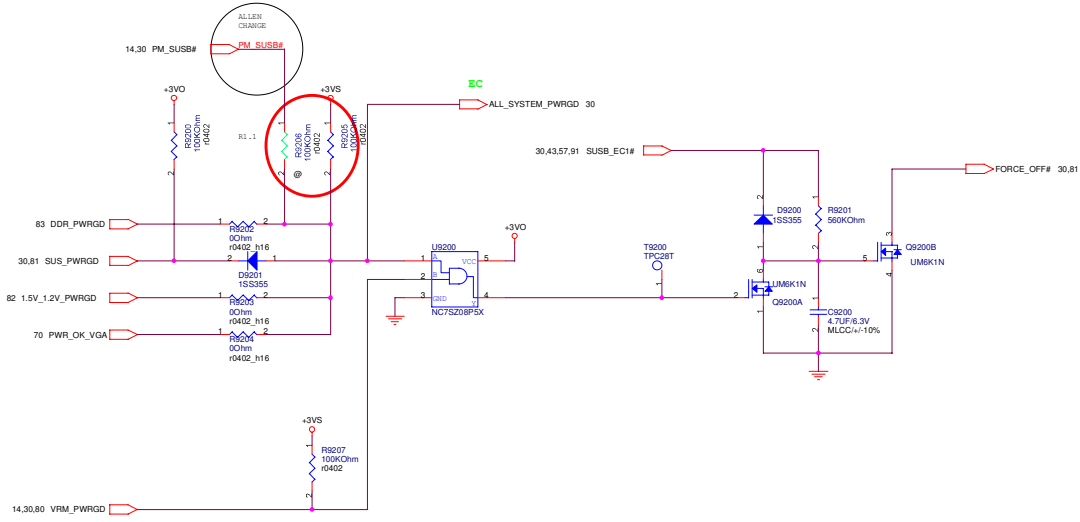
<Variant Name>

ASUS		Title :POWER_LOAD_SWITCH	
ASUSTEK COMPUTER INC. NB		Engineer: <u>Louis_Lin</u>	
Size	Project Name	Rev	
Custom	A8ND	1.1	
Date: 2007.01.01.2007	Sheet	BT	of 94

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POWER GOOD DETECTOR



<Variant Name>

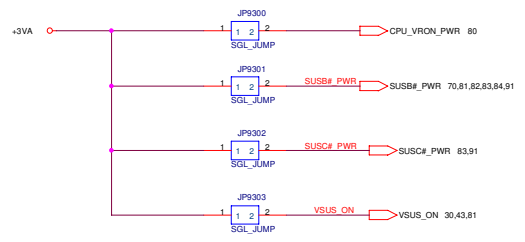
ASUS		Title :POWER_PROTECT	
ASUSTeK COMPUTER INC. NB		Engineer: Louis Lin	
Size	Project Name		Rev
Custom	A8ND		1.1
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AC_BAT_SYS	AC_BAT_SYS	45,70,80,81,82,83,87,88
BAT	BAT	88
BAT_CON	BAT_CON	60,88
+2.5VREF	+2.5VREF	84,87,88,90
+3VA	+3VA	13,14,30,57,81,82
+5VAD	+5VAD	81,84,90
+5VO	+5VO	81,82,83,84,88,90,91
+5VSUS	+5VSUS	56,81,87
+5V	+5V	3,8,43,52,57,68,70,91
+5VS	+5VS	15,31,36,38,46,47,50,51,56,57,68,80,91
+3VO	+3VO	81,82,84,91,92
+3VSUS	+3VSUS	3,10,11,12,13,14,30,33,43,53,57,81,82
+3V	+3V	34,43,45,53,56,57,61,62,84,91
+3VS	+3VS	3,7,8,10,11,12,13,14,15,30,36,38,40,41,42,43,45,46,47,49,50,51,53,55,57,62,68,70,80,82,91,92
+12VSUS	+12VSUS	81,91
+12V	+12V	38,42,52,57,84,91
+12VS	+12VS	38,45,57,70,91
+1.8VO	+1.8VO	70,83,91
+1.8V	+1.8V	3,5,7,8,9,57,83
+1.8VS	+1.8VS	3,11,57,91
+0.9VS	+0.9VS	91
+0.9VO	+0.9VO	83,91
+1.5VO	+1.5VO	82
+1.5VS	+1.5VS	43,53,57,70,82
+2.5VO	+2.5VO	84
+2.5VS	+2.5VS	3,57,70,84
+VDDRE	+VDDRE	5,80
+1.25VS	+1.25VS	
+1.2VSUS	+1.2VSUS	11,13,84
+1.2VS	+1.2VS	10,12,13,57,82
+1.2VO	+1.2VO	82,91

FOR POWER TEST



ASUS		Title : POWER_SIGNAL
Engineer: Louis Lin.		
Size	Project Name	Rev
Custom	A8ND	1.1
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