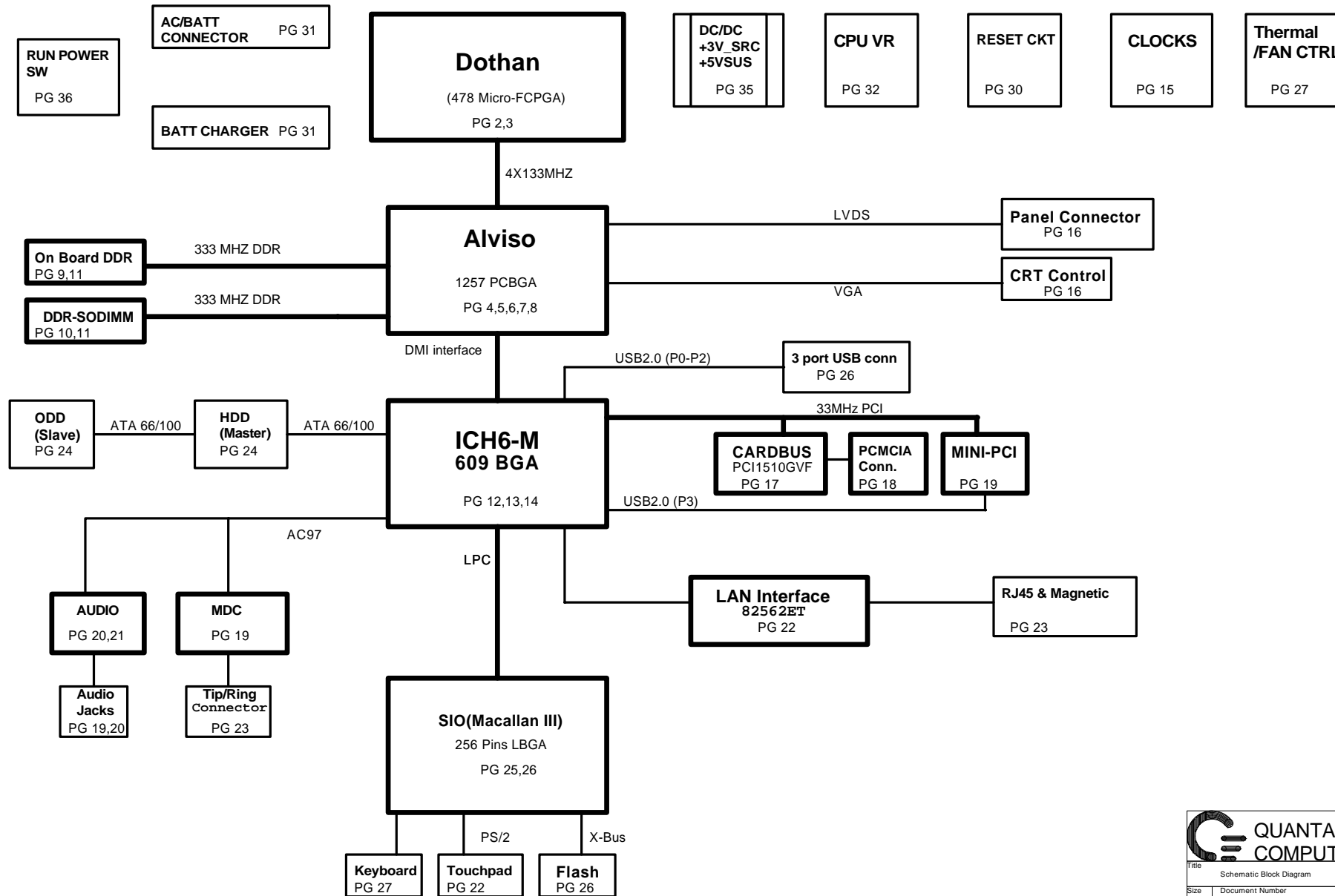
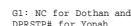


Tonga & Tyler (T2)

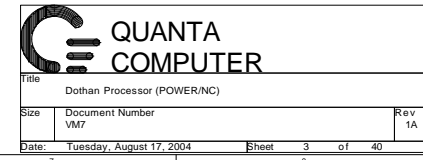
VER : 1A



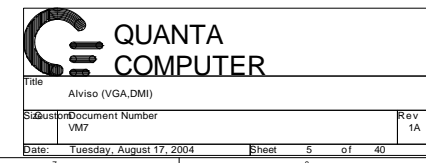


Signal	Resistor Value	Connect To	Resistor Placement
TDI	150 ohm +/- 5%	VCCP	Within 2.0" of the CPU
TMS	39 ohm +/- 5%	VCCP	Within 2.0" of the CPU
TRST#	680 ohm +/- 5%	GND	Within 2.0" of the CPU
TCX	27 ohm +/- 5%	GND	Within 2.0" of the CPU
TDO	Open	VCCP	Within 2.0" of the CPU

Note: Populate R178, R176, C339 when ITP connector is populated

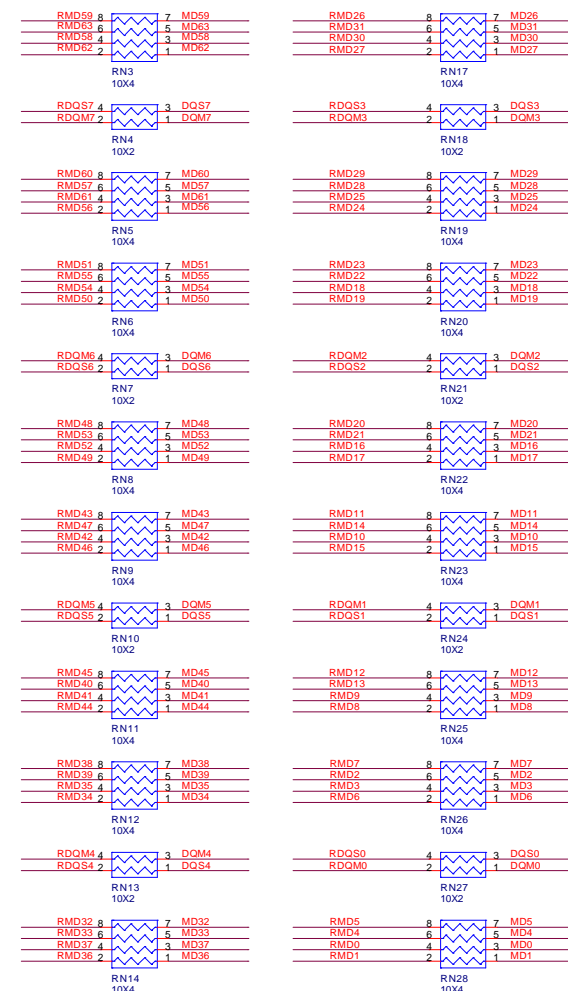
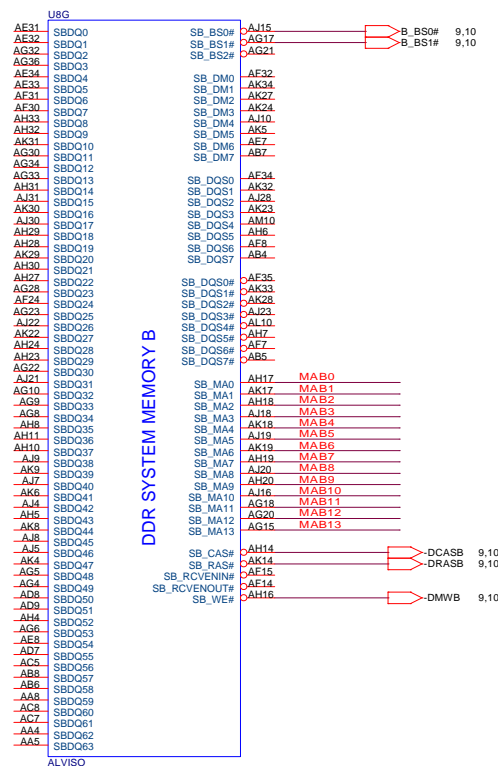
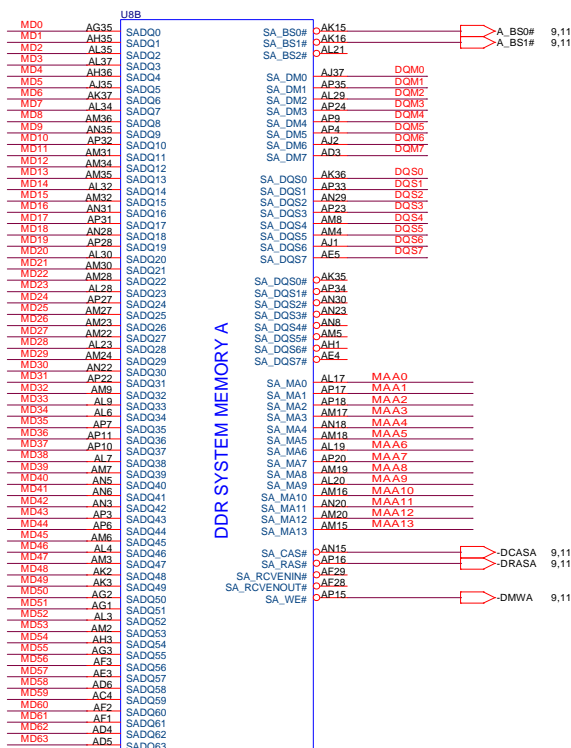


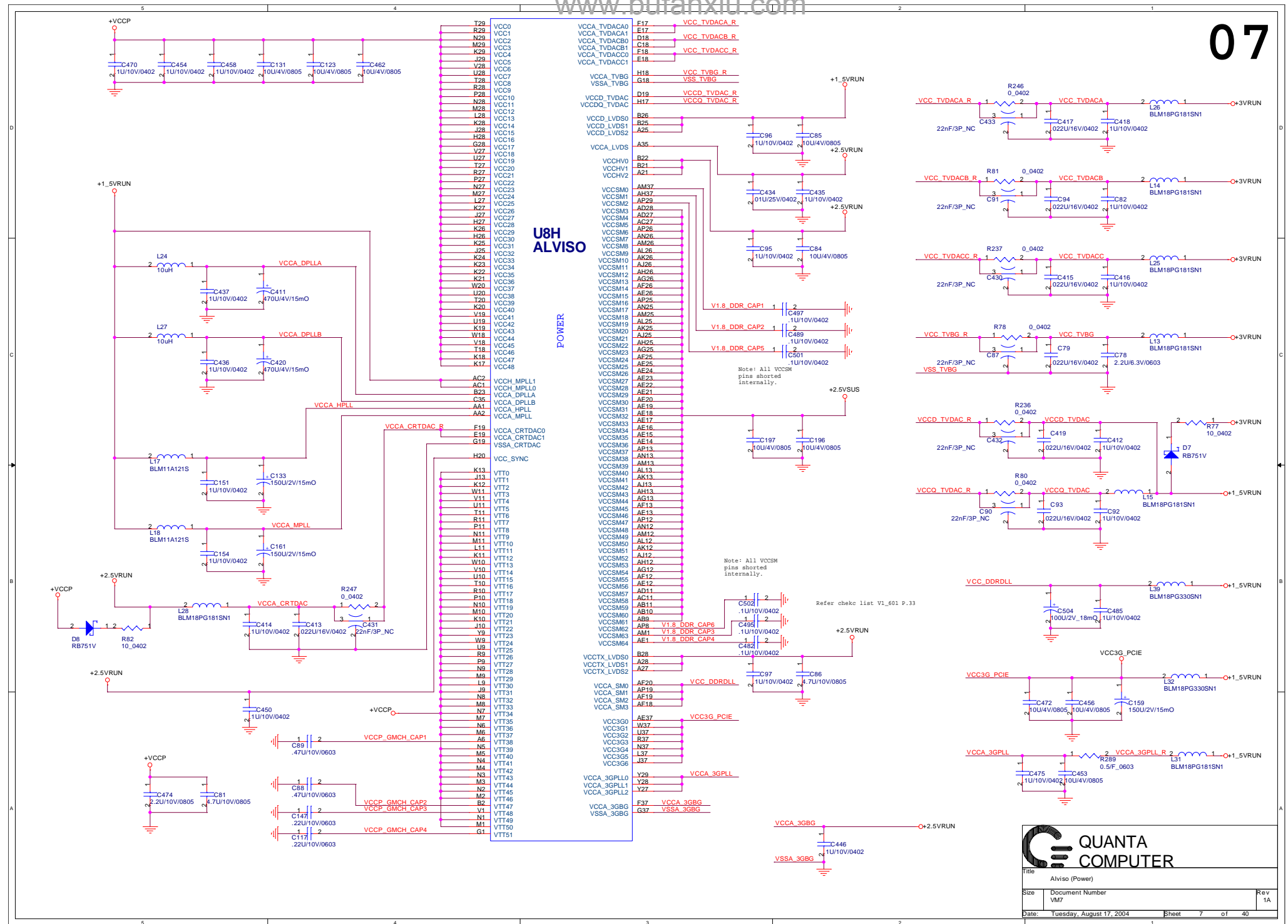




MAB[0..13] → MAB[0..13] 9,10
 MAA[0..13] → MAA[0..13] 9,11
 -CSA[0..3] → -CSA[0..3] 5,9,10,11
 CKE[0..3] → CKE[0..3] 5,9,10,11

RMD[0..63] → RMD[0..63] 9,10,11
 RDQM[0..7] → RDQM[0..7] 9,10,11
 RDQS[0..7] → RDQS[0..7] 9,10,11







L17	VCC_NCTF78	VSS_NCTF68	Y12
L18	VCC_NCTF77	VSS_NCTF67	Y11
L19	VCC_NCTF76	VSS_NCTF66	Y10
L20	VCC_NCTF75	VSS_NCTF65	Y9
L21	VCC_NCTF74	VSS_NCTF64	Y8
L22	VCC_NCTF73	VSS_NCTF63	Y7
L23	VCC_NCTF72	VSS_NCTF62	Y6
L24	VCC_NCTF71	VSS_NCTF61	Y5
L25	VCC_NCTF70	VSS_NCTF60	Y4
L26	VCC_NCTF69	VSS_NCTF59	Y3
L27	VCC_NCTF68	VSS_NCTF58	Y2
L28	VCC_NCTF67	VSS_NCTF57	Y1
L29	VCC_NCTF66	VSS_NCTF56	Y0
L30	VCC_NCTF65	VSS_NCTF55	Y0
L31	VCC_NCTF64	VSS_NCTF54	Y0
L32	VCC_NCTF63	VSS_NCTF53	Y0
L33	VCC_NCTF62	VSS_NCTF52	Y0
L34	VCC_NCTF61	VSS_NCTF51	Y0
L35	VCC_NCTF60	VSS_NCTF50	Y0
L36	VCC_NCTF59	VSS_NCTF49	Y0
L37	VCC_NCTF58	VSS_NCTF48	Y0
L38	VCC_NCTF57	VSS_NCTF47	Y0
L39	VCC_NCTF56	VSS_NCTF46	Y0
L40	VCC_NCTF55	VSS_NCTF45	Y0
L41	VCC_NCTF54	VSS_NCTF44	Y0
L42	VCC_NCTF53	VSS_NCTF43	Y0
L43	VCC_NCTF52	VSS_NCTF42	Y0
L44	VCC_NCTF51	VSS_NCTF41	Y0
L45	VCC_NCTF50	VSS_NCTF40	Y0
L46	VCC_NCTF49	VSS_NCTF39	Y0
L47	VCC_NCTF48	VSS_NCTF38	Y0
L48	VCC_NCTF47	VSS_NCTF37	Y0
L49	VCC_NCTF46	VSS_NCTF36	Y0
L50	VCC_NCTF45	VSS_NCTF35	Y0
L51	VCC_NCTF44	VSS_NCTF34	Y0
L52	VCC_NCTF43	VSS_NCTF33	Y0
L53	VCC_NCTF42	VSS_NCTF32	Y0
L54	VCC_NCTF41	VSS_NCTF31	Y0
L55	VCC_NCTF40	VSS_NCTF30	Y0
L56	VCC_NCTF39	VSS_NCTF29	Y0
L57	VCC_NCTF38	VSS_NCTF28	Y0
L58	VCC_NCTF37	VSS_NCTF27	Y0
L59	VCC_NCTF36	VSS_NCTF26	Y0
L60	VCC_NCTF35	VSS_NCTF25	Y0
L61	VCC_NCTF34	VSS_NCTF24	Y0
L62	VCC_NCTF33	VSS_NCTF23	Y0
L63	VCC_NCTF32	VSS_NCTF22	Y0
L64	VCC_NCTF31	VSS_NCTF21	Y0
L65	VCC_NCTF30	VSS_NCTF20	Y0
L66	VCC_NCTF29	VSS_NCTF19	Y0
L67	VCC_NCTF28	VSS_NCTF18	Y0
L68	VCC_NCTF27	VSS_NCTF17	Y0
L69	VCC_NCTF26	VSS_NCTF16	Y0
L70	VCC_NCTF25	VSS_NCTF15	Y0
L71	VCC_NCTF24	VSS_NCTF14	Y0
L72	VCC_NCTF23	VSS_NCTF13	Y0
L73	VCC_NCTF22	VSS_NCTF12	Y0
L74	VCC_NCTF21	VSS_NCTF11	Y0
L75	VCC_NCTF20	VSS_NCTF10	Y0
L76	VCC_NCTF19	VSS_NCTF9	Y0
L77	VCC_NCTF18	VSS_NCTF8	Y0
L78	VCC_NCTF17	VSS_NCTF7	Y0
L79	VCC_NCTF16	VSS_NCTF6	Y0
L80	VCC_NCTF15	VSS_NCTF5	Y0
L81	VCC_NCTF14	VSS_NCTF4	Y0
L82	VCC_NCTF13	VSS_NCTF3	Y0
L83	VCC_NCTF12	VSS_NCTF2	Y0
L84	VCC_NCTF11	VSS_NCTF1	Y0
L85	VCC_NCTF10	VSS_NCTF0	Y0
L86	VCC_NCTF9	VSS_NCTF0	Y0
L87	VCC_NCTF8	VSS_NCTF0	Y0
L88	VCC_NCTF7	VSS_NCTF0	Y0
L89	VCC_NCTF6	VSS_NCTF0	Y0
L90	VCC_NCTF5	VSS_NCTF0	Y0
L91	VCC_NCTF4	VSS_NCTF0	Y0
L92	VCC_NCTF3	VSS_NCTF0	Y0
L93	VCC_NCTF2	VSS_NCTF0	Y0
L94	VCC_NCTF1	VSS_NCTF0	Y0
L95	VCC_NCTF0	VSS_NCTF0	Y0
L96	VCC_NCTF0	VSS_NCTF0	Y0
L97	VCC_NCTF0	VSS_NCTF0	Y0
L98	VCC_NCTF0	VSS_NCTF0	Y0
L99	VCC_NCTF0	VSS_NCTF0	Y0
L100	VCC_NCTF0	VSS_NCTF0	Y0

U8D
ALVISO NCTF

+VCCP

+2.5VSUS

L12	VTT_NCTF17	VSS_NCTF17	Y12
L13	VTT_NCTF16	VSS_NCTF16	Y11
L14	VTT_NCTF15	VSS_NCTF15	Y10
L15	VTT_NCTF14	VSS_NCTF14	Y9
L16	VTT_NCTF13	VSS_NCTF13	Y8
L17	VTT_NCTF12	VSS_NCTF12	Y7
L18	VTT_NCTF11	VSS_NCTF11	Y6
L19	VTT_NCTF10	VSS_NCTF10	Y5
L20	VTT_NCTF9	VSS_NCTF9	Y4
L21	VTT_NCTF8	VSS_NCTF8	Y3
L22	VTT_NCTF7	VSS_NCTF7	Y2
L23	VTT_NCTF6	VSS_NCTF6	Y1
L24	VTT_NCTF5	VSS_NCTF5	Y0
L25	VTT_NCTF4	VSS_NCTF4	Y0
L26	VTT_NCTF3	VSS_NCTF3	Y0
L27	VTT_NCTF2	VSS_NCTF2	Y0
L28	VTT_NCTF1	VSS_NCTF1	Y0
L29	VTT_NCTF0	VSS_NCTF0	Y0

+VCCP

U8E
ALVISO VSS

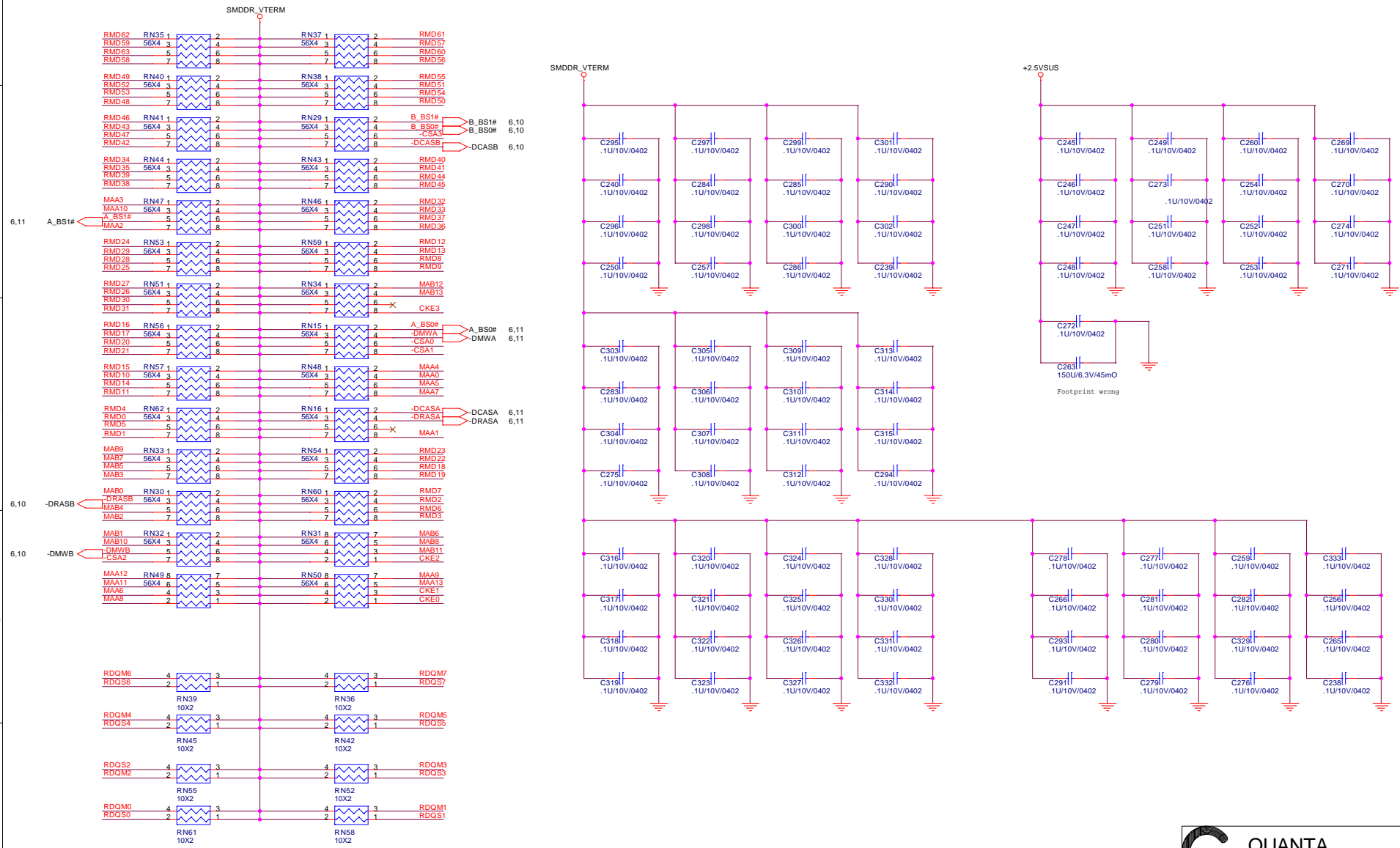
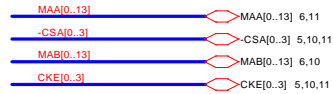
B24	VSS135	VSS135	Y1
B25	VSS134	VSS134	Y0
B26	VSS133	VSS133	Y0
B27	VSS132	VSS132	Y0
B28	VSS131	VSS131	Y0
B29	VSS130	VSS130	Y0
B30	VSS129	VSS129	Y0
B31	VSS128	VSS128	Y0
B32	VSS127	VSS127	Y0
B33	VSS126	VSS126	Y0
B34	VSS125	VSS125	Y0
B35	VSS124	VSS124	Y0
B36	VSS123	VSS123	Y0
B37	VSS122	VSS122	Y0
B38	VSS121	VSS121	Y0
B39	VSS120	VSS120	Y0
B40	VSS119	VSS119	Y0
B41	VSS118	VSS118	Y0
B42	VSS117	VSS117	Y0
B43	VSS116	VSS116	Y0
B44	VSS115	VSS115	Y0
B45	VSS114	VSS114	Y0
B46	VSS113	VSS113	Y0
B47	VSS112	VSS112	Y0
B48	VSS111	VSS111	Y0
B49	VSS110	VSS110	Y0
B50	VSS109	VSS109	Y0
B51	VSS108	VSS108	Y0
B52	VSS107	VSS107	Y0
B53	VSS106	VSS106	Y0
B54	VSS105	VSS105	Y0
B55	VSS104	VSS104	Y0
B56	VSS103	VSS103	Y0
B57	VSS102	VSS102	Y0
B58	VSS101	VSS101	Y0
B59	VSS100	VSS100	Y0
B60	VSS99	VSS99	Y0
B61	VSS98	VSS98	Y0
B62	VSS97	VSS97	Y0
B63	VSS96	VSS96	Y0
B64	VSS95	VSS95	Y0
B65	VSS94	VSS94	Y0
B66	VSS93	VSS93	Y0
B67	VSS92	VSS92	Y0
B68	VSS91	VSS91	Y0
B69	VSS90	VSS90	Y0
B70	VSS89	VSS89	Y0
B71	VSS88	VSS88	Y0
B72	VSS87	VSS87	Y0
B73	VSS86	VSS86	Y0
B74	VSS85	VSS85	Y0
B75	VSS84	VSS84	Y0
B76	VSS83	VSS83	Y0
B77	VSS82	VSS82	Y0
B78	VSS81	VSS81	Y0
B79	VSS80	VSS80	Y0
B80	VSS79	VSS79	Y0
B81	VSS78	VSS78	Y0
B82	VSS77	VSS77	Y0
B83	VSS76	VSS76	Y0
B84	VSS75	VSS75	Y0
B85	VSS74	VSS74	Y0
B86	VSS73	VSS73	Y0
B87	VSS72	VSS72	Y0
B88	VSS71	VSS71	Y0
B89	VSS70	VSS70	Y0
B90	VSS69	VSS69	Y0
B91	VSS68	VSS68	Y0
B92	VSS67	VSS67	Y0
B93	VSS66	VSS66	Y0
B94	VSS65	VSS65	Y0
B95	VSS64	VSS64	Y0
B96	VSS63	VSS63	Y0
B97	VSS62	VSS62	Y0
B98	VSS61	VSS61	Y0
B99	VSS60	VSS60	Y0
B100	VSS59	VSS59	Y0
B101	VSS58	VSS58	Y0
B102	VSS57	VSS57	Y0
B103	VSS56	VSS56	Y0
B104	VSS55	VSS55	Y0
B105	VSS54	VSS54	Y0
B106	VSS53	VSS53	Y0
B107	VSS52	VSS52	Y0
B108	VSS51	VSS51	Y0
B109	VSS50	VSS50	Y0
B110	VSS49	VSS49	Y0
B111	VSS48	VSS48	Y0
B112	VSS47	VSS47	Y0
B113	VSS46	VSS46	Y0
B114	VSS45	VSS45	Y0
B115	VSS44	VSS44	Y0
B116	VSS43	VSS43	Y0
B117	VSS42	VSS42	Y0
B118	VSS41	VSS41	Y0
B119	VSS40	VSS40	Y0
B120	VSS39	VSS39	Y0
B121	VSS38	VSS38	Y0
B122	VSS37	VSS37	Y0
B123	VSS36	VSS36	Y0
B124	VSS35	VSS35	Y0
B125	VSS34	VSS34	Y0
B126	VSS33	VSS33	Y0
B127	VSS32	VSS32	Y0
B128	VSS31	VSS31	Y0
B129	VSS30	VSS30	Y0
B130	VSS29	VSS29	Y0
B131	VSS28	VSS28	Y0
B132	VSS27	VSS27	Y0
B133	VSS26	VSS26	Y0
B134	VSS25	VSS25	Y0
B135	VSS24	VSS24	Y0
B136	VSS23	VSS23	Y0
B137	VSS22	VSS22	Y0
B138	VSS21	VSS21	Y0
B139	VSS20	VSS20	Y0
B140	VSS19	VSS19	Y0
B141	VSS18	VSS18	Y0
B142	VSS17	VSS17	Y0
B143	VSS16	VSS16	Y0
B144	VSS15	VSS15	Y0
B145	VSS14	VSS14	Y0
B146	VSS13	VSS13	Y0
B147	VSS12	VSS12	Y0
B148	VSS11	VSS11	Y0
B149	VSS10	VSS10	Y0
B150	VSS9	VSS9	Y0
B151	VSS8	VSS8	Y0
B152	VSS7	VSS7	Y0
B153	VSS6	VSS6	Y0
B154	VSS5	VSS5	Y0
B155	VSS4	VSS4	Y0
B156	VSS3	VSS3	Y0
B157	VSS2	VSS2	Y0
B158	VSS1	VSS1	Y0
B159	VSS0	VSS0	Y0



RMD[0..63] RMD[0..63] 6,10,11

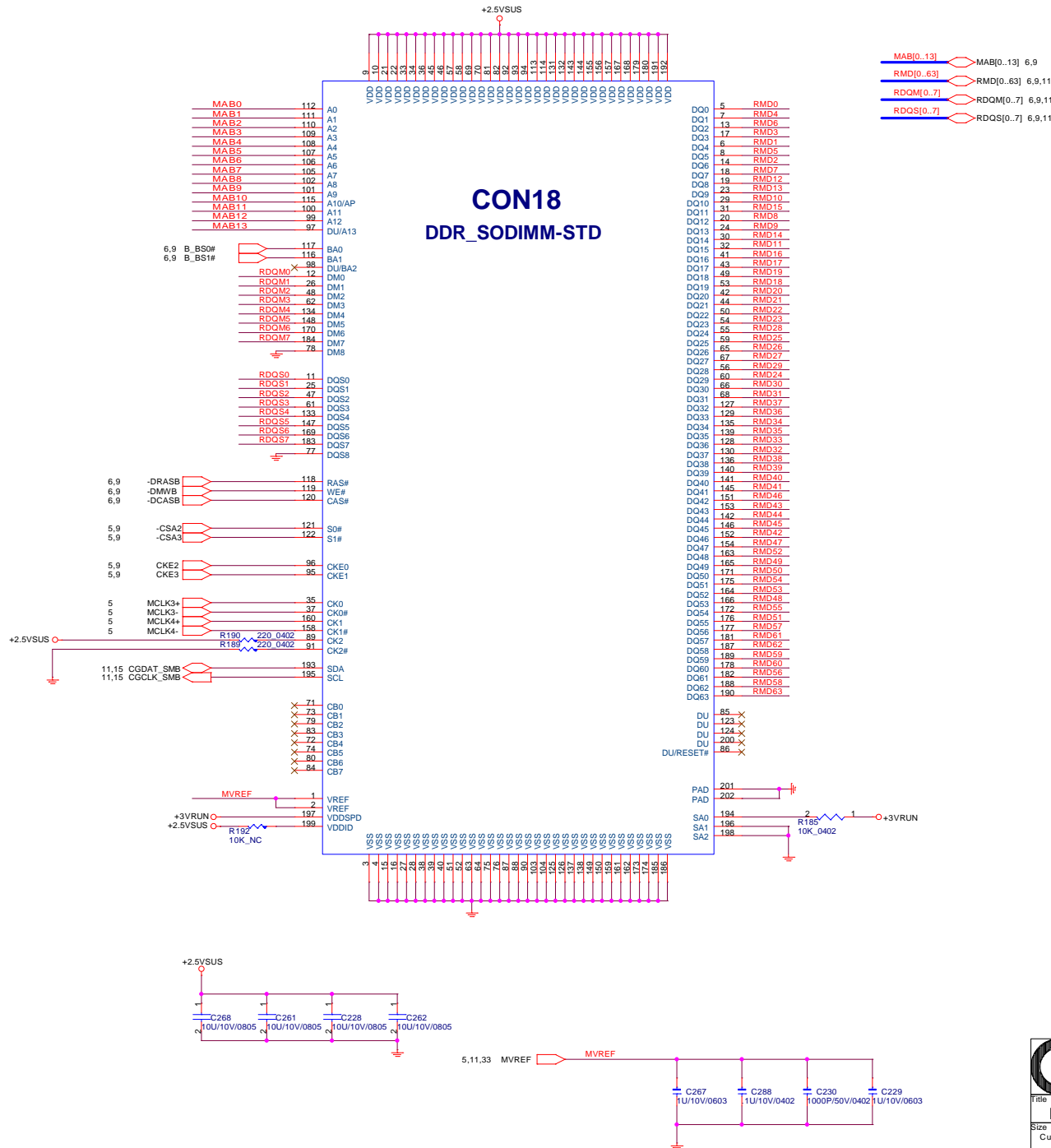
RDQM[0..7] RDQM[0..7] 6,10,11

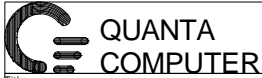
RDQS[0..7] RDQS[0..7] 6,10,11



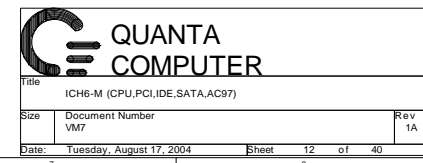
DDR - Modules

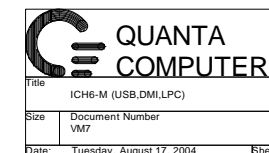
10





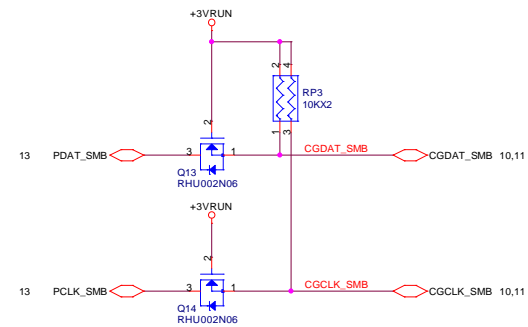
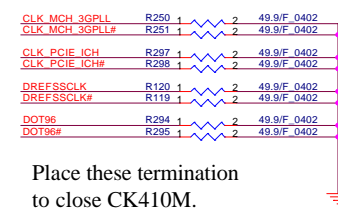
Title DDR (on board)			
Size Custom	Document Number Trend Mark	Rev 1A	
Date: Tuesday, August 17, 2004		Sheet 11	of 40



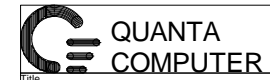




Place these termination
to close CK410M.

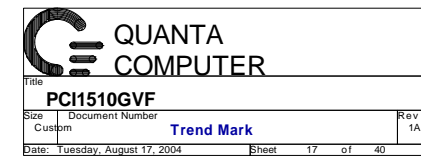


These are for backdrive issue

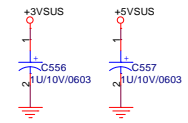
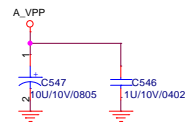
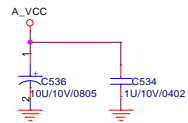
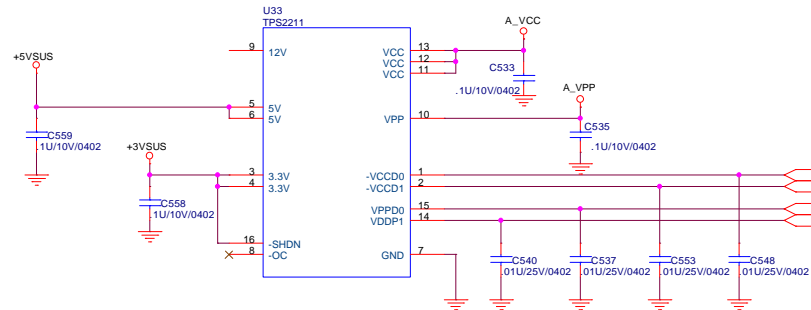


Title			
CLOCK GENERATOR			
Size	Document Number	Rev	
	VM7	1A	
Date:	Tuesday, August 17, 2004	Sheet	15 of 40





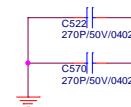
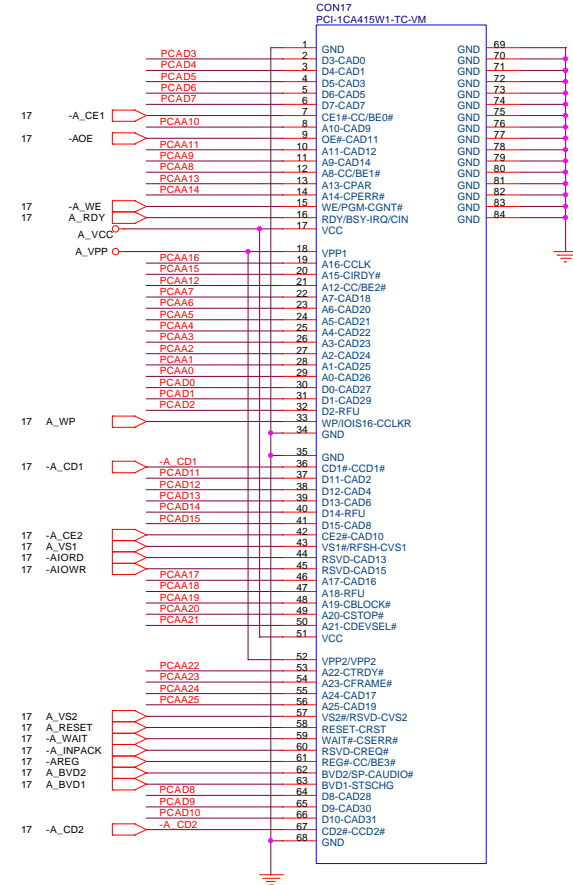
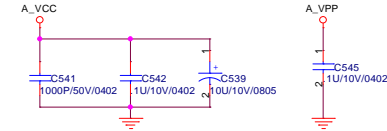
PCMCIA POWER SWITCH



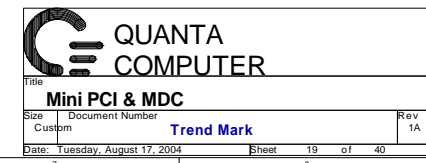
PCMCIA SOCKET

17 PCAA[0..25] PCAA[0..25]

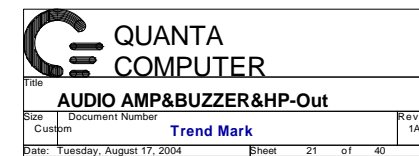
17 PCAD[0..15] PCAD[0..15]

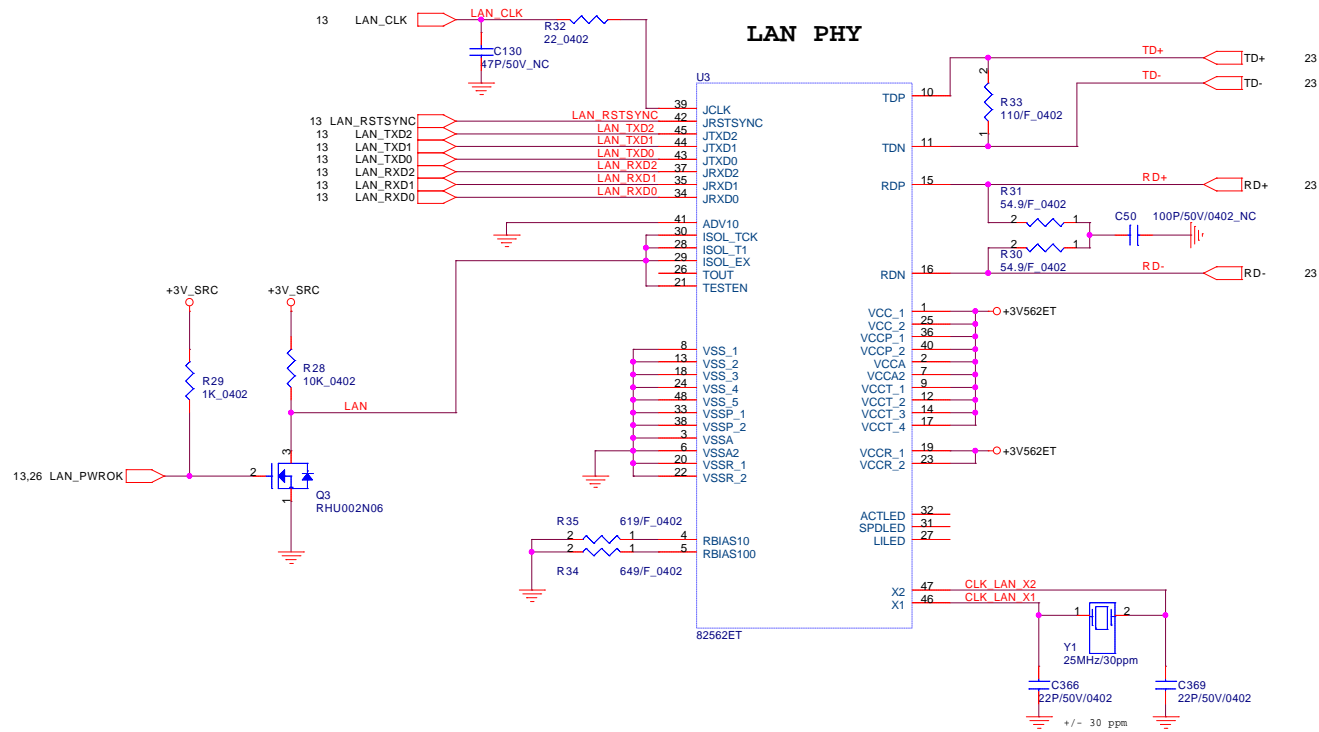


Change
PCMCIA Conn.

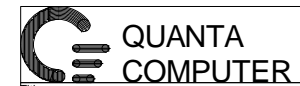
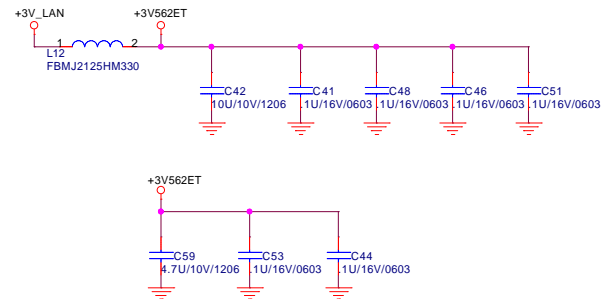
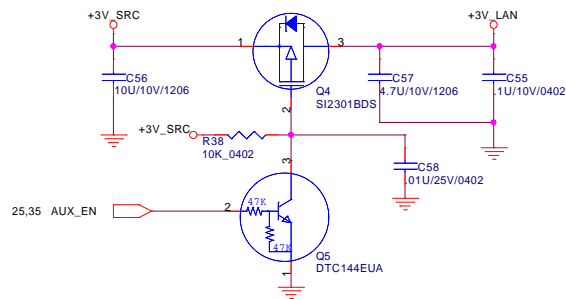








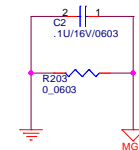
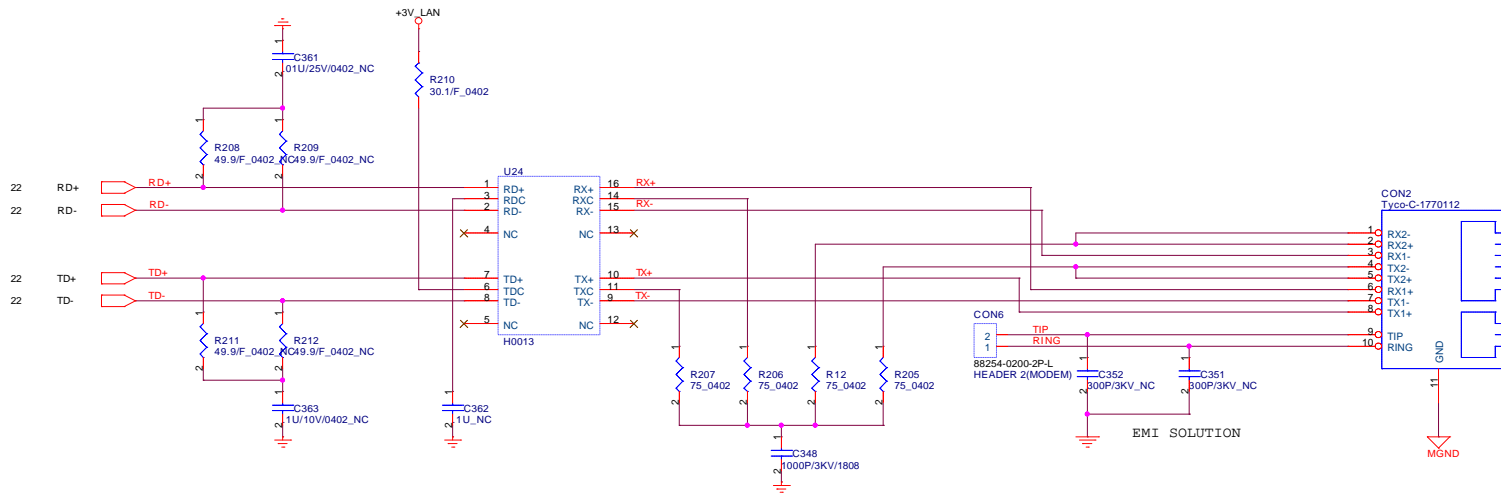
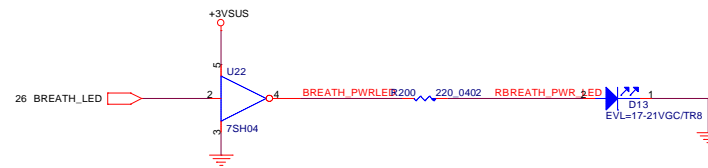
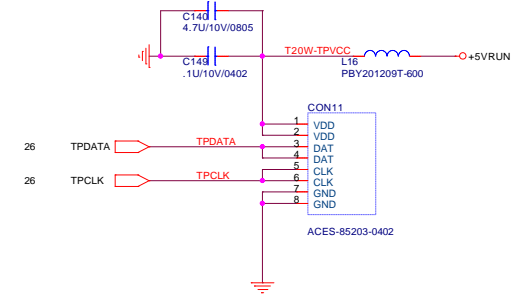
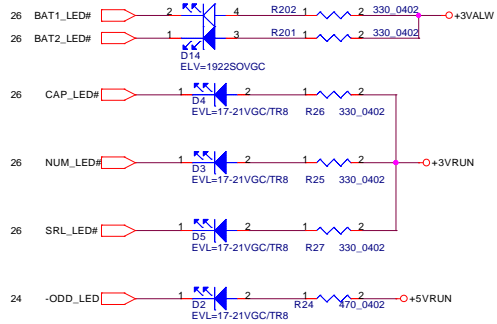
Over 500mA



Title			LAN INTERFACE
Size	Document Number	Rev	
Custom	VM7	1A	
Date:	Tuesday, August 17, 2004	Sheet	22 of 40

TOUCHPAD BOARD CON

23



QUANTA
COMPUTER

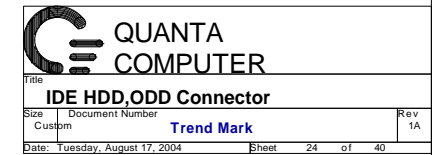
TP & LAN Connector

Document Number
Custom

Date: Tuesday, August 17, 2004

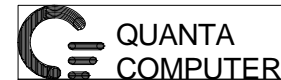
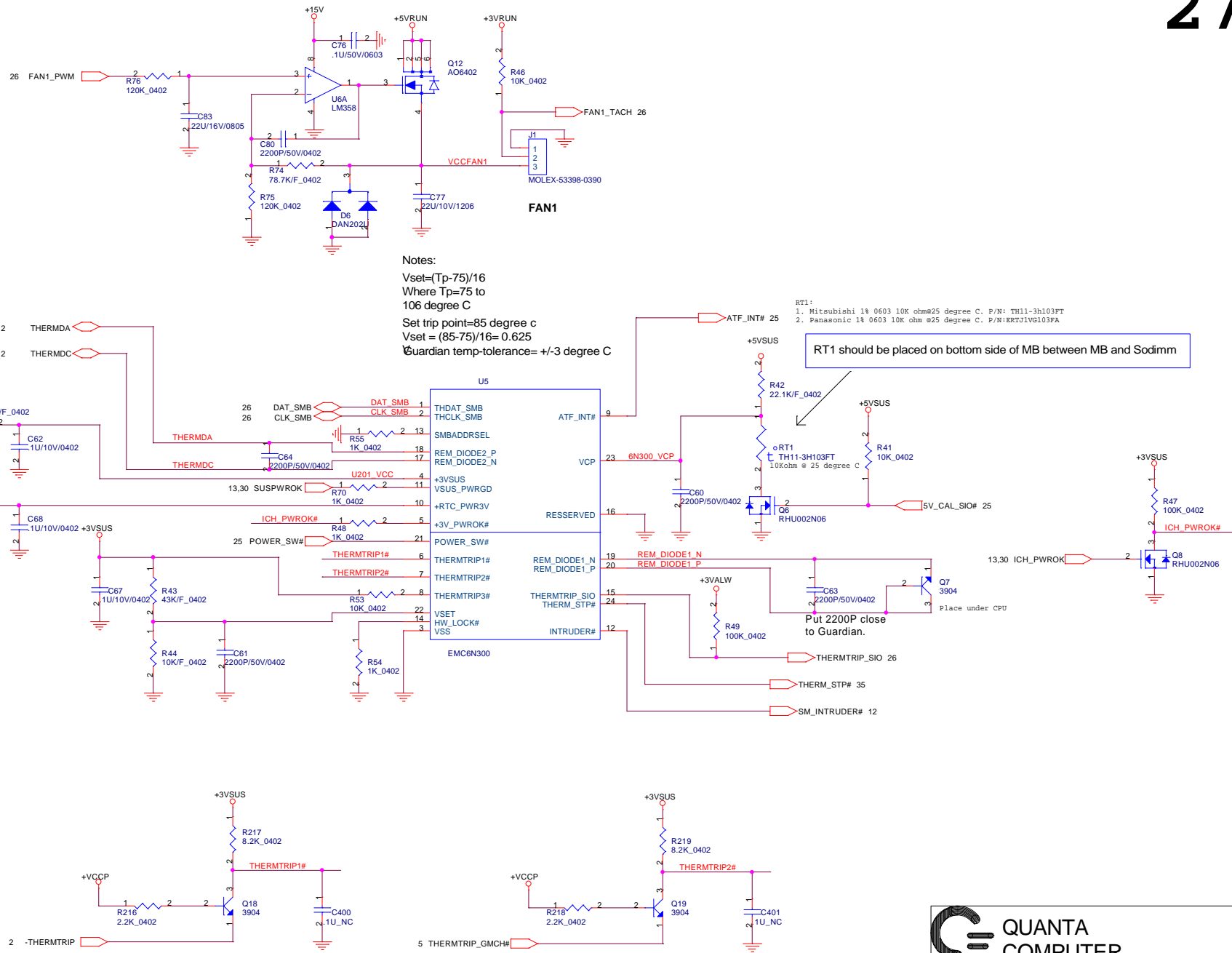
Sheet 23 of 40

Rev 1A

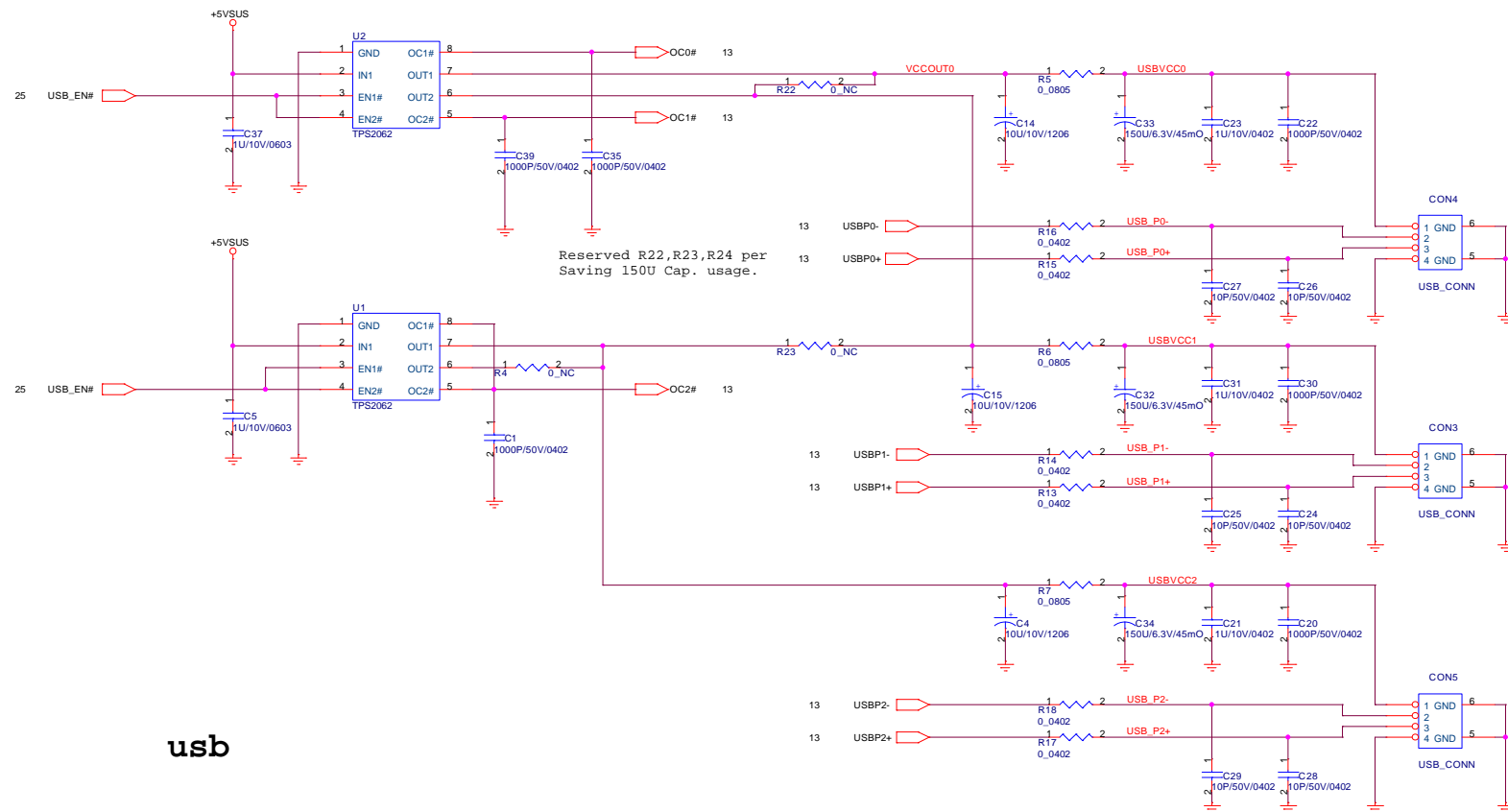




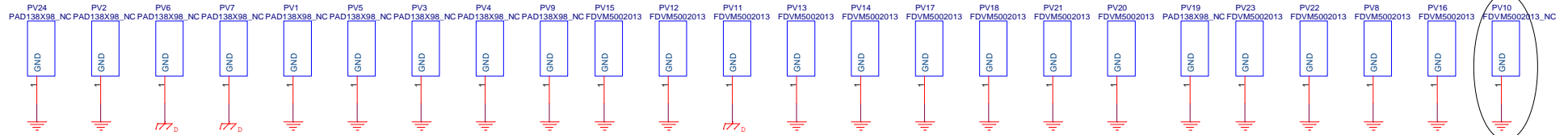




Title: FAN & THERMAL		
Size: VM7	Document Number: VM7	Rev: 1A
Date: Tuesday, August 17, 2004	Sheet: 27	of 40



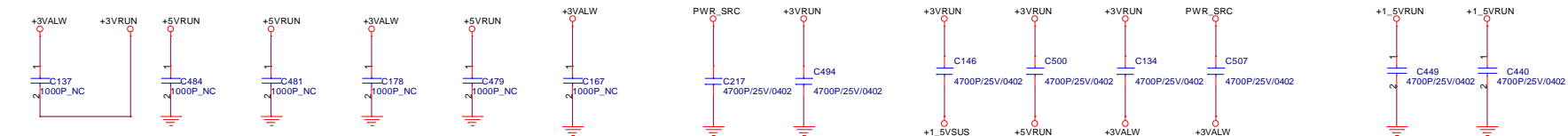
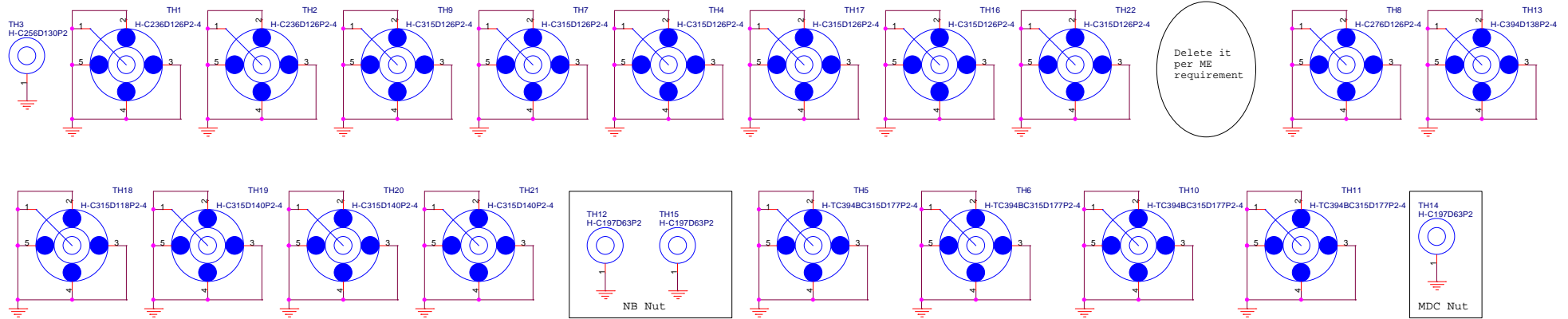
usb



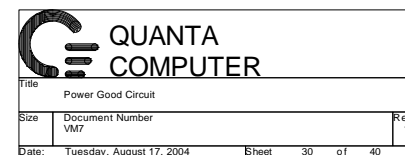
A_HPCASE-GND

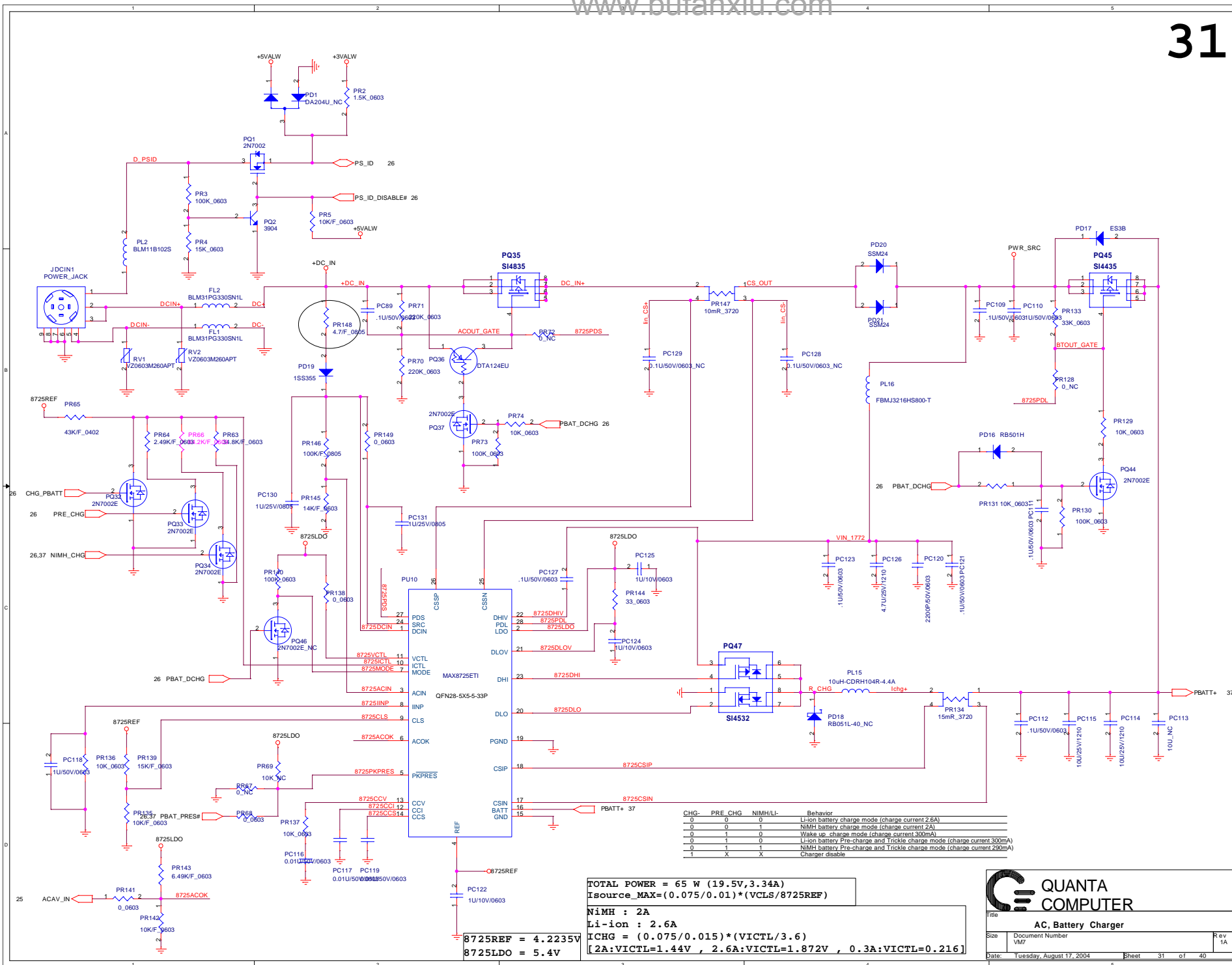
EMI added

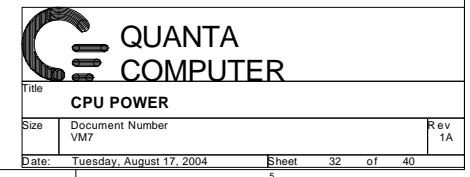
EMI SOLUTION

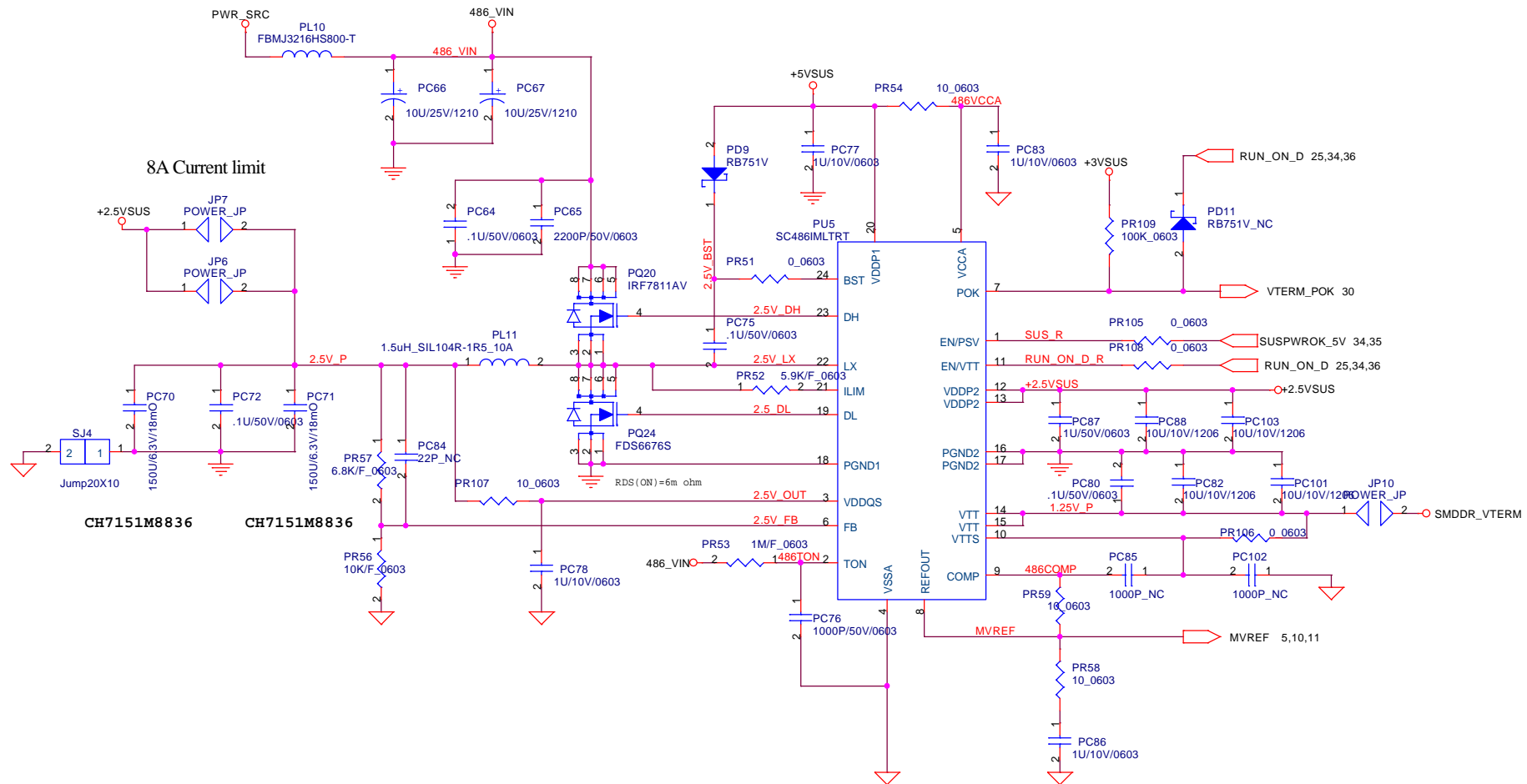


EMI require in 0810







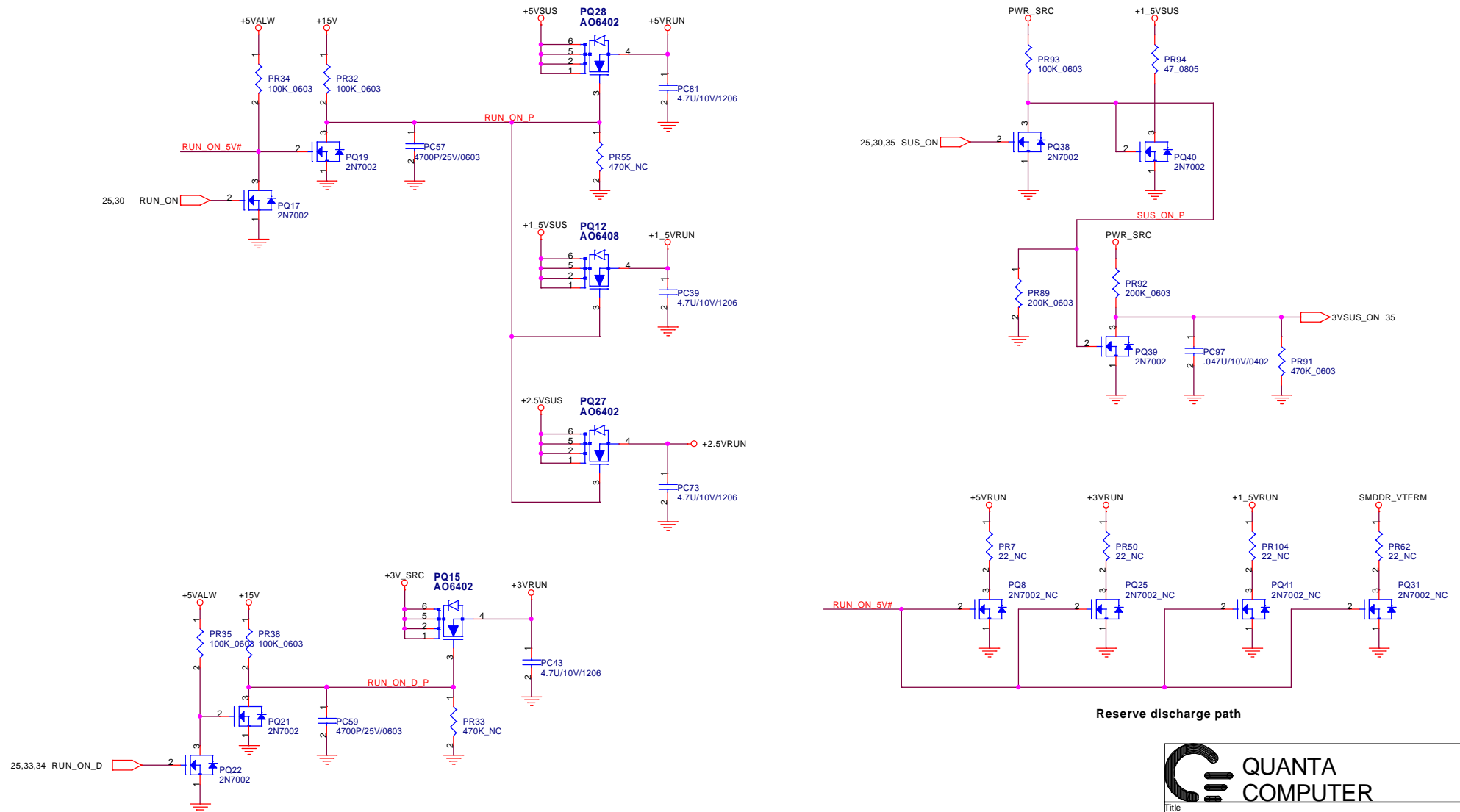


Title			2.5VSUS,SMDR_VTER
Size	Document Number	Rev	
	VM7	1A	
Date:	Tuesday, August 17, 2004	Sheet	33 of 40





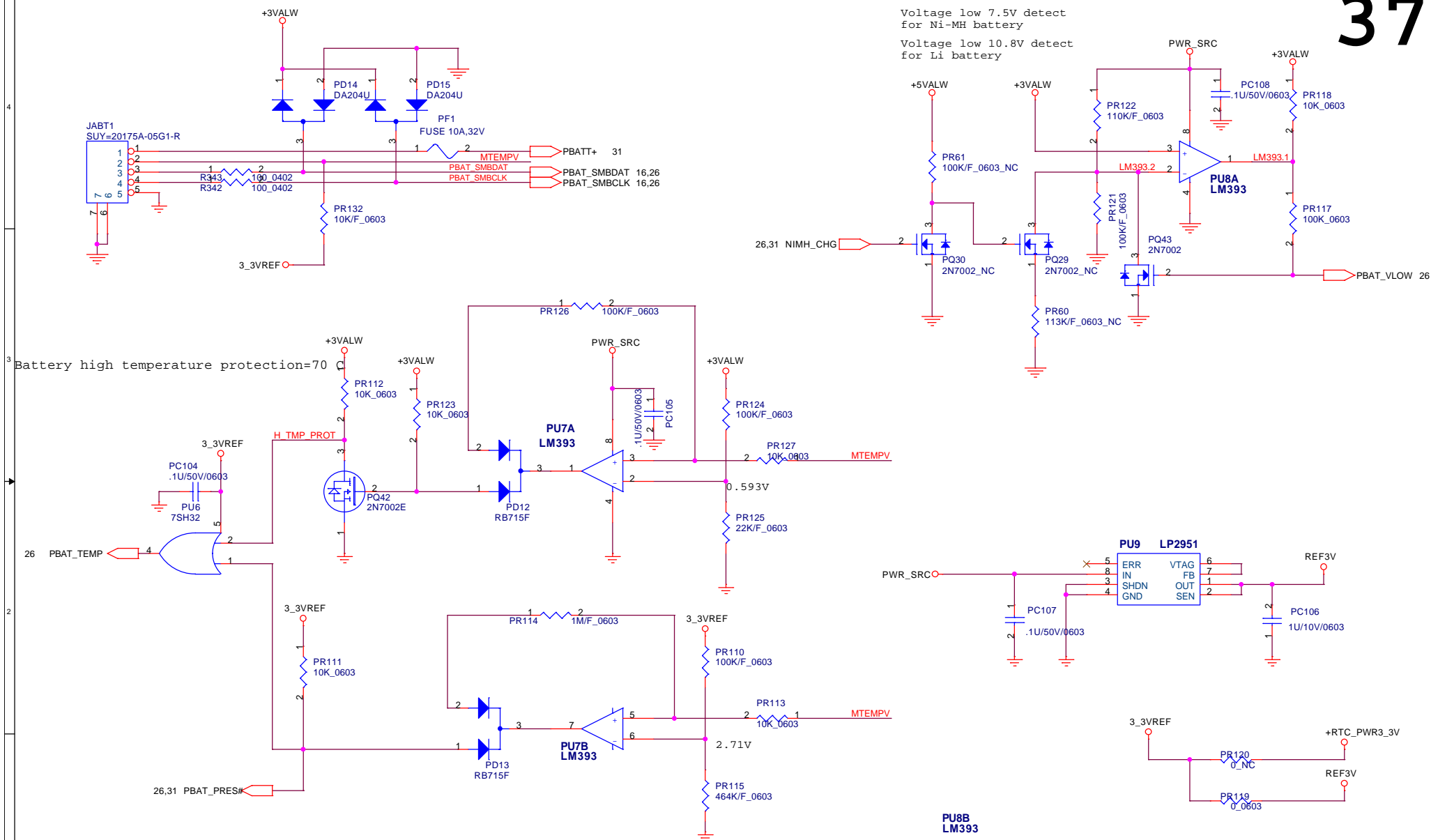
Sheet 35 of 40



Reserve discharge path



Title		
RUN POWER SW		
Size	Document Number	Rev
VM7		1A
Date:	Tuesday, August 17, 2004	Sheet 36 of 40



When BATTERY not insert MTEMPV 3.3V
 When BATTERY -10 C => NTC=46.6K MTEMPV 2.71V
 When BATTERY 0C => NTC=28.8K MTEMPV=1.45V
 When BATTERY 25 C => NTC=10K MTEMPV=1.65V
 When BATTERY 60 C => NTC=3K MTEMPV=0.76V
 When BATTERY 70 C => NTC=2.19K MTEMPV=0.593V

	PWR_SRC									
	+3VAL	+3VALL	BAT 25C	BAT-20C	BAT 80C	BAT NOT IN	PBAT PRES#	PBAT TEMP	PBAT VLOW	
When Battery Only	V	-	X	X	X	-	Lo	Not Active	Not Active	
When AC and Battery in	V	V	V	V	V	-	Lo	Lo	Active	
	V	V	V	V	V	-	Hi	Hi	Active	
When AC In , Battery Not In	V	V	X	X	X	V	Hi	X	X	



Battery connector & Protection

Size	Document Number	Rev
VM7		1A
Date:	Tuesday, August 17, 2004	Sheet 37 of 40