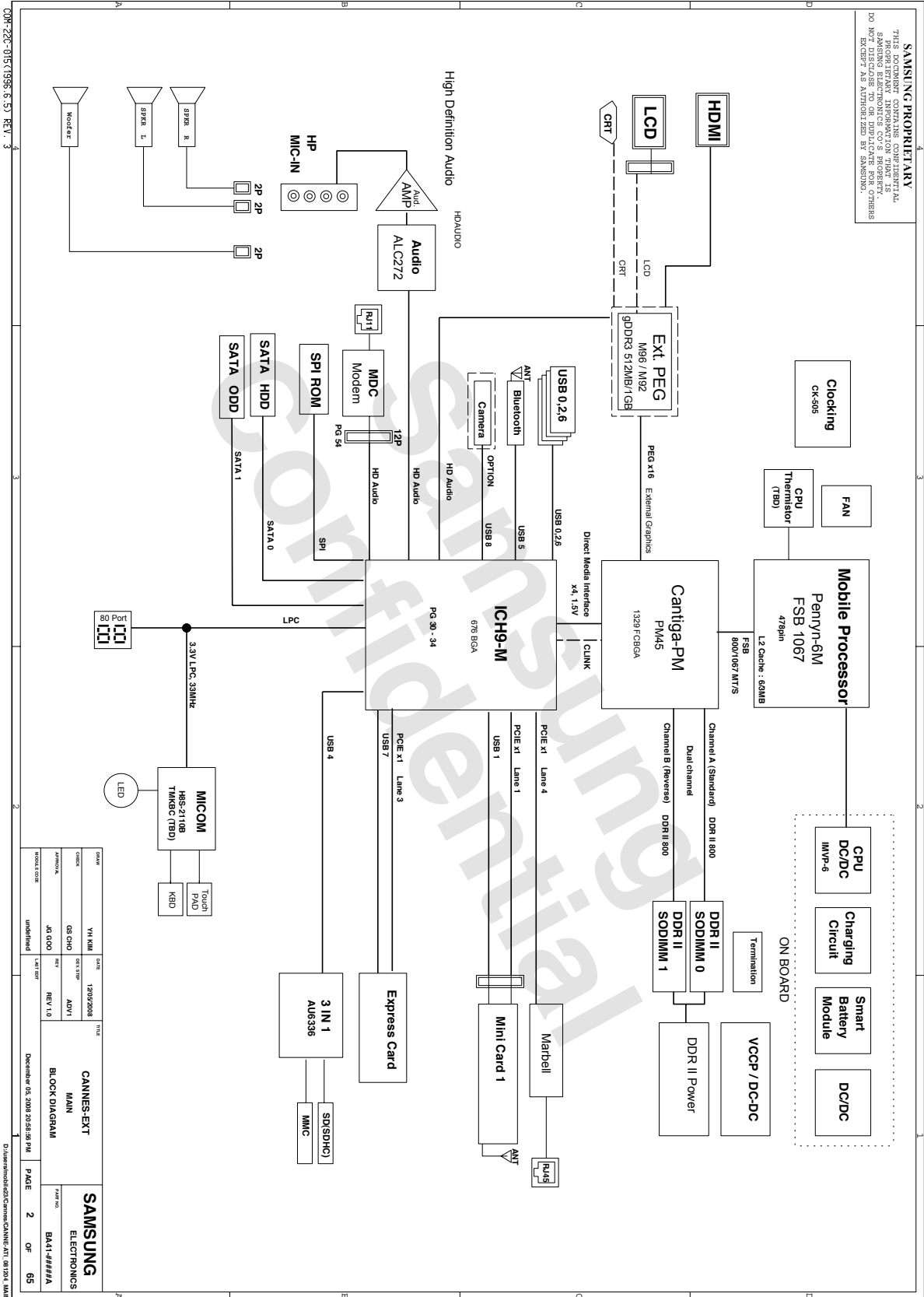


8. Block Diagram and Schematic

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<h1 style="margin: 0;">CANNES-EXT(GFX:AMD)</h1>																					
<p>CPU : Intel Penryn</p> <p>Chip Set : PM45 & ICH9M</p> <p>Graphic : M96/M92/gDDR3 1G</p> <p>Remarks : Montevina Platform</p>																					
<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="padding: 5px;">Model Name : CANNES-EXT</td> </tr> <tr> <td style="padding: 5px;">PBA Name : MAIN</td> </tr> <tr> <td style="padding: 5px;">PCB Code : BA41-XXXXXXA</td> </tr> <tr> <td style="padding: 5px;">Dev. Step : MP</td> </tr> <tr> <td style="padding: 5px;">Revision : 1.0</td> </tr> <tr> <td style="padding: 5px;">T.R. Date : 2009.04.21</td> </tr> </table>		Model Name : CANNES-EXT	PBA Name : MAIN	PCB Code : BA41-XXXXXXA	Dev. Step : MP	Revision : 1.0	T.R. Date : 2009.04.21														
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Design	CHECK	APPROVAL																			
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SAMSUNG	BA41-####A																				
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SCHEMATIC ANNOTATIONS AND BOARD INFORMATION

BOARD INFORMATION

PCI Devices	IDSEL#	REQ/GNT#	Interrupts
Cardbus	AD25	3	A, B, C
USB	AD28(Internal)	-	USB2.0 #0 (USB) : A USB2.0 #1 (USB) : D USB2.0 #2 (USB) : E USB2.0 #3 (USB) : F USB2.0 #4 (EHCI) : H
Hub to PCI	AD30(Internal)	-	B
LP PC bridge/IDE/COM/SMBUS	AD33(Internal)	-	B
Internal MAC	AD24(Internal)	-	F
AC Link	-	-	F
GLAN	-	-	F

Voltage Rails

Primary DC system power supply (7 to 21V)
 VCC_CORE Core Voltage for CPU
 VCC_CORE Core Voltage for GPU & CH9M
 P1.8V Core Voltage for CPU
 P3.3V_MIOEM P3.3V always power rail for MIOEM
 P1.8V 1.8V switched power rail (off in S4-S5)
 P1.8V 1.8V switched power rail (off in S4-S5)
 P1.8V 1.8V power rail for DDR (off in S4-S5)
 P3.3V 3.3V power rail for DDR (off in S4-S5)
 P3.3V 3.3V power rail for DDR (off in S4-S5)
 P3.3V 3.3V switched power rail (off in S4-S5)
 P5.0V_AUX 5.0V switched power rail (off in S4-S5)
 P5.0V_AUX 5.0V always power rail

USB PORT Assign	PORT #	ASSIGNED TO	PC Express Assign	PORT #	ASSIGNED TO
0	SYSTEM PORT 0	1	Mini-Card 1 (M-LAN)		
1	SYSTEM PORT 1	2	NC		
2	SYSTEM PORT 2	3	Mini-Card 2 (ROBSON or DVB-T)		
3	NC	4	NC		
4	Bluetooth	5	NC		
5	Bluetooth				
6	Camera				
7	NC				
8	NC				

Crystal / Oscillator	TYPE	FREQUENCY	DEVICE	USAGE
Crystal	32.768KHz	IC94M1	Real Time Clock	
Crystal	10MHz	MICOM	HD4F72169/2160	
Crystal	14.318MHz	CLOCK-Generator	CK-505	
Crystal	25MHz	LAN	Intel LAN	

LCD Pannel Detect (TBD)	Devices	Resolution	PANNEL_DETECT_0

IC / SMB Address	Devices	Address	Hex	Bus	SMBUS Master
IC94M1	GPU Thermal Sensor	0111 101x	7Ah	-	Thermal Sensor
SOD1MM0	SOD1MM0	1000 000x	7Ah	-	-
SOD1MM0	SOD1MM0	0001 000x	7Ah	-	-
SOD1MM0	Thermal Sensor on SOD1MM0	0011 000x	30h	-	-
SOD1MM0	Thermal Sensor on SOD1MM1	0011 010x	30h	-	-
SOD1MM0	Thermal Sensor on SOD1MM2	1101 010x	20h	-	-

Clock, Unused, Clock Output Disable

REVISION HISTORY

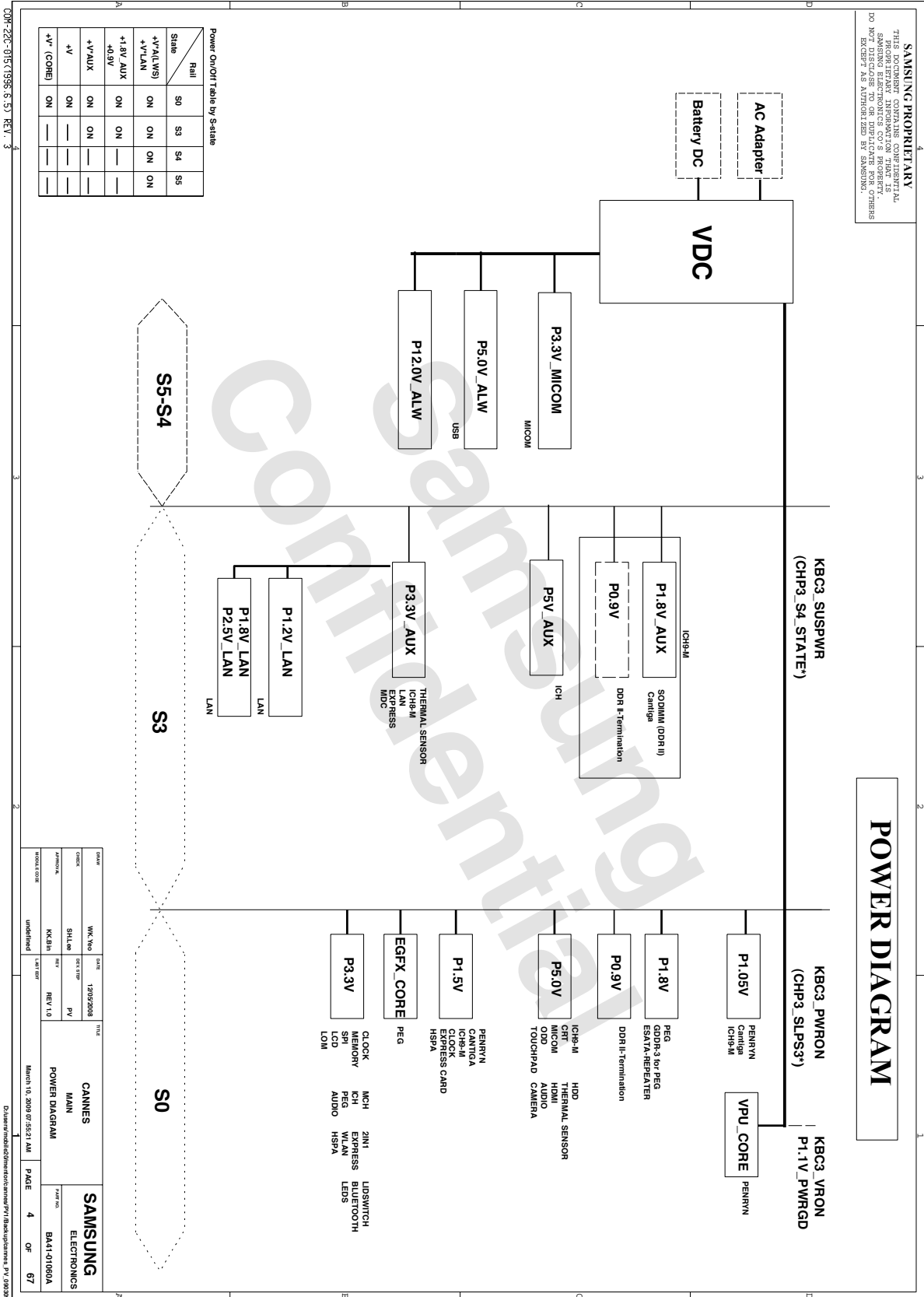
See rev notes for more information.

DATE	DESCRIPTION	BY	CHK	REV	FILE
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	SHL_Nol				MAIN
	KCB_Nol				BOARD INFO

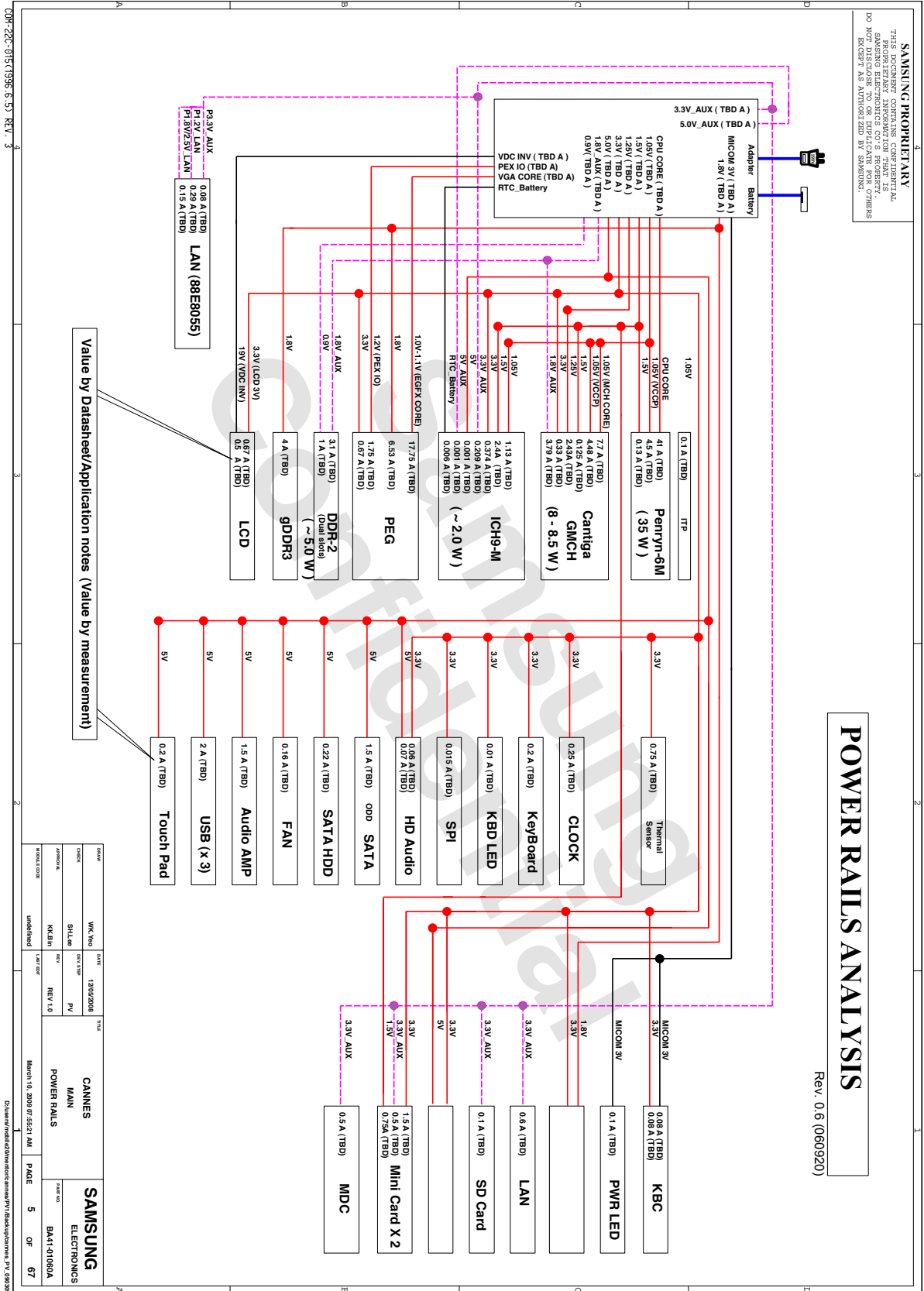
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12/05/2008	WK_Nol				CANNES
	SHL_Nol				MAIN
	KCB_Nol				BOARD INFO

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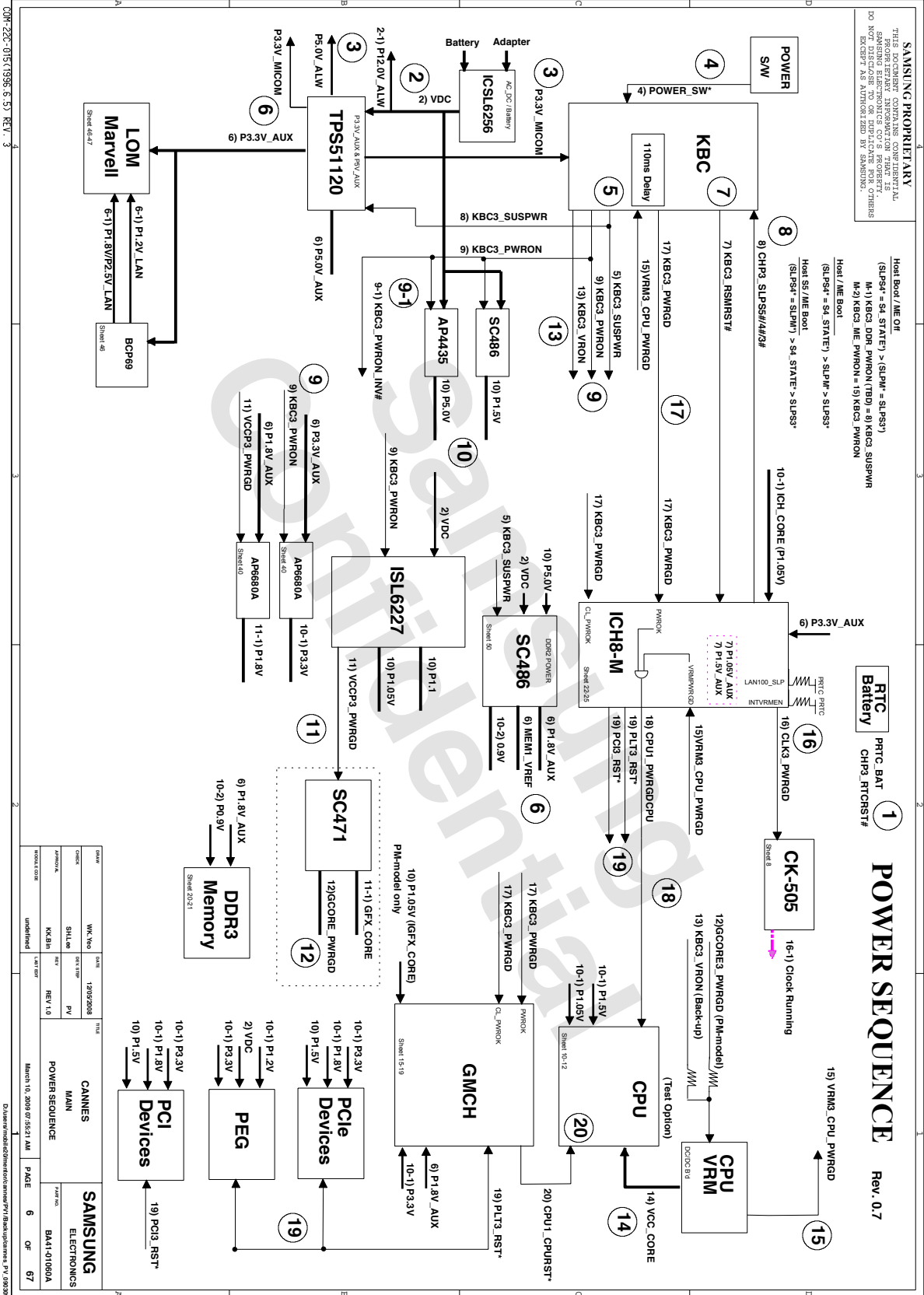
8. Block Diagram and Schematic



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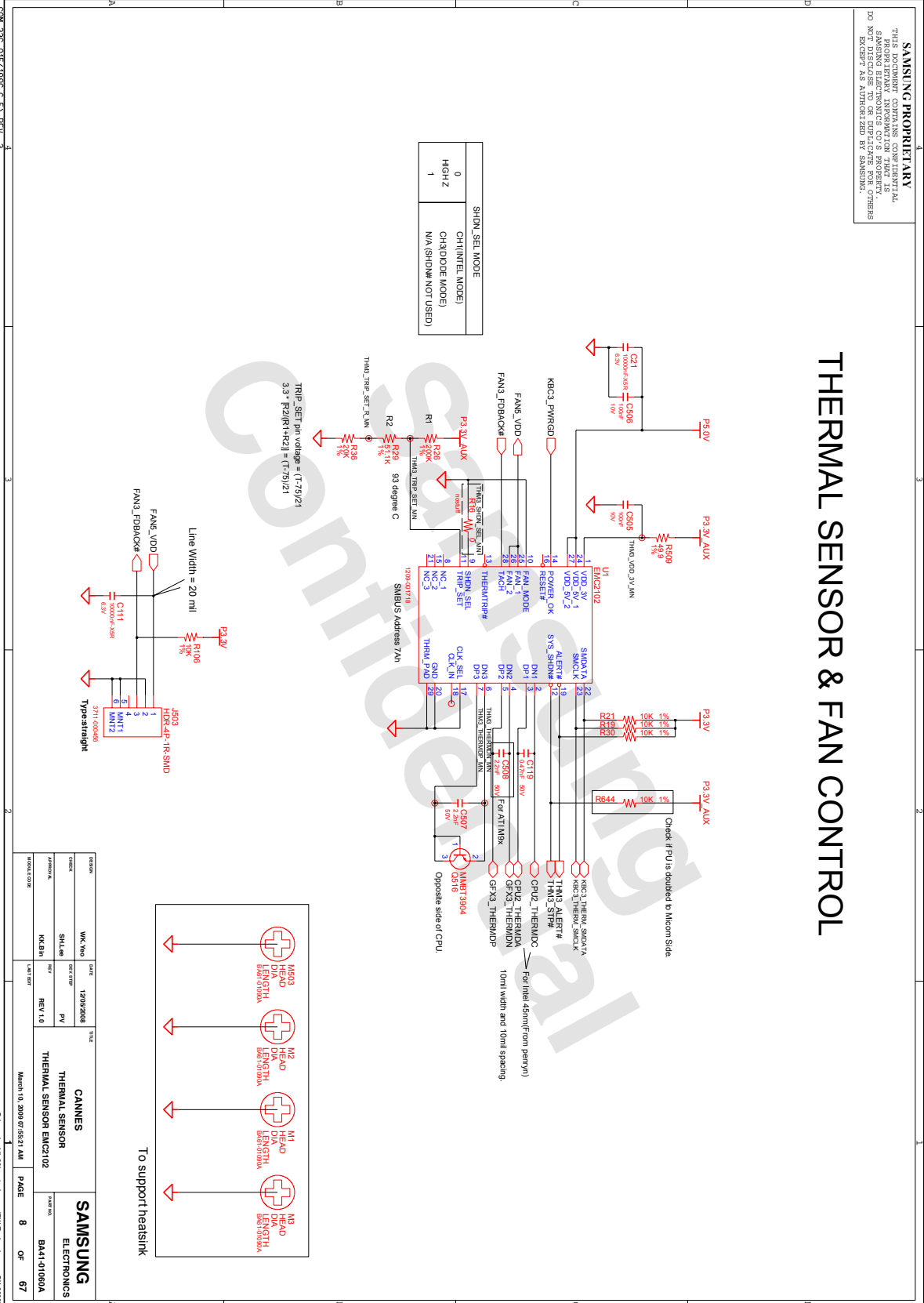


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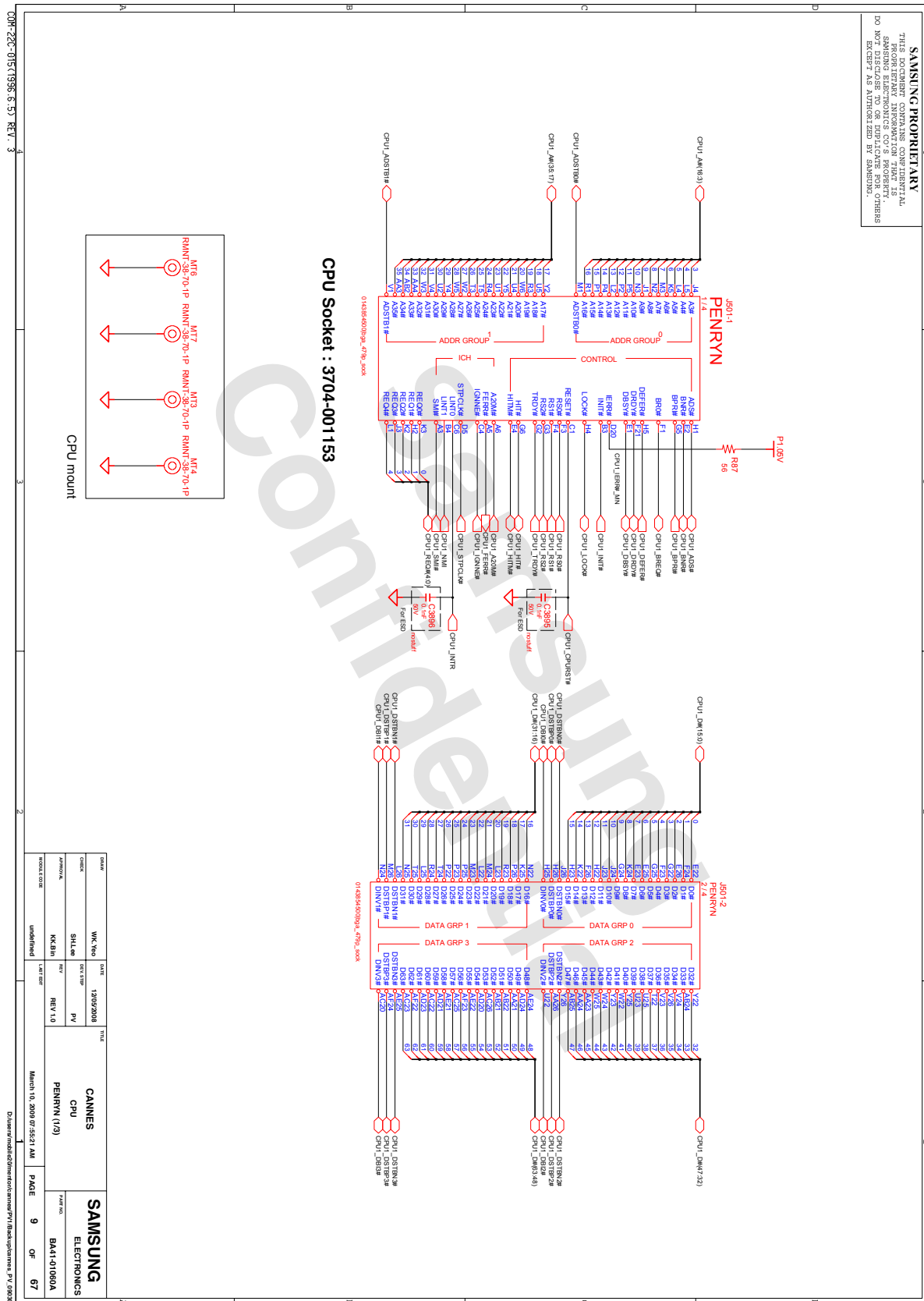


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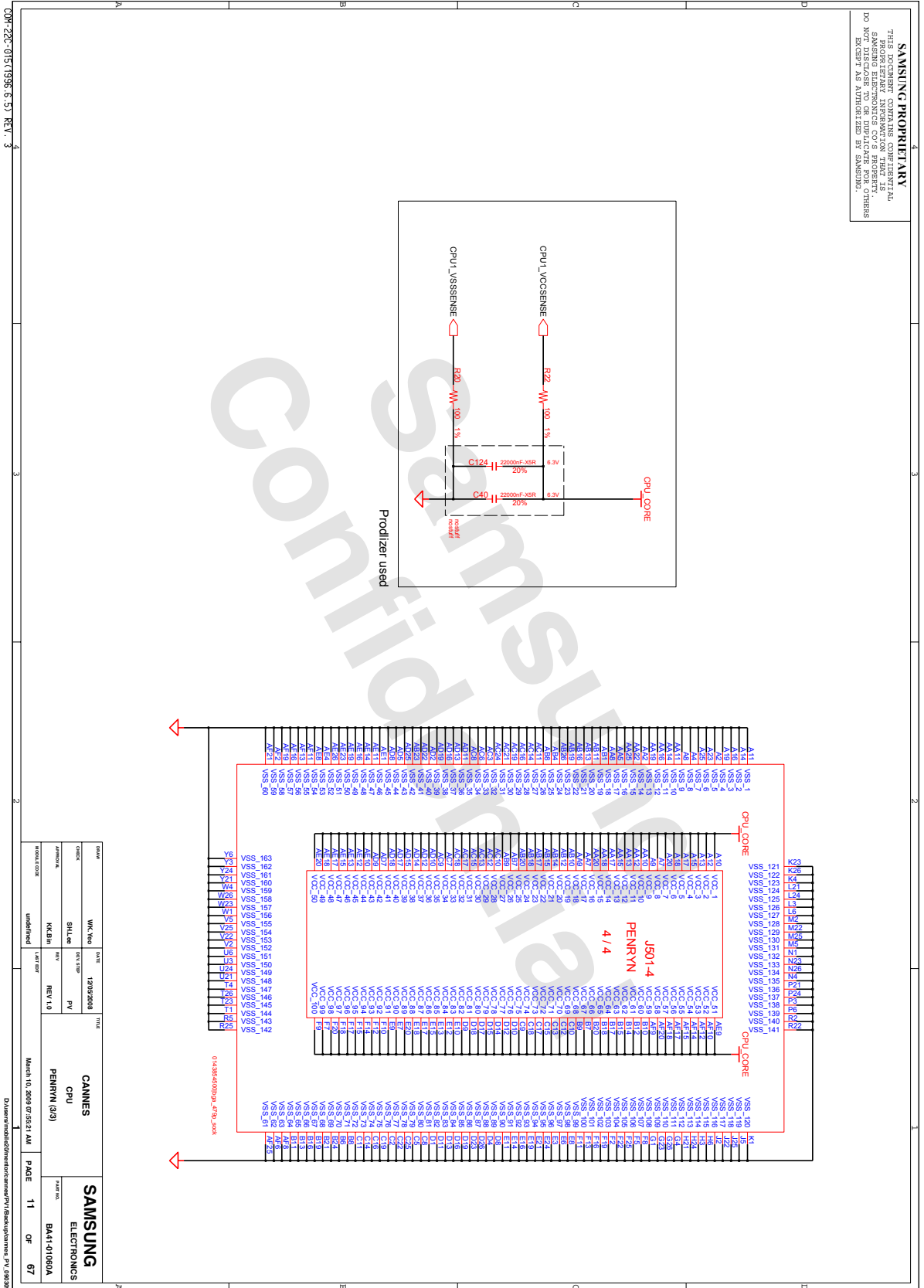
THERMAL SENSOR & FAN CONTROL



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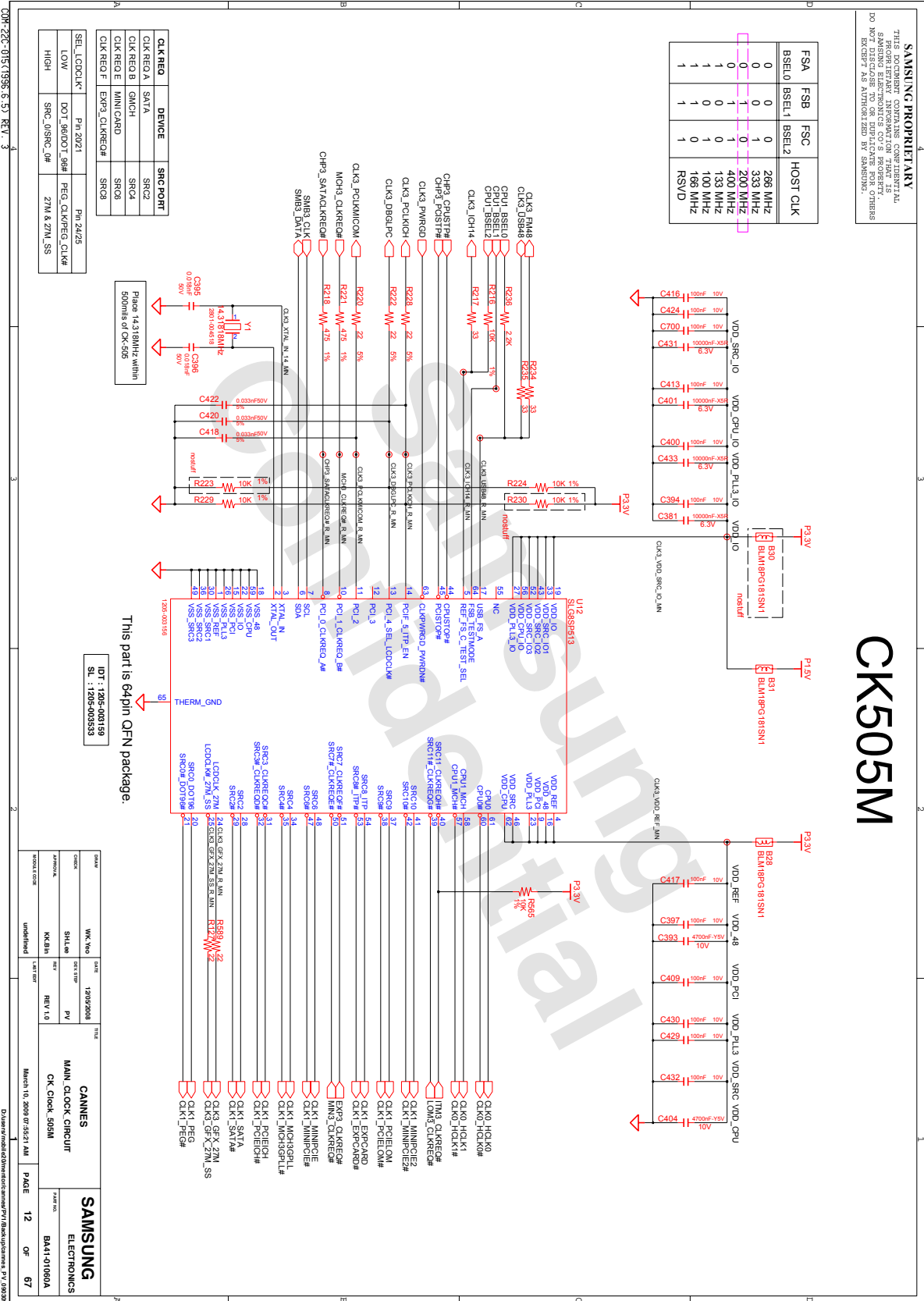


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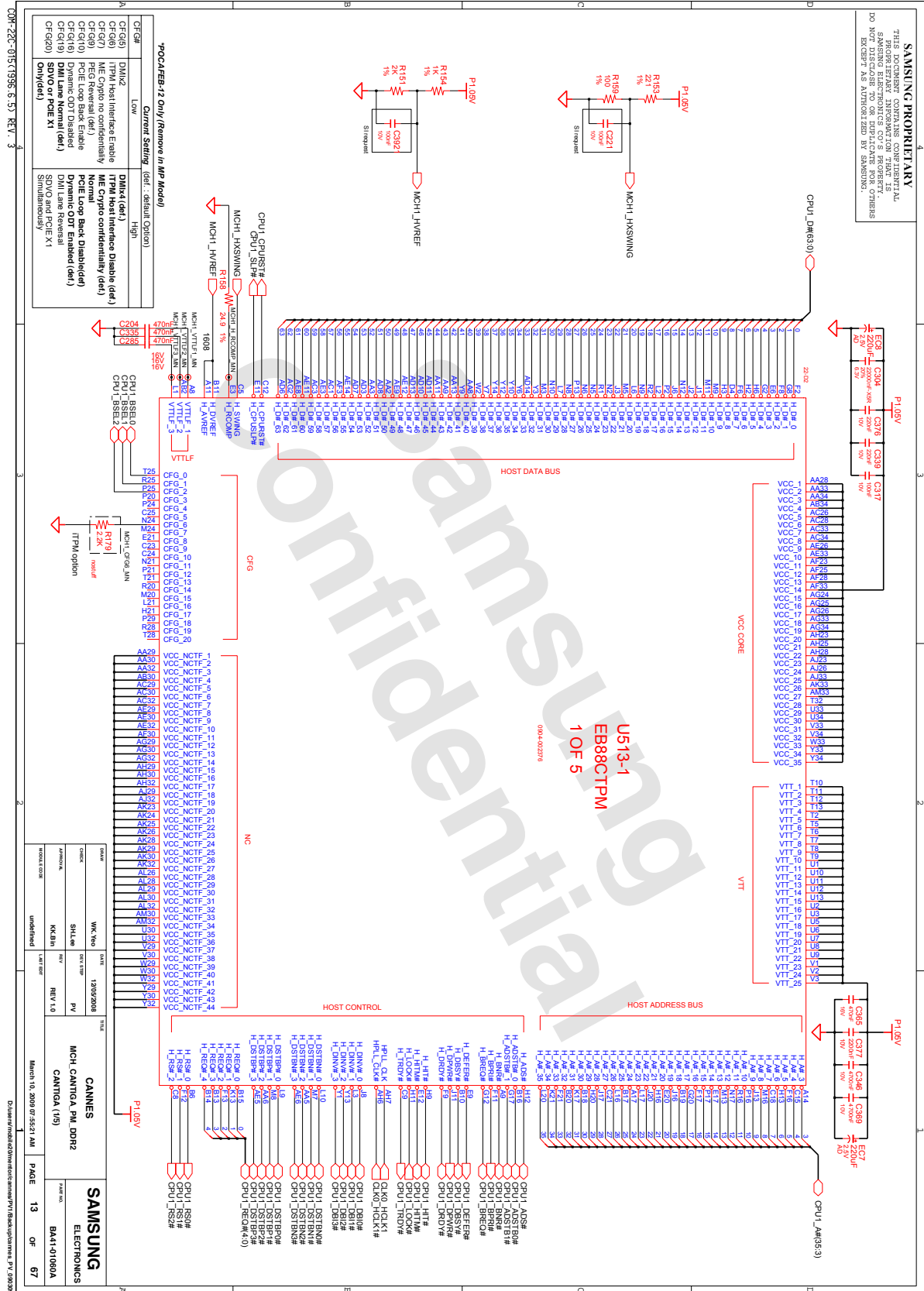
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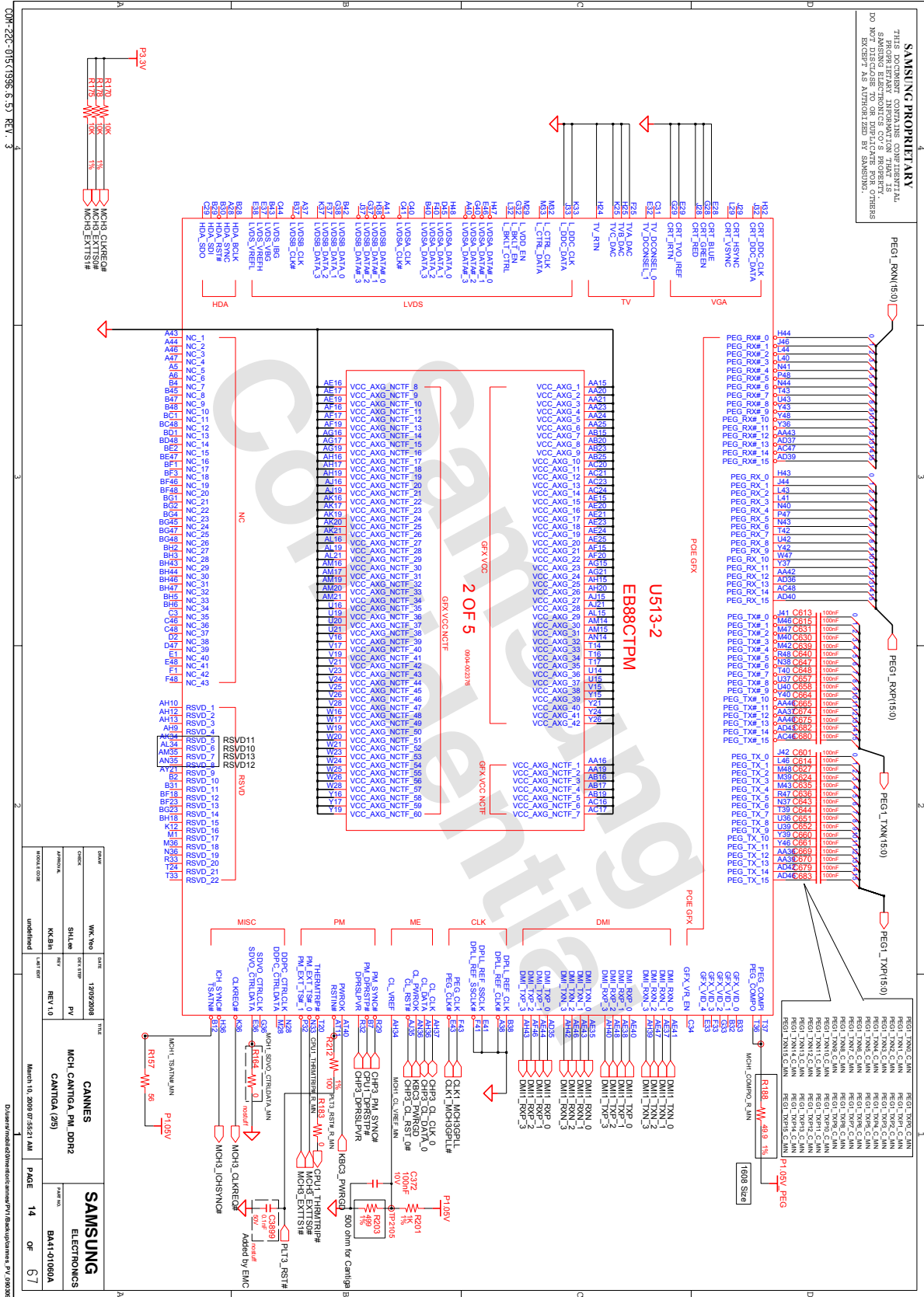
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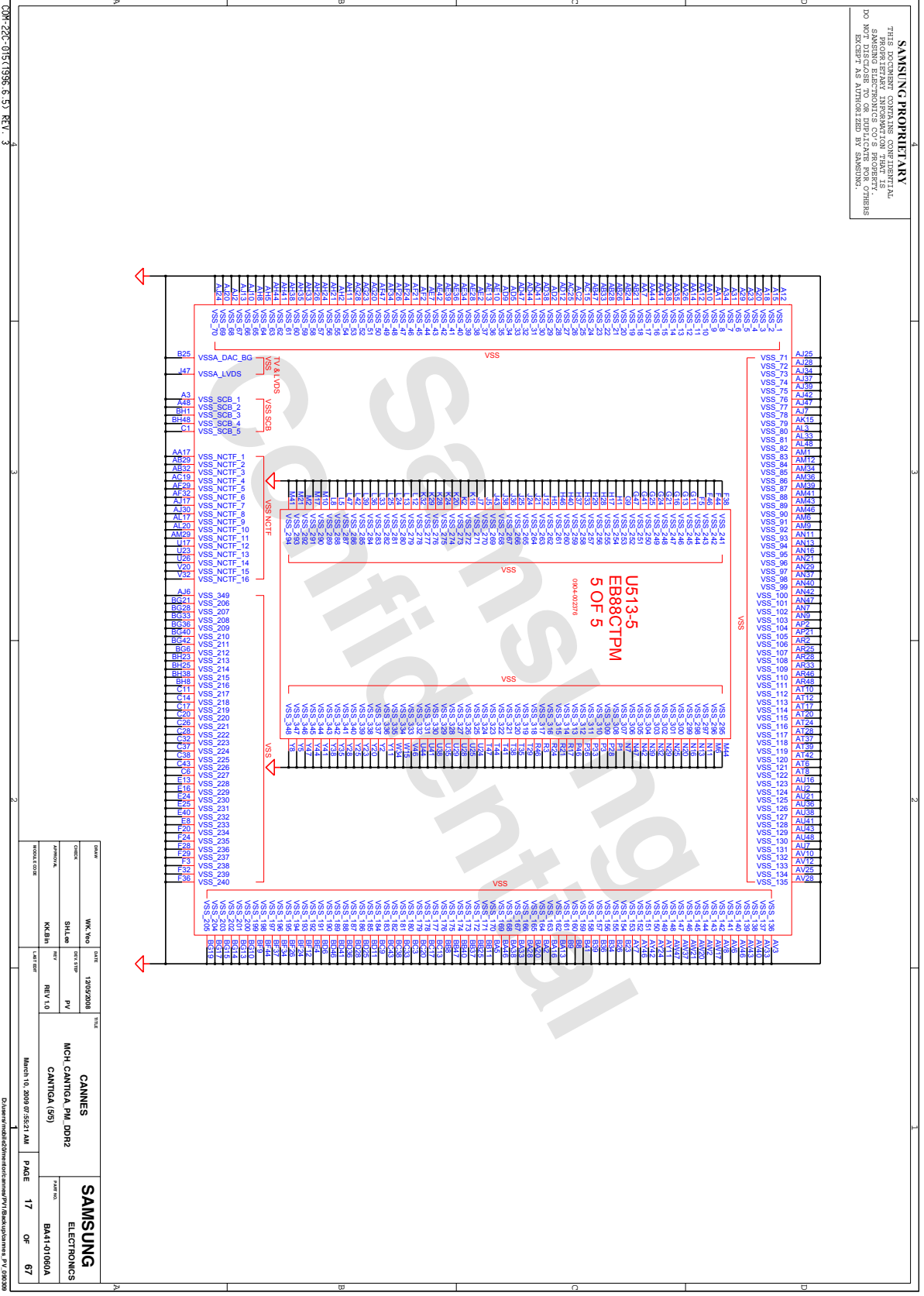
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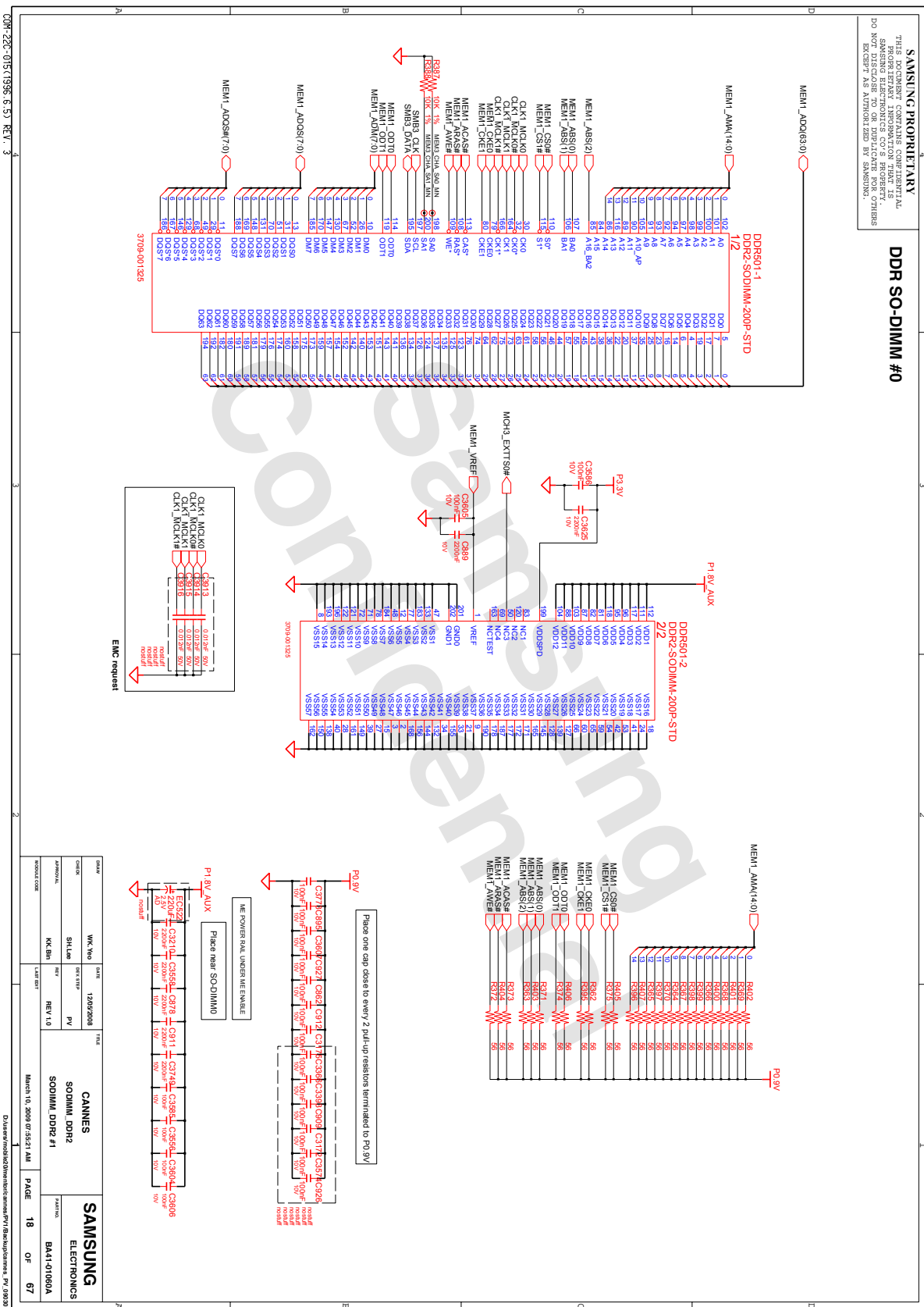
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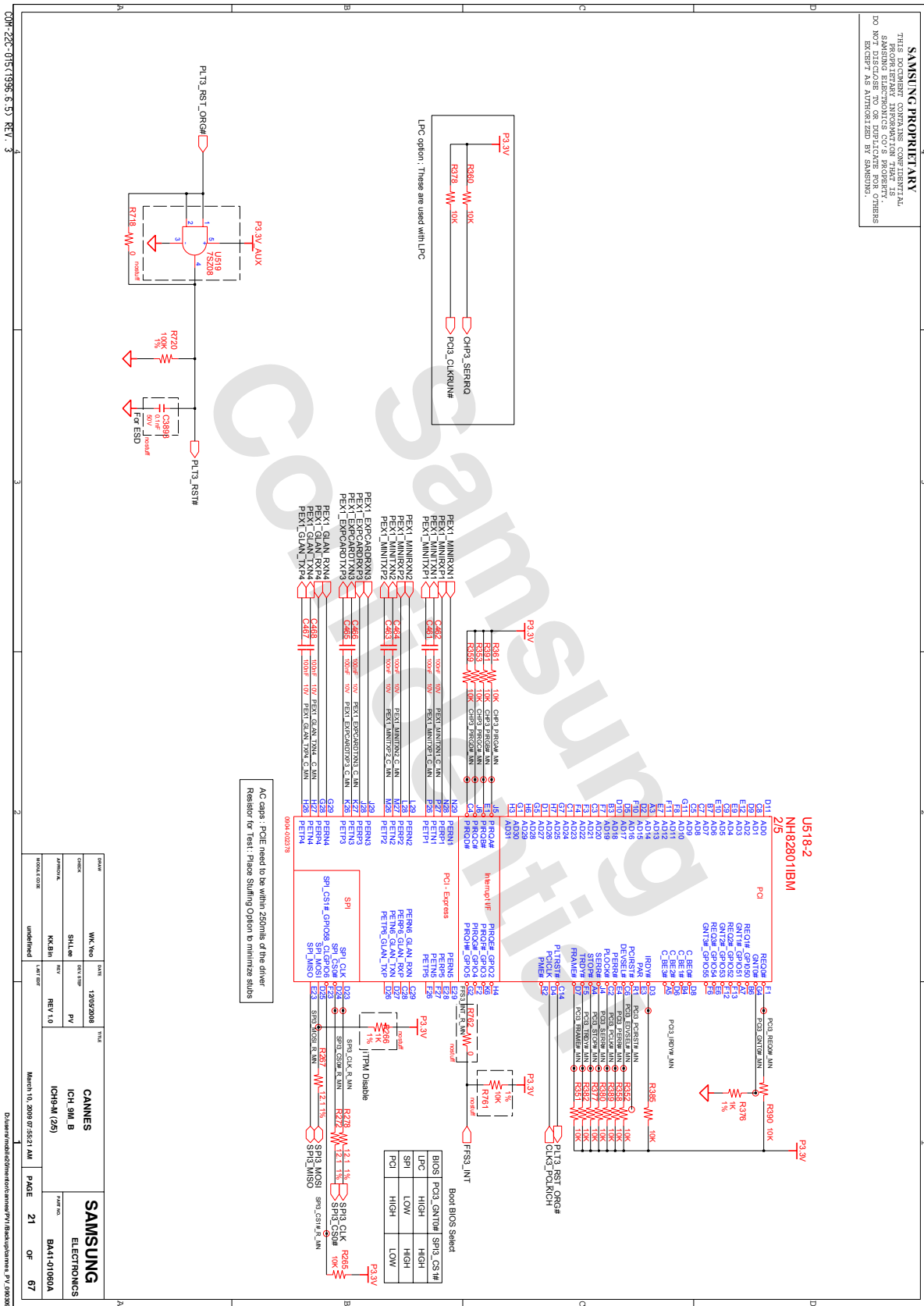
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APP	REV	REV 1.0	CANTIGA (95)
DESIGNER	KKBBH		
DATE	March 10, 2009 07:25:21 AM	PAGE	17
OF	67		

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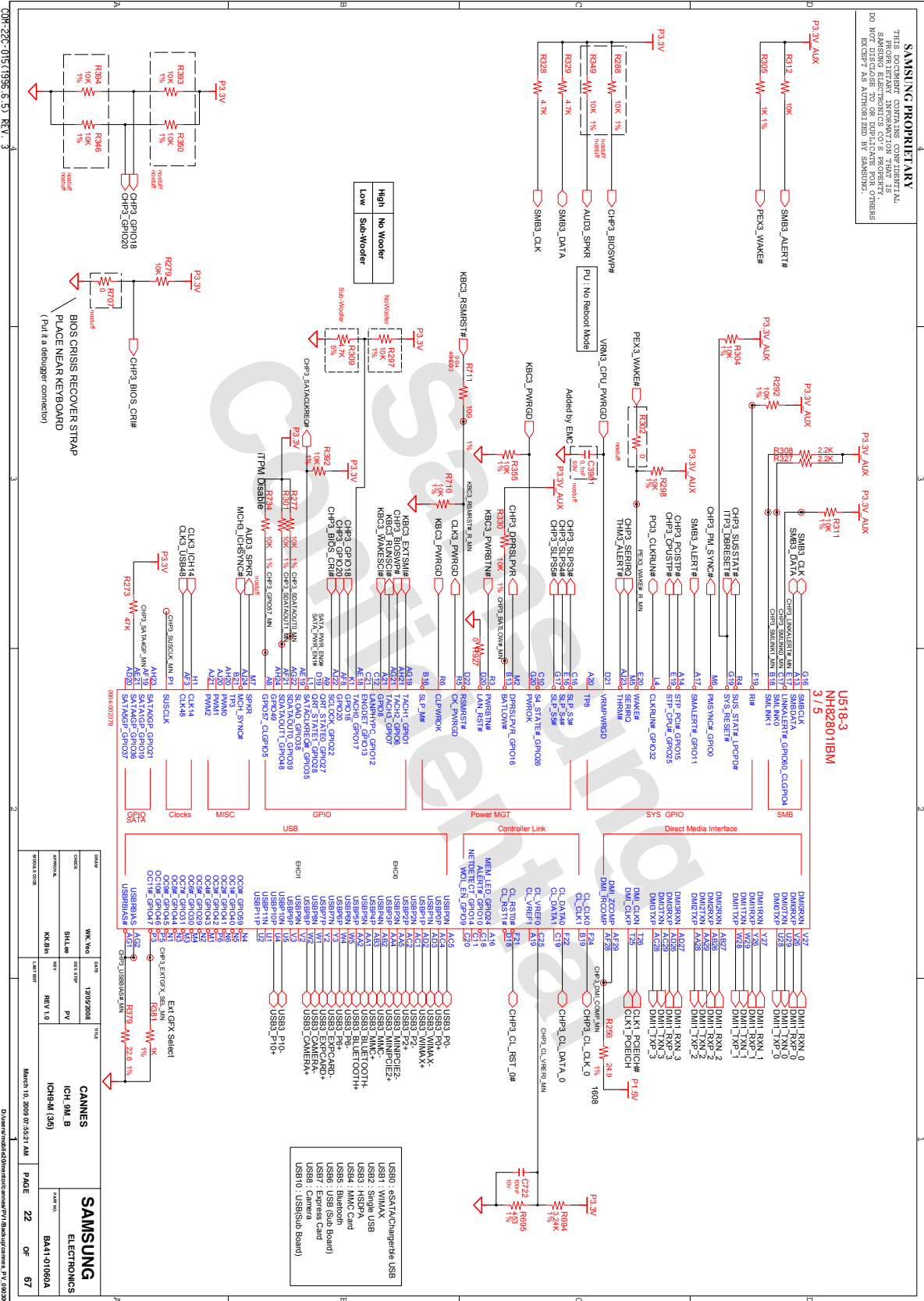
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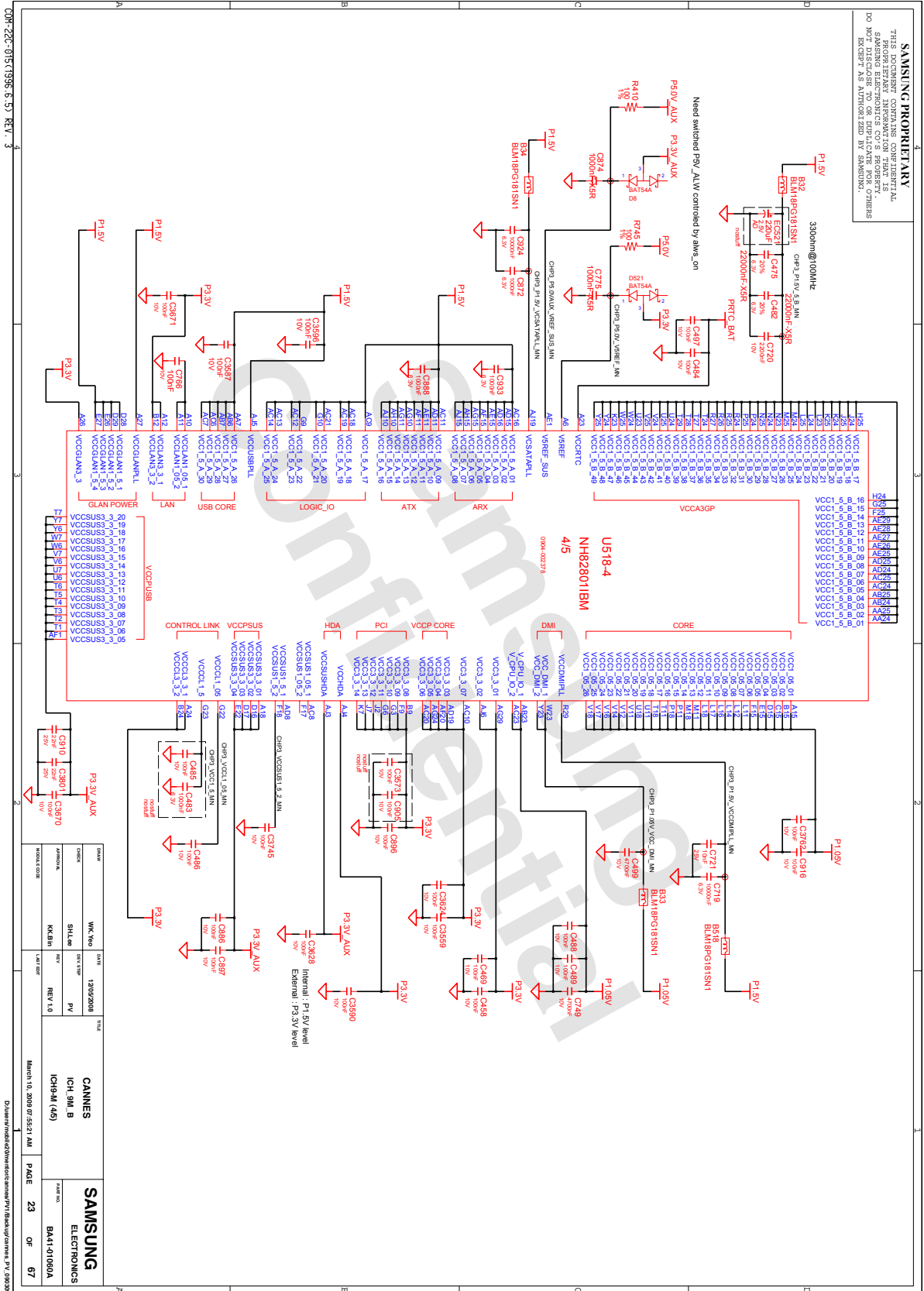


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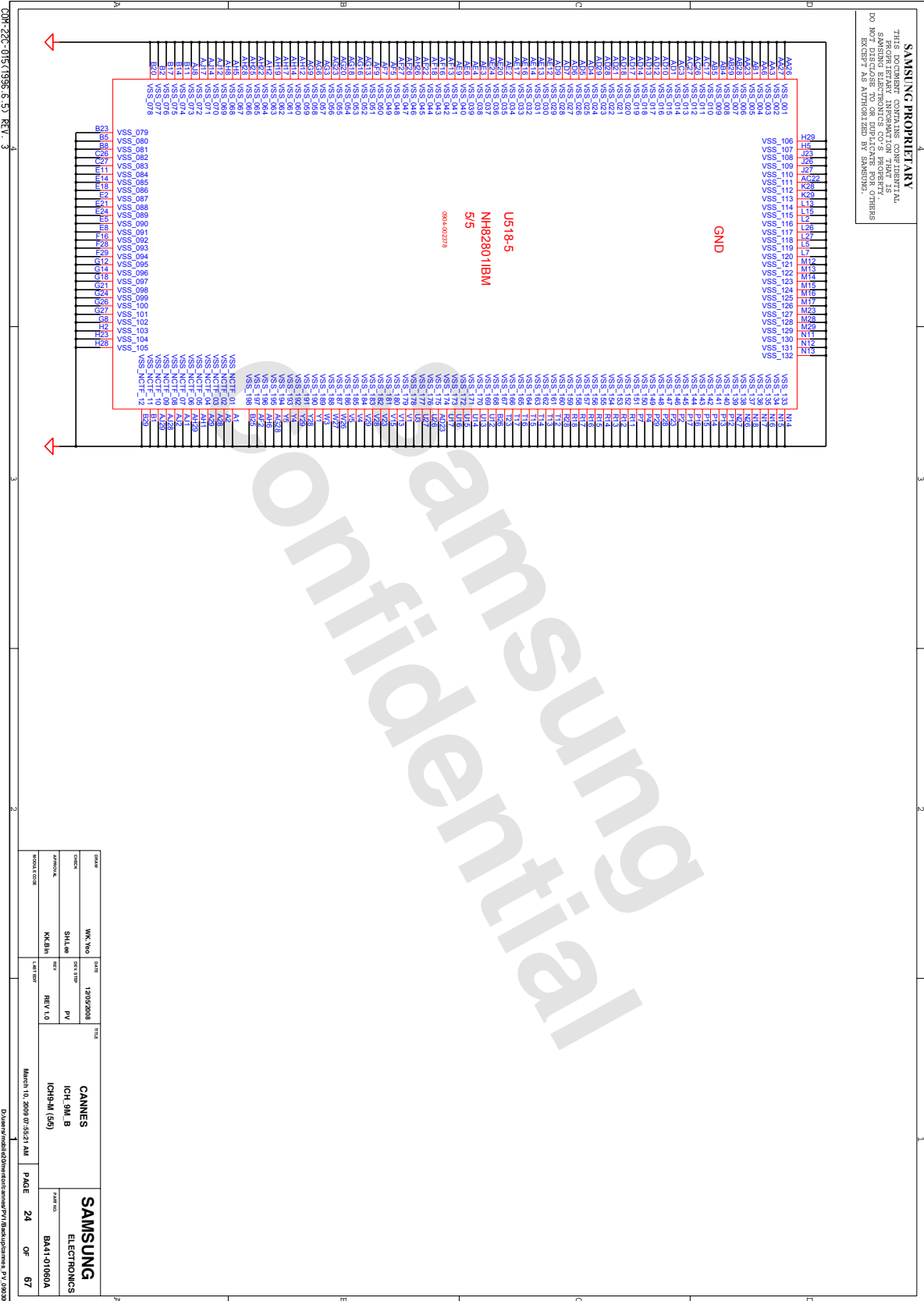
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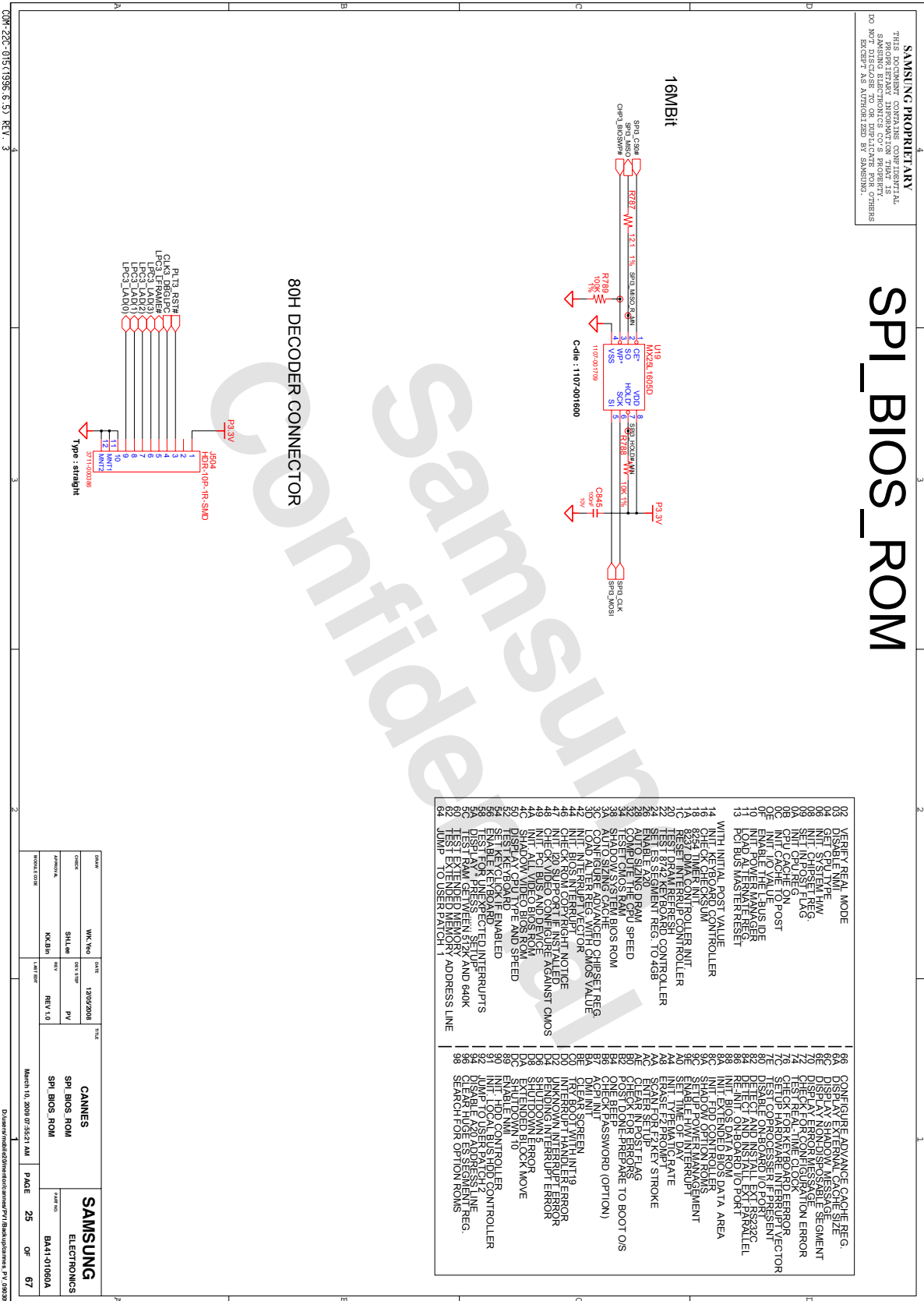


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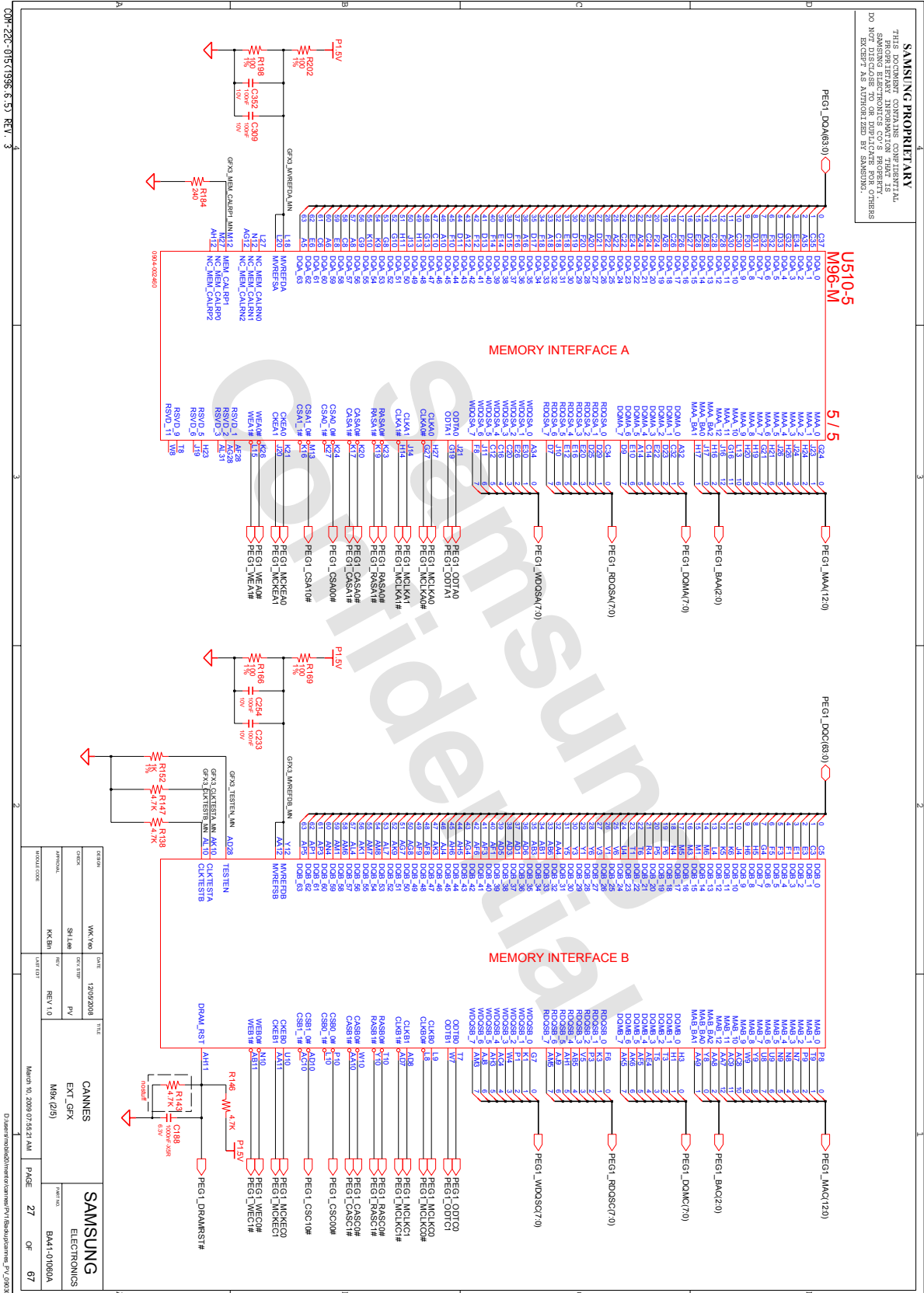


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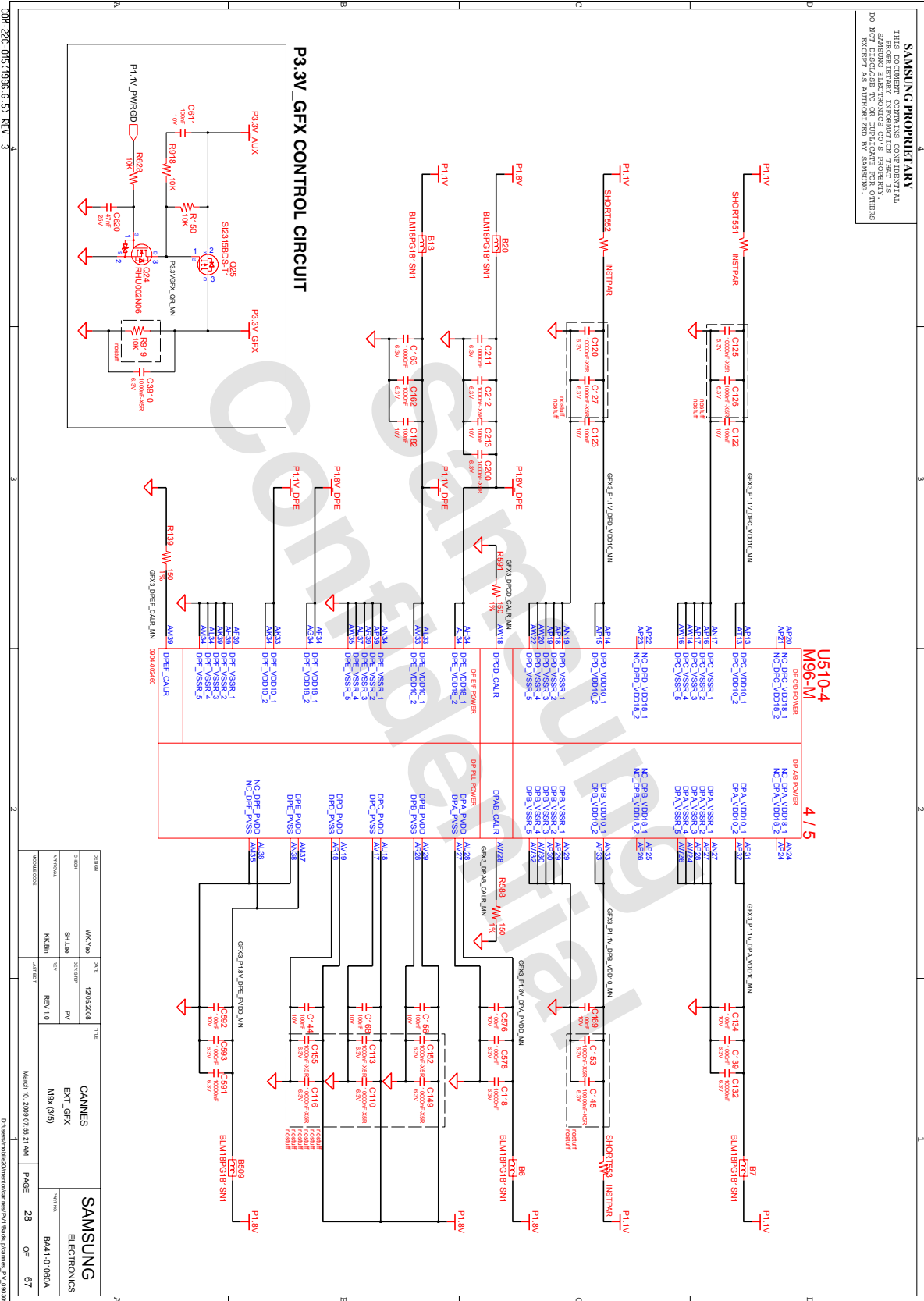


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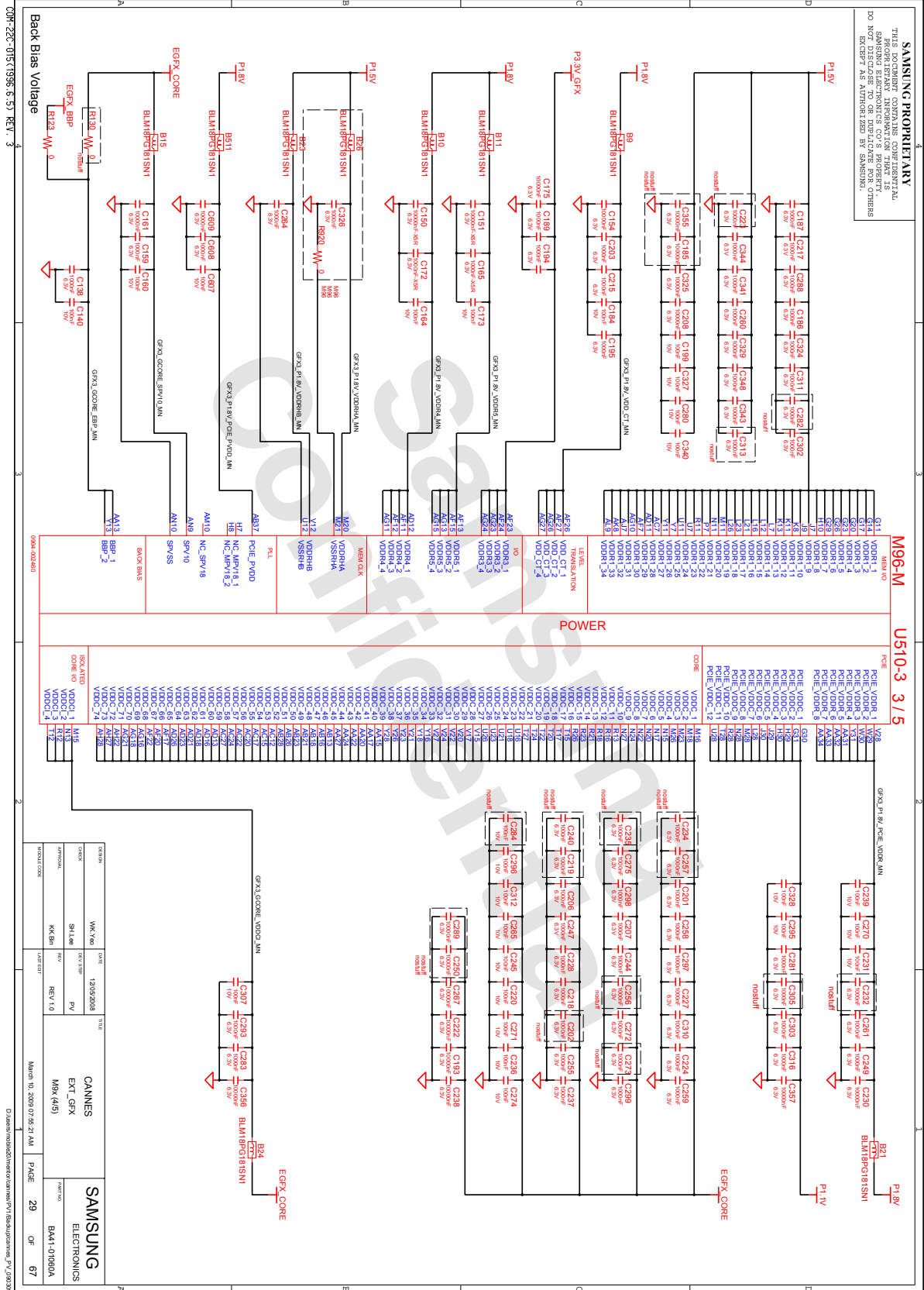


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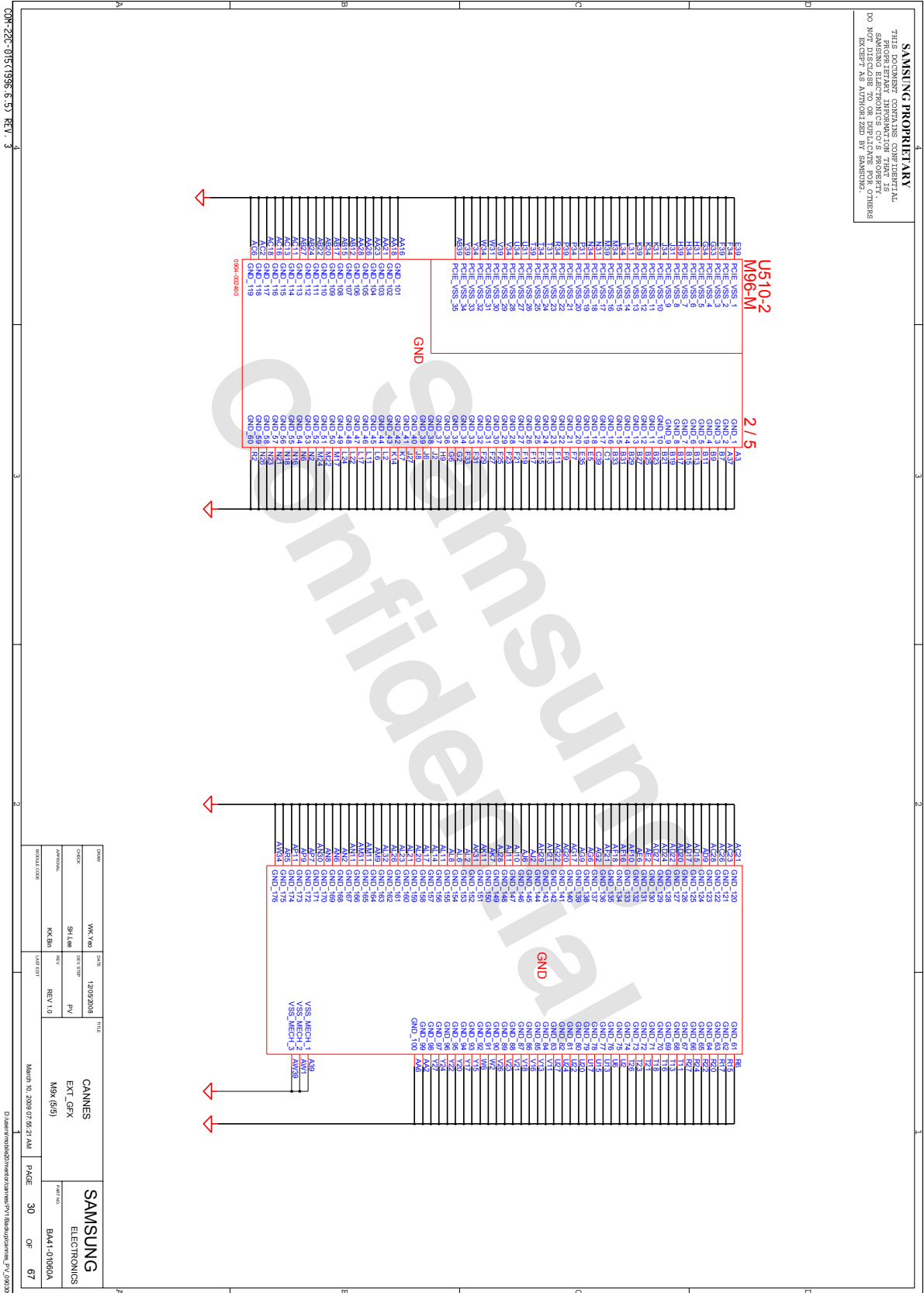
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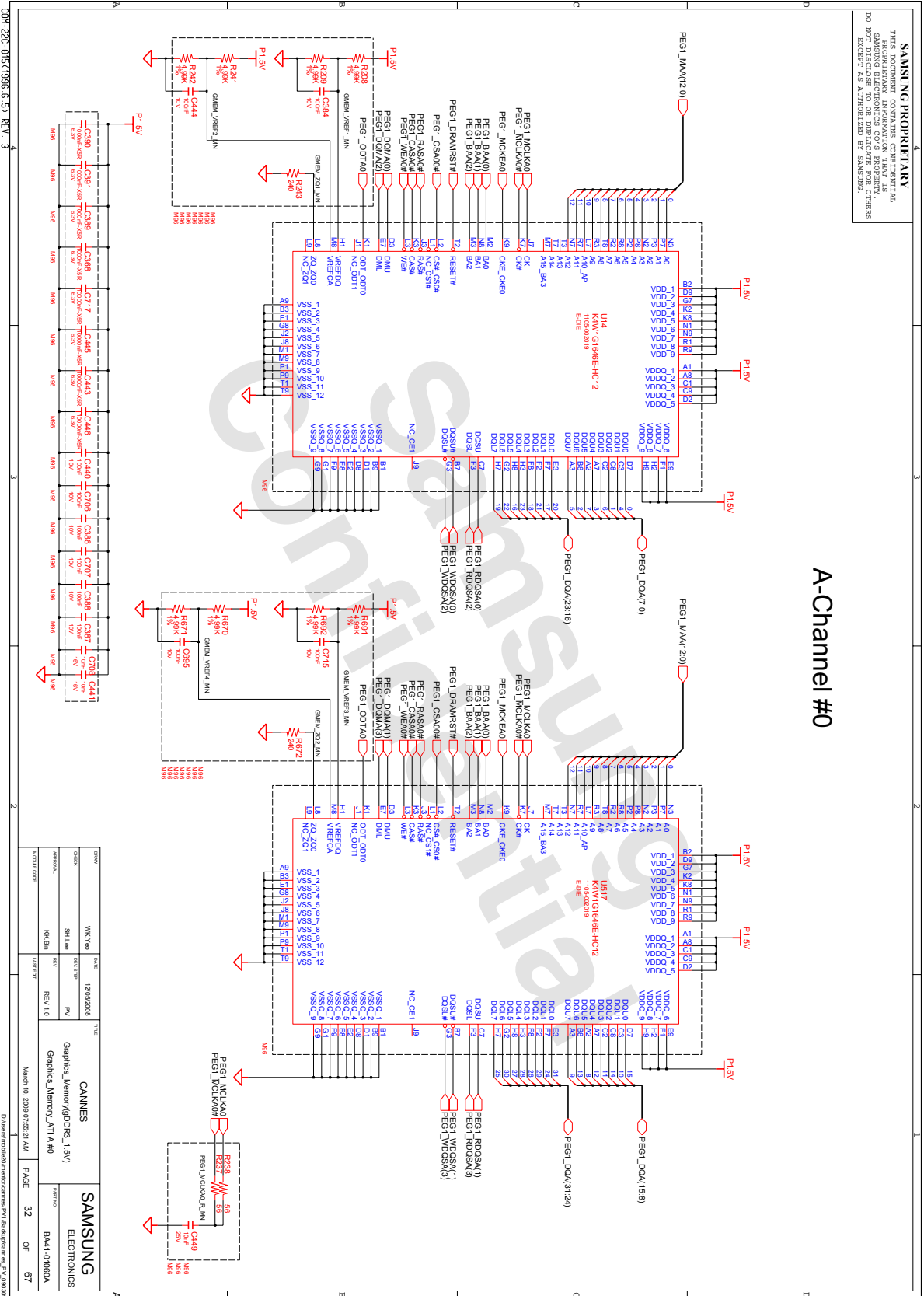
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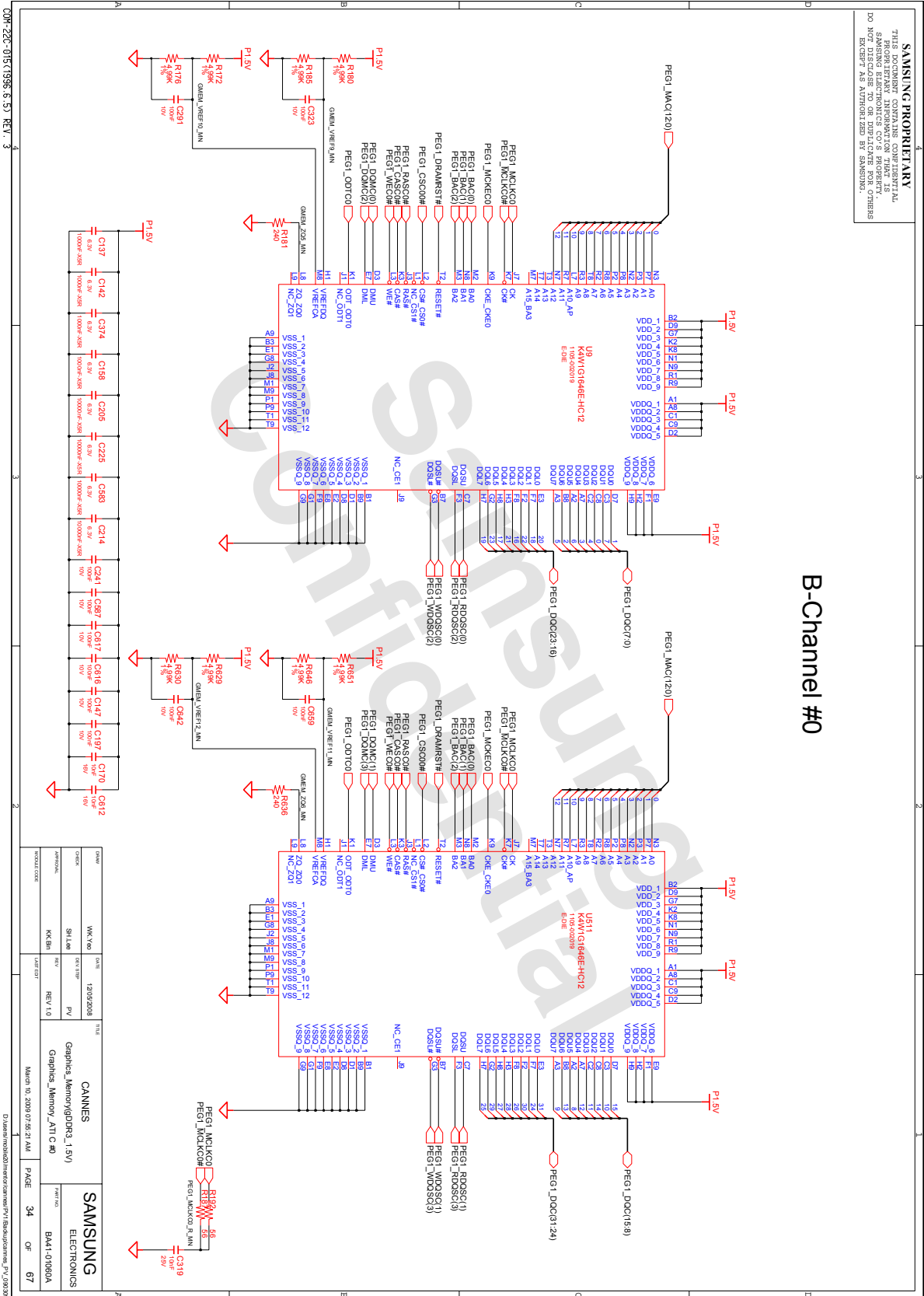
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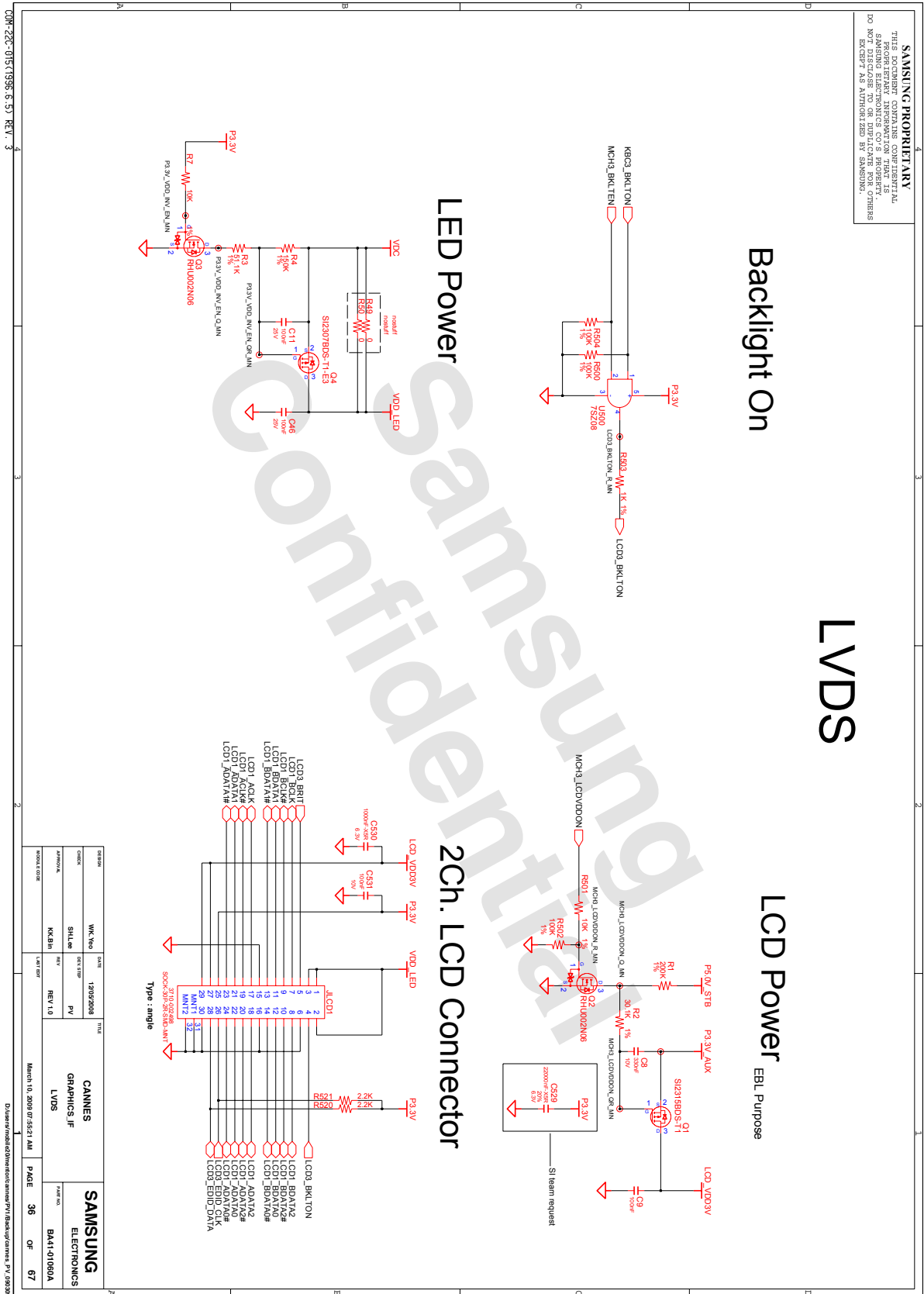
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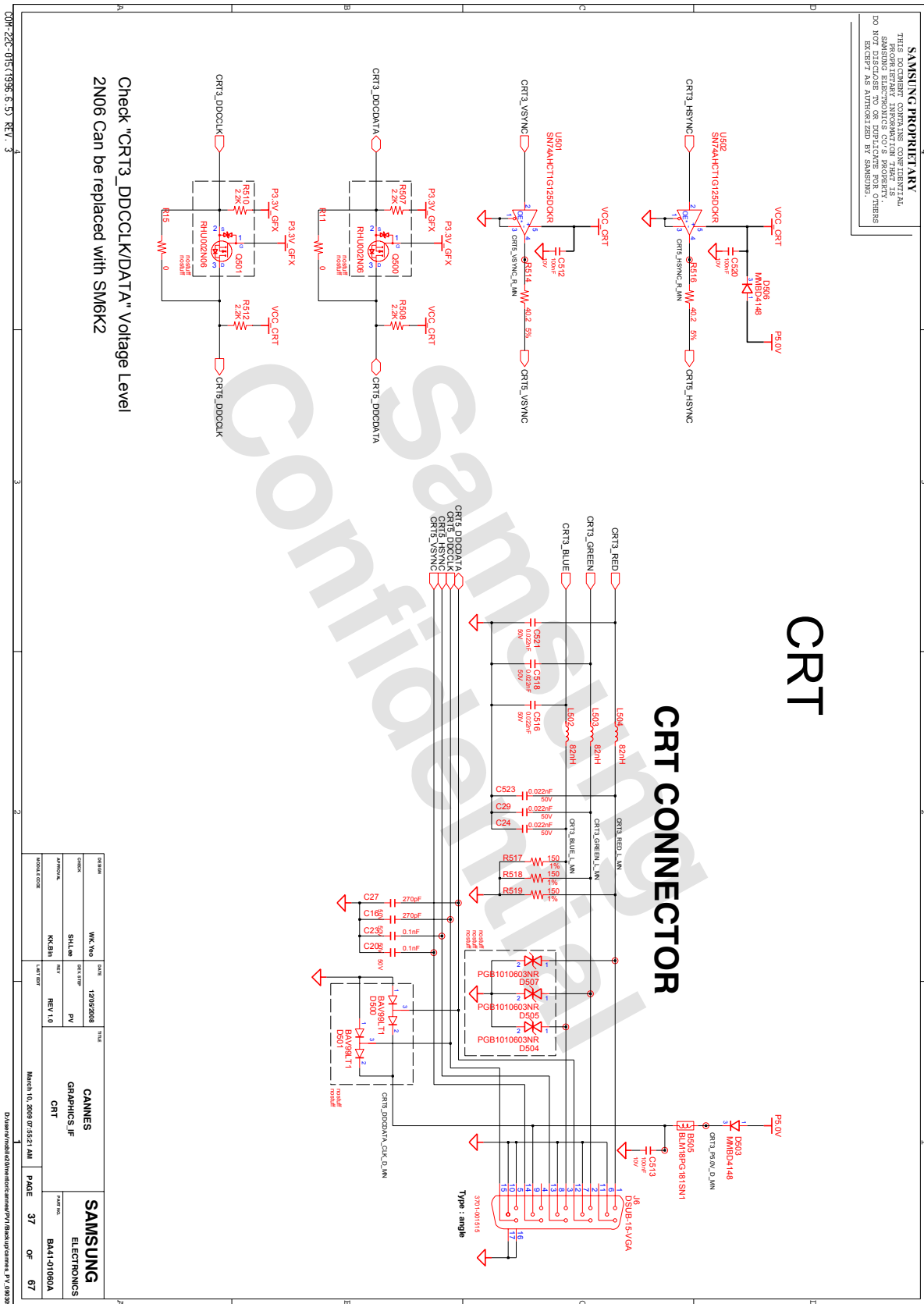
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CRT

CRT CONNECTOR

DATE	FILE	REV	DESCRIPTION	REV	DATE
12/09/2008					
01/11/2009					
03/19/2009					
07/23/2011					

DESIGNER	CHECKER	DATE	REV	DESCRIPTION
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KKBBH		REV 1.0		

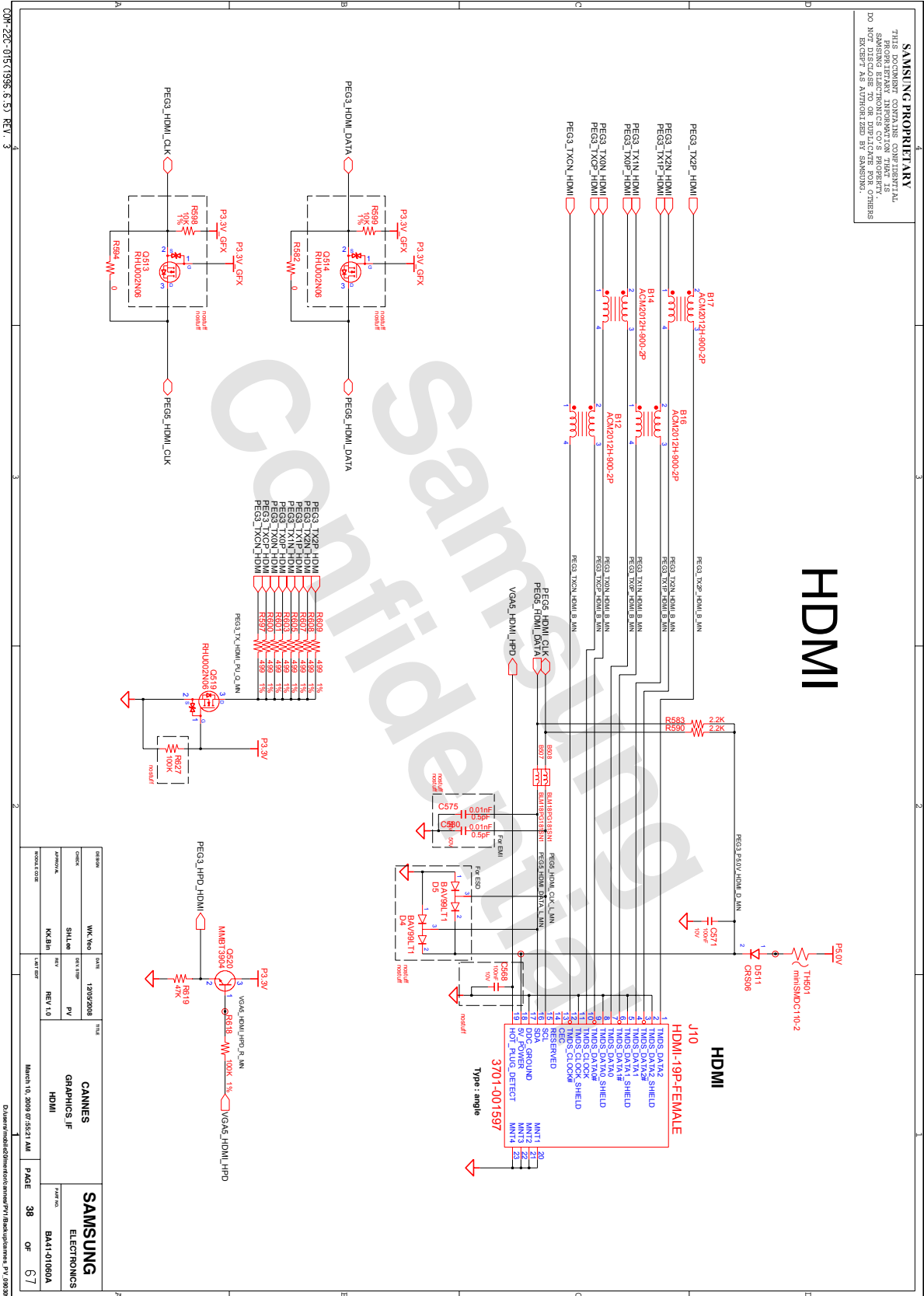
REV	DATE	DESCRIPTION
1	12/09/2008	INITIAL RELEASE

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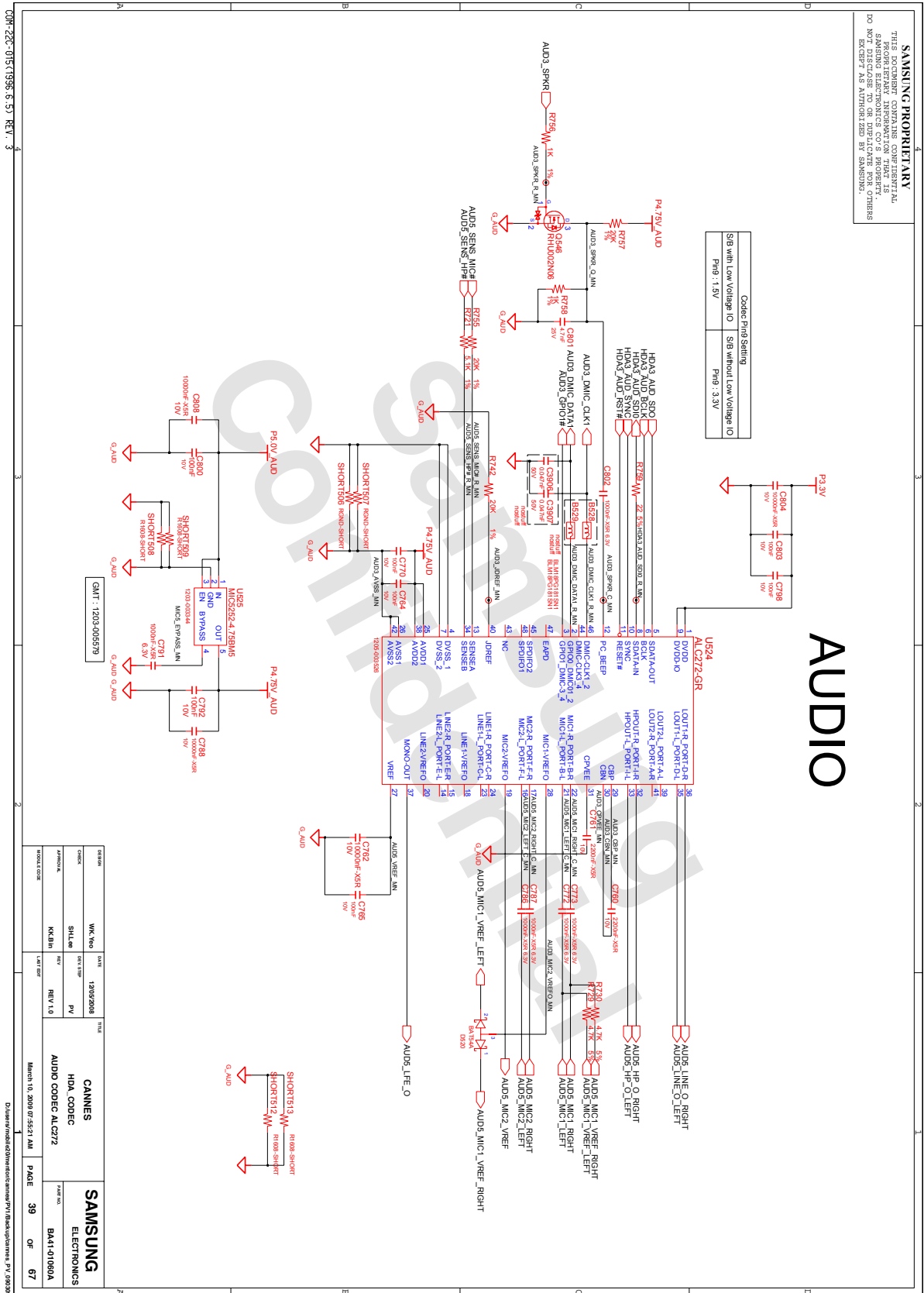
Diagram/Module/Device/Component/Part Number/Company, P/N 090300

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BA41-01080A

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DATE: 12/09/2018
 CHK: SHL/SHL
 REV: PV
 FILE: AUDIO_CODEC_ALI272

DATE: 12/09/2018
 CHK: SHL/SHL
 REV: PV
 FILE: AUDIO_CODEC_ALI272

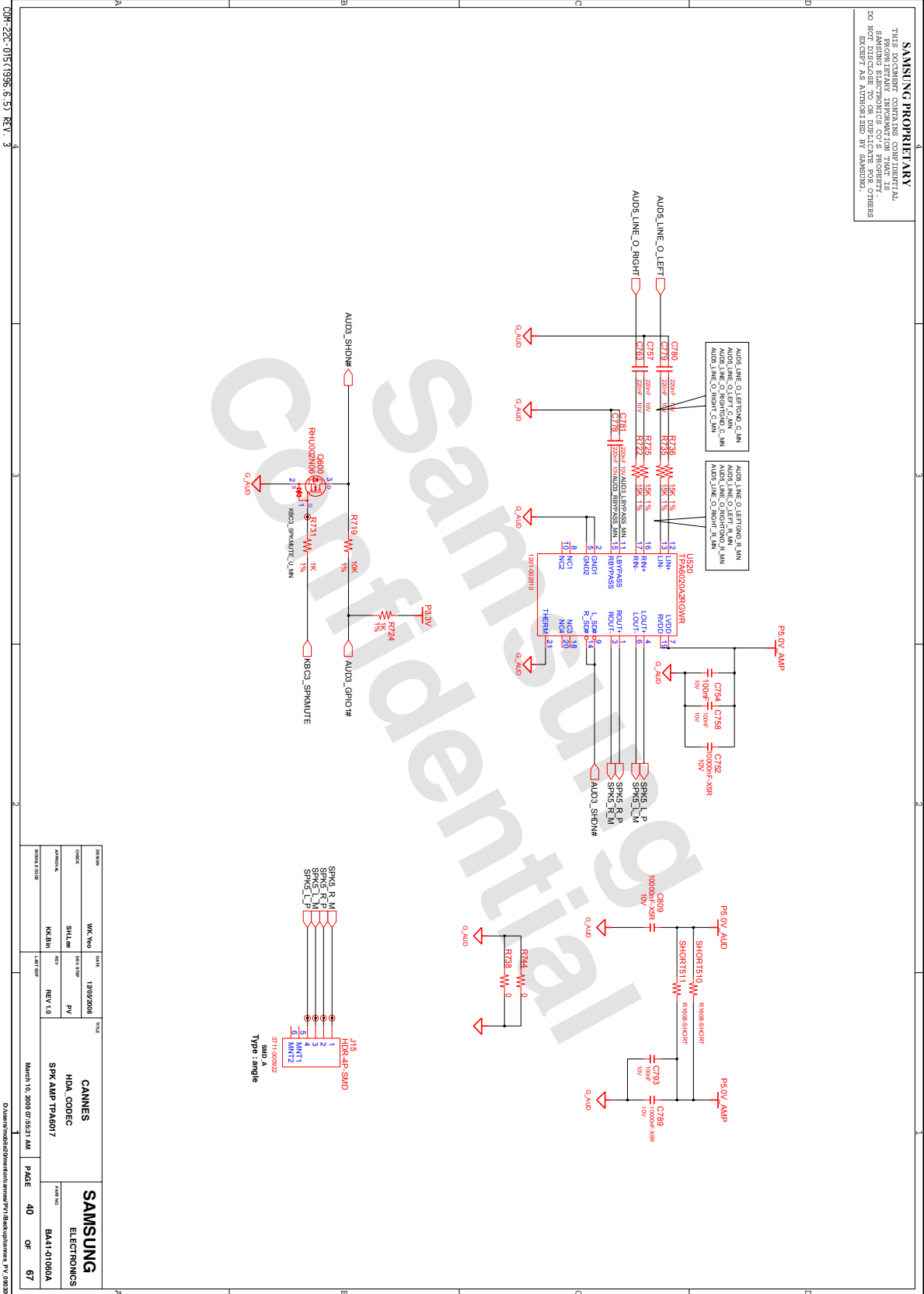
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March 10, 2009 07:55:21 AM PAGE 39 OF 67

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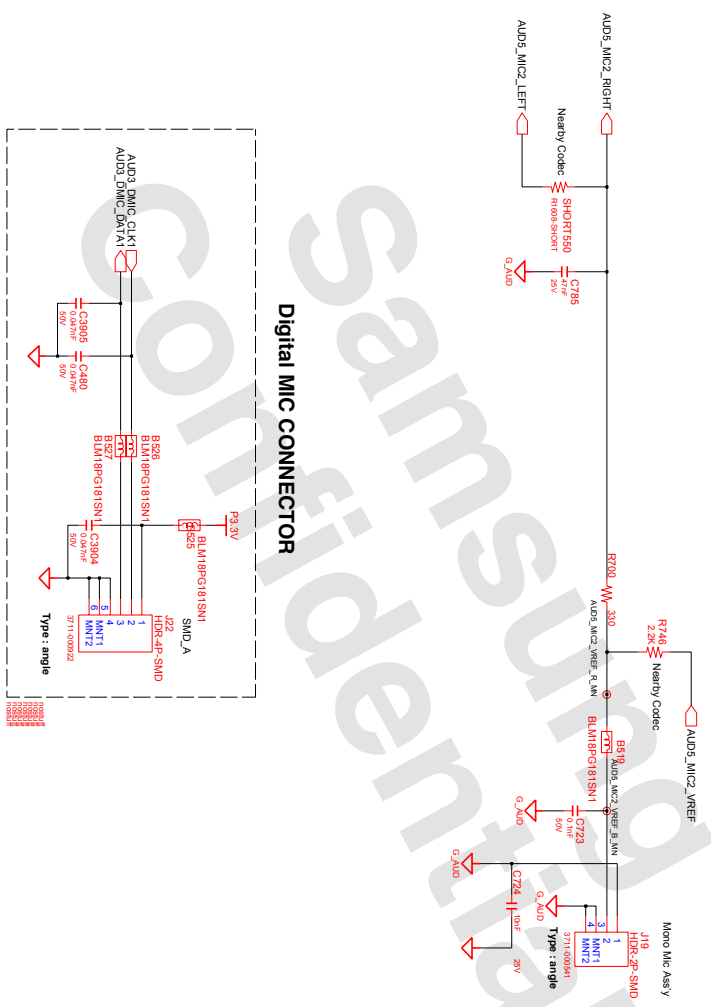
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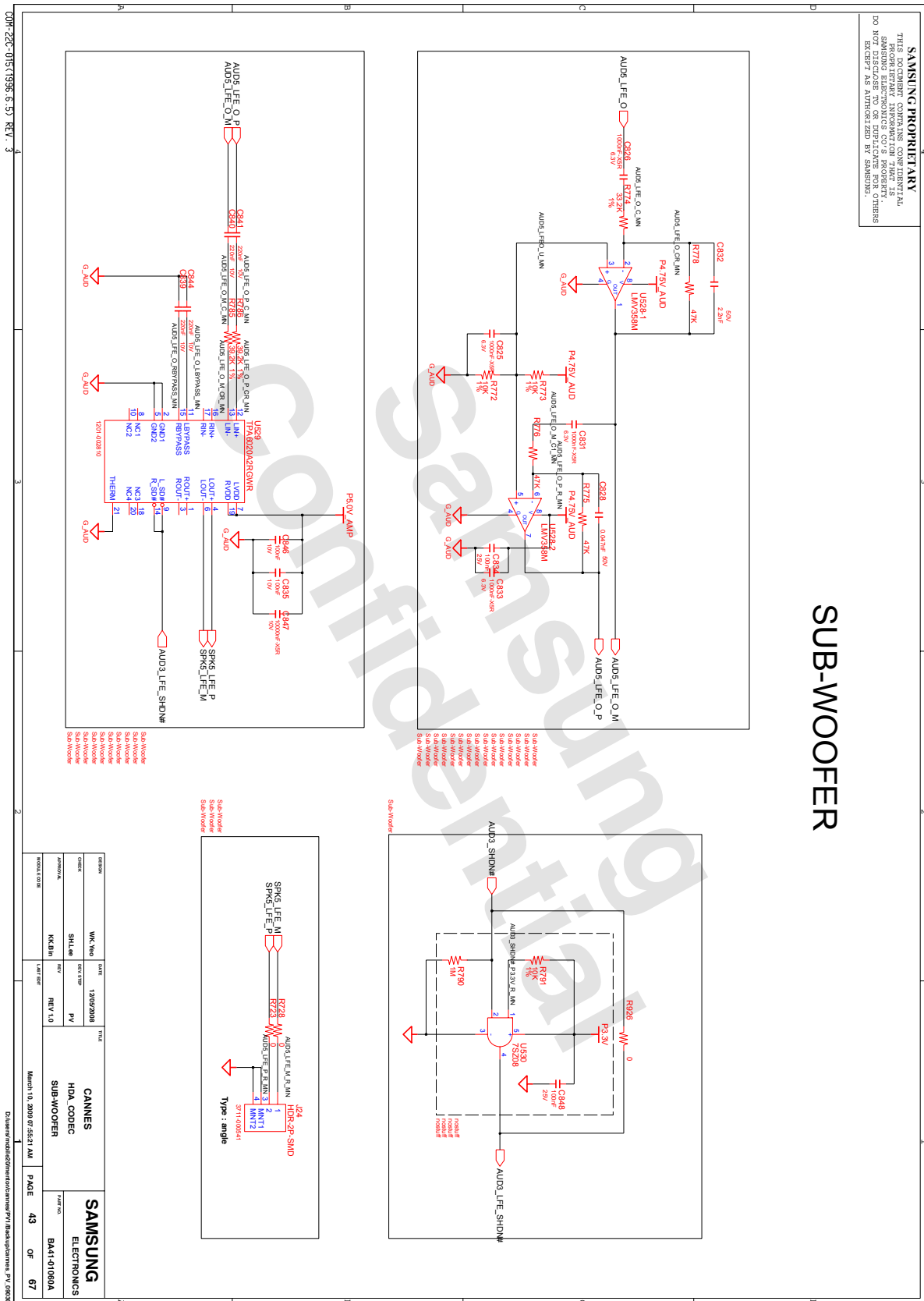
Digital MIC CONNECTOR

DATE	12/05/2008	FILE	CANNIS	SAMSUNG
DESIGNER	WKC/Noi	REV. NO.	HDA_CODEC	ELECTRONICS
CHECKER	SHL/Noi	REV. DATE	MIC AMP LHW958	BA41-01080A
APPROVAL	KCB/Noi	REV. 1/0		
DESIGNED BY	WKC/Noi	REV. DATE	March 10, 2009 07:25:51 AM	PAGE 41 OF 67
CHECKED BY	SHL/Noi	REV. DATE		
APPROVED BY	KCB/Noi	REV. DATE		

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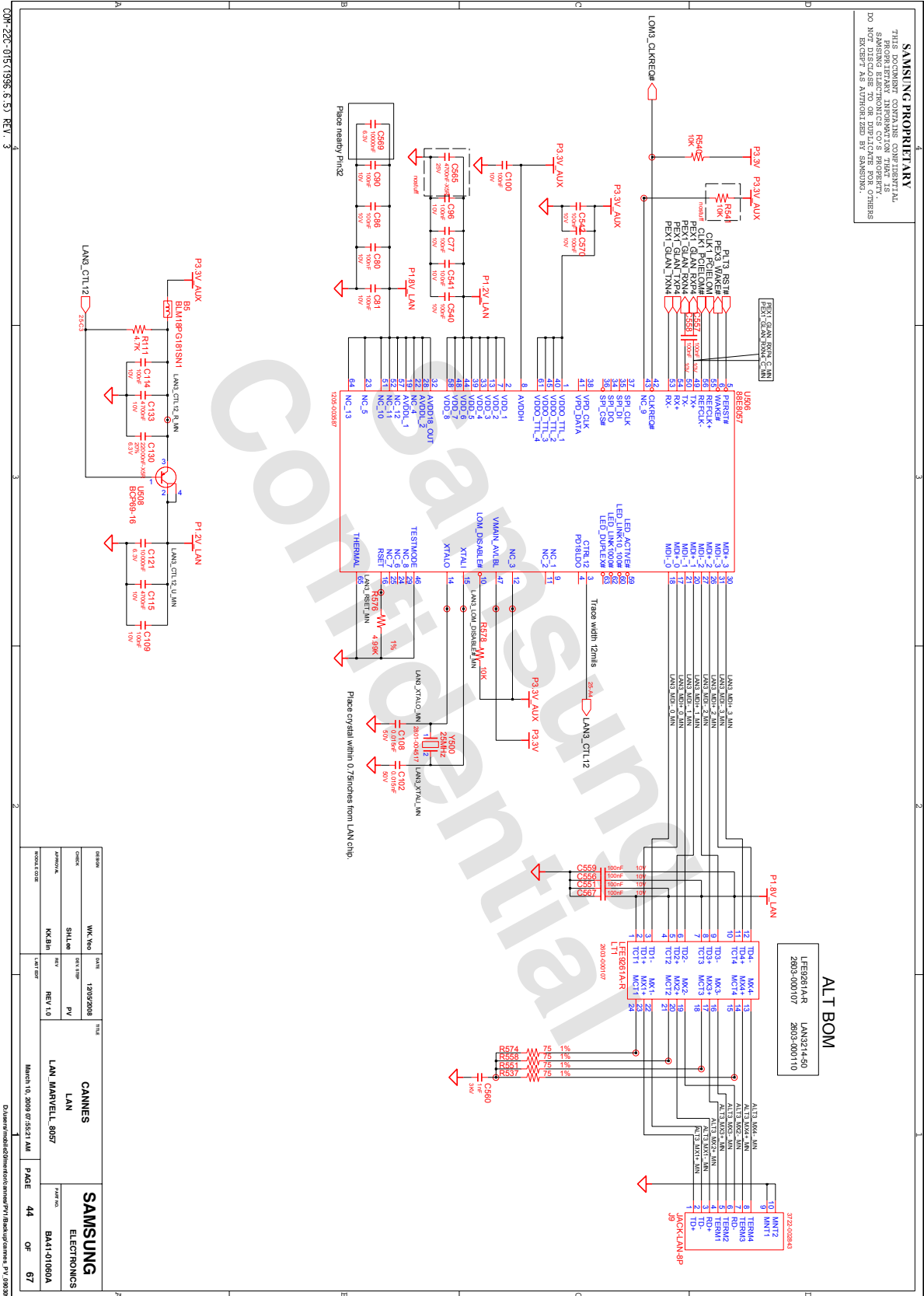
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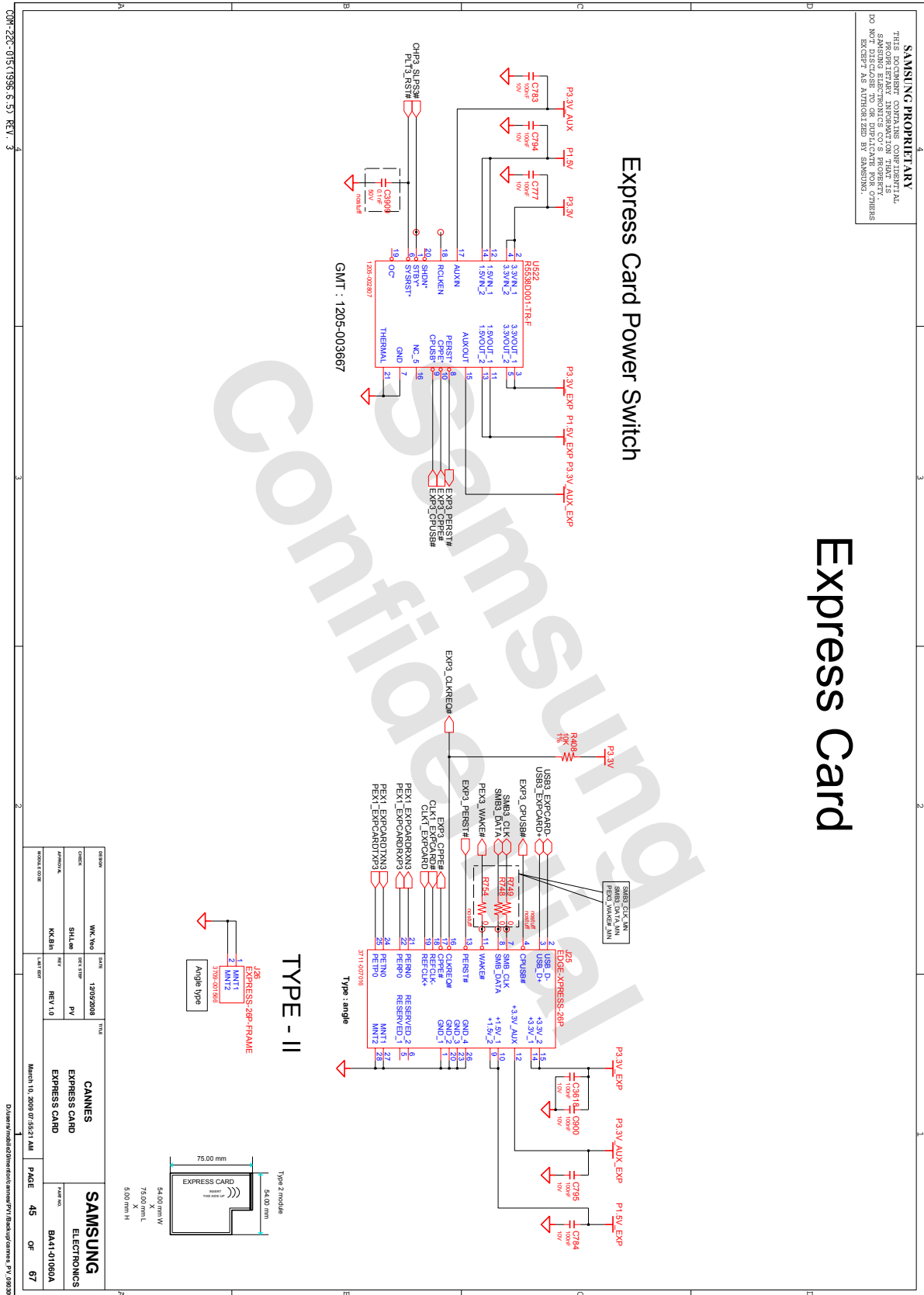


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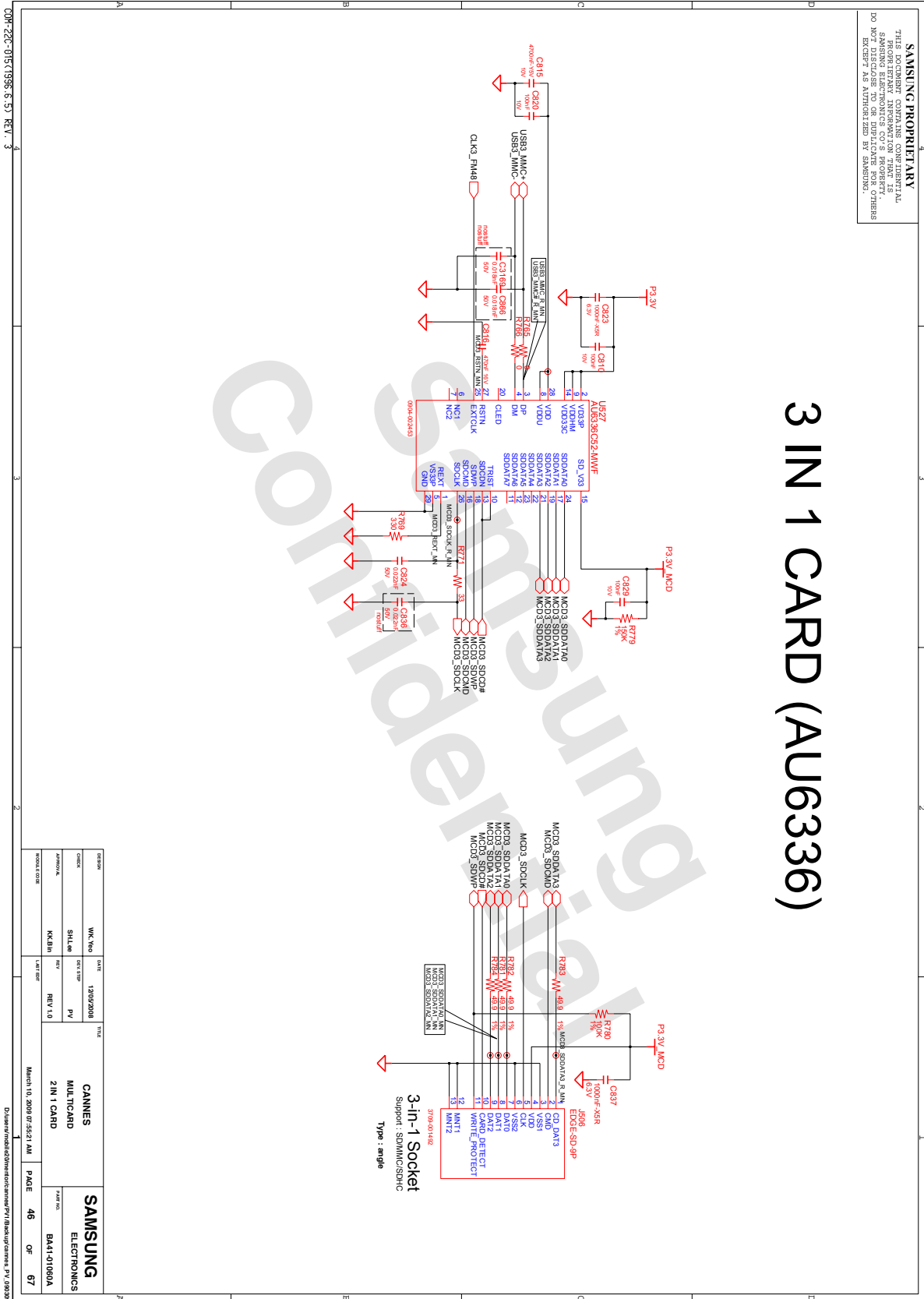


8. Block Diagram and Schematic

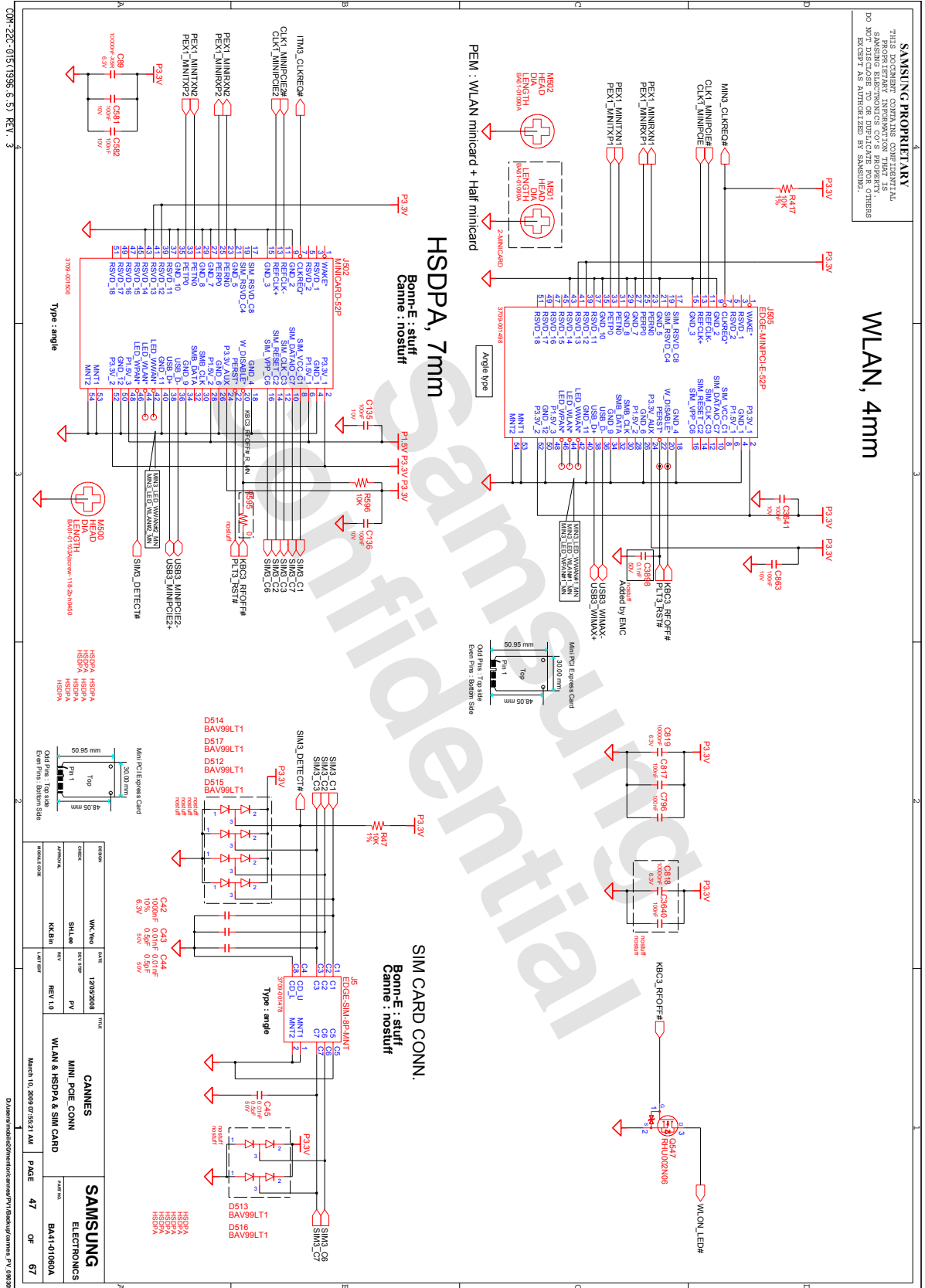


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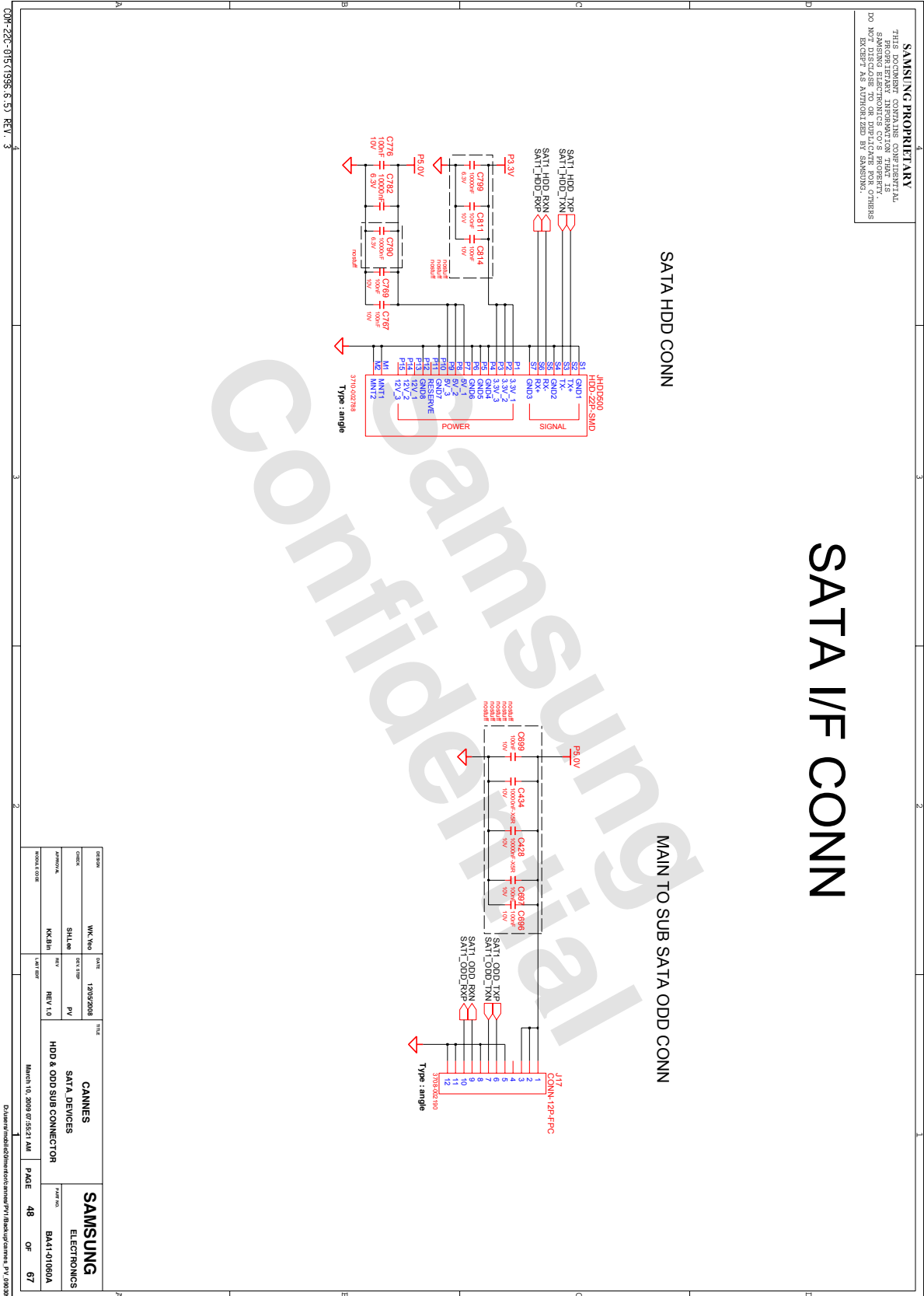


8. Block Diagram and Schematic



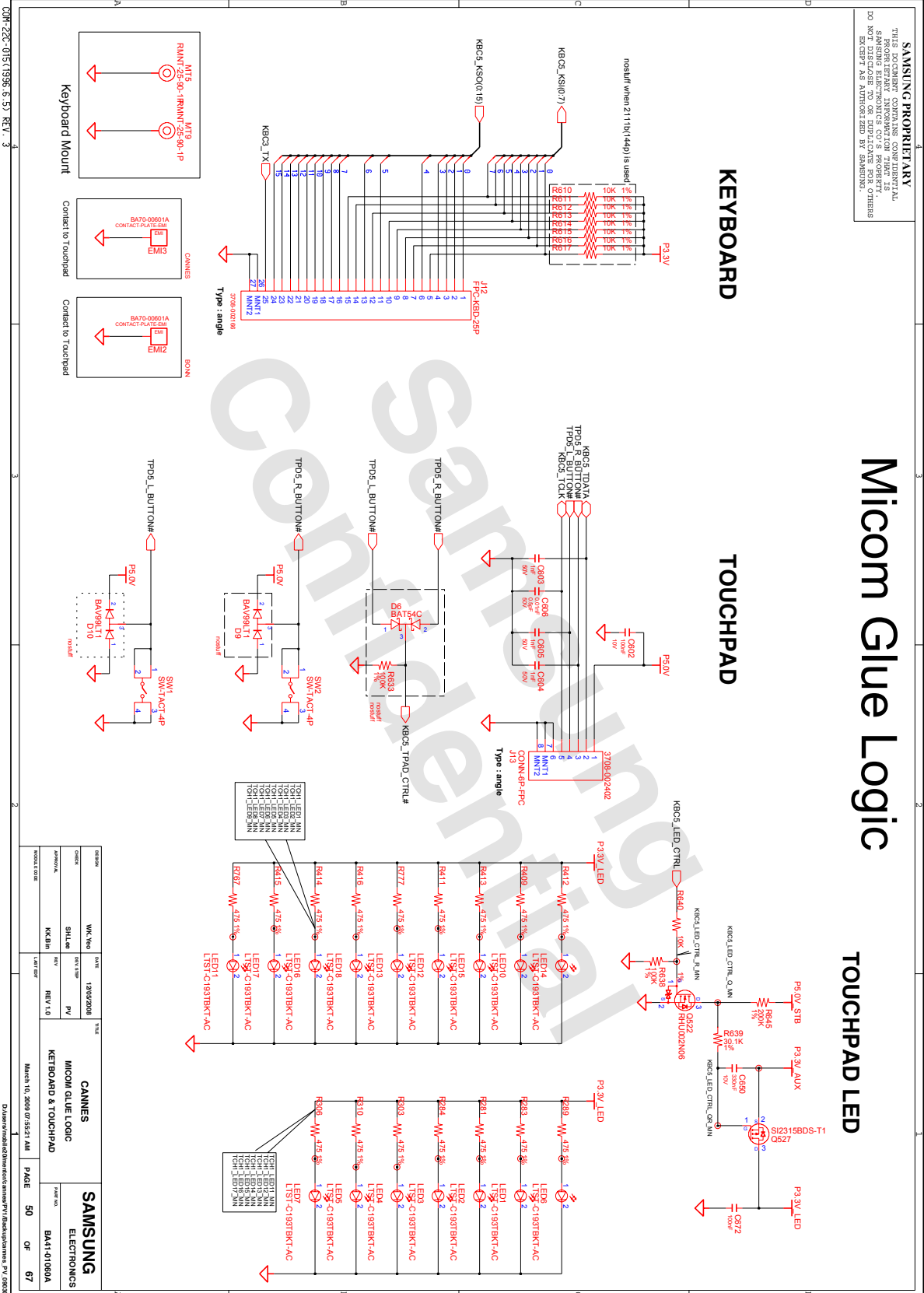
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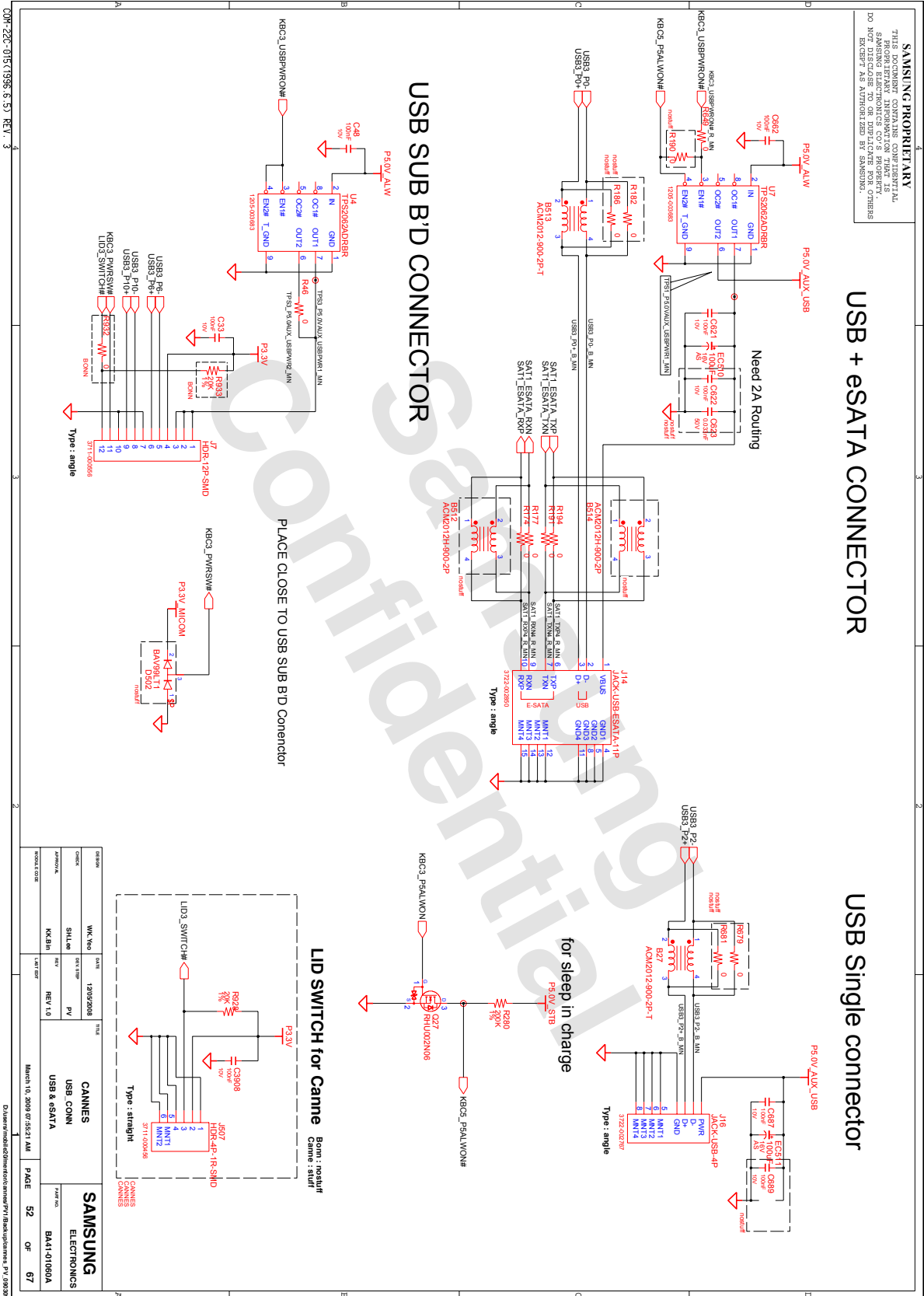
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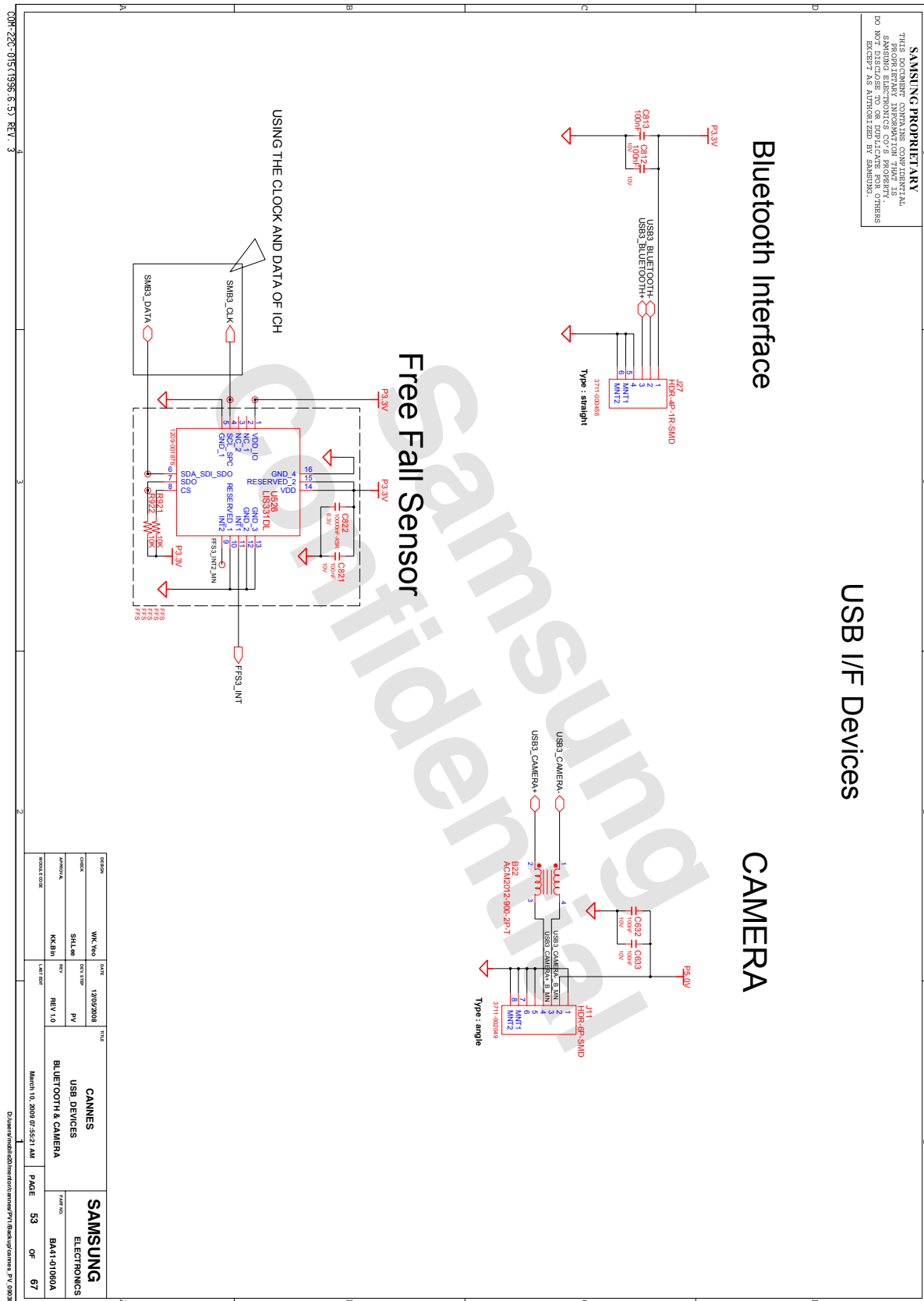


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8. Block Diagram and Schematic

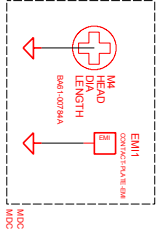
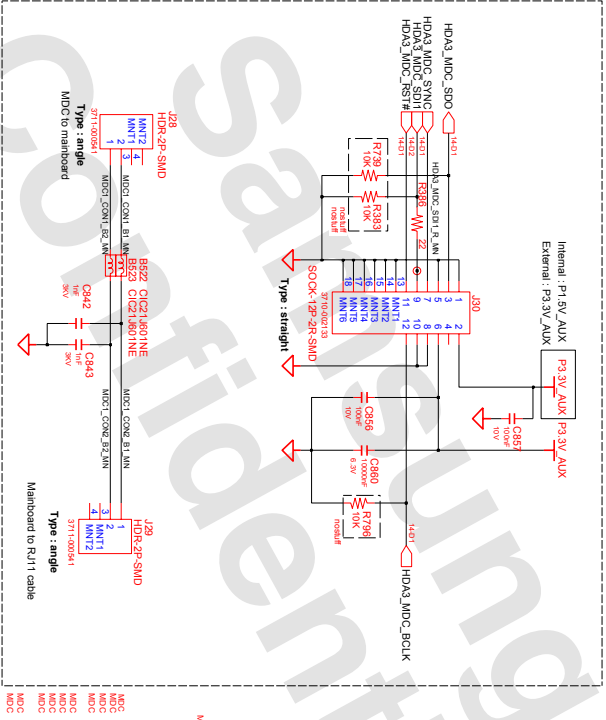


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MDC connector



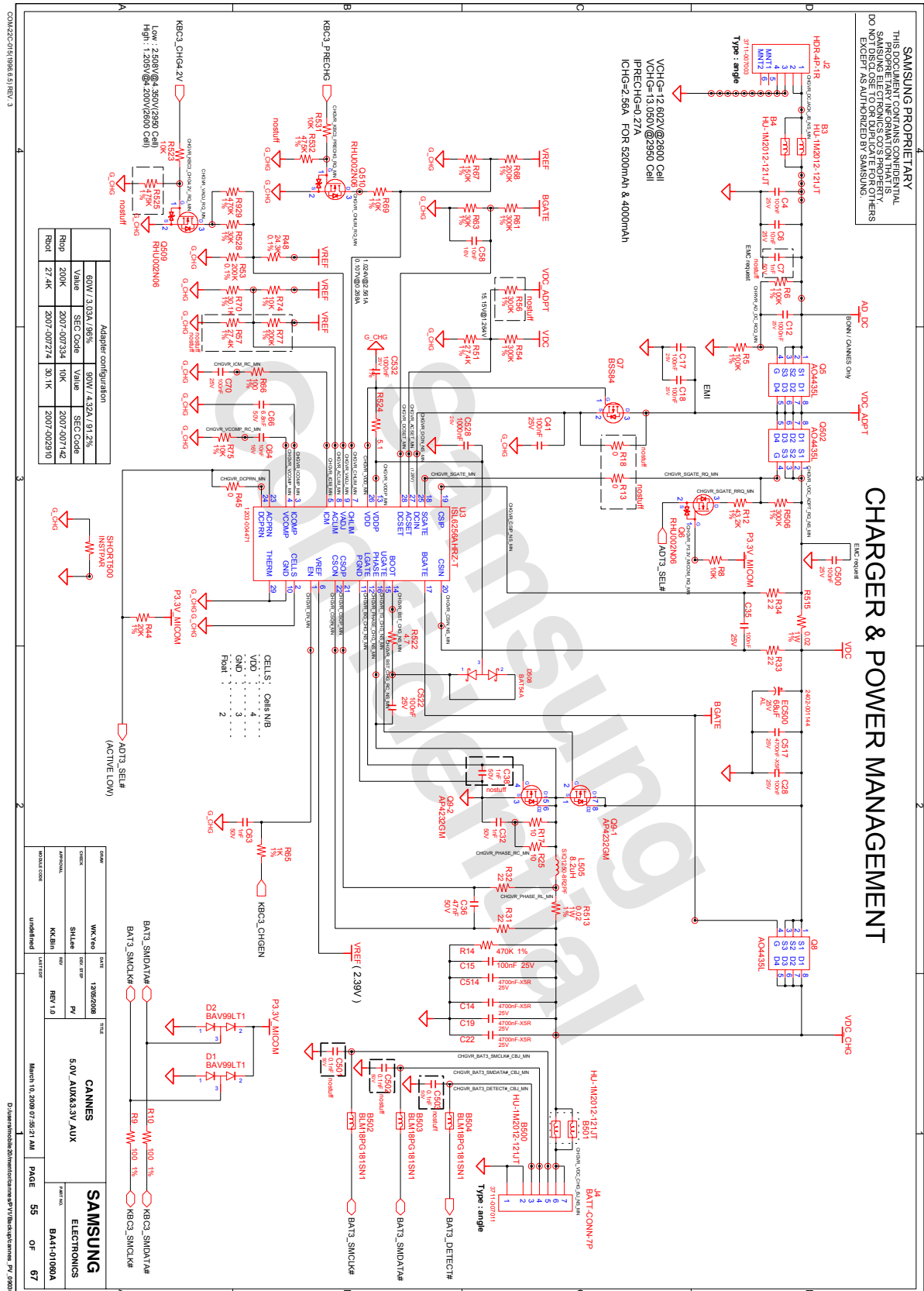
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CHK	1209/2008	1209/2008			
CHK	SHL	REV	PV	MDC CONNECTOR	
CHK	KKB	REV	REV 1.0	MDC CONNECTOR	BA41-01060A
CHK	under	REV			

007-226-0151396 3.57 REV. 3

March 10, 2009 07:53:24 AM PAGE 54 OF 67

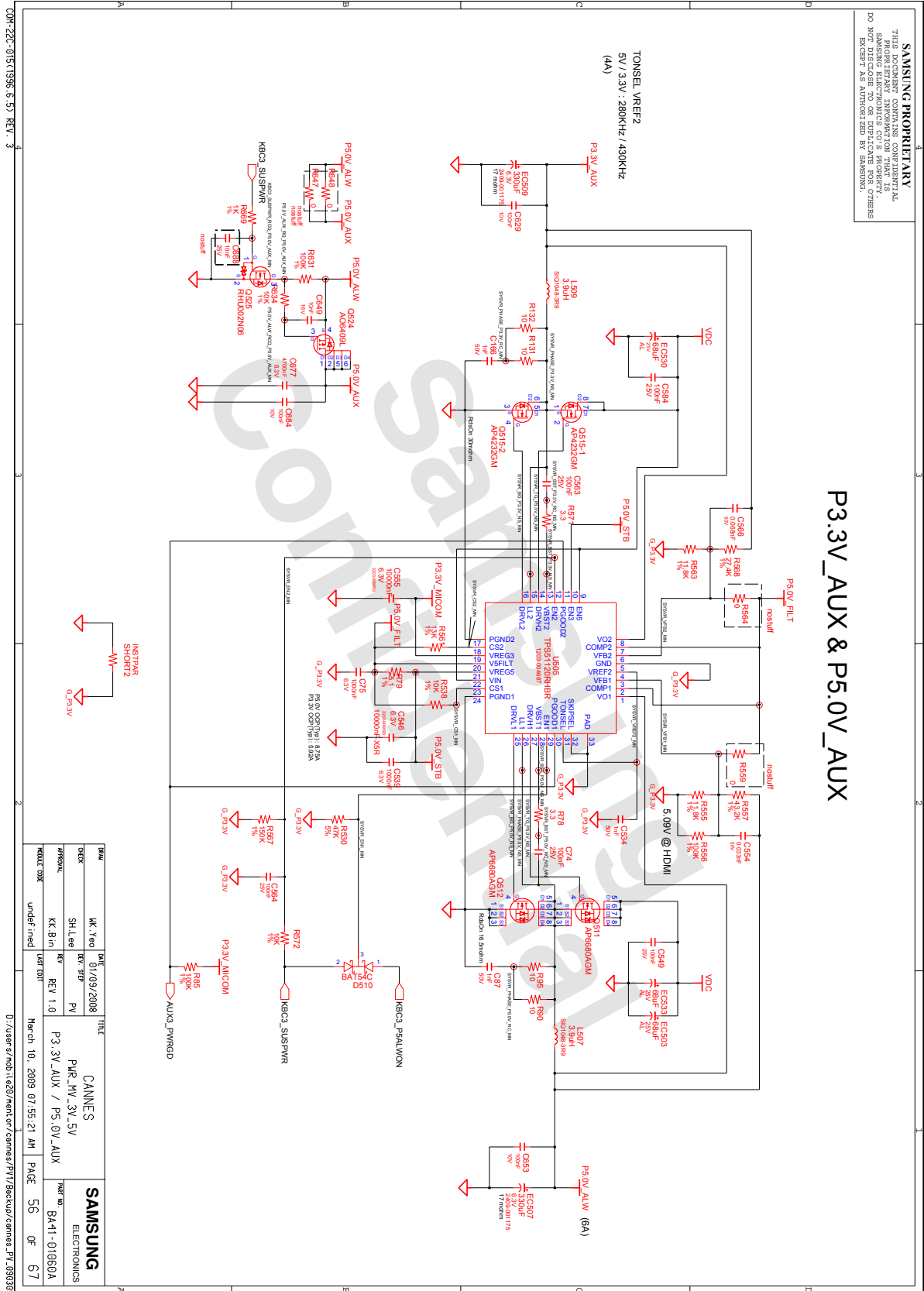
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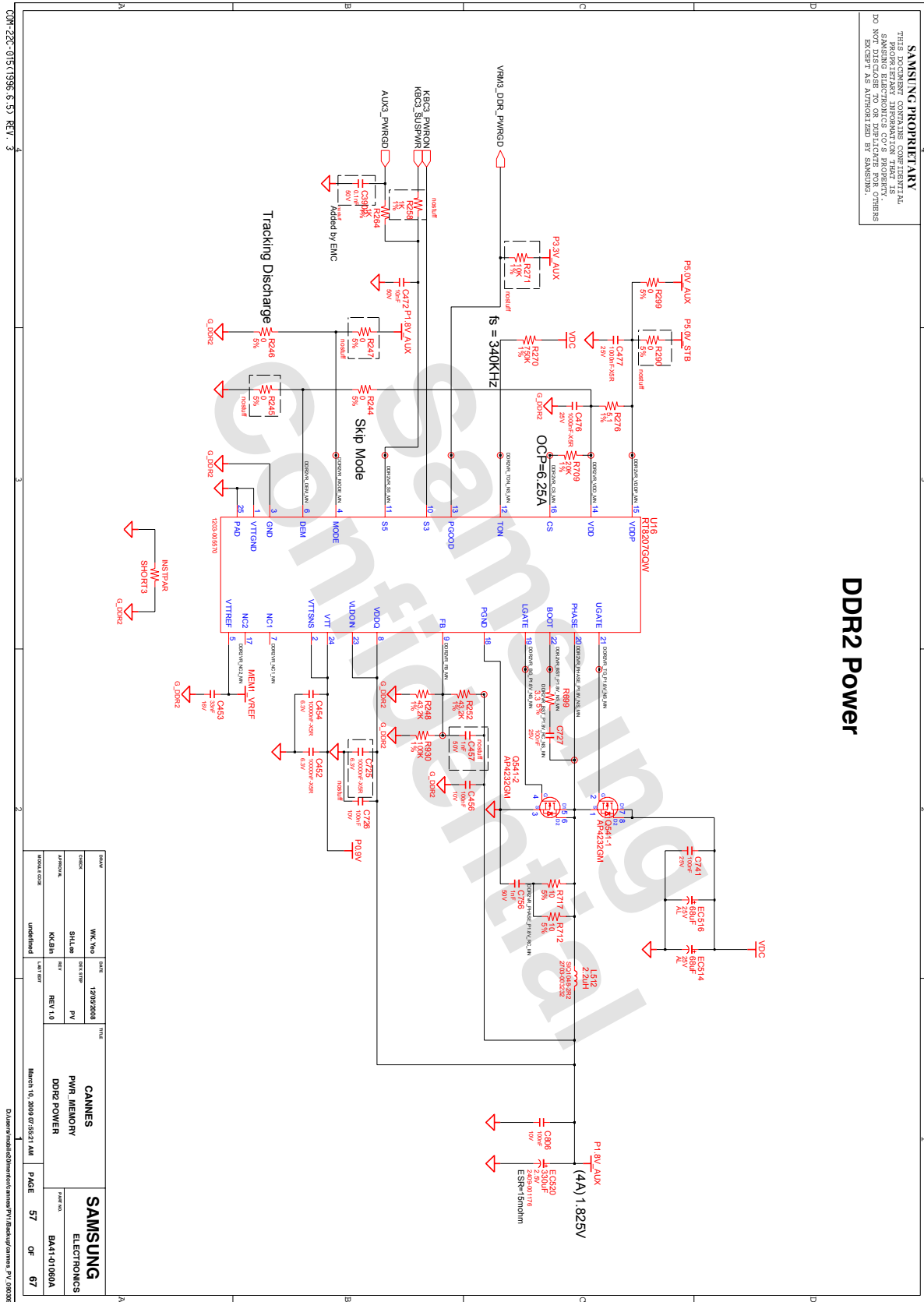


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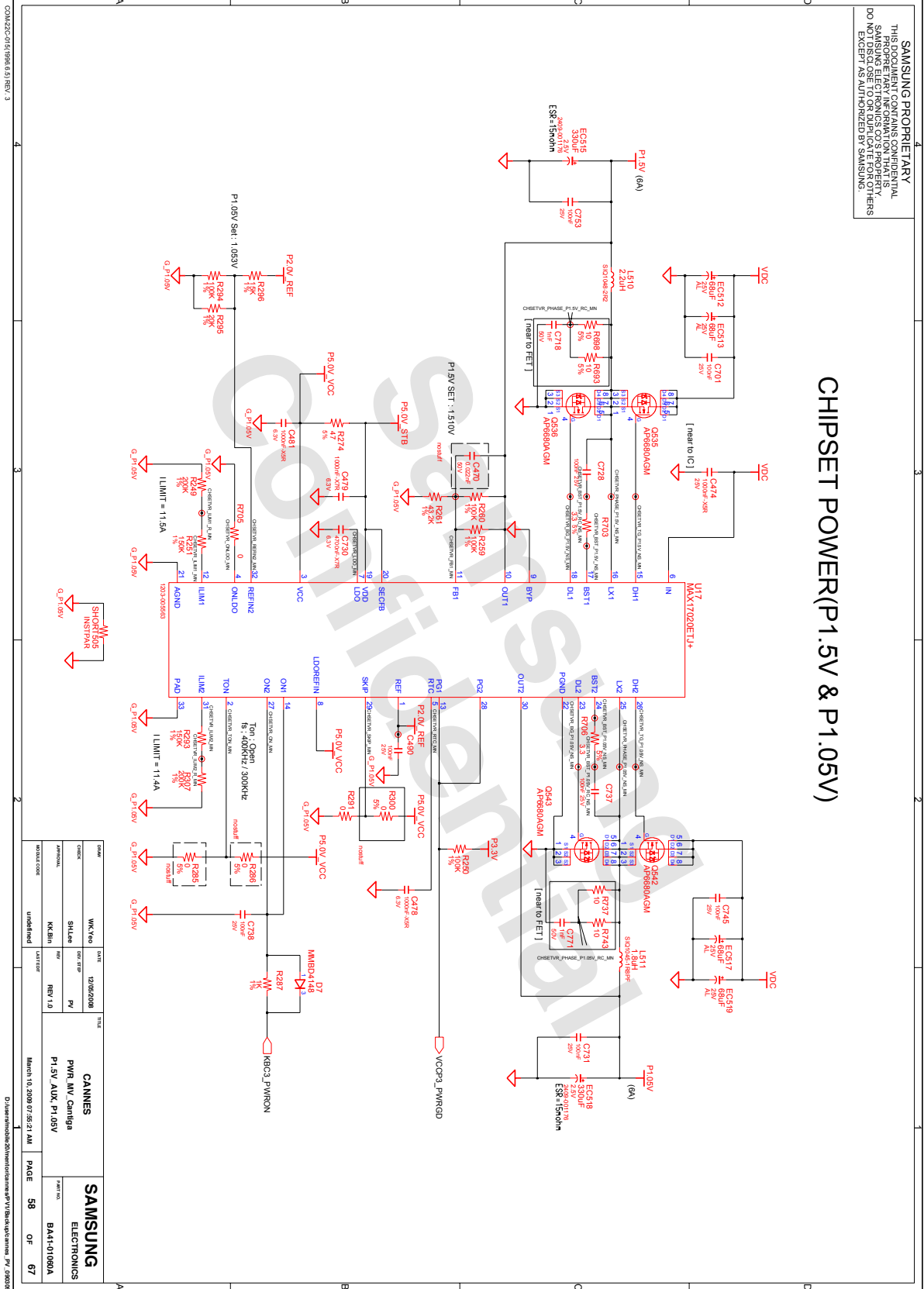
8. Block Diagram and Schematic



8. Block Diagram and Schematic

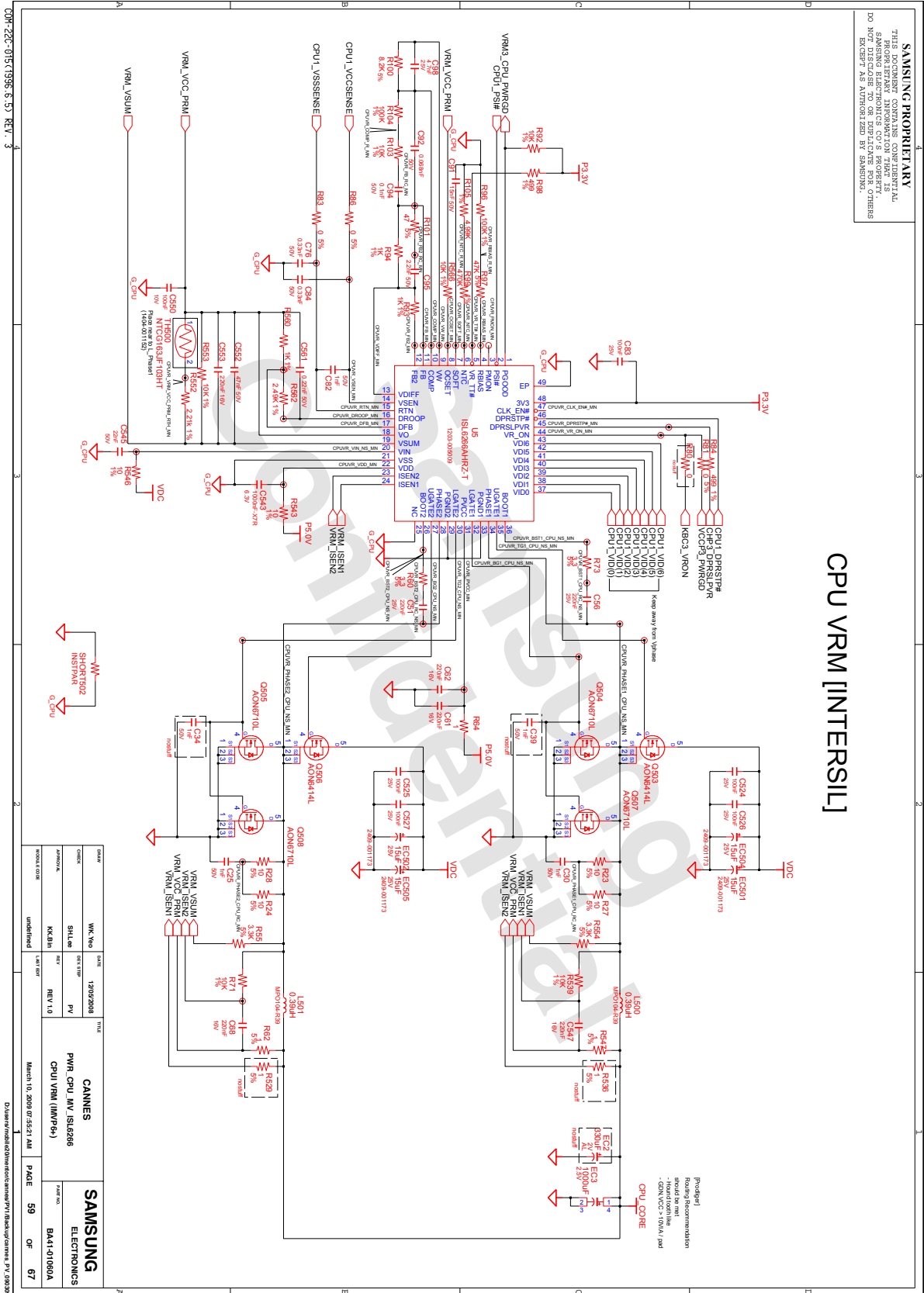
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CHIPSET POWER(P1.5V & P1.05V)



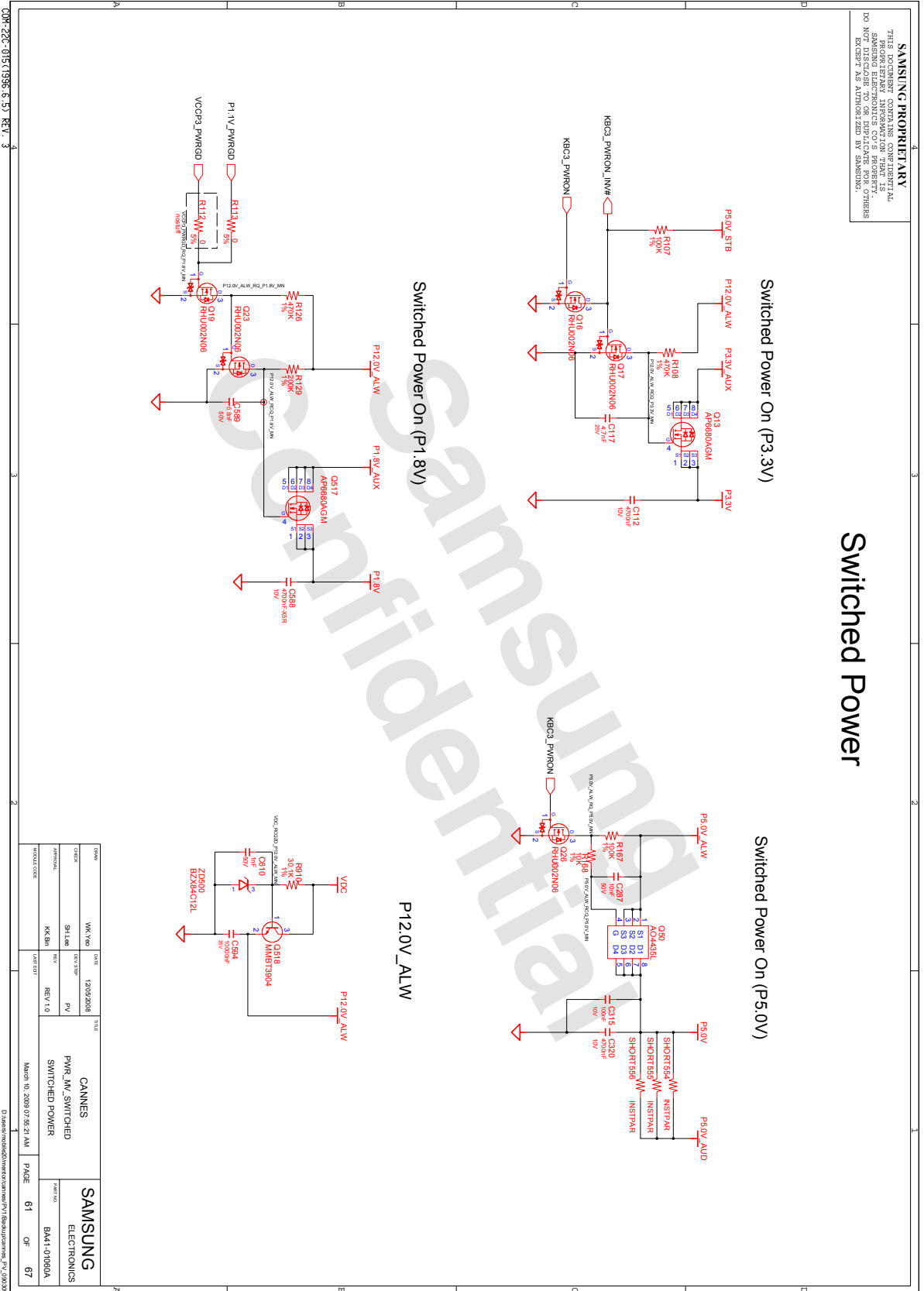
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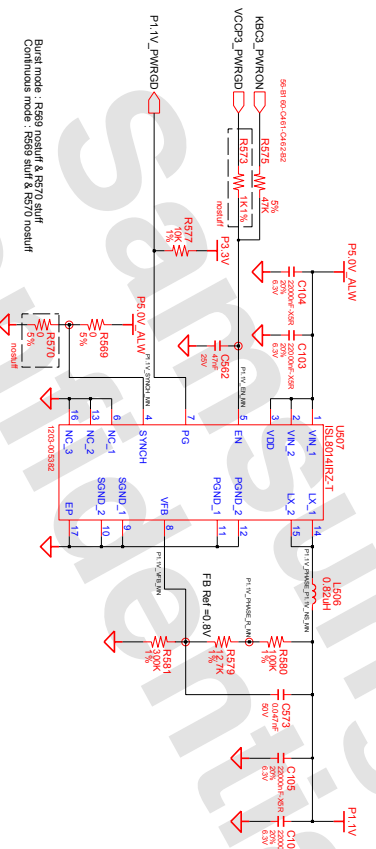


8. Block Diagram and Schematic

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P1.1V

P1.1V(External Gfx)



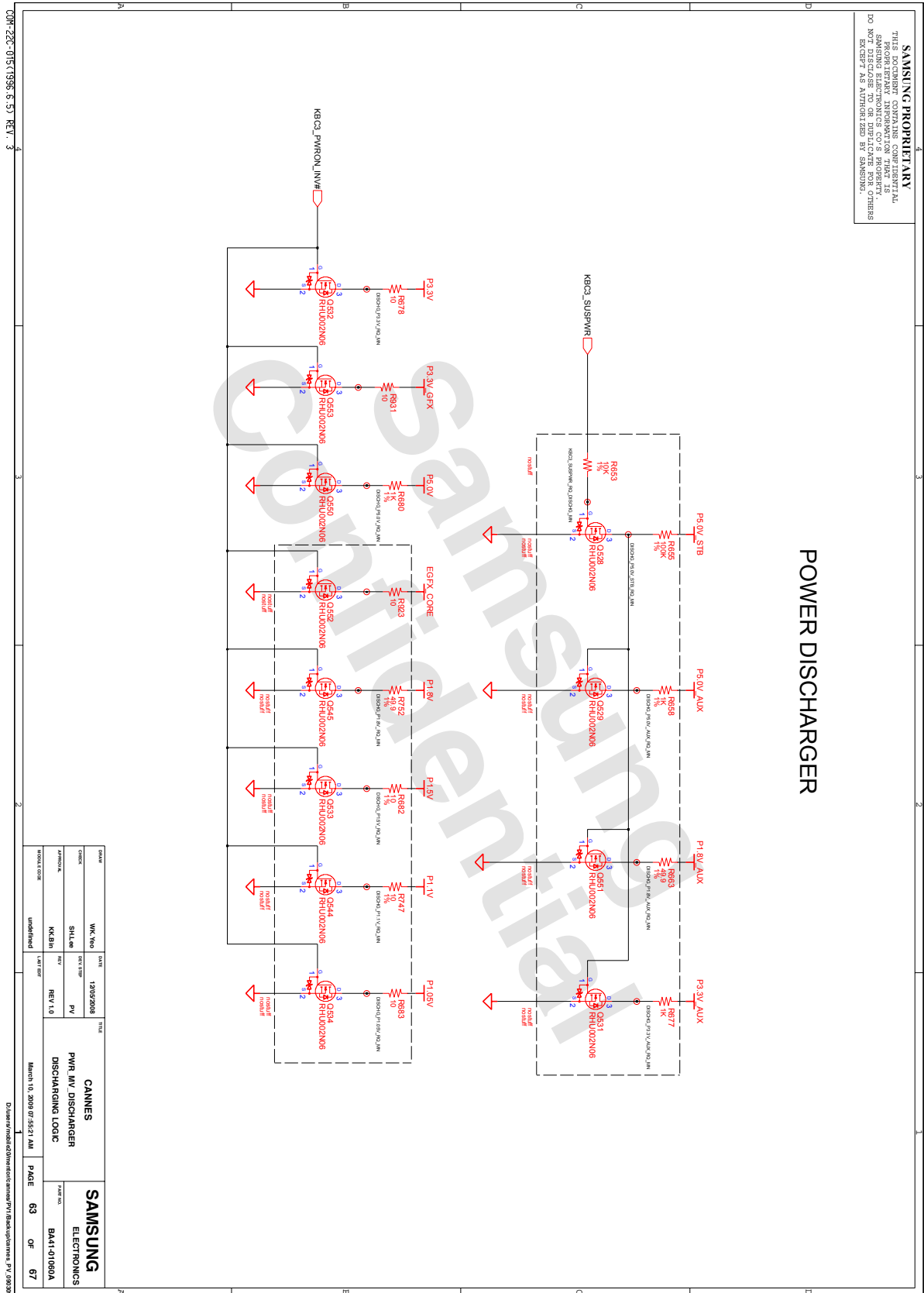
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				OF	67

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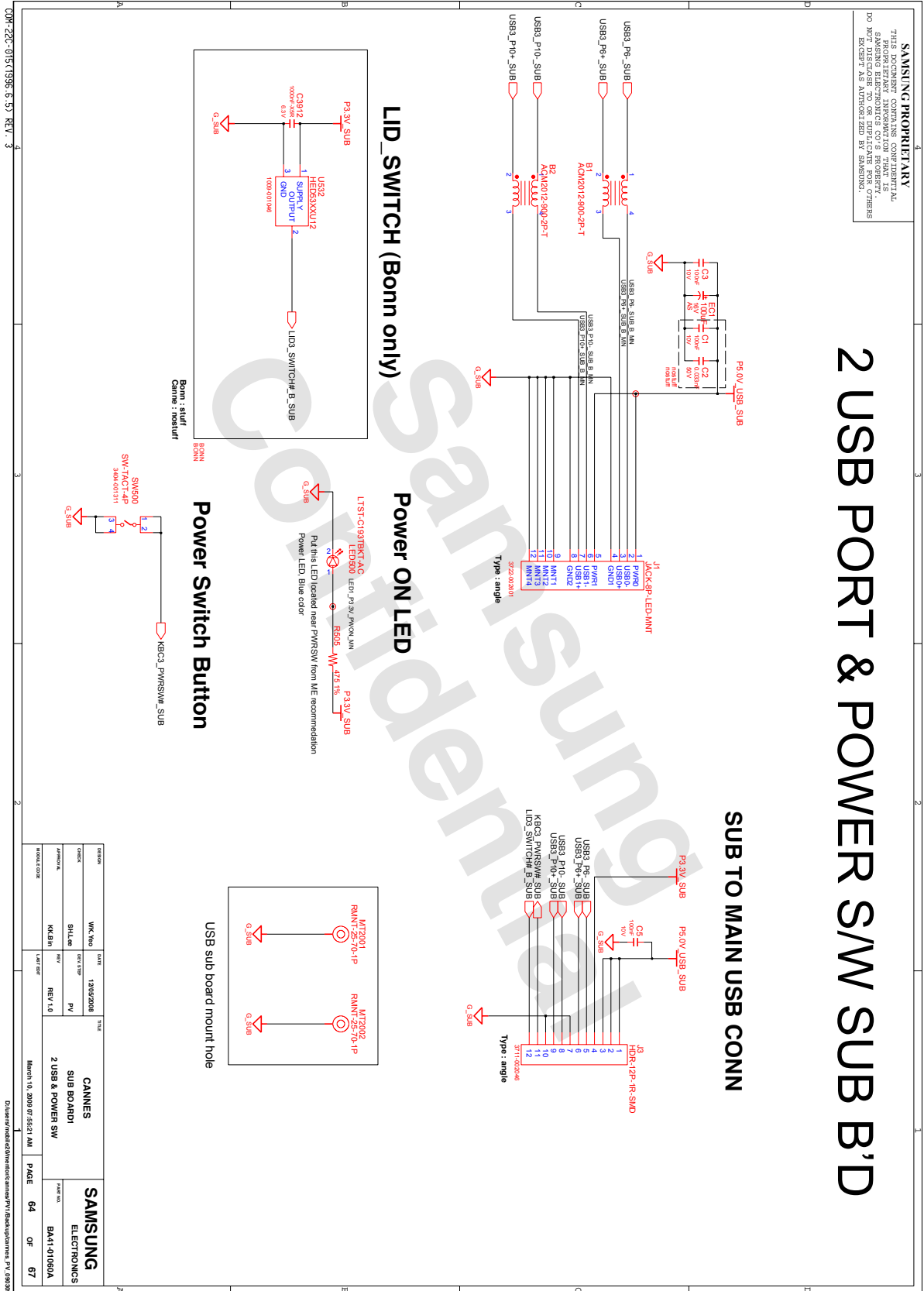
Daegu/Incheon/Seoul/Busan/Changwon/Jeju/Jeonju

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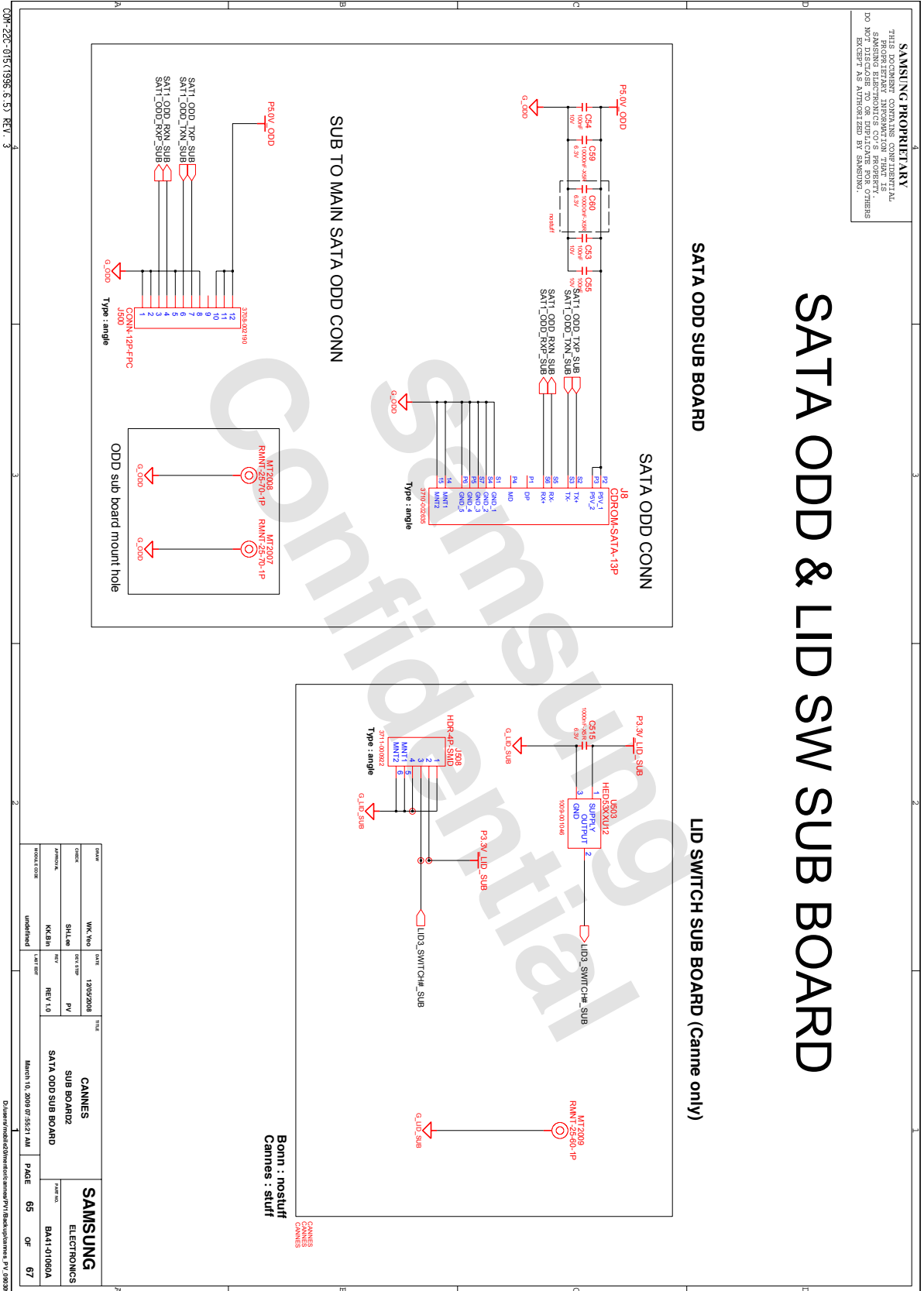
8. Block Diagram and Schematic



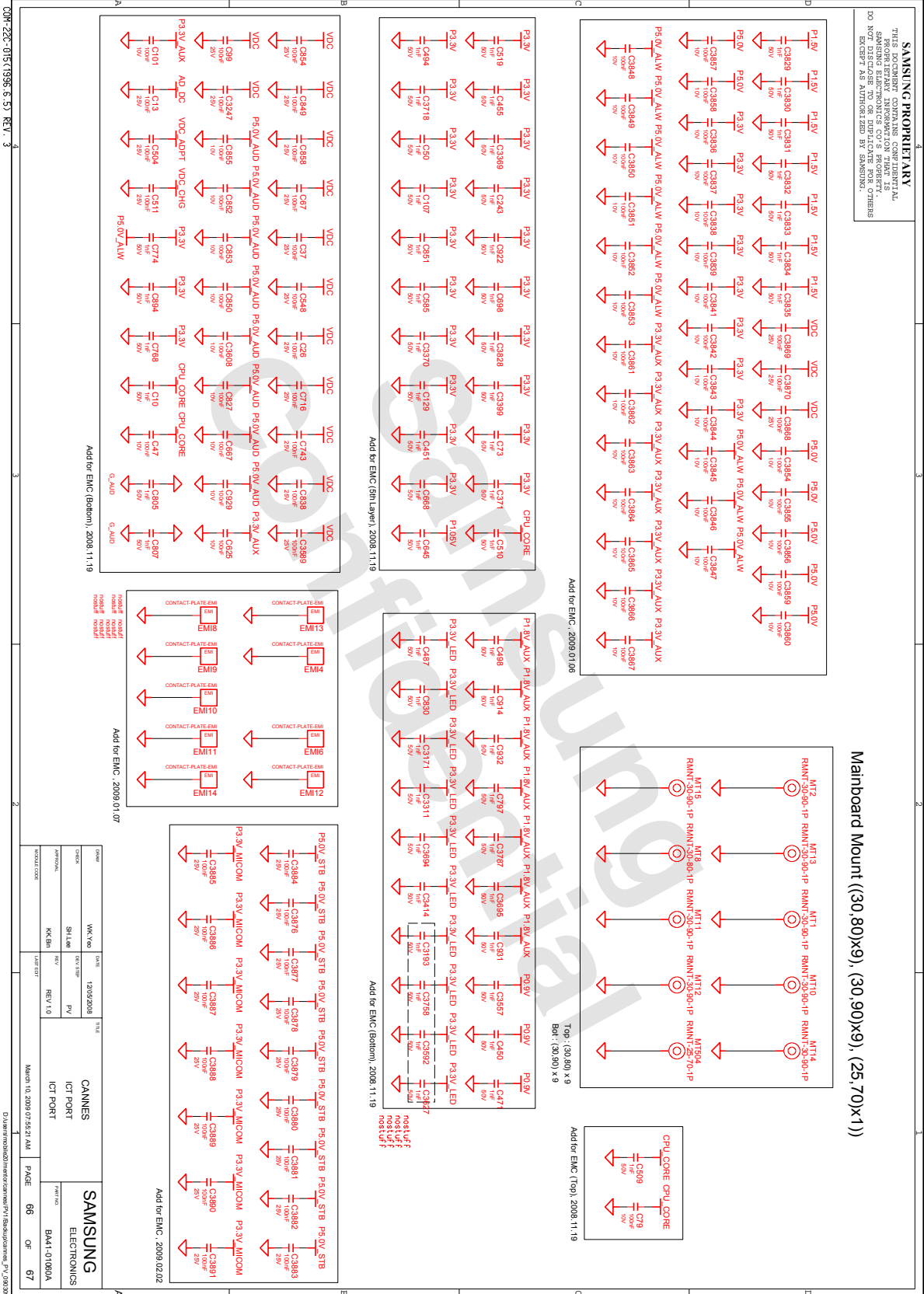
8. Block Diagram and Schematic



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