

# 8 Schematic Diagrams and PCB Silkscreen

## 8-1 MAIN BOARD

### 8-1-1 Schematic Diagrams

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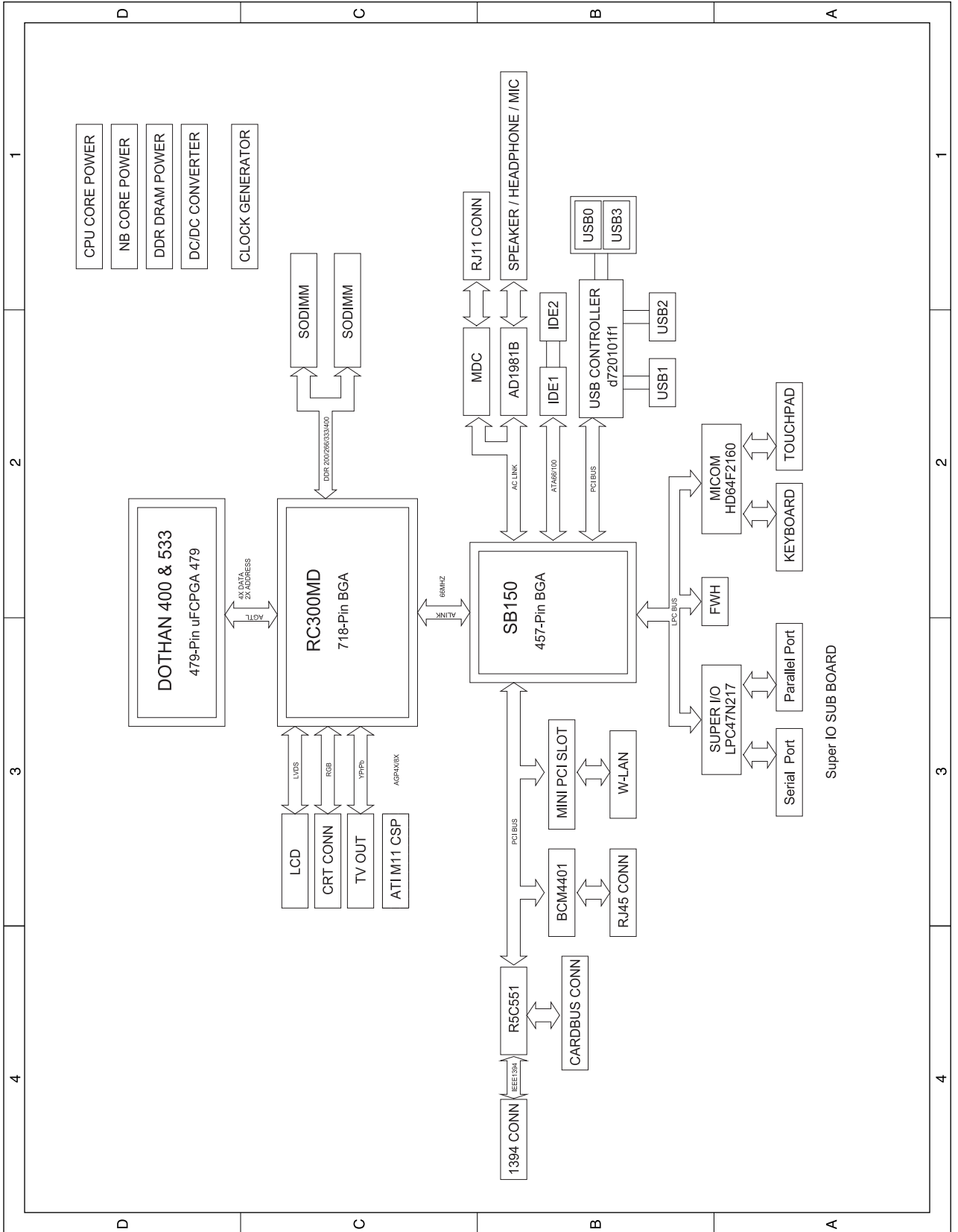
# OSCAR-G

CPU : Intel P-M DOTHAN  
 Chip Set : ATI RC300MD + SB150  
 Remarks : Additional USB Controller Used

Model Name :	Oscar-G
PBA Name :	
PCB Code :	BA41-00478A
Dev. Step :	MP
Revision :	1.1
T.R. Date :	2004-12-11

DRAW	CHECK	APPROVAL

8-1-1(a) Main Board Schematic Sheet 2 of 51(Block Diagram)





8-1-1(b) Main Board Schematic Sheet 3 of 51(Board Information)

SCHEMATIC ANNOTATIONS AND BOARD INFORMATION

PCI DEVICES

DEVICES	IDSEL#	REQ/INT#	INTERRUPTS
MINIPCI	AD19	0	C, A
CARDBUS CONTROLLER	AD20	1	A, B
LAN CONTROLLER	AD21	2	C
USB CONTROLLER	AD22	3	D, C, B

VOLTAGE RAILS

POWER	VOLTAGE	ACTIVE SCOPE	PAGE	REMARKS
PSV	6.19V	S0-S6	49	
PSV_ALW	5V	OFF IN S3-S5	49	
PSV_AUX	5V	S0-S5	45	SWITCHED FROM VDC
P3.3V	3.3V	OFF IN S0-S5	45	SWITCHED FROM P3.3V AUX
P3.3V_MCOM	3.3V	S0-S3	49	SWITCHED FROM VDC
P3.3V_AUX	3.3V	S0-S3	48	
P1.5V_AUX	1.5V	S0-S3	48	
P1.25V	1.25V	S0-S3	43	
VCC_CORE	VID[6:0]	OFF IN S0-S5	46	
VCCP	1.25V	OFF IN S0-S5	47	
AVDD_NB	2.5V	OFF IN S3-S5	15	SWITCHED FROM P2.5V
P1.8V_AUX	1.8V	OFF IN S3-S5	15	
P1.8V	1.8V	OFF IN S3-S5	48	SWITCHED FROM P1.8V
AVDD0	1.8V	OFF IN S3-S5	15	SWITCHED FROM P1.8V
AVDD1	1.8V	OFF IN S3-S5	15	SWITCHED FROM P1.8V
VCC_ODD	1.5V	OFF IN S3-S5	45	
VCC_EB	1.5V	OFF IN S3-S5	45	
P3.3V_AVDDC	3.3V	S0-S3	28	SWITCHED FROM P3.3V AUX
USB_AVDD	3.3V	S0-S3	29	SWITCHED FROM P3.3V AUX
V_BAT	3.0V	S0-S5	27	
P2.5V_AUX	2.5V	OFF IN S3-S5	29	SWITCHED FROM P2.5V AUX
AVDD_CK	2.5V	OFF IN S3-S5	29	SWITCHED FROM P2.5V AUX
P2.5V_AUX_SB	2.5V	S0-S3	29	SWITCHED FROM P2.5V AUX
P2.5V_AUX	2.5V	S0-S5	45	SWITCHED FROM PSV / ALW / VDC
VGA_CORE	1.01/2V	OFF IN S3-S5	43	
P1.8V_VGA	1.8V	OFF IN S3-S5	48	SWITCHED FROM P2.5V AUX
VCC_CB	3.3V/5V	OFF IN S3-S5	34	
VPP_CB	VAR	OFF IN S3-S5	34	
AVDD	5V	OFF IN S0-S5	36	SWITCHED FROM PSV

I2C / SMB ADDRESS

DEVICES	ADDRESS	HEX	BUS
MASTER			
SOUTH BRIDGE	1001 110X	9Ch	SMBUS MASTER
ADM1032(CPU THERMAL SENSOR)	1001 110X	9Ch	THERMAL SENSOR
ADM1032(GFX THERMAL SENSOR)	1001 110X	9Ch	THERMAL SENSOR
SODIMM1	1010 001X	A2h	

USB PORT ASSIGN

PORT NUMBER	ASSIGNED TO
0	SYSTEM PORT A
1	SYSTEM PORT B
2	SYSTEM PORT C
3	SYSTEM PORT D
4	RESERVED

CPU CORE VOLTAGE TABLE

VID5	VID4	VID3	VID2	VID1	VID0	VOLTAGE	VID5	VID4	VID3	VID2	VID1	VID0	VOLTAGE
0	0	0	0	0	0	1.789V	1	0	0	0	0	0	1.196V
0	0	0	0	0	1	1.692V	1	0	0	0	0	1	1.180V
0	0	0	0	1	0	1.676V	1	0	0	0	1	0	1.164V
0	0	0	1	0	0	1.660V	1	0	0	0	1	0	1.148V
0	0	0	1	0	1	1.644V	1	0	0	1	0	0	1.132V
0	0	0	1	0	1	1.628V	1	0	0	1	0	0	1.116V
0	0	0	1	1	0	1.612V	1	0	0	1	1	0	1.100V
0	0	0	1	1	1	1.596V	1	0	0	1	1	0	1.084V
0	0	0	1	1	1	1.580V	1	0	0	1	1	0	1.068V
0	0	0	1	1	1	1.564V	1	0	0	1	1	0	1.052V
0	0	1	0	0	0	1.548V	1	0	1	0	0	0	1.036V
0	0	1	0	1	0	1.532V	1	0	1	0	1	0	1.020V
0	0	1	0	1	1	1.516V	1	0	1	1	0	0	1.004V
0	0	1	1	0	0	1.500V	1	0	1	1	0	1	0.988V
0	0	1	1	1	0	1.484V	1	0	1	1	1	0	0.972V
0	0	1	1	1	1	1.468V	1	0	1	1	1	1	0.956V
0	1	0	0	0	0	1.452V	1	1	0	0	0	0	0.940V
0	1	0	0	1	0	1.436V	1	1	0	1	0	0	0.924V
0	1	0	0	1	1	1.420V	1	1	0	1	1	0	0.908V
0	1	0	1	0	0	1.404V	1	1	1	0	0	1	0.892V
0	1	0	1	0	1	1.388V	1	1	1	0	1	1	0.876V
0	1	0	1	0	1	1.372V	1	1	1	0	1	1	0.860V
0	1	0	1	1	0	1.356V	1	1	1	1	0	1	0.844V
0	1	0	1	1	1	1.340V	1	1	1	1	1	1	0.828V
0	1	0	1	1	1	1.324V	1	1	1	1	1	1	0.812V
0	1	0	1	1	1	1.308V	1	1	1	1	1	1	0.796V
0	1	0	1	1	1	1.292V	1	1	1	1	1	1	0.780V
0	1	0	1	1	1	1.276V	1	1	1	1	1	1	0.764V
0	1	1	0	0	0	1.260V	1	1	1	1	0	0	0.748V
0	1	1	1	0	0	1.244V	1	1	1	1	0	0	0.732V
0	1	1	1	1	0	1.228V	1	1	1	1	1	0	0.716V
0	1	1	1	1	1	1.212V	1	1	1	1	1	1	0.700V

DOTHAN@400MHz  
HIGHEST FREQ. [ 0 1 0 0 1 1 0 1 3.356V ] LOWEST FREQ.

DOTHAN@533MHz  
HIGHEST FREQ. [ 0 1 0 0 1 1 1 1 3.340V ]

DOTHAN@600MHz  
HIGHEST FREQ. [ 0 1 0 0 1 1 1 1 3.324V ]

DOTHAN@666MHz  
HIGHEST FREQ. [ 0 1 0 0 1 1 1 1 3.308V ]

DOTHAN@732MHz  
HIGHEST FREQ. [ 0 1 0 0 1 1 1 1 3.292V ]

DOTHAN@800MHz  
HIGHEST FREQ. [ 0 1 0 0 1 1 1 1 3.276V ]

DOTHAN@866MHz  
HIGHEST FREQ. [ 0 1 0 0 1 1 1 1 3.260V ]

DOTHAN@932MHz  
HIGHEST FREQ. [ 0 1 0 0 1 1 1 1 3.244V ]

DOTHAN@1000MHz  
HIGHEST FREQ. [ 0 1 0 0 1 1 1 1 3.228V ]

DOTHAN@1066MHz  
HIGHEST FREQ. [ 0 1 0 0 1 1 1 1 3.212V ]

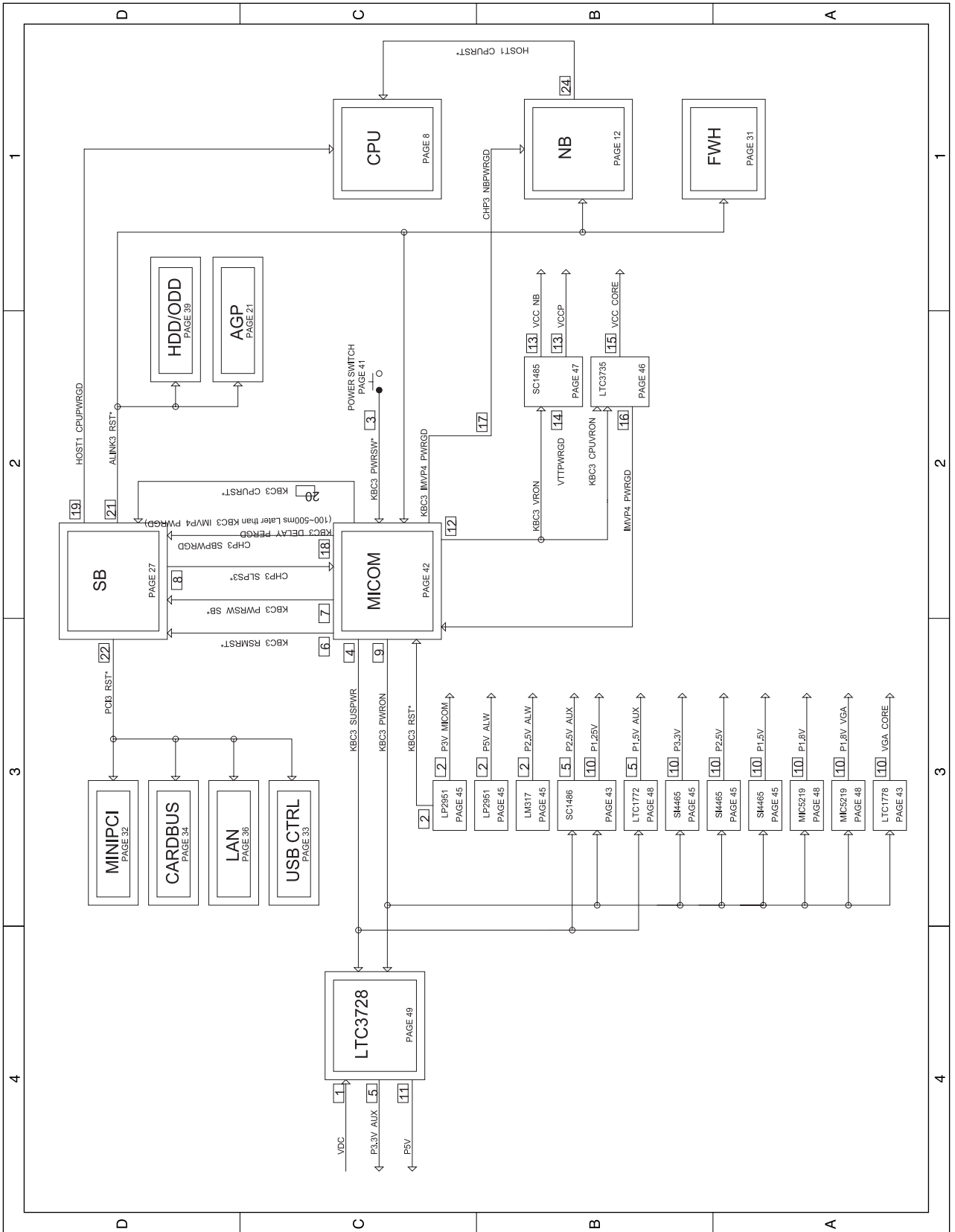
CPU STRAPS

	Intel® Pentium-M	Intel® Celeron-M
DOTHAN	DOTHAN@533MHz	DOTHAN@533MHz
FSB	FSB 533MHz	FSB 400MHz
R888	NO STUFF	STUFF
R869	STUFF	NO STUFF
B16	STUFF	NO STUFF
B17	NO STUFF	STUFF
Sheet 17 R252	NO STUFF	STUFF

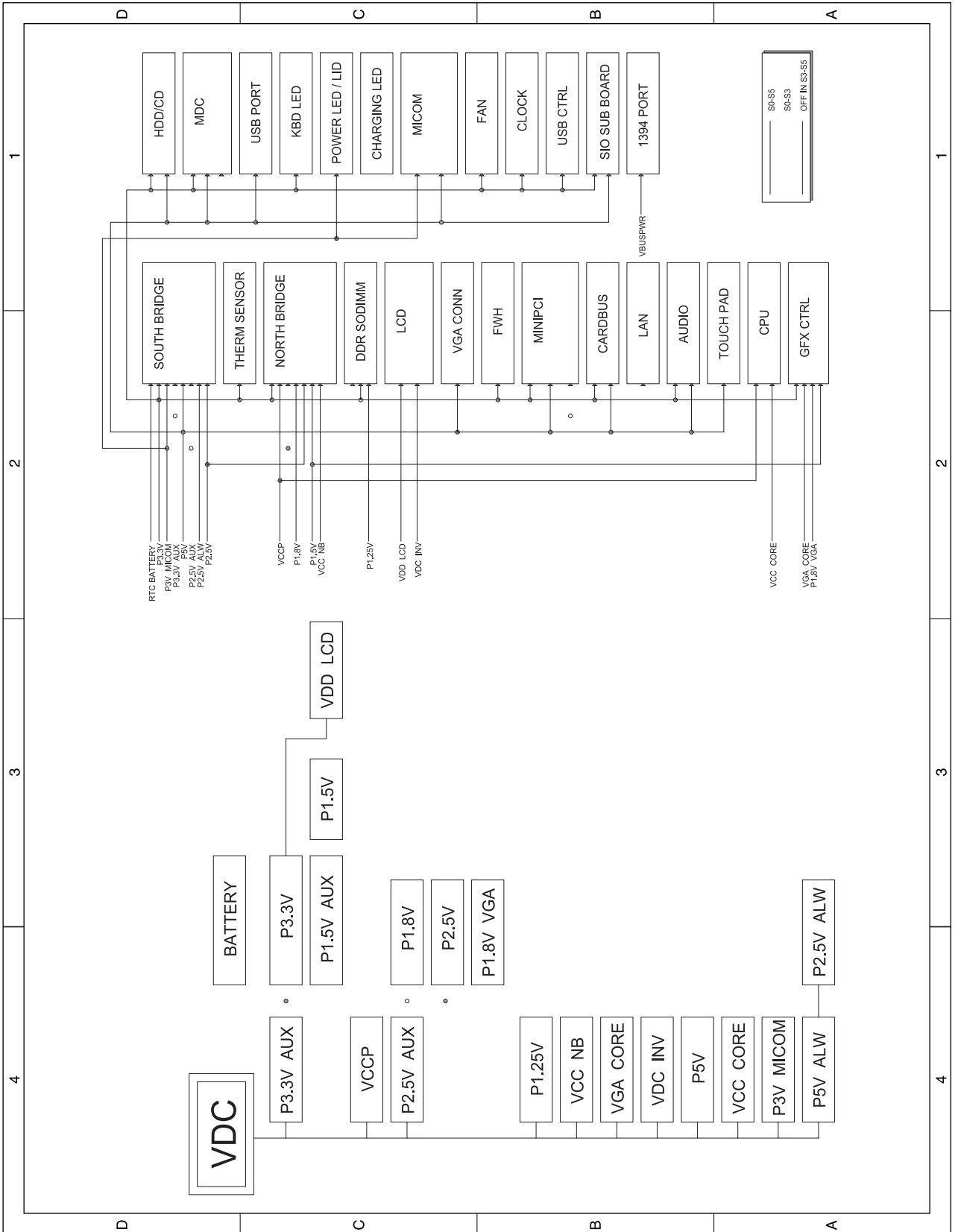
REVISION HISTORY

SEE REVISION CHANGE NOTE FOR MORE INFORMATION.

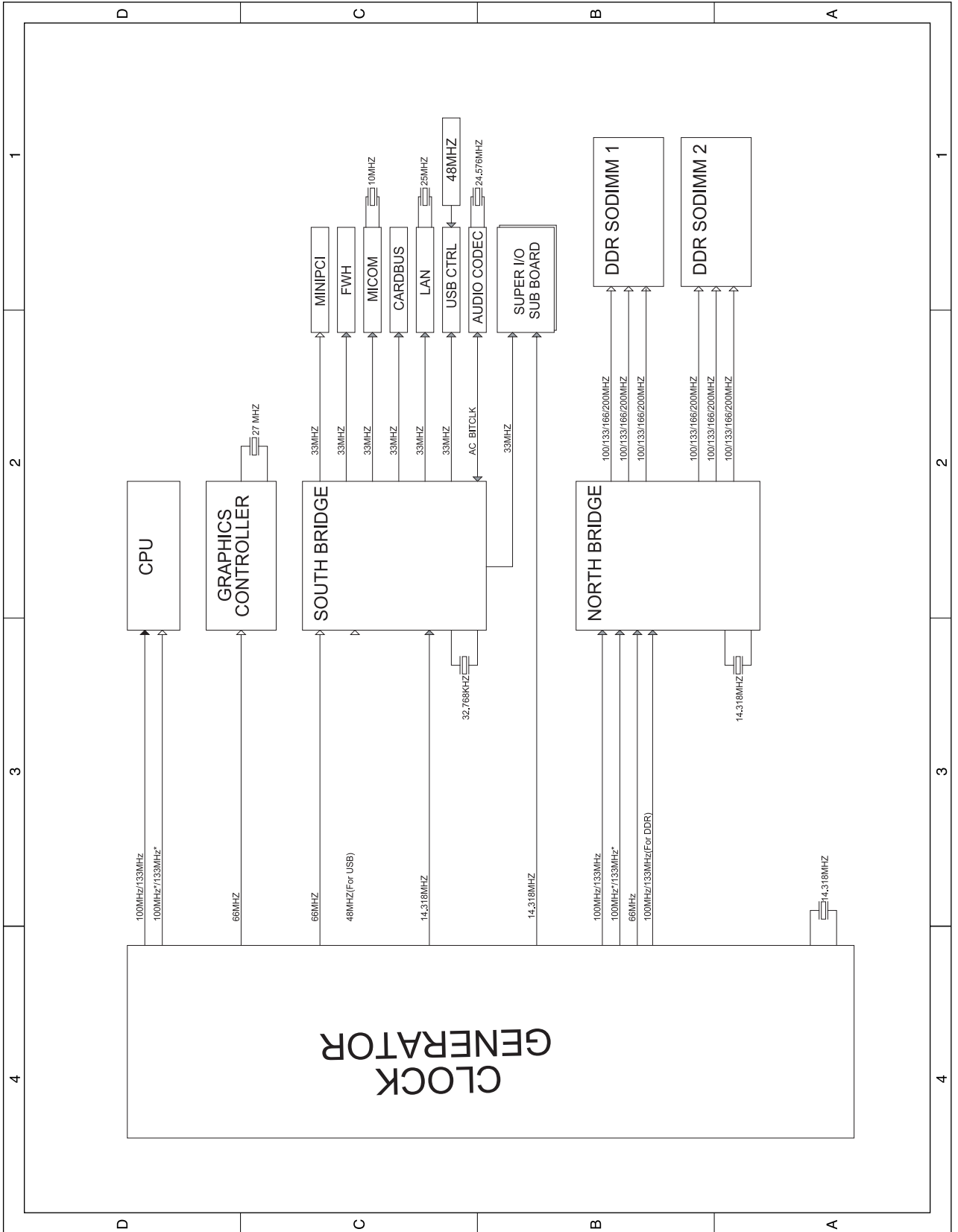
8-1-1(c) Main Board Schematic Sheet 4 of 51(Power On Sequence)



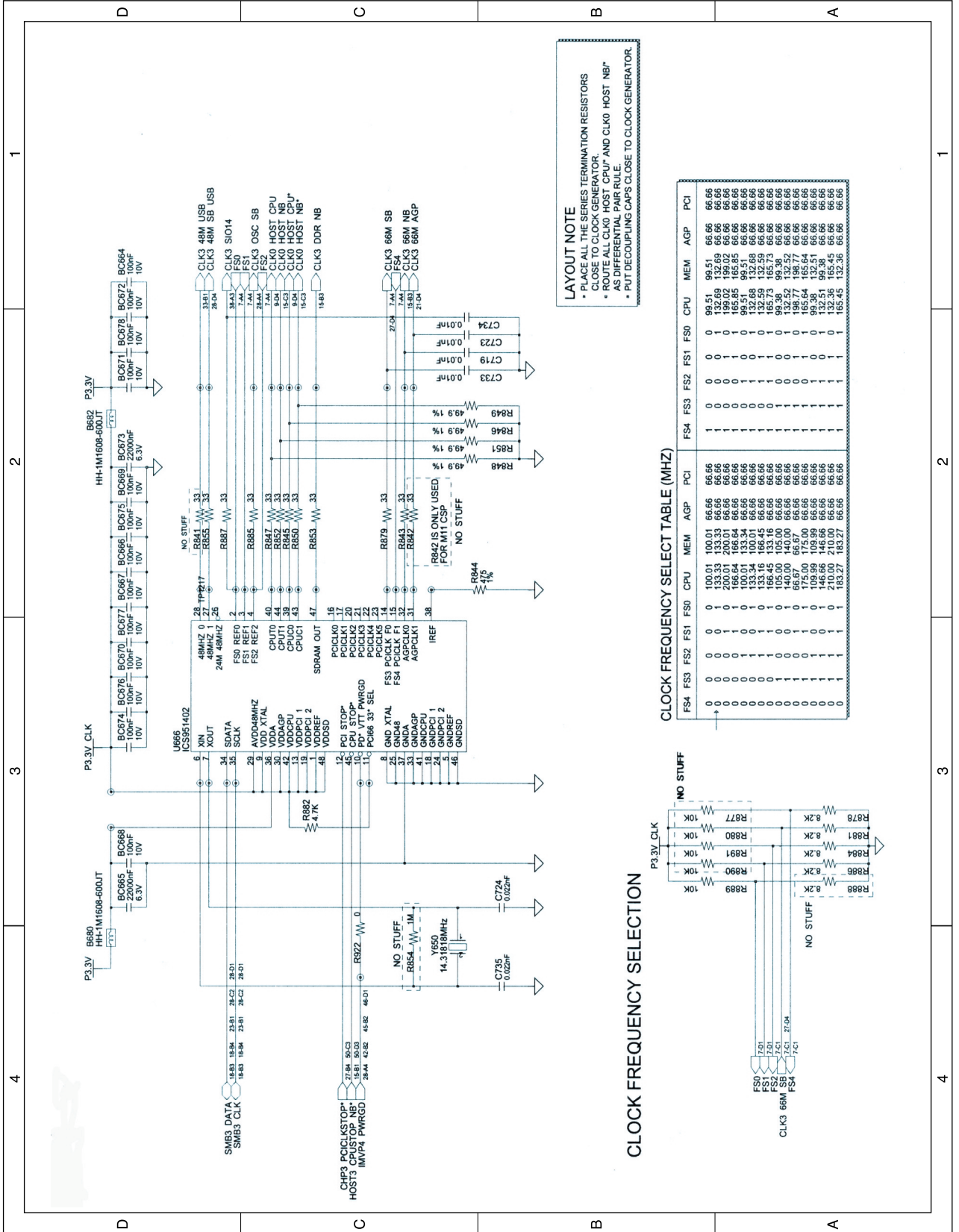
8-1-1(d) Main Board Schematic Sheet 5 of 51(Power Diagram)



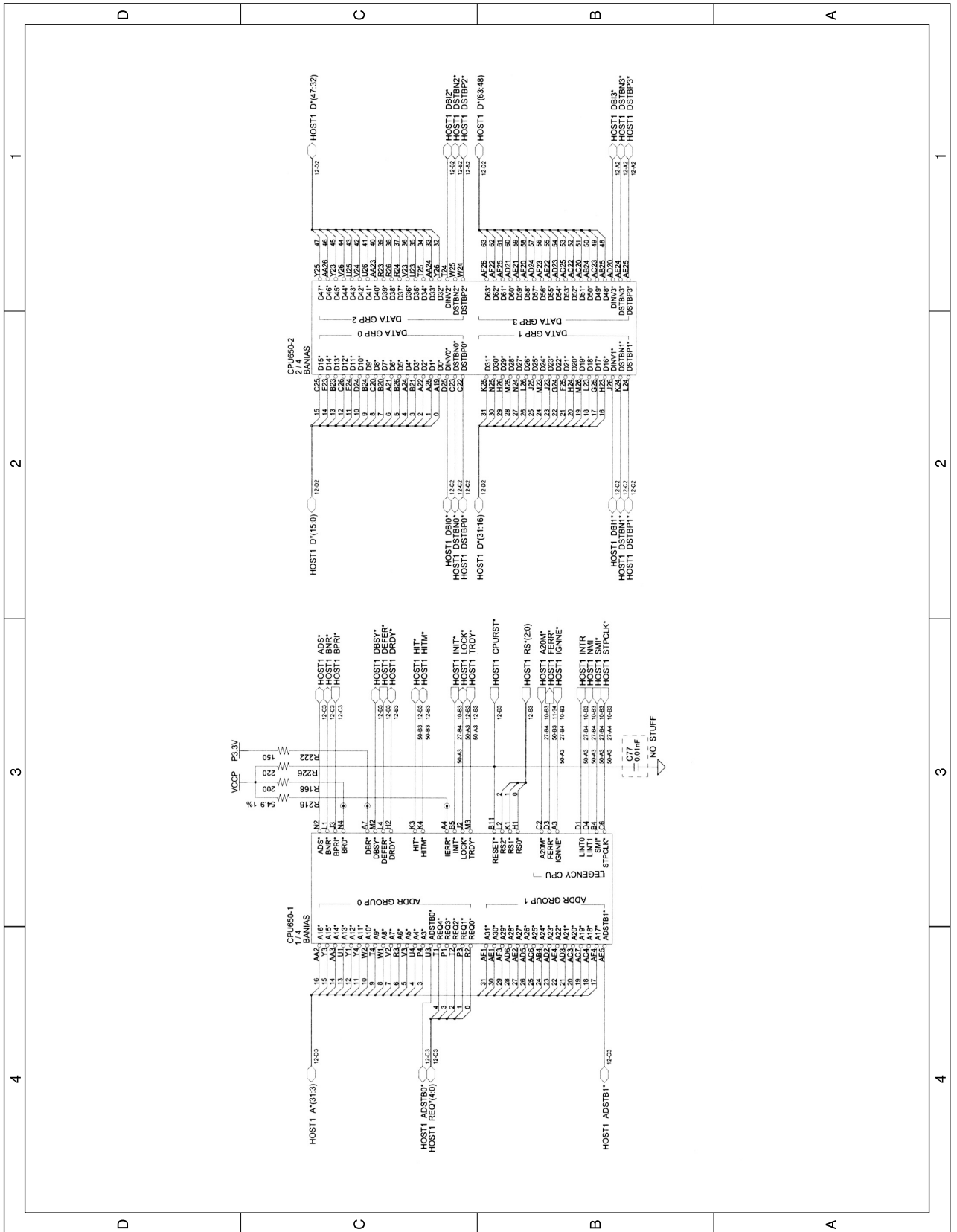
8-1-1(e) Main Board Schematic Sheet 6 of 51(Clock Diagram)



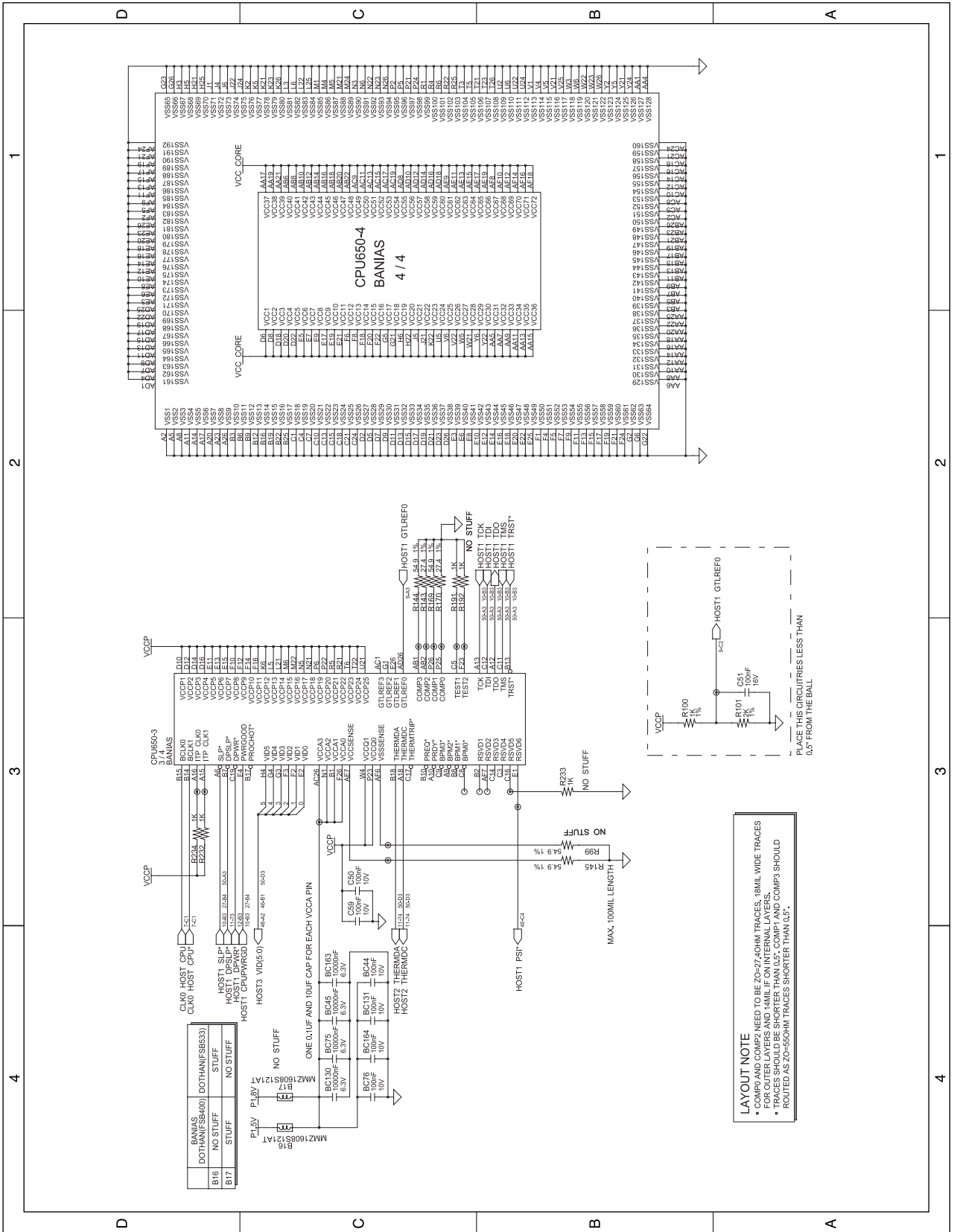
8-1-1(f) Main Board Schematic Sheet 7 of 51(Clock Diagram)



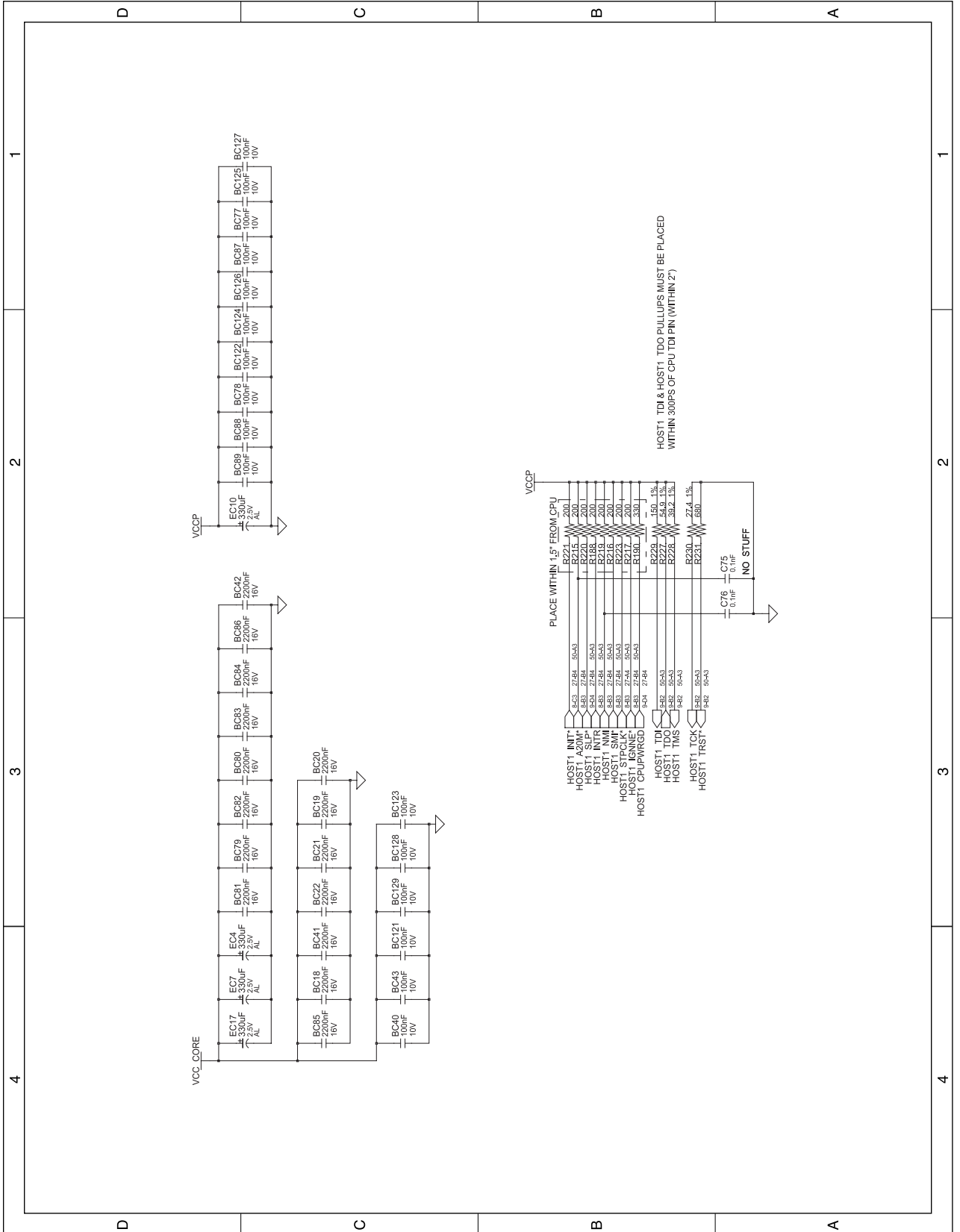
8-1-1(g) Main Board Schematic Sheet 8 of 51 (CPU[1/2])



8-1-1(h) Main Board Schematic Sheet 9 of 51(CPU[2/2])

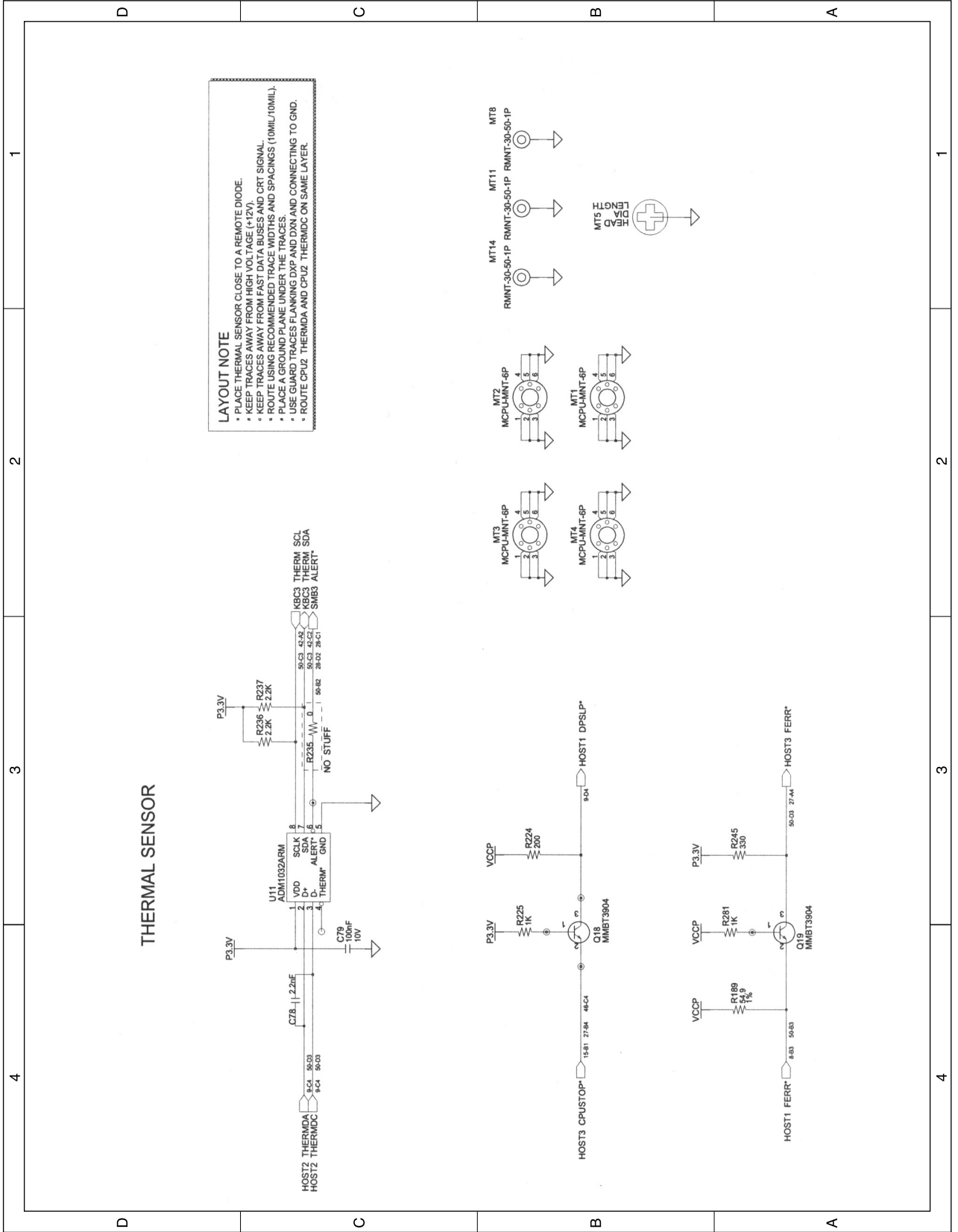


8-1-1(i) Main Board Schematic Sheet 10 of 51(CPU Pullups / Decouplings)



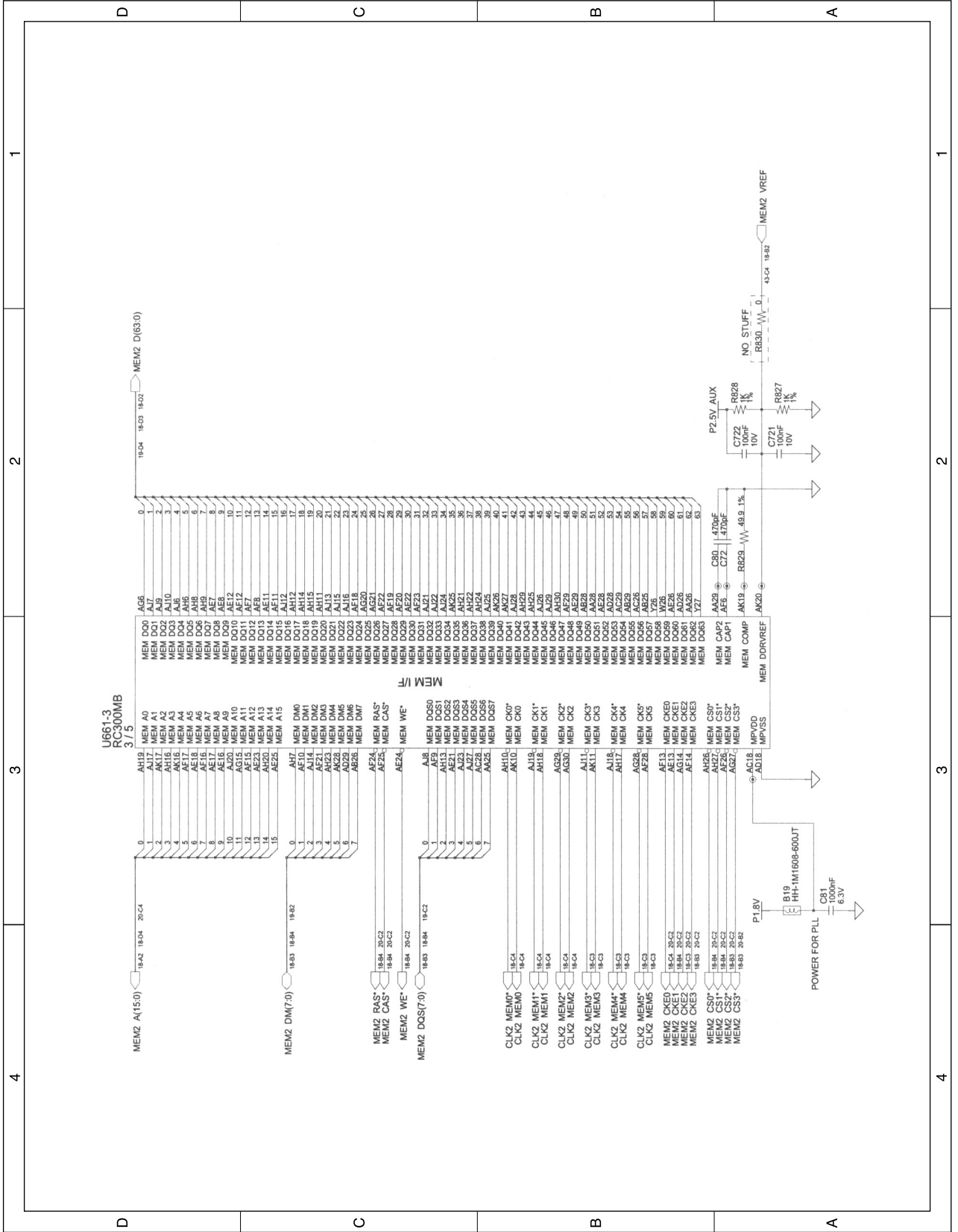


8-1-1(j) Main Board Schematic Sheet 11 of 51(Thermal Sensor / CPU ETC)

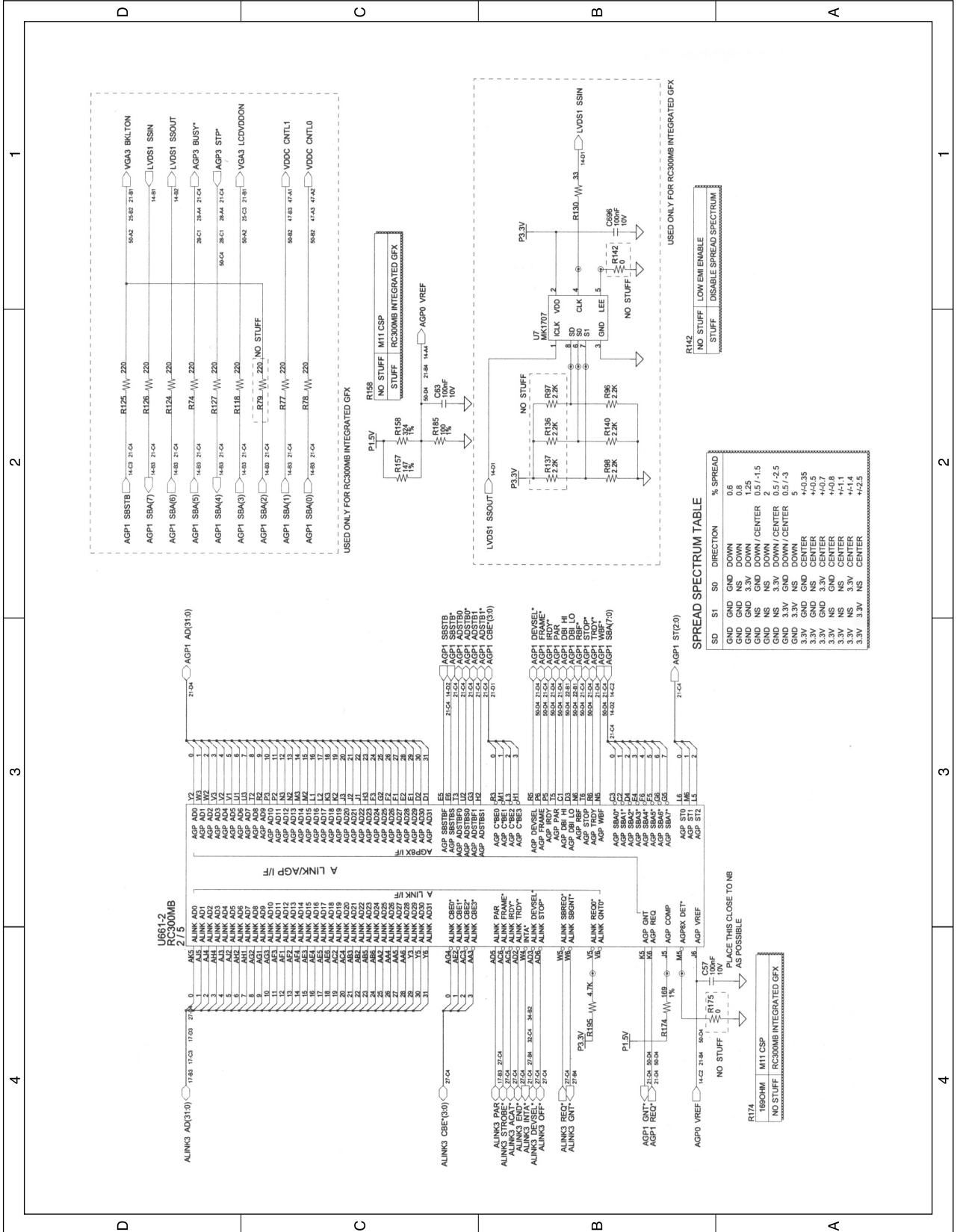




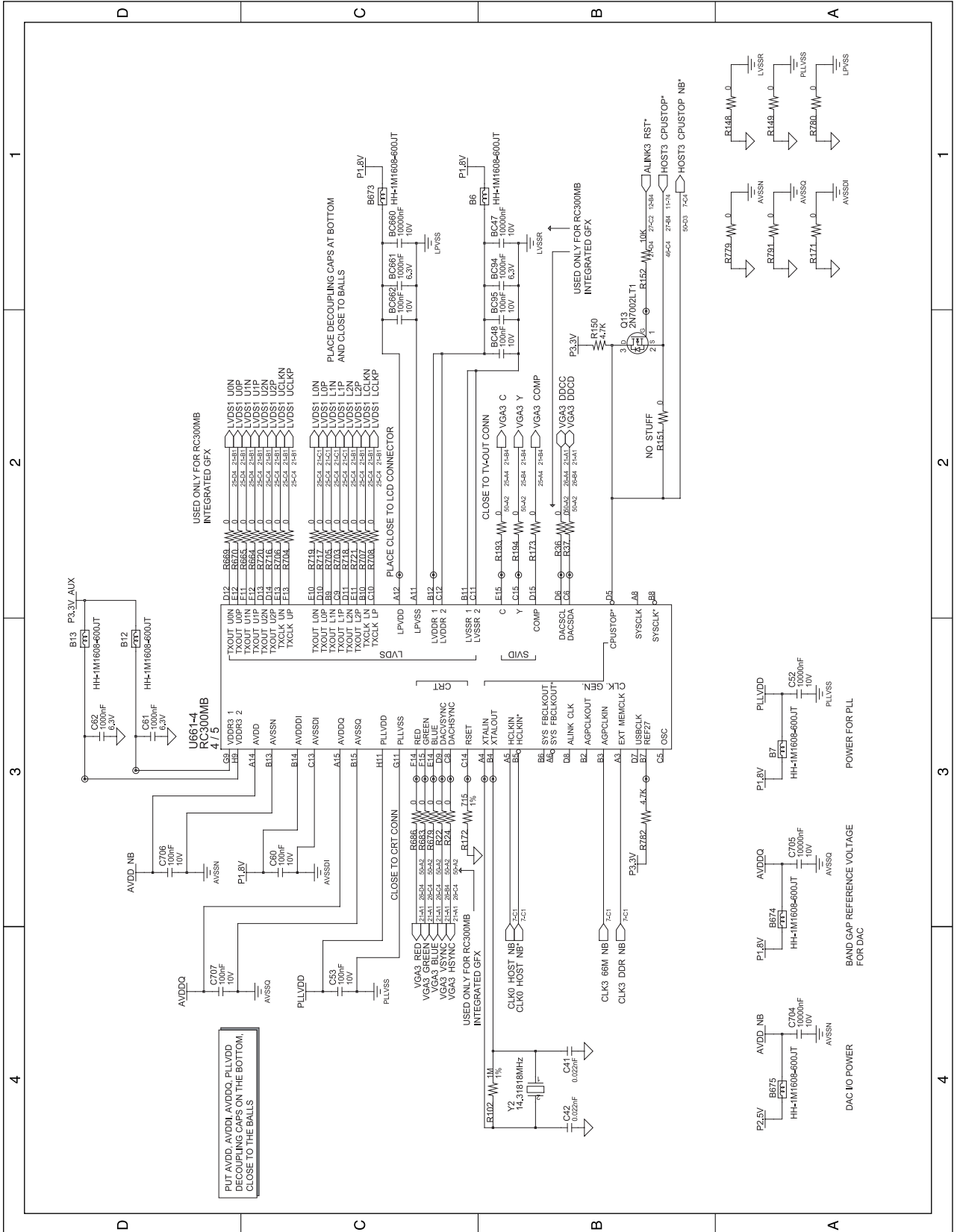
8-1-1(I) Main Board Schematic Sheet 13 of 51(North Bridge[2/5])



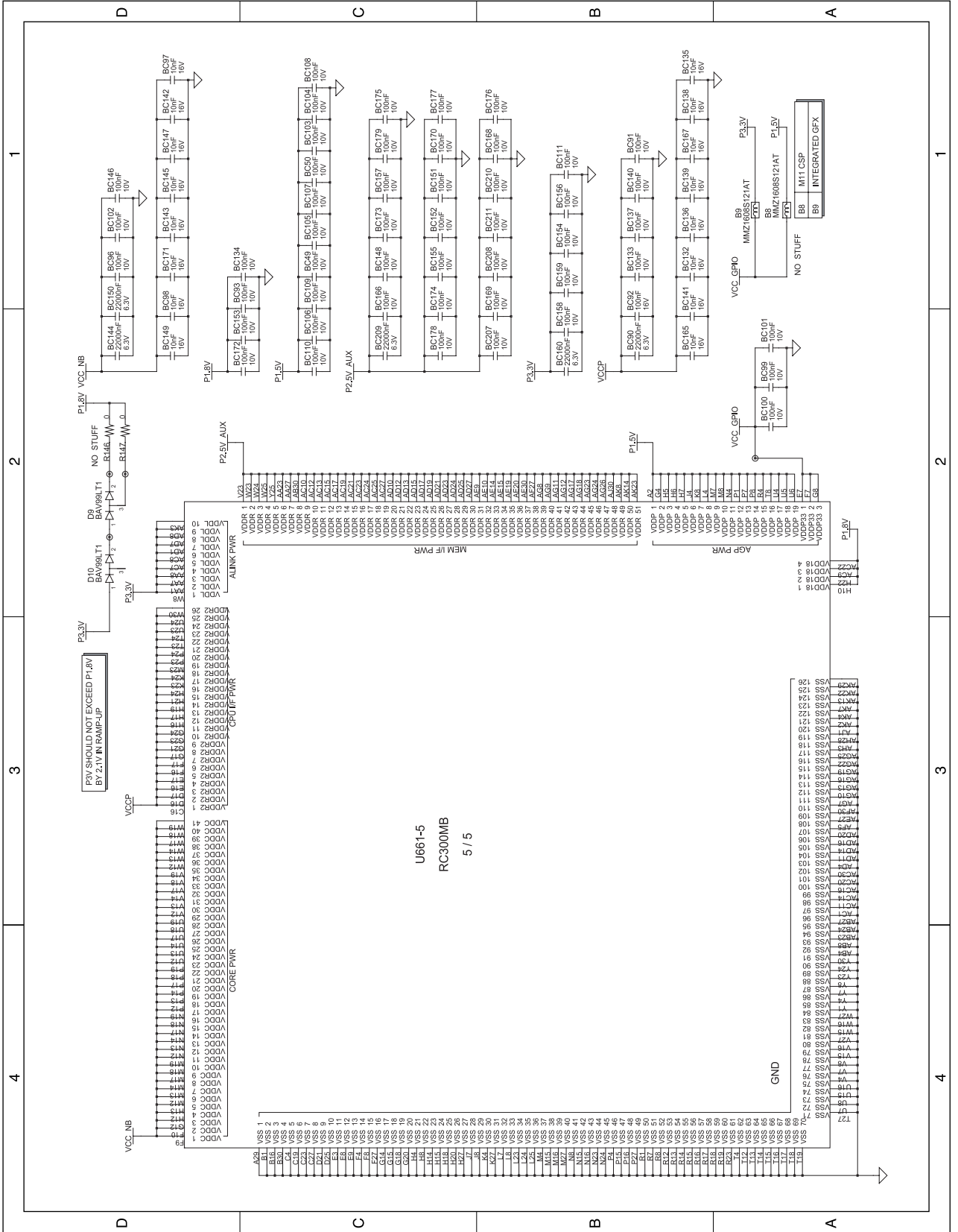
8-1-1(m) Main Board Schematic Sheet 14 of 51(North Bridge[3/5])



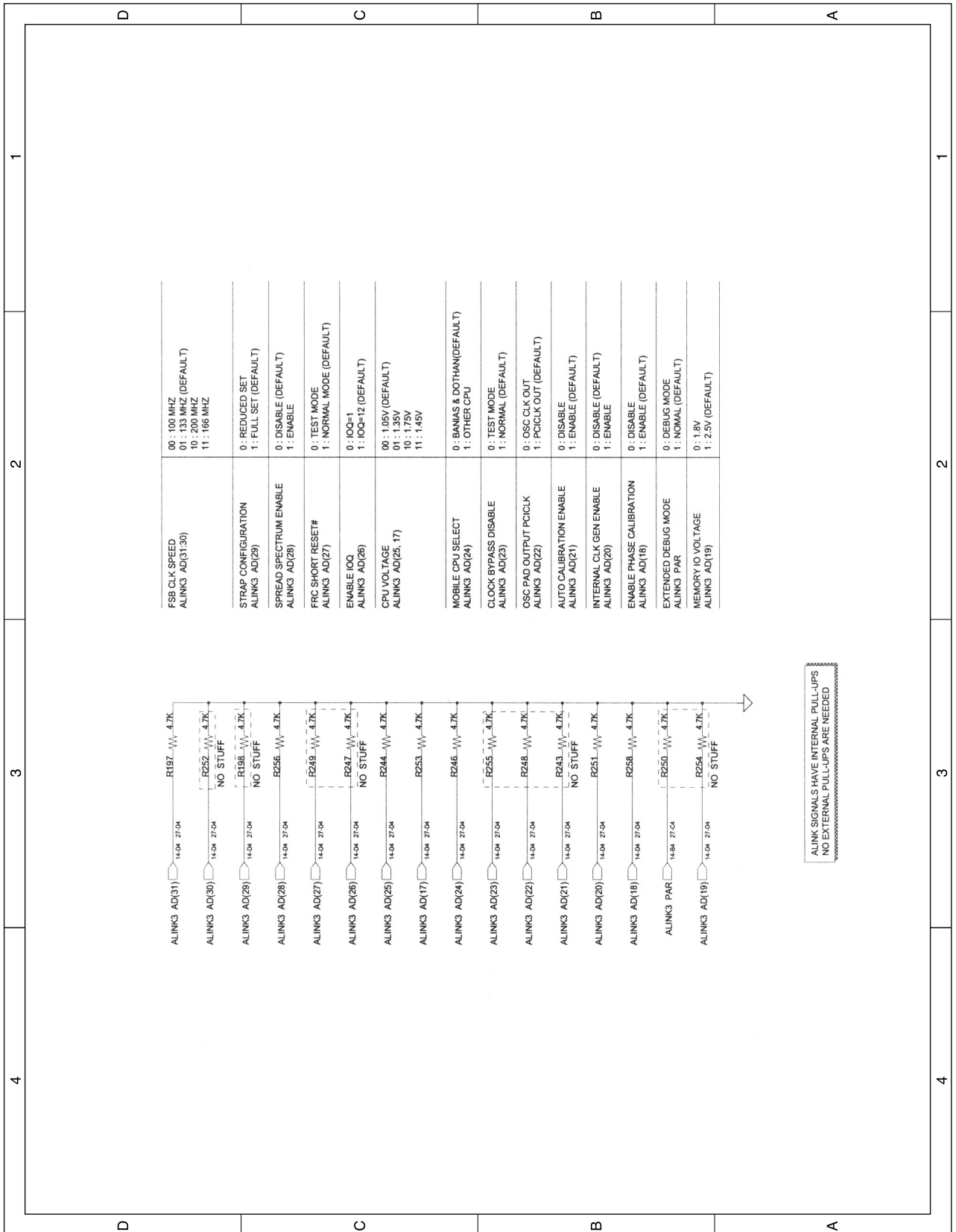
8-1-1(n) Main Board Schematic Sheet 15 of 51(North Bridge[4/5])



8-1-1(o) Main Board Schematic Sheet 16 of 51(North Bridge[5/5])

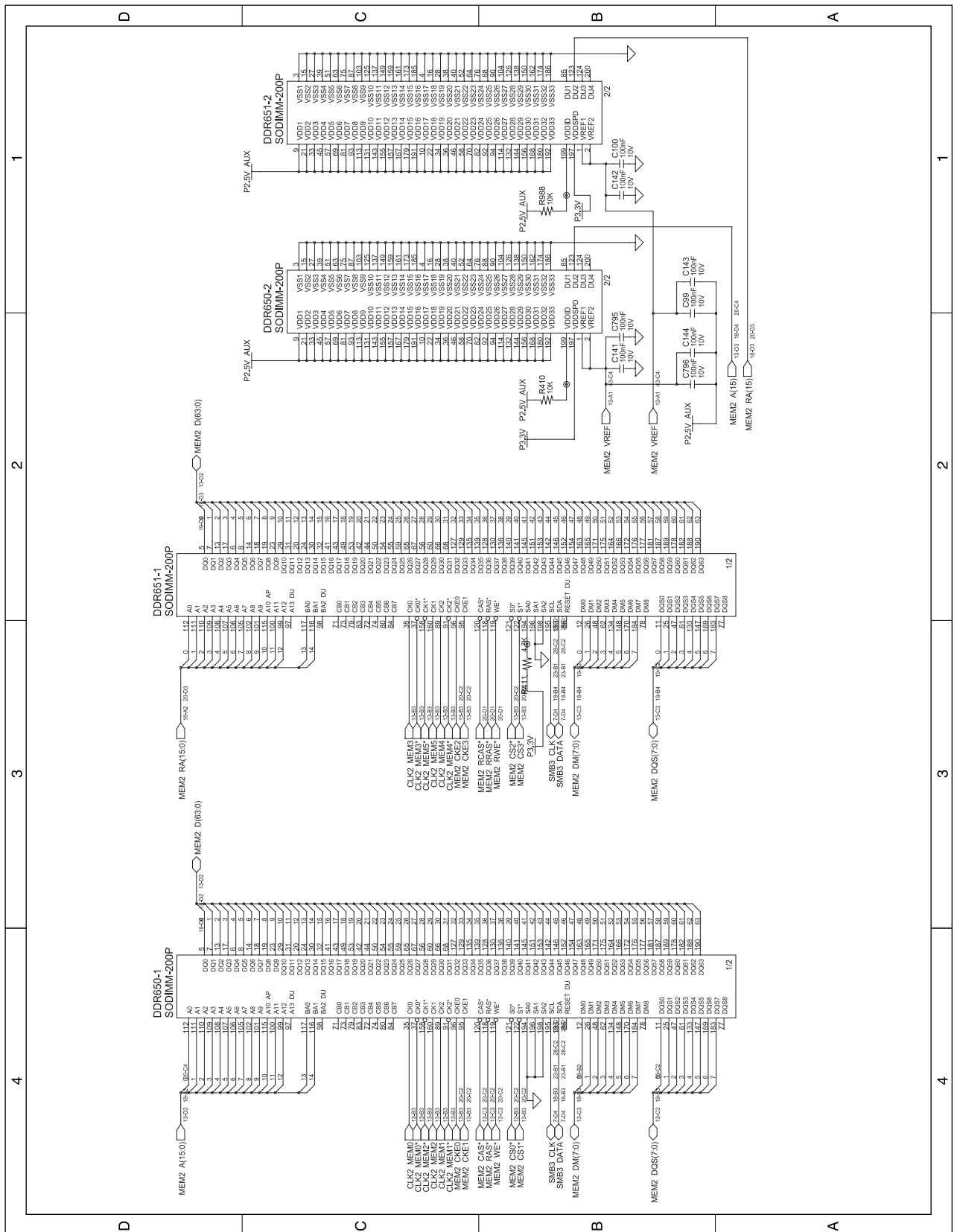


8-1-1(p) Main Board Schematic Sheet 17 of 51(North Bridge Straps)



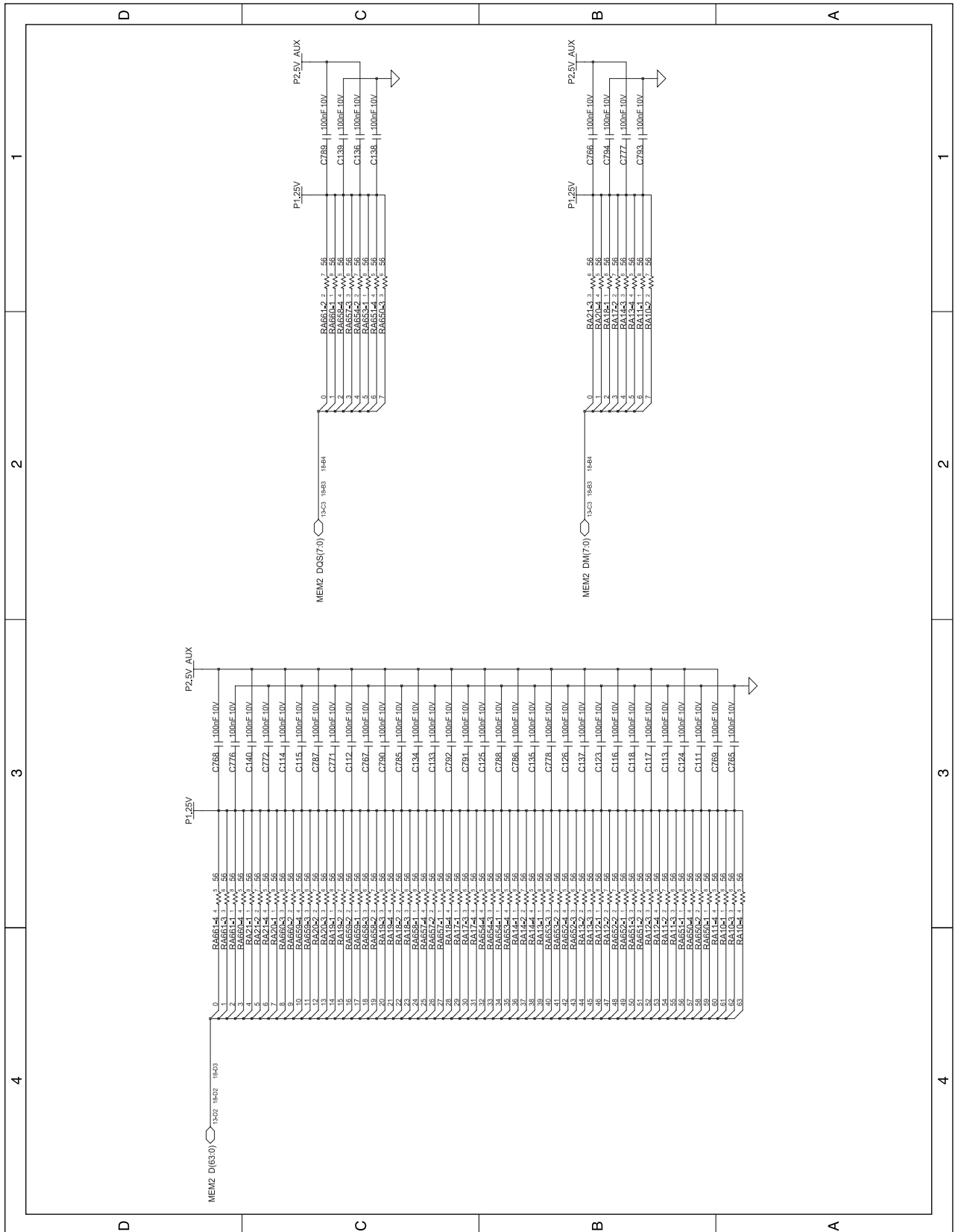
ALINK SIGNALS HAVE INTERNAL PULL-UPS  
NO EXTERNAL PULL-UPS ARE NEEDED

8-1-1(q) Main Board Schematic Sheet 18 of 51(DDR SODIMM)

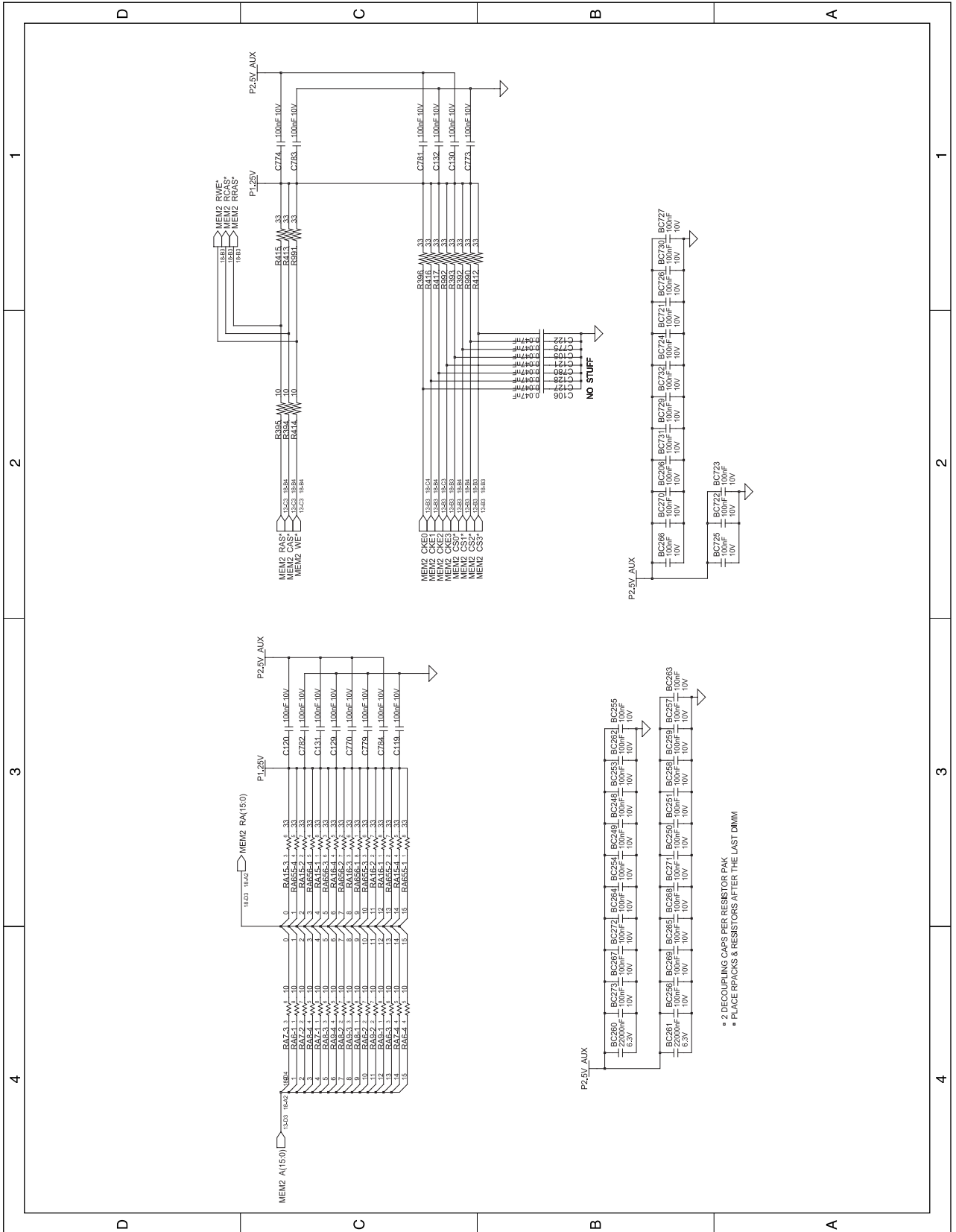




8-1-1(r) Main Board Schematic Sheet 19 of 51(DDR Termination[1/2])

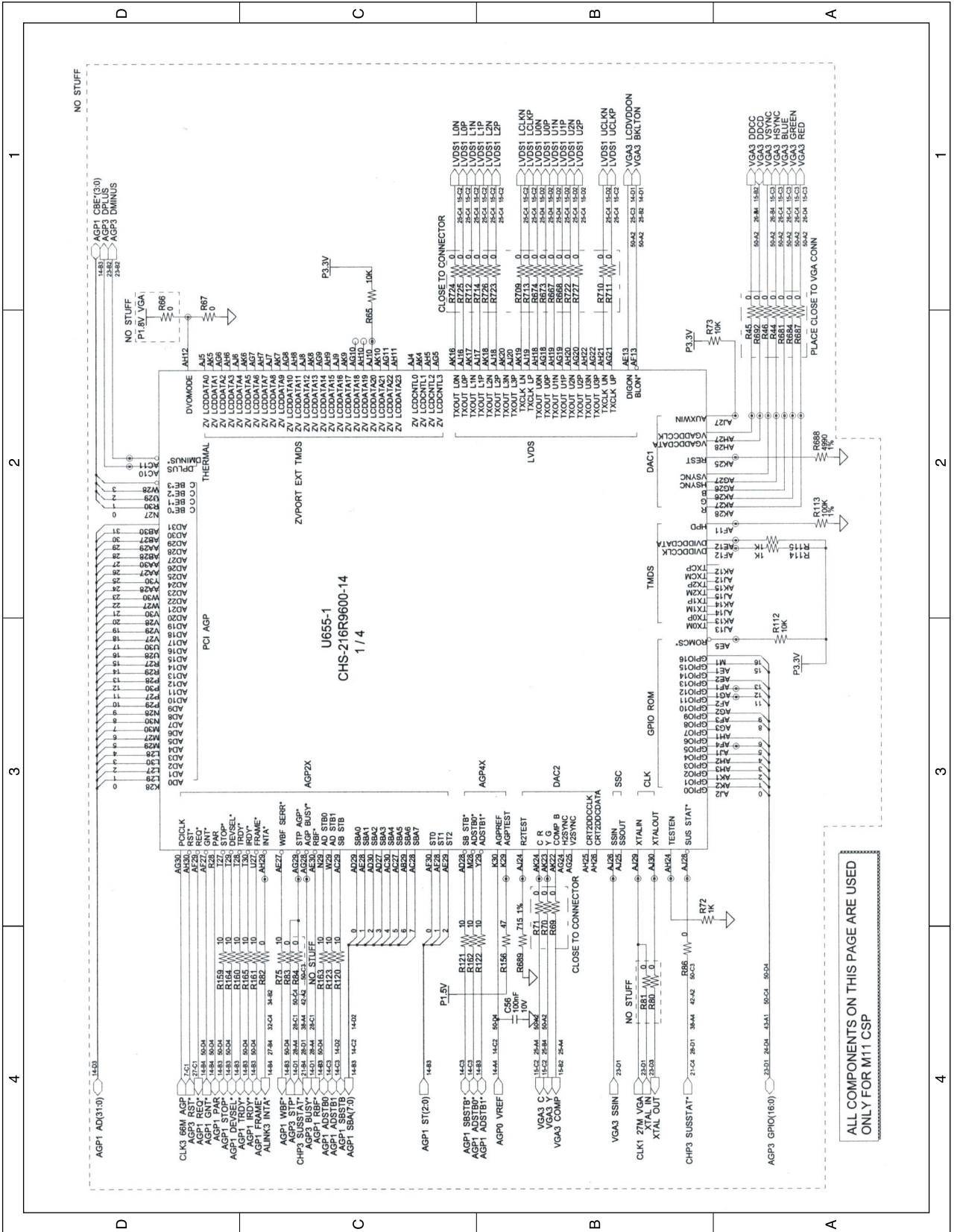


8-1-1(s) Main Board Schematic Sheet 20 of 51(DDR Termination[2/2])



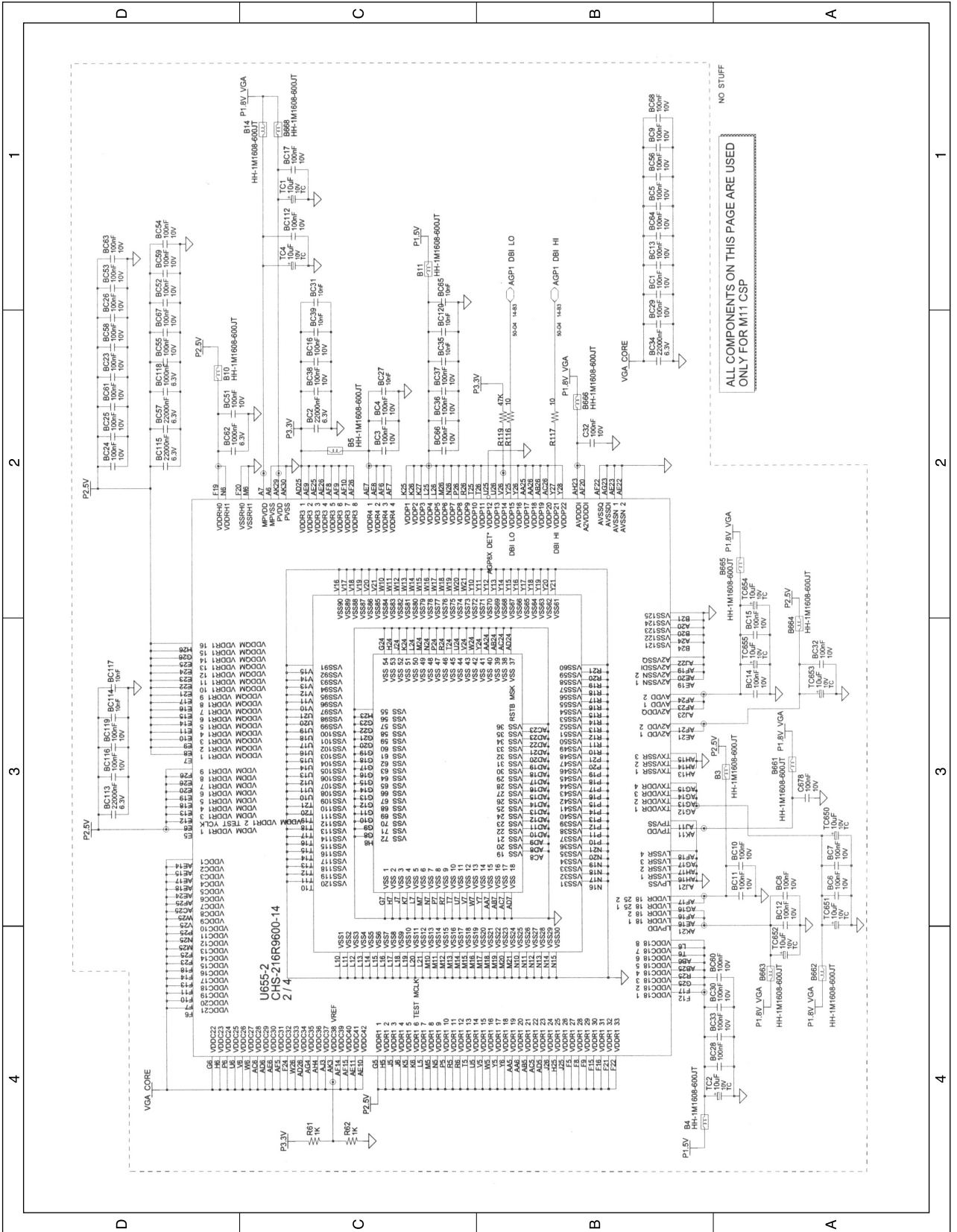
• 2 DECOUPLING CAPS PER RESISTOR PAIR  
 • PLACE PACKS & RESISTORS AFTER THE LAST DIMM

8-1-1(t) Main Board Schematic Sheet 21 of 51(Graphics Controller[1/3])

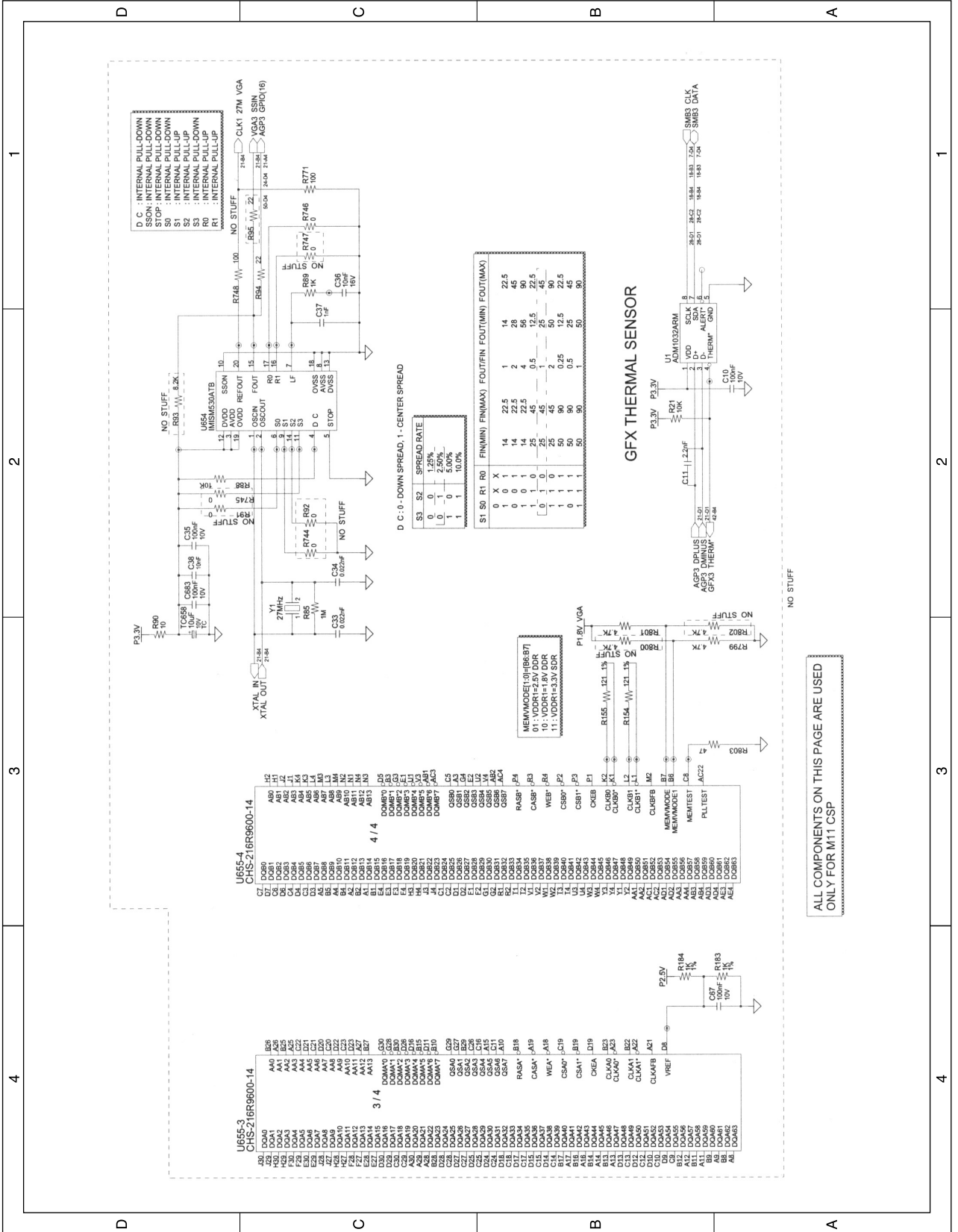


ALL COMPONENTS ON THIS PAGE ARE USED ONLY FOR M11 CSP

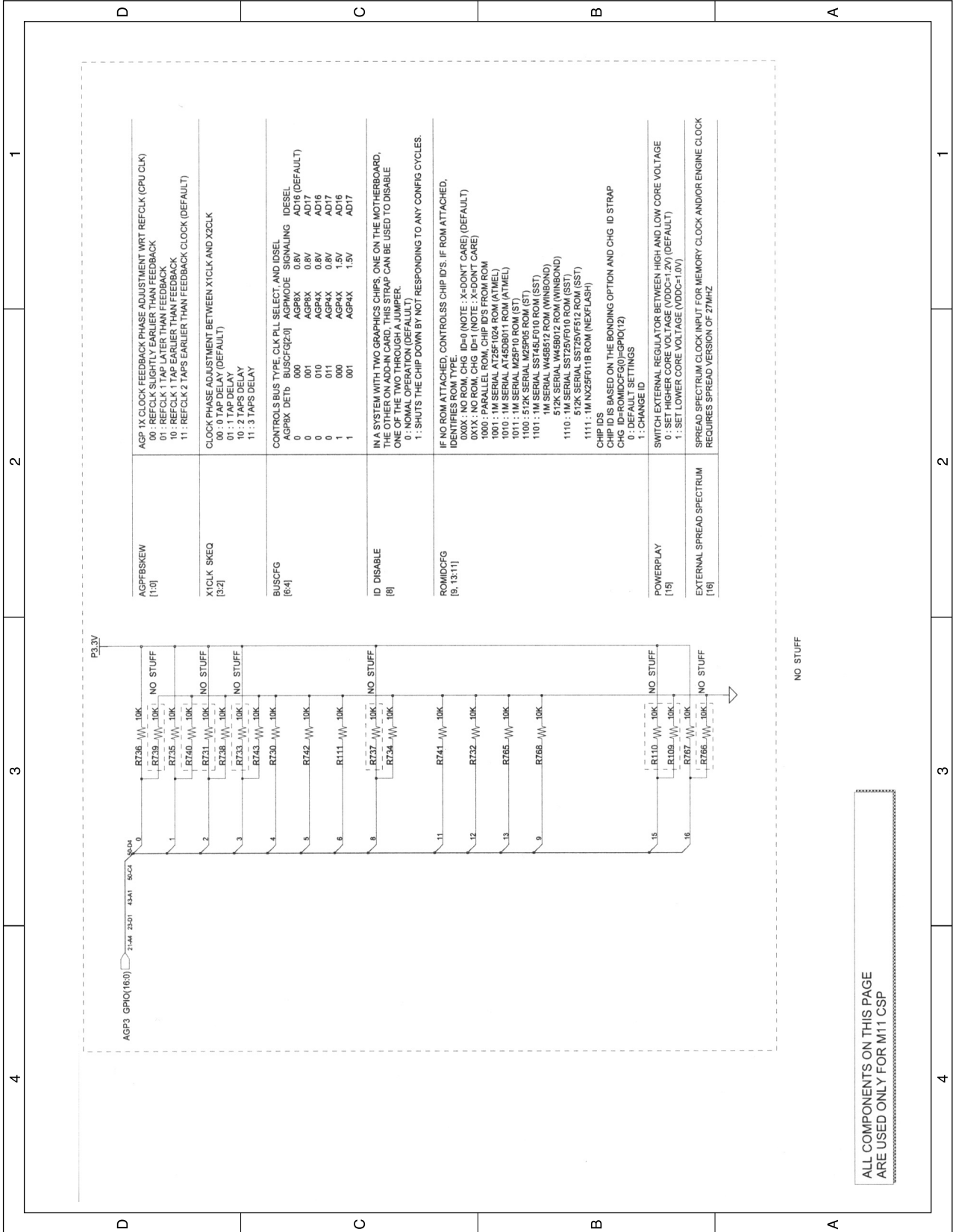
8-1-1(u) Main Board Schematic Sheet 22 of 51(Graphics Controller[2/3])



8-1-1(v) Main Board Schematic Sheet 23 of 51 (Graphics Controller[3/3])



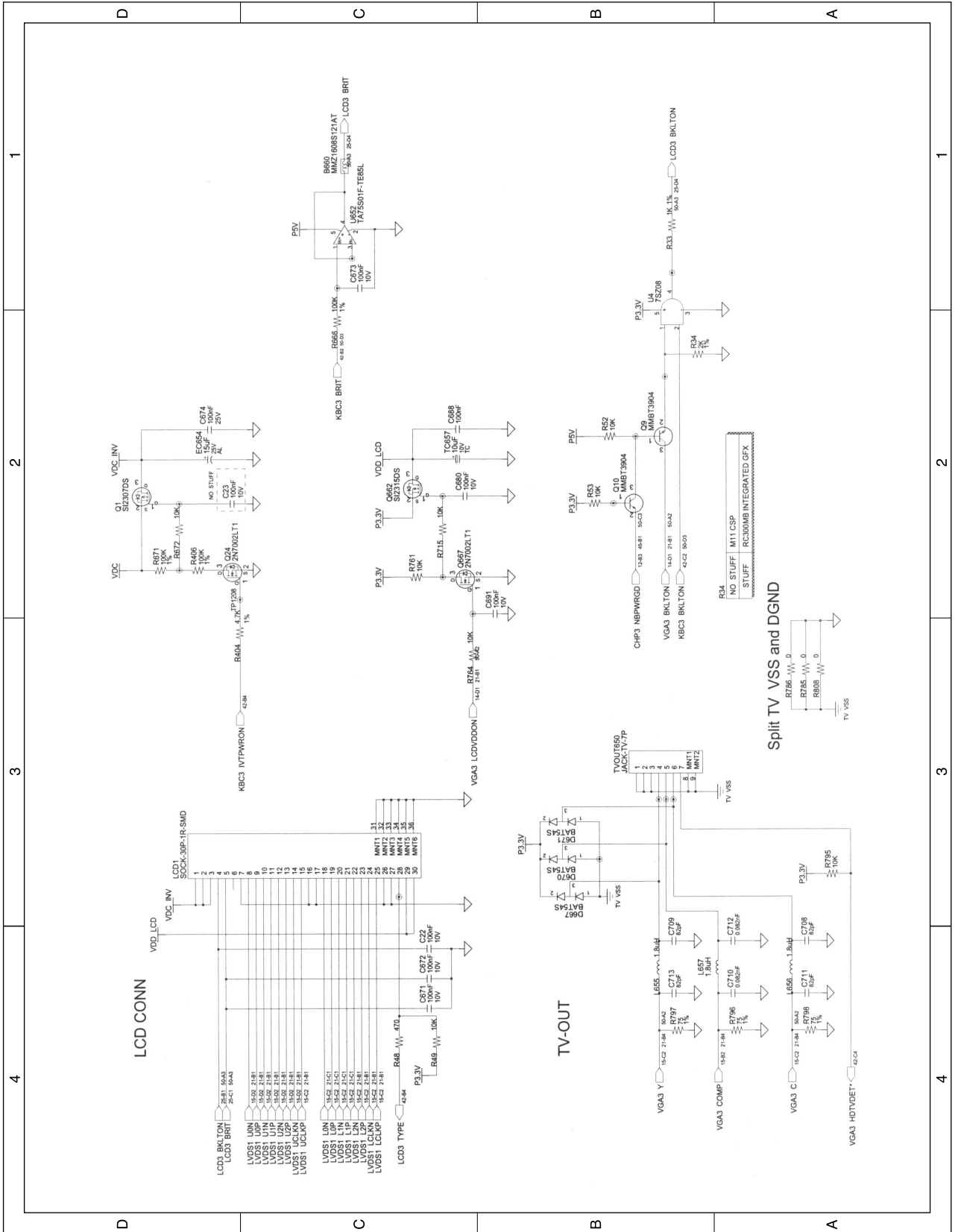
8-1-1(w) Main Board Schematic Sheet 24 of 51 (Graphics Straps)



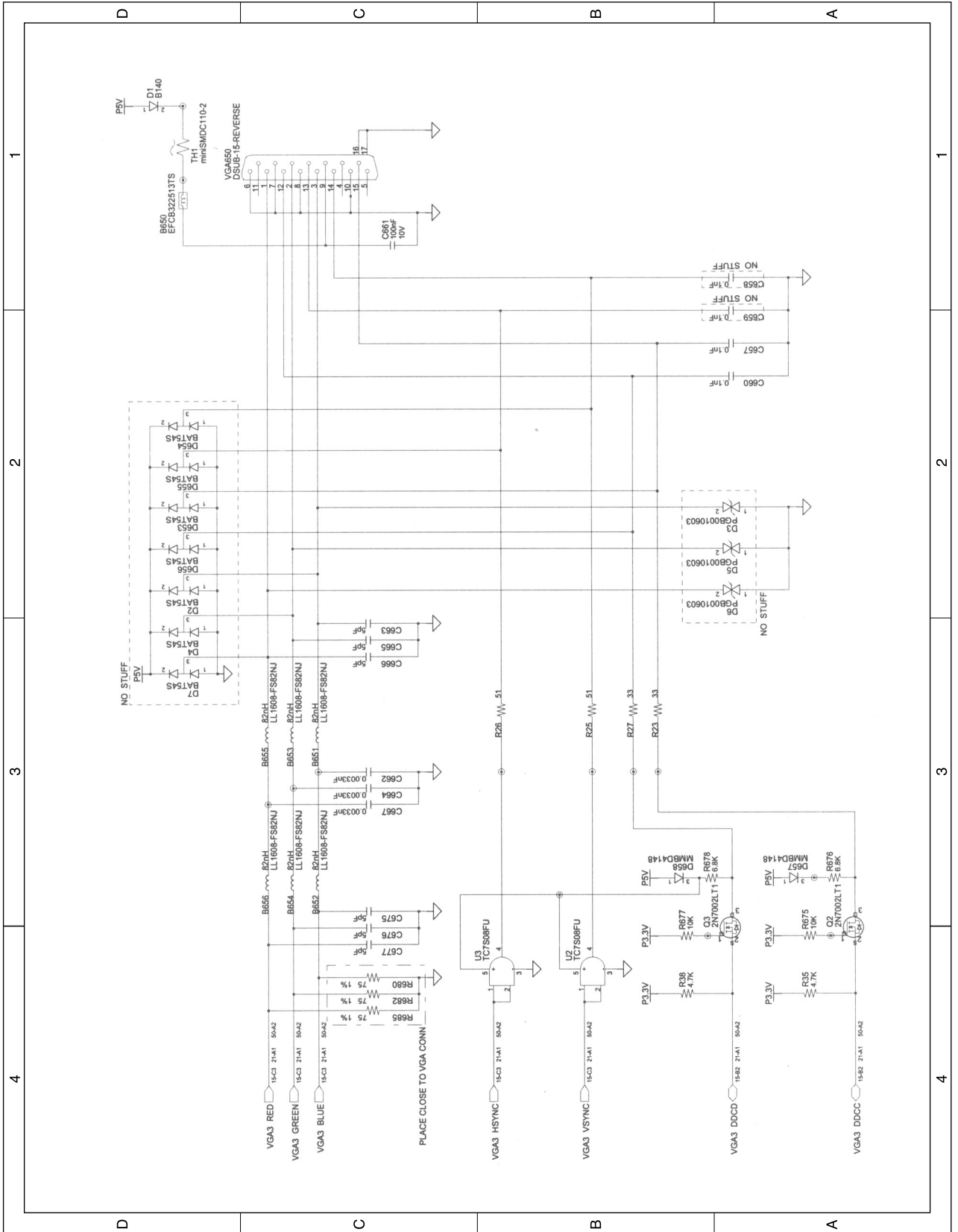
AGPFSKEW [10]	AGP 1X CLOCK FEEDBACK PHASE ADJUSTMENT WRT REFCLK (CPU CLK) 00: REFCLK SLIGHTLY EARLIER THAN FEEDBACK 01: REFCLK 1 TAP LATER THAN FEEDBACK 10: REFCLK 1 TAP EARLIER THAN FEEDBACK 11: REFCLK 2 TAPS EARLIER THAN FEEDBACK CLOCK (DEFAULT)
X1CLK SKEQ [3:2]	CLOCK PHASE ADJUSTMENT BETWEEN X1CLK AND X2CLK 00: 0 TAP DELAY (DEFAULT) 01: 1 TAP DELAY 10: 2 TAP DELAY 11: 3 TAP DELAY
BUSCFG [6:4]	CONTROLS BUS TYPE, CLK PL SELECT AND DSEL AGPFX DETS BUSCFG[2:0] AGPMODE SIGNALING DSESEL 0 000 001 AGPFX 0.8V AD16 (DEFAULT) 0 001 AGPFX 0.8V AD17 0 010 AGPFX 0.8V AD16 0 011 AGPFX 0.8V AD17 1 000 AGPFX 1.5V AD16 1 001 AGPFX 1.5V AD17
ID DISABLE [8]	IN A SYSTEM WITH TWO GRAPHICS CHIPS, ONE ON THE MOTHERBOARD, THE OTHER ON ADD-IN CARD, THIS STRAP CAN BE USED TO DISABLE ONE OF THE TWO THROUGH A JUMPER. 0: NORMAL OPERATION (DEFAULT) 1: SHUTS THE CHIP DOWN BY NOT RESPONDING TO ANY CONFIG CYCLES.
ROMIDCFG [9, 13:11]	IF NO ROM ATTACHED, CONTROLS CHIP ID'S. IF ROM ATTACHED, ID#s are based on the bonding option and CHG ID strap. ID#X: NO ROM, CHG ID=0 (NOTE: X=DONT CARE) (DEFAULT) 0X1X: NO ROM, CHG ID=1 (NOTE: X=DONT CARE) 1000: PARALLEL ROM, CHIP ID'S FROM ROM 1001: 1M SERIAL AT725F024 ROM (ATMEL) 1010: 1M SERIAL AT7450B011 ROM (ATMEL) 1011: 1M SERIAL M25P10 ROM (ST) 1100: 512K SERIAL M25P16 ROM (ST) 1101: 1M SERIAL W45B012 ROM (WINBOND) 1102: 1M SERIAL W45B012 ROM (WINBOND) 512K SERIAL W45B012 ROM (WINBOND) 1110: 1M SERIAL SST25VF10 ROM (SST) 512K SERIAL SST25VF12 ROM (SST) 1111: 1M X25F011B ROM (NEXFLASH)
POWERPLAY [15]	CHIP ID'S CHIP ID IS BASED ON THE BONDING OPTION AND CHG ID STRAP CHG ID#ROMIDCFG[0]-GPIO[12] 0: DEFAULT SETTINGS 1: CHANGE ID SWITCH EXTERNAL REGULATOR BETWEEN HIGH AND LOW CORE VOLTAGE 0: SET HIGHER CORE VOLTAGE (VDDC=1.2V) (DEFAULT) 1: SET LOWER CORE VOLTAGE (VDDC=1.0V)
EXTERNAL SPREAD SPECTRUM [16]	SPREAD SPECTRUM CLOCK INPUT FOR MEMORY CLOCK AND/OR ENGINE CLOCK REQUIRES SPREAD VERSION OF 27MHZ

ALL COMPONENTS ON THIS PAGE ARE USED ONLY FOR M111 CSP

8-1-1(x) Main Board Schematic Sheet 25 of 51(CD / TV-OUT Connector)

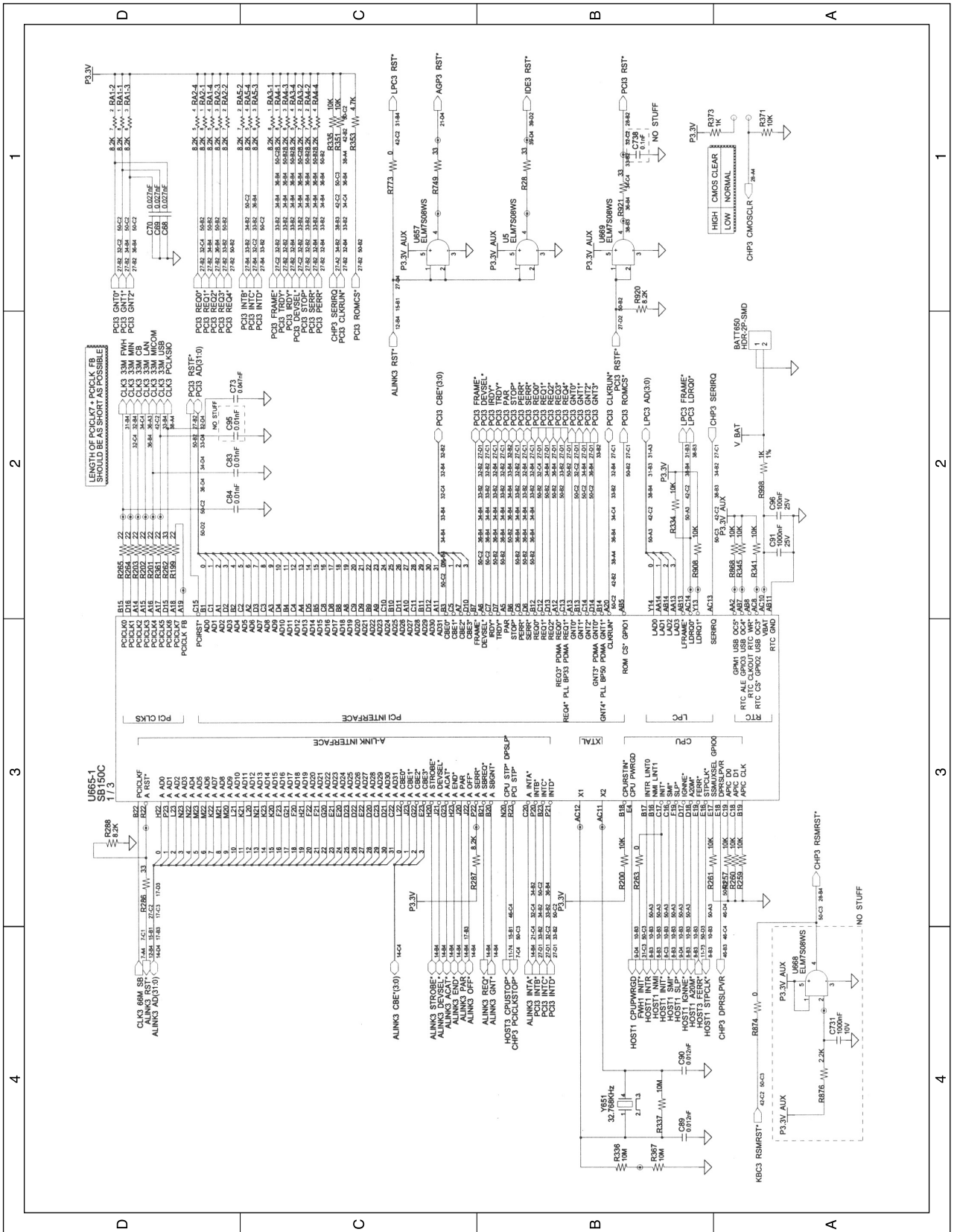


8-1-1(y) Main Board Schematic Sheet 26 of 51(CRT Connector)

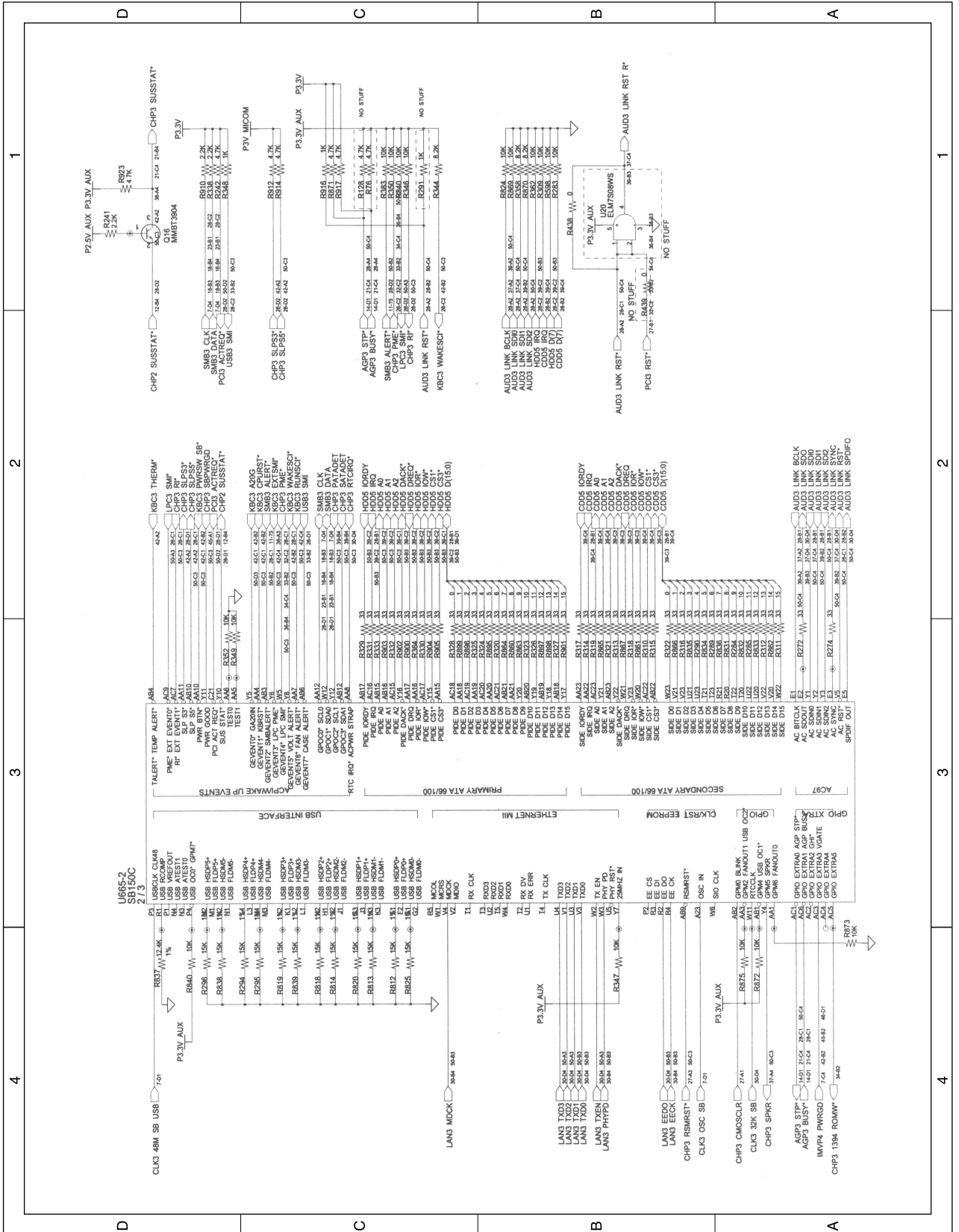




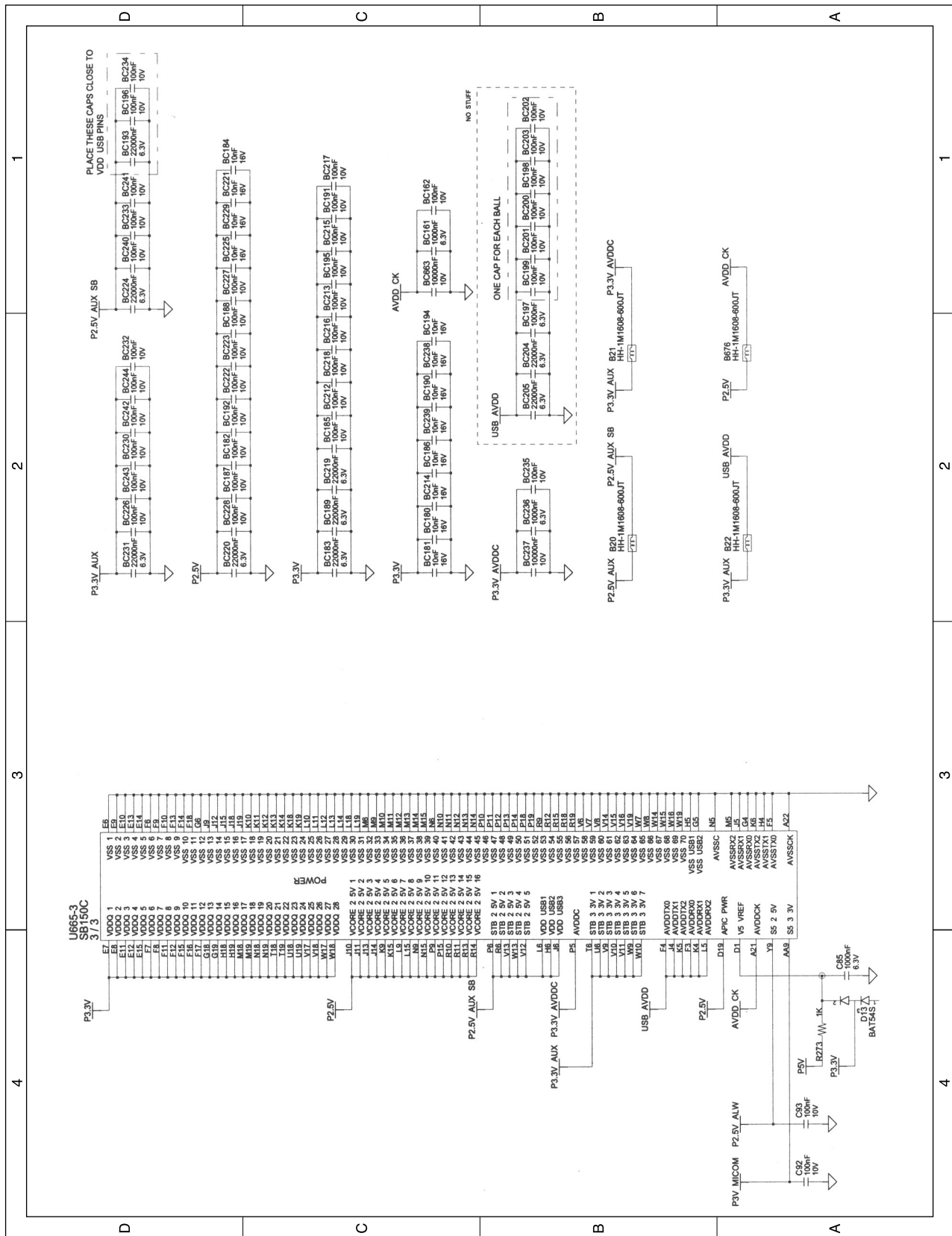
8-1-1(z) Main Board Schematic Sheet 27 of 51 (South Bridge[1/3])



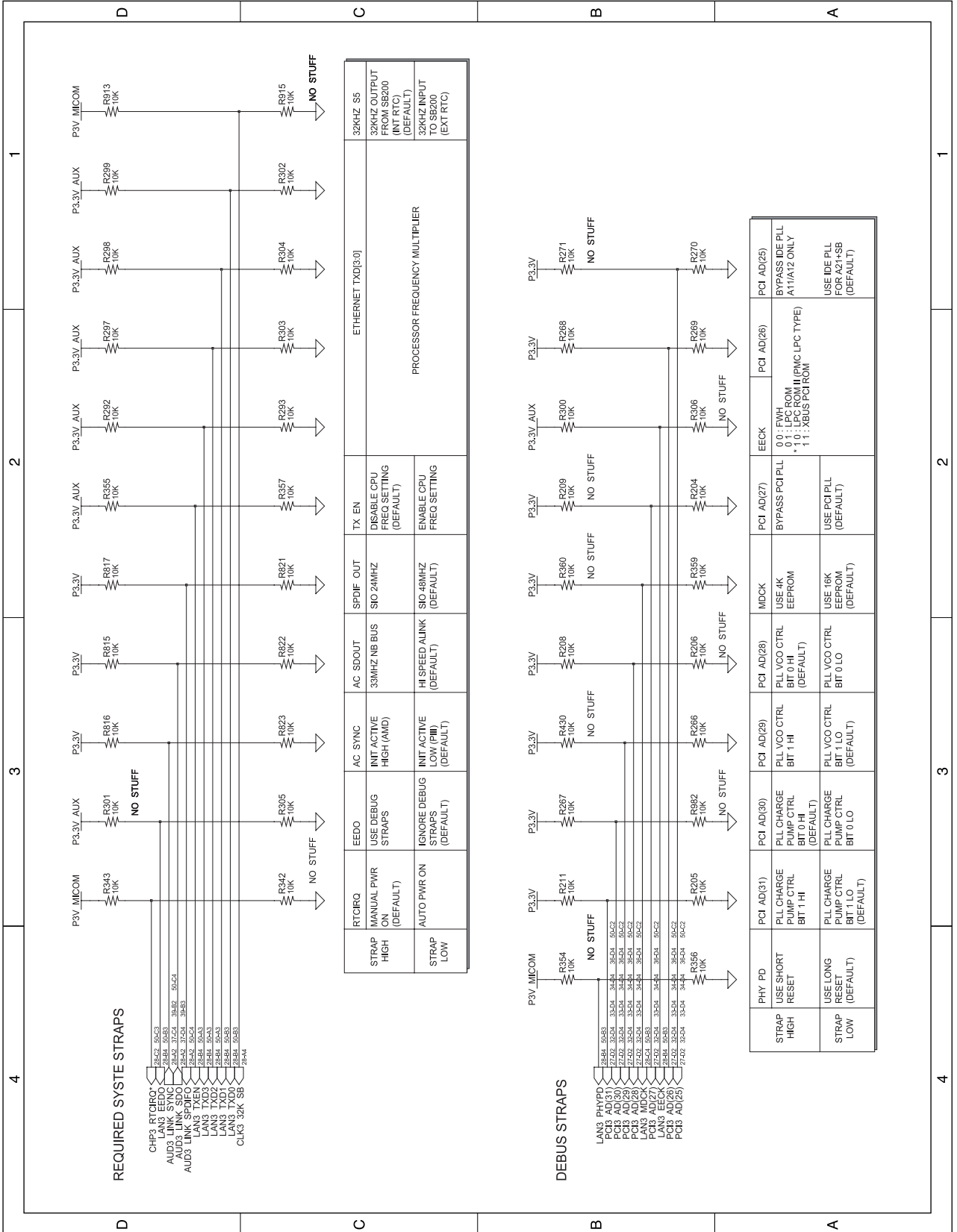
8-1-1(aa) Main Board Schematic Sheet 28 of 51(South Bridge[2/3])



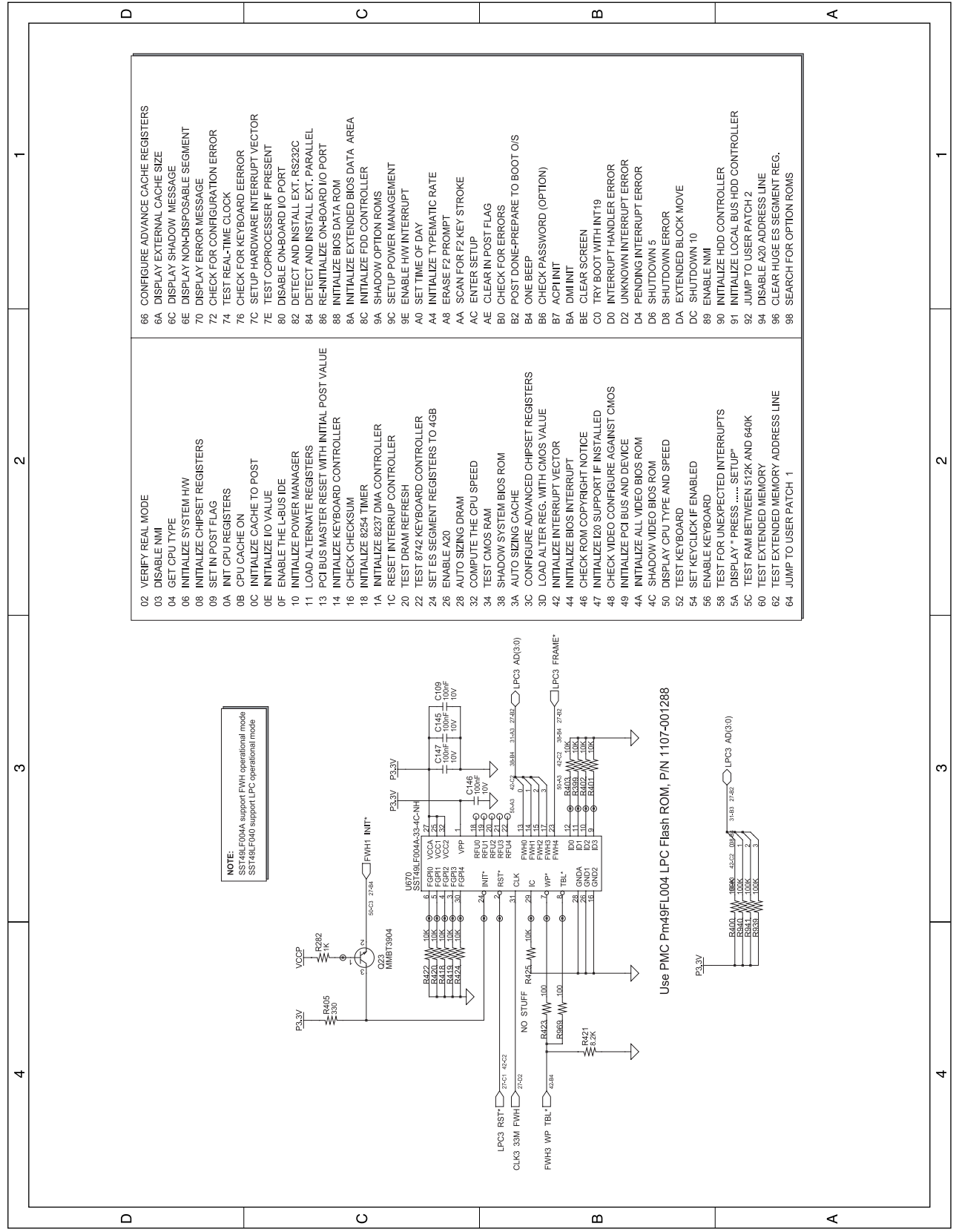
8-1-1(bb) Main Board Schematic Sheet 29 of 51(South Bridge[3/3])



8-1-1(cc) Main Board Schematic Sheet 30 of 51(South Bridge Straps)

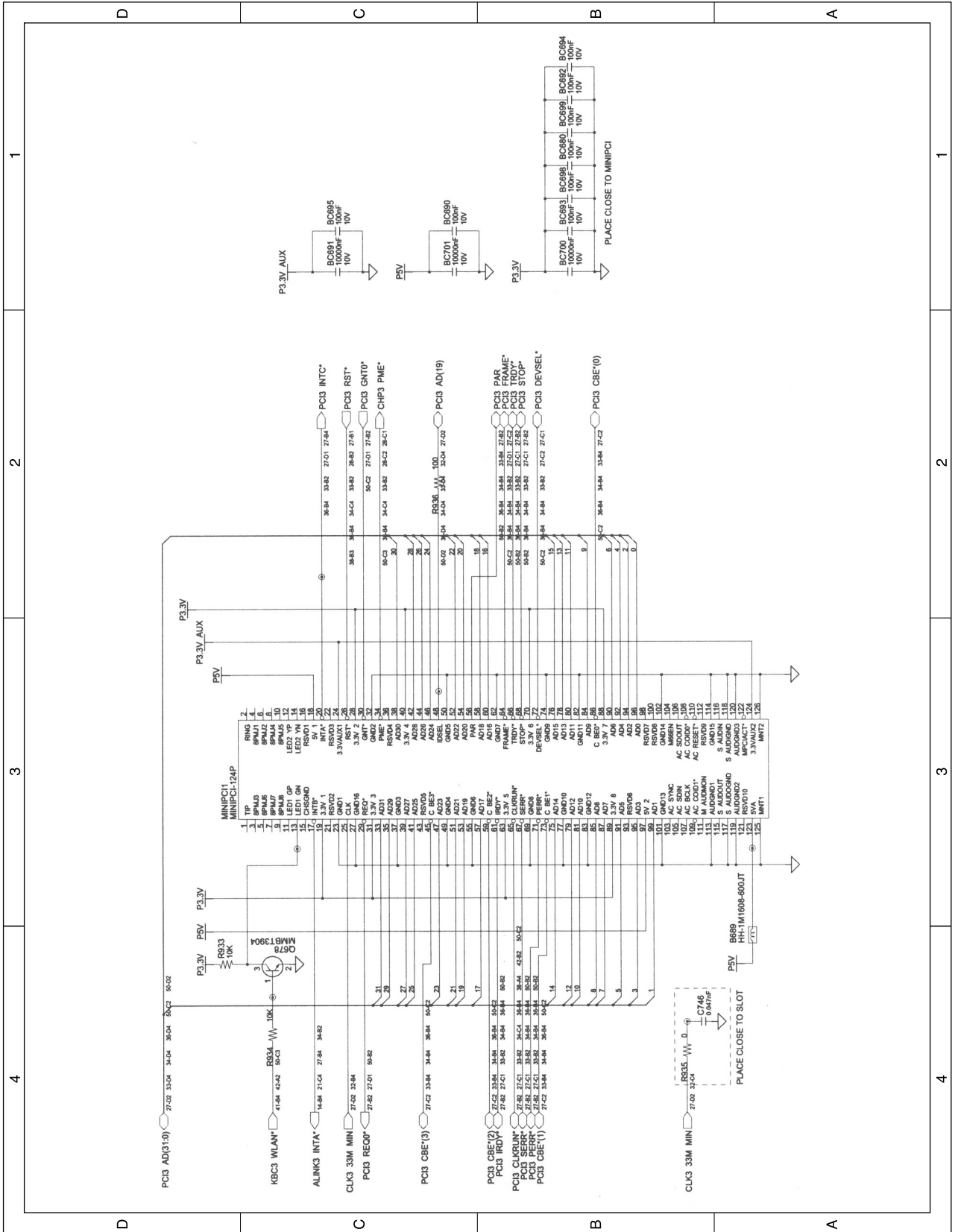


8-1-1(dd) Main Board Schematic Sheet 31 of 51(FWH)



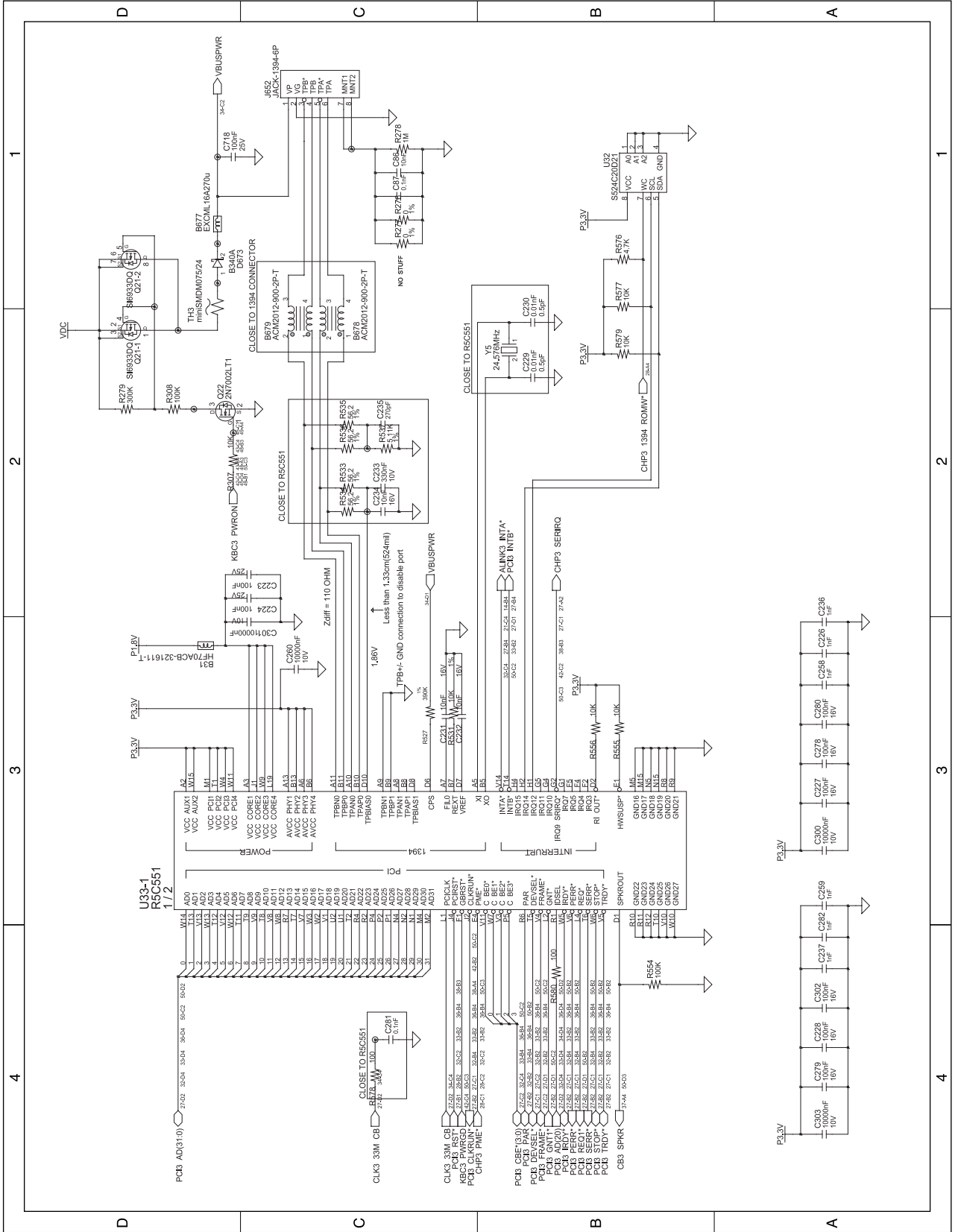
02	VERIFY REAL MODE	66	CONFIGURE ADVANCE CACHE REGISTERS
03	DISABLE NMI	6A	DISPLAY EXTERNAL CACHE SIZE
04	GET CPU TYPE	6C	DISPLAY SHADOW MESSAGE
06	INITIALIZE SYSTEM HIW	6E	DISPLAY NON-DISPOSABLE SEGMENT
08	INITIALIZE CHIPSET REGISTERS	70	DISPLAY ERROR MESSAGE
09	SET IN POST FLAG	72	CHECK FOR CONFIGURATION ERROR
0A	INIT CPU REGISTERS	74	TEST REAL-TIME CLOCK
0B	CPU CACHE ON	76	CHECK FOR KEYBOARD ERROR
0C	INITIALIZE CACHE TO POST	7C	SETUP HARDWARE INTERRUPT VECTOR
0E	INITIALIZE I/O VALUE	7E	TEST COPROCESSER IF PRESENT
0F	ENABLE THE L-BUS IDE	80	DISABLE ON-BOARD I/O PORT
10	INITIALIZE POWER MANAGER	82	DETECT AND INSTALL EXT. RS232C
11	LOAD ALTERNATE REGISTERS	84	DETECT AND INSTALL EXT. PARALLEL
13	PCI BUS MASTER RESET WITH INITIAL POST VALUE	86	RE-INITIALIZE ON-BOARD I/O PORT
14	INITIALIZE KEYBOARD CONTROLLER	88	INITIALIZE BIOS DATA ROM
16	CHECK CHECKSUM	8A	INITIALIZE EXTENDED BIOS DATA AREA
18	INITIALIZE 8254 TIMER	8C	INITIALIZE FDD CONTROLLER
1A	INITIALIZE 8237 DMA CONTROLLER	9A	SHADOW OPTION ROMS
1C	RESET INTERRUPT CONTROLLER	9C	SETUP POWER MANAGEMENT
20	TEST DRAM REFRESH	9E	ENABLE HW INTERRUPT
22	TEST 8742 KEYBOARD CONTROLLER	A0	SET TIME OF DAY
24	SET ES SEGMENT REGISTERS TO 4GB	A4	INITIALIZE TYPEMATIC RATE
26	ENABLE A20	A8	ERASE F2 PROMPT
28	AUTO SIZING DRAM	AA	SCAN FOR F2 KEY STROKE
32	COMPUTE THE CPU SPEED	AC	ENTER SETUP
34	TEST CMOS RAM	AE	CLEAR IN POST FLAG
38	SHADOW SYSTEM BIOS ROM	B0	CHECK FOR ERRORS
3A	AUTO SIZING CACHE	B2	POST DONE-PREPARE TO BOOT O/S
3C	CONFIGURE ADVANCED CHIPSET REGISTERS	B4	ONE BEEP
3D	LOAD ALTER REG. WITH CMOS VALUE	B6	CHECK PASSWORD (OPTION)
42	INITIALIZE INTERRUPT VECTOR	B7	ACPI INIT
44	CHECK ROM COPYRIGHT NOTICE	BA	DMI INIT
46	INITIALIZE E20 SUPPORT IF INSTALLED	BE	CLEAR SCREEN
48	CHECK VIDEO CONFIGURE AGAINST CMOS	C0	TRY BOOT WITH INT19
49	INITIALIZE PCI BUS AND DEVICE	D0	UNKNOWN INTERRUPT ERROR
4A	INITIALIZE ALL VIDEO BIOS ROM	D2	PENDING INTERRUPT ERROR
4C	SHADOW VIDEO BIOS ROM	D4	SHUTDOWN 5
50	DISPLAY CPU TYPE AND SPEED	D8	SHUTDOWN ERROR
52	TEST KEYBOARD	DA	EXTENDED BLOCK MOVE
54	SET KEYCLICK IF ENABLED	DC	SHUTDOWN 10
56	ENABLE KEYBOARD	89	ENABLE NMI
58	TEST FOR UNEXPECTED INTERRUPTS	90	INITIALIZE HDD CONTROLLER
5A	DISPLAY *PRESS ..... SETUP*	91	INITIALIZE LOCAL BUS HDD CONTROLLER
5C	TEST RAM BETWEEN 512K AND 640K	92	JUMP TO USER PATCH 2
60	TEST EXTENDED MEMORY ADDRESS LINE	94	DISABLE A20 ADDRESS LINE
62	TEST EXTENDED MEMORY ADDRESS LINE	96	CLEAR HUGE ES SEGMENT REG.
64	JUMP TO USER PATCH 1	98	SEARCH FOR OPTION ROMS

8-1-1(ee) Main Board Schematic Sheet 32 of 51(MINIPCI)



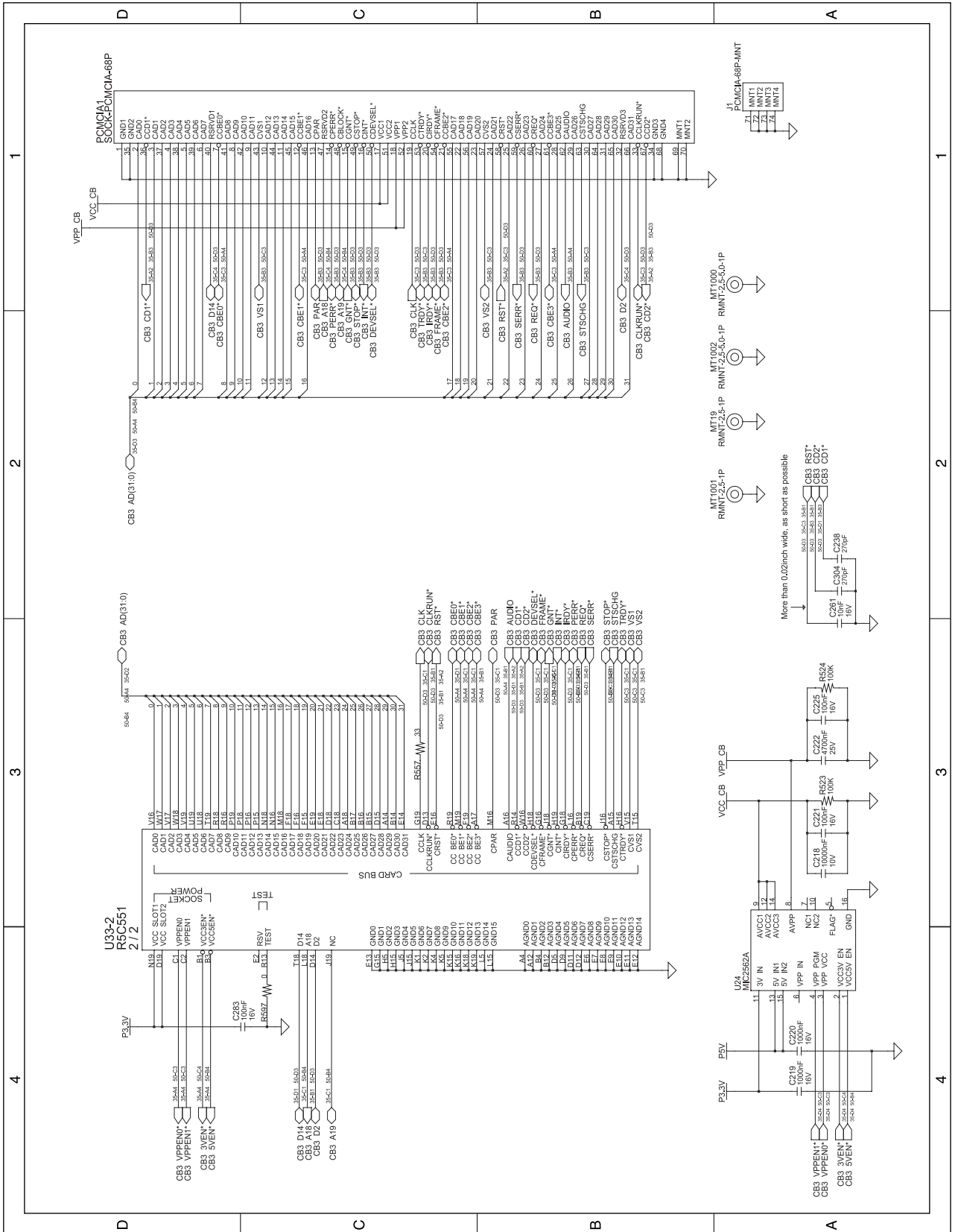


8-1-1(gg) Main Board Schematic Sheet 34 of 51(Cardbus Controller)

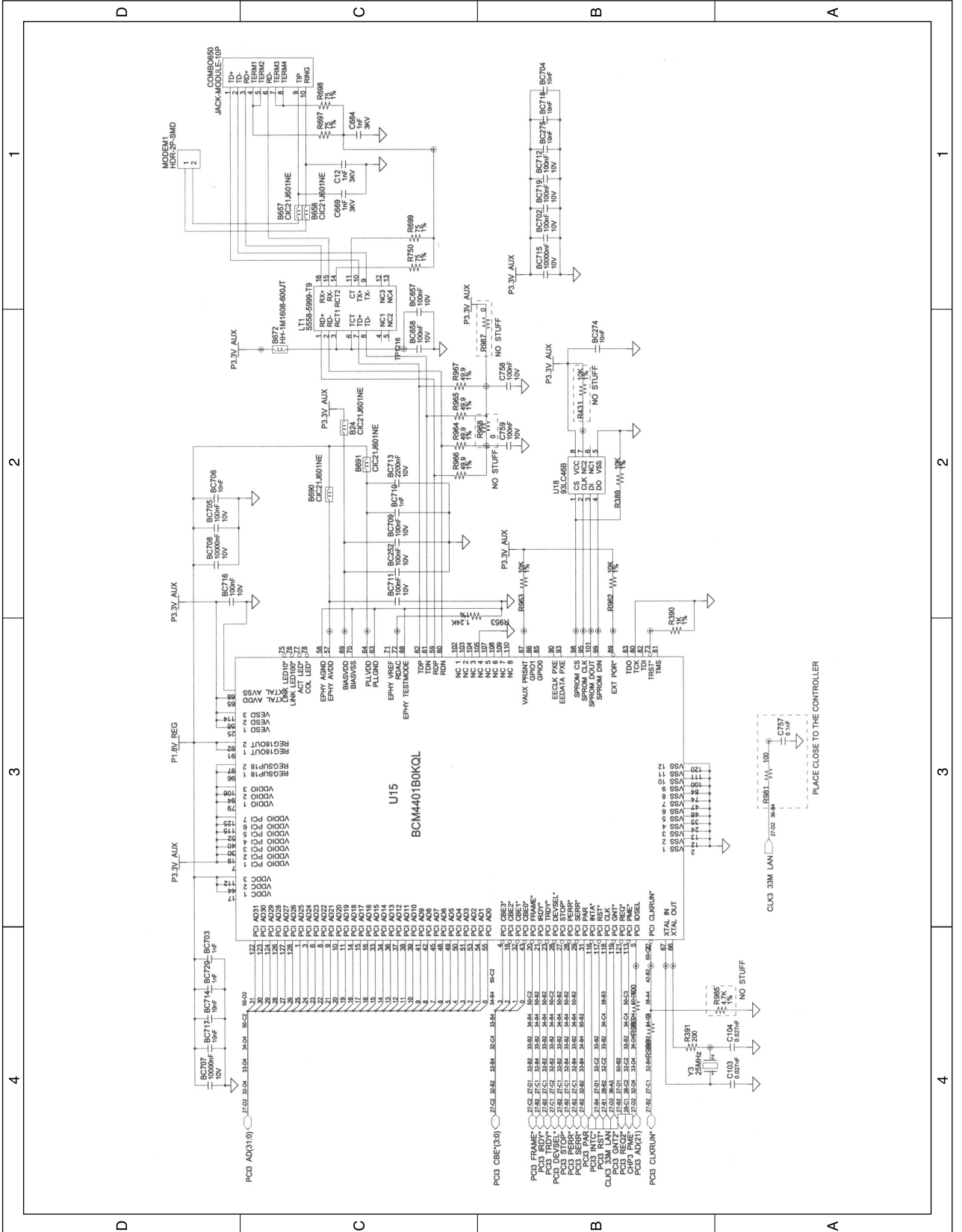




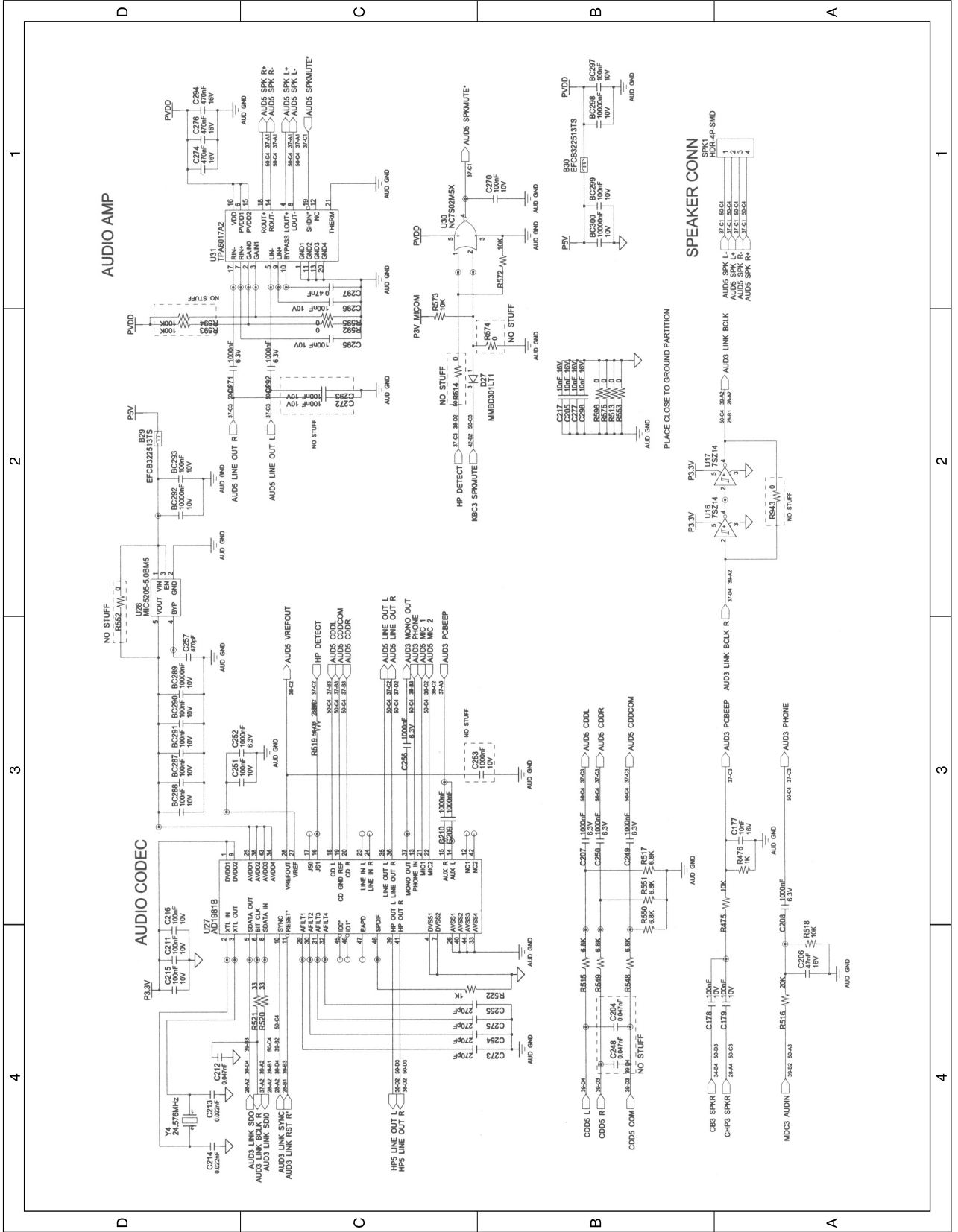
8-1-1(hh) Main Board Schematic Sheet 35 of 51(Cardbus Connector)



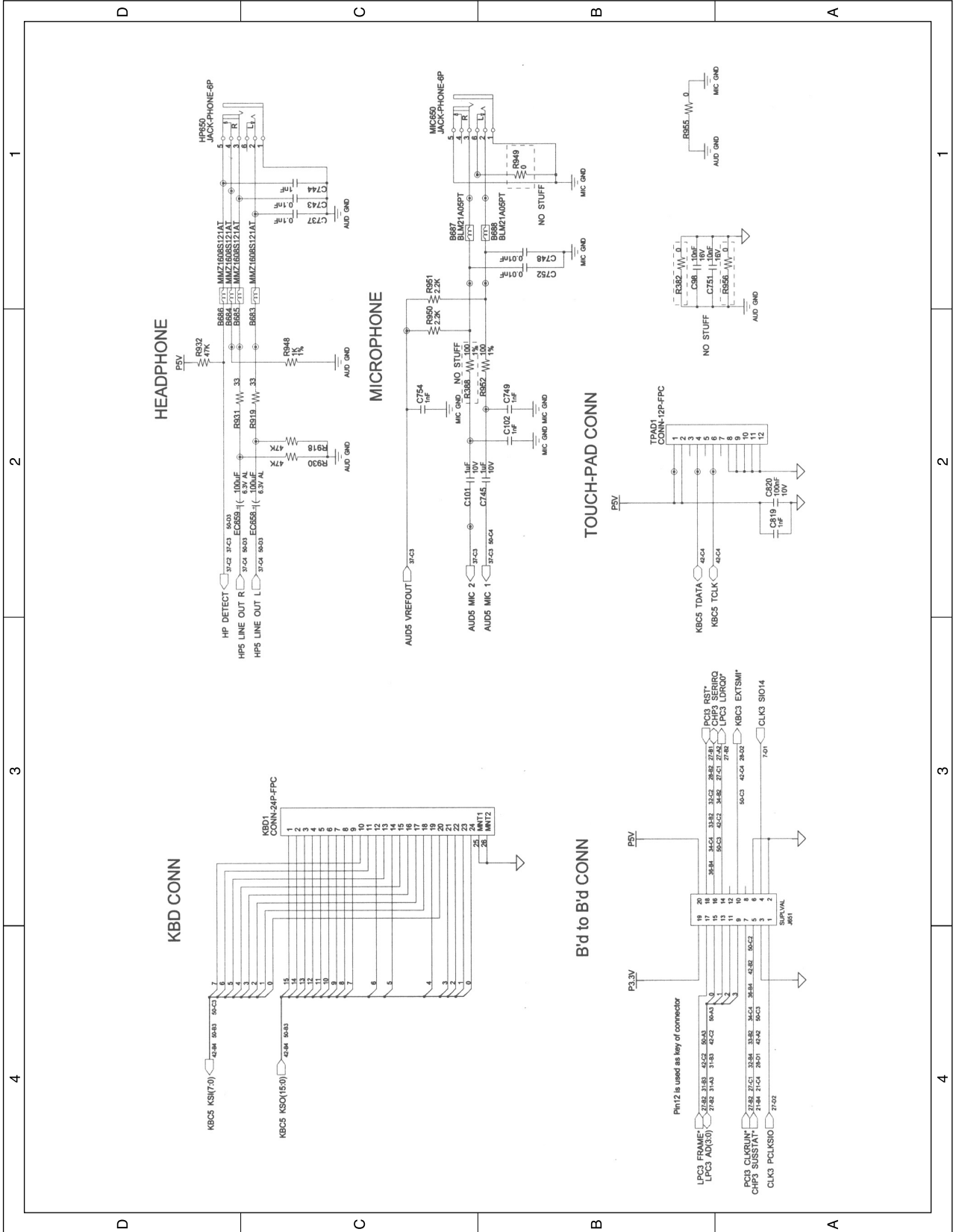
8-1-1(ii) Main Board Schematic Sheet 36 of 51(LAN Controller)



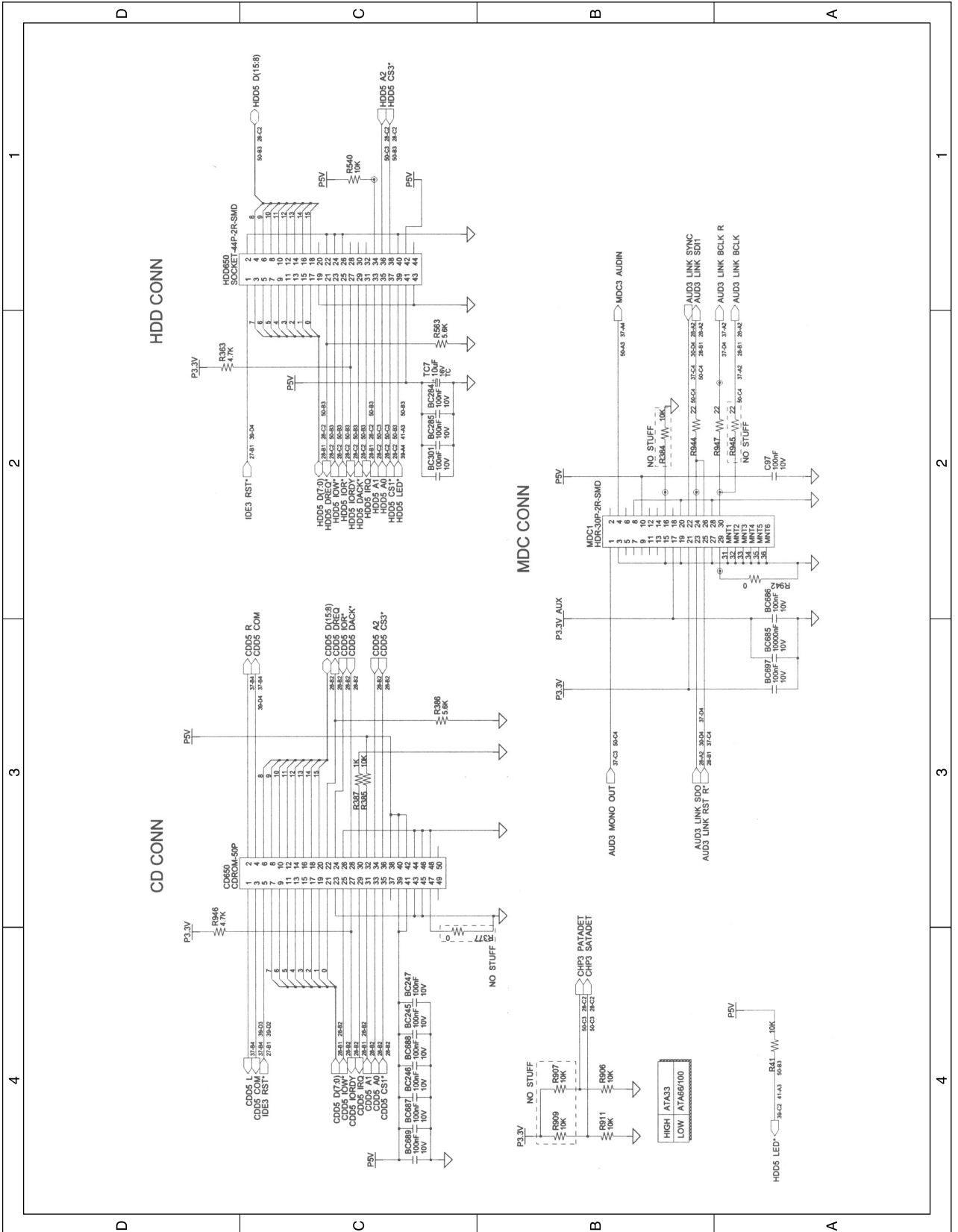
8-1-1(jj) Main Board Schematic Sheet 37 of 51(AC97 Audio Codec / AMP)



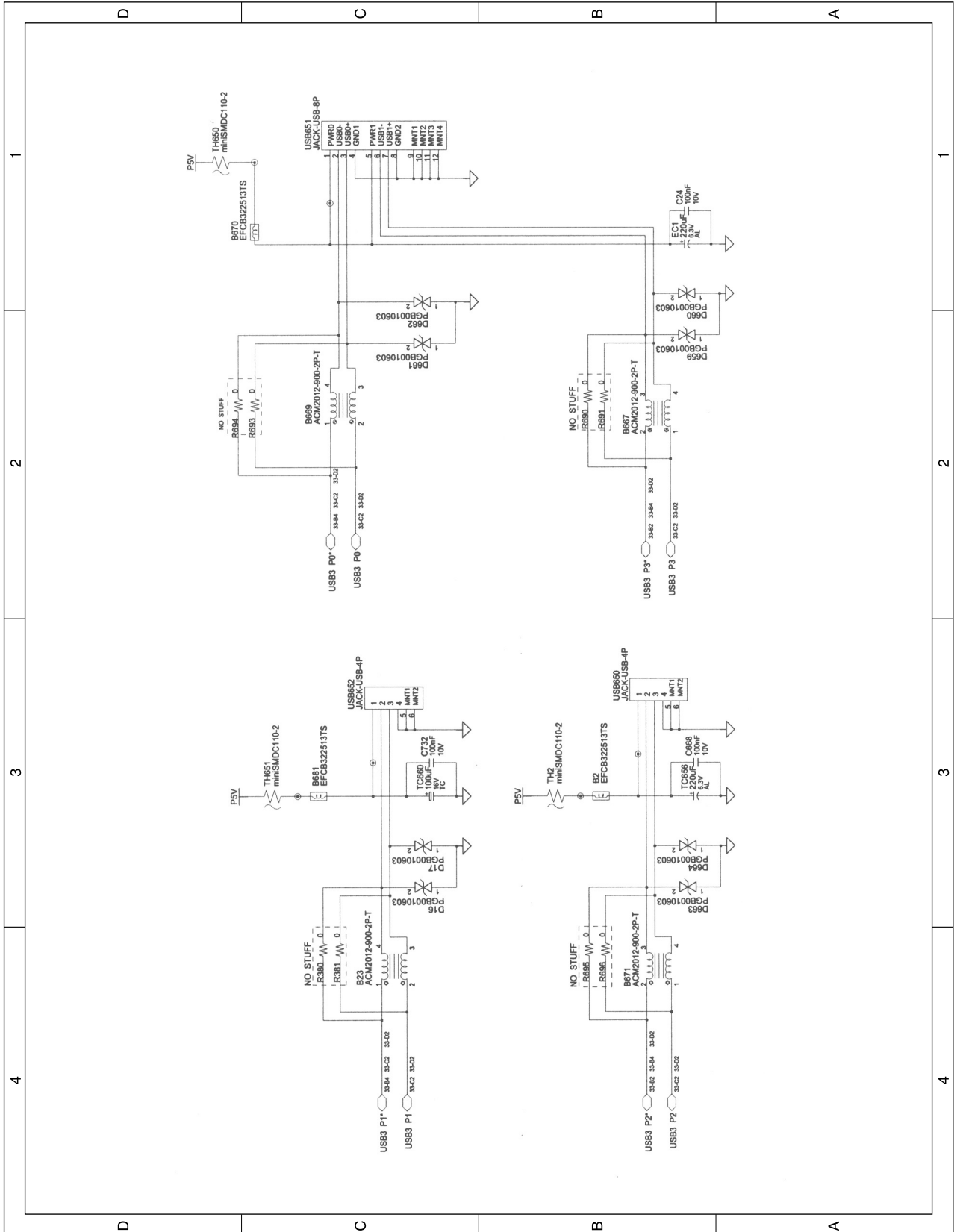
8-1-1(kk) Main Board Schematic Sheet 38 of 51(HP / MIC / KBD /Touch Pad Connector)



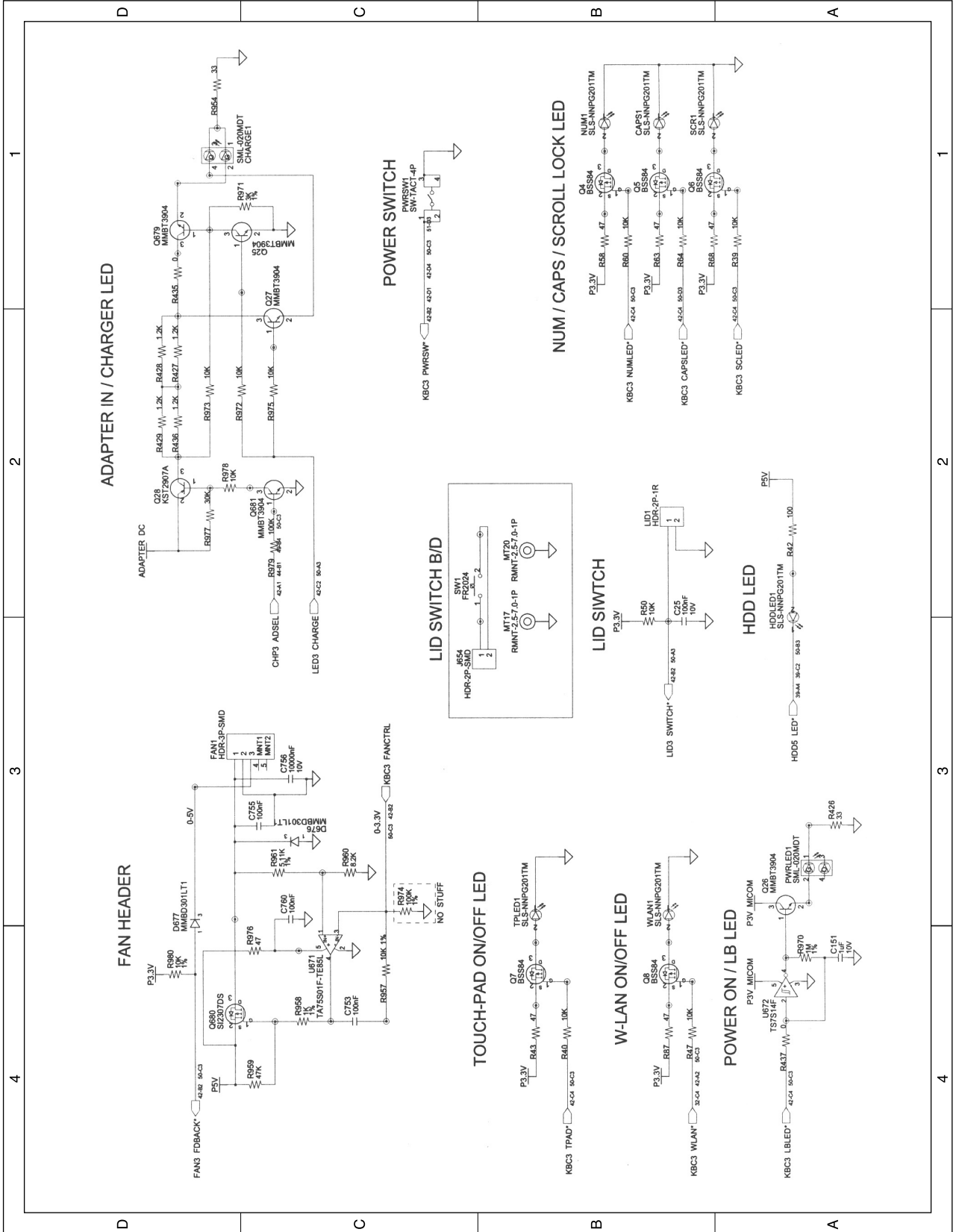
8-1-1(II) Main Board Schematic Sheet 39 of 51(HDD / CD/ MDC Connector)



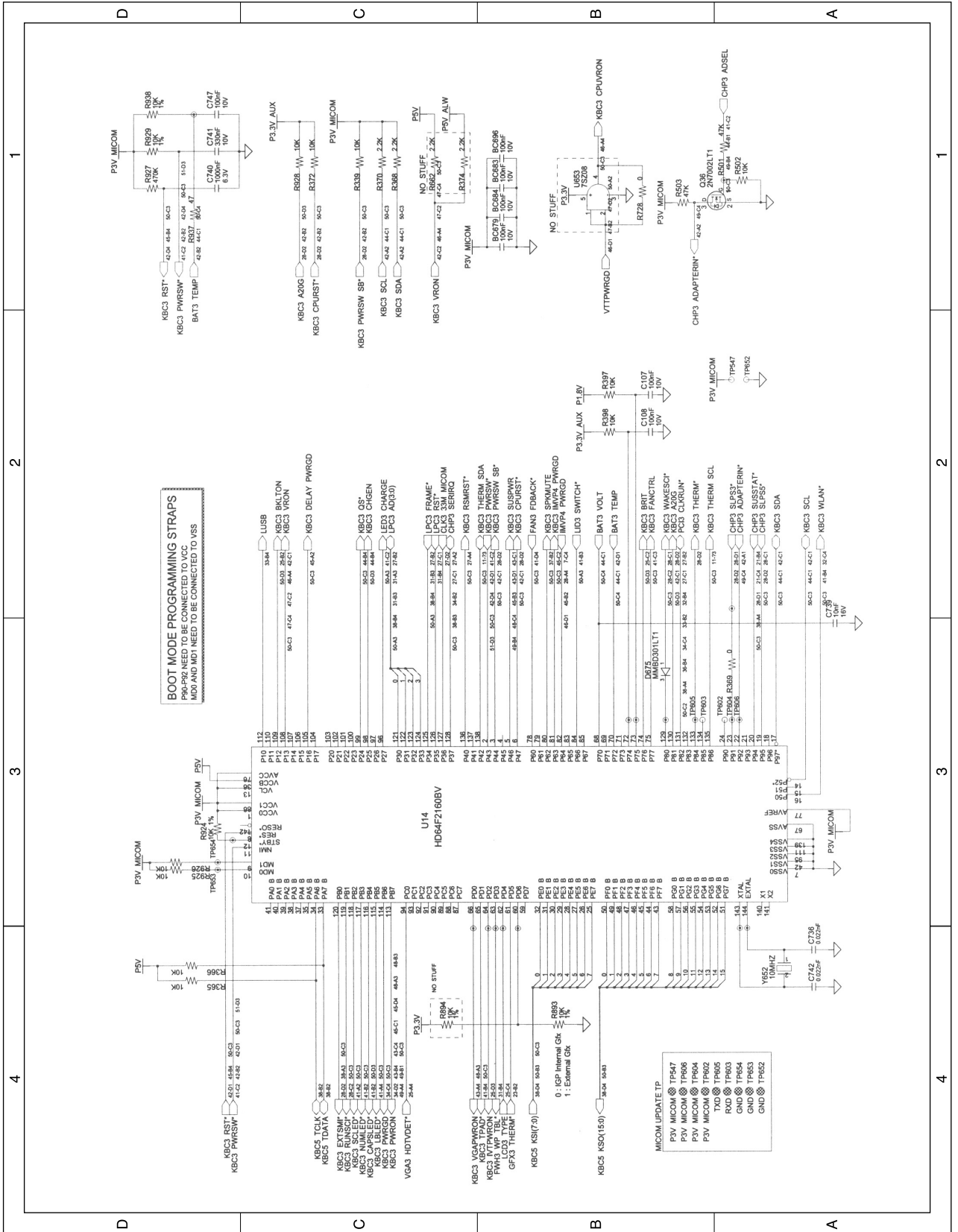
8-1-1(mm) Main Board Schematic Sheet 40 of 51(USB Ports)



8-1-1(nn) Main Board Schematic Sheet 41 of 51(FAN / LED / SWITCH)

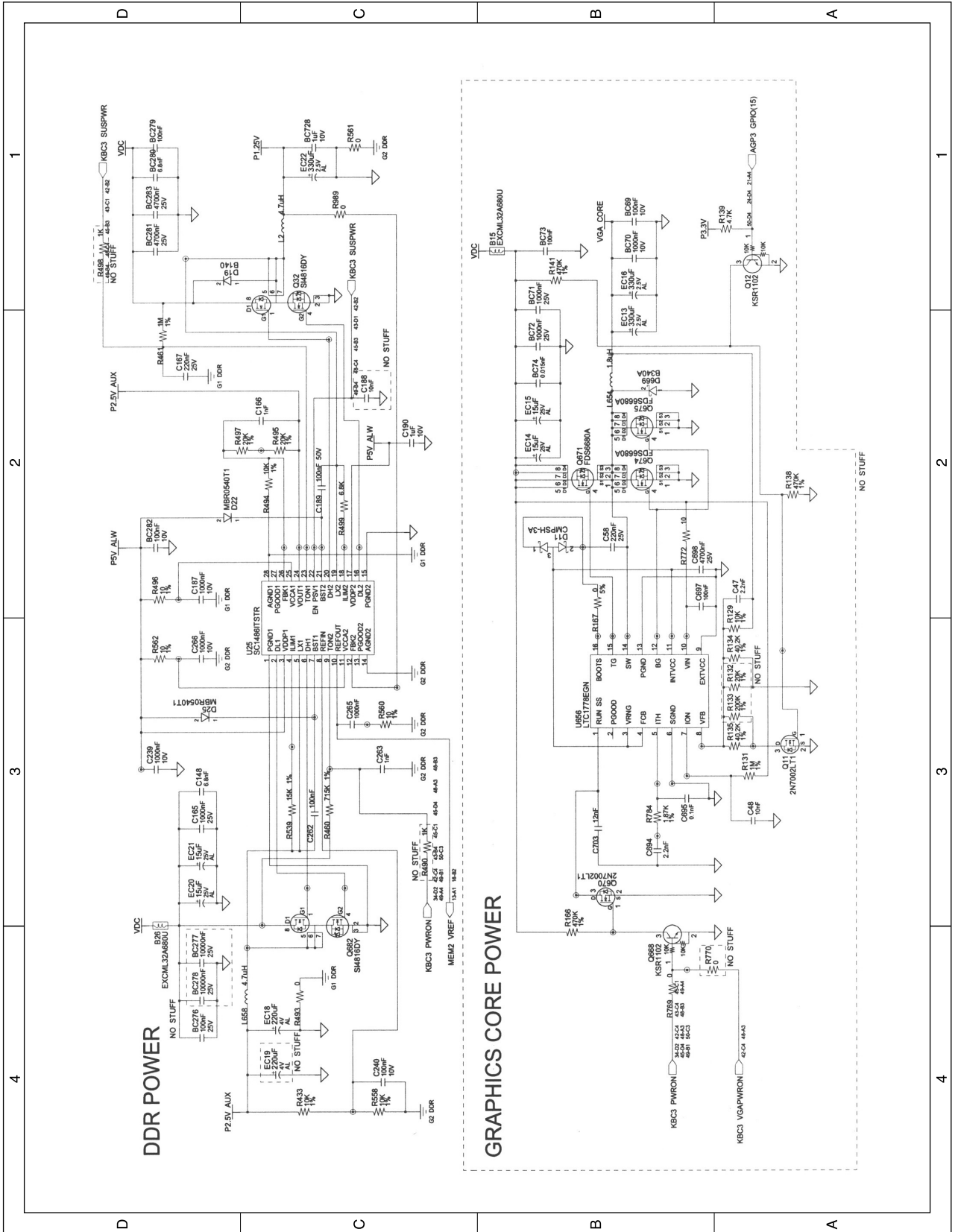


8-1-1(o) Main Board Schematic Sheet 42 of 51(MICOM)

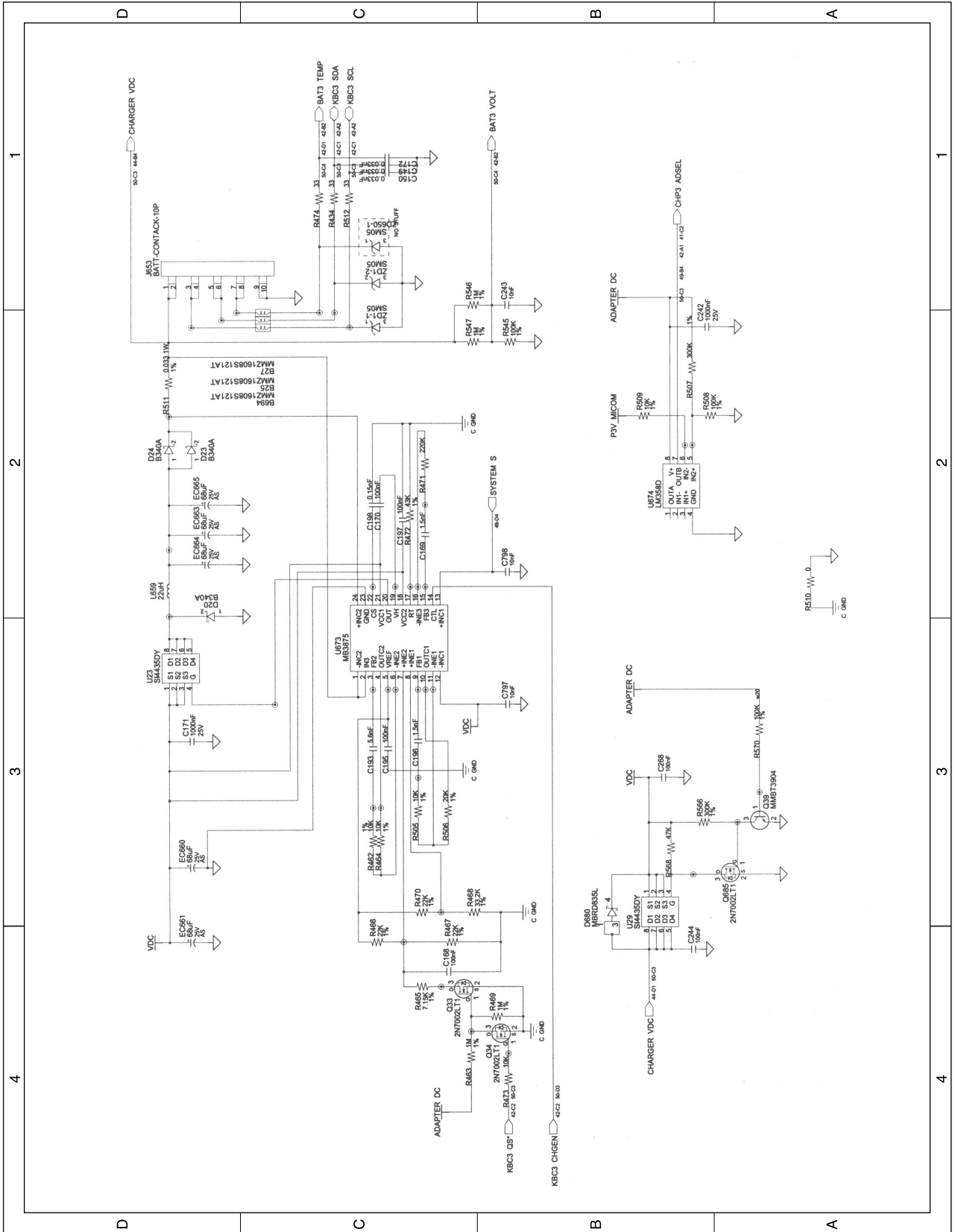




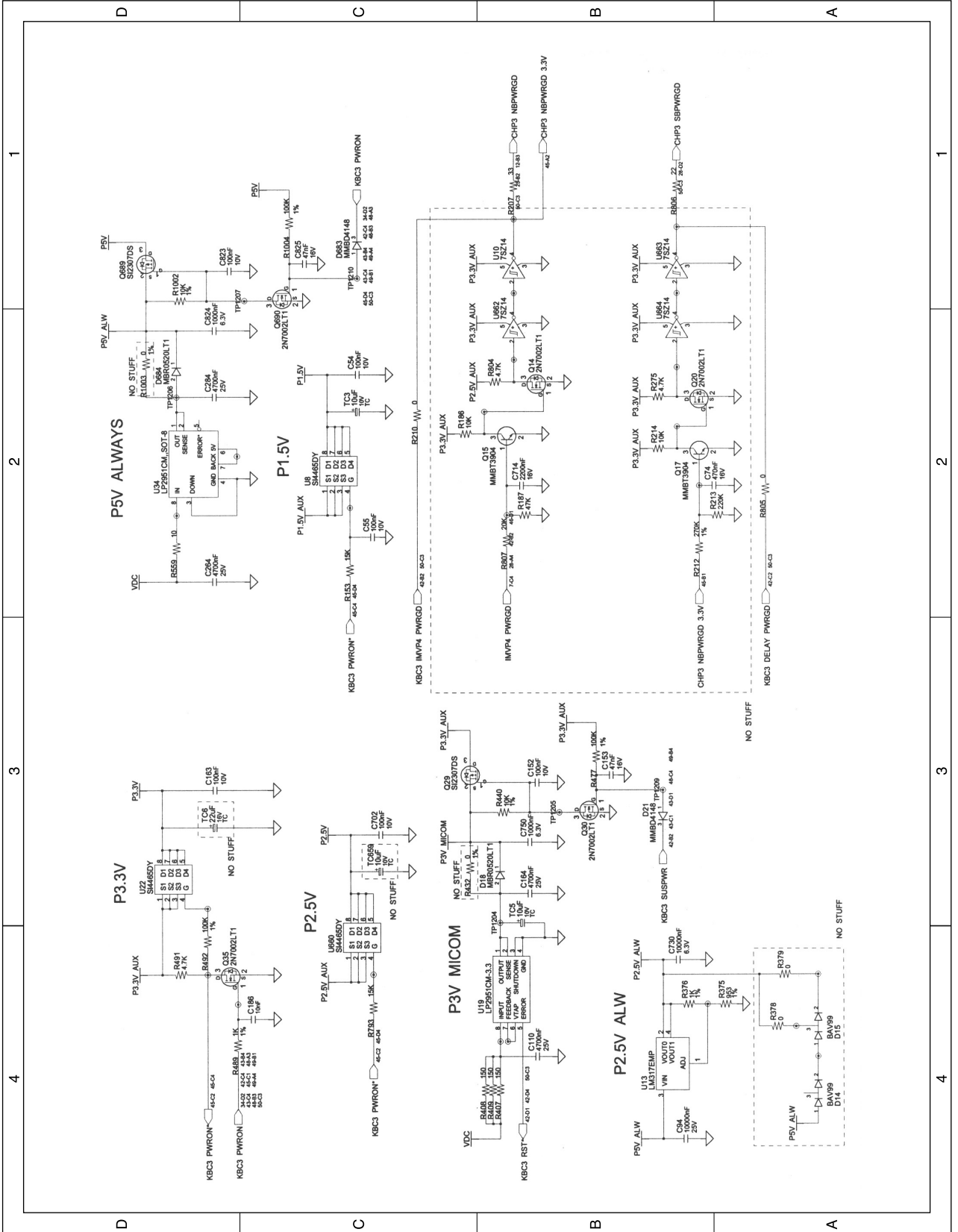
8-1-1(pp) Main Board Schematic Sheet 43 of 51(DDR / Graphics Core Power)



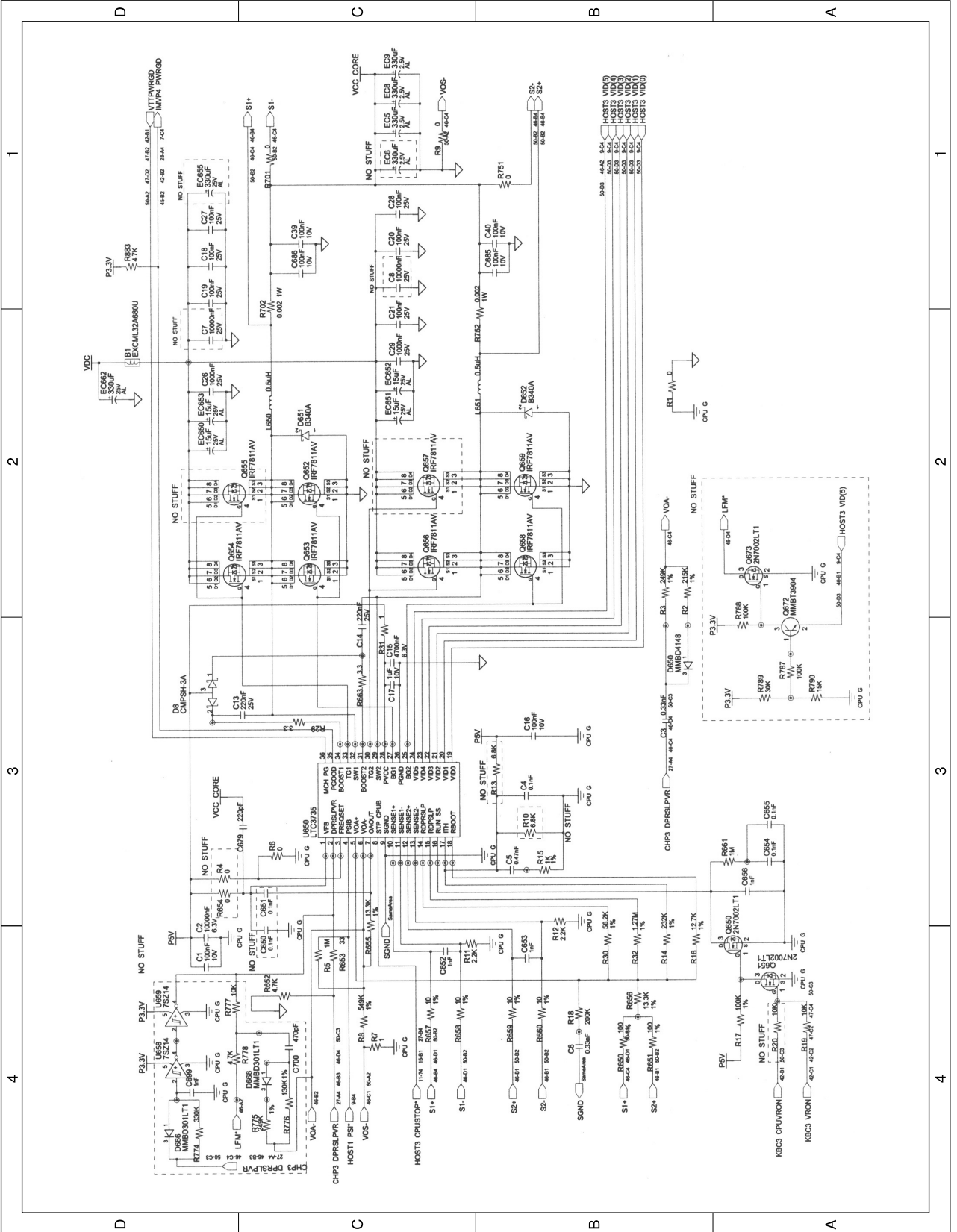
8-1-1(qq) Main Board Schematic Sheet 44 of 51(CHARGER)



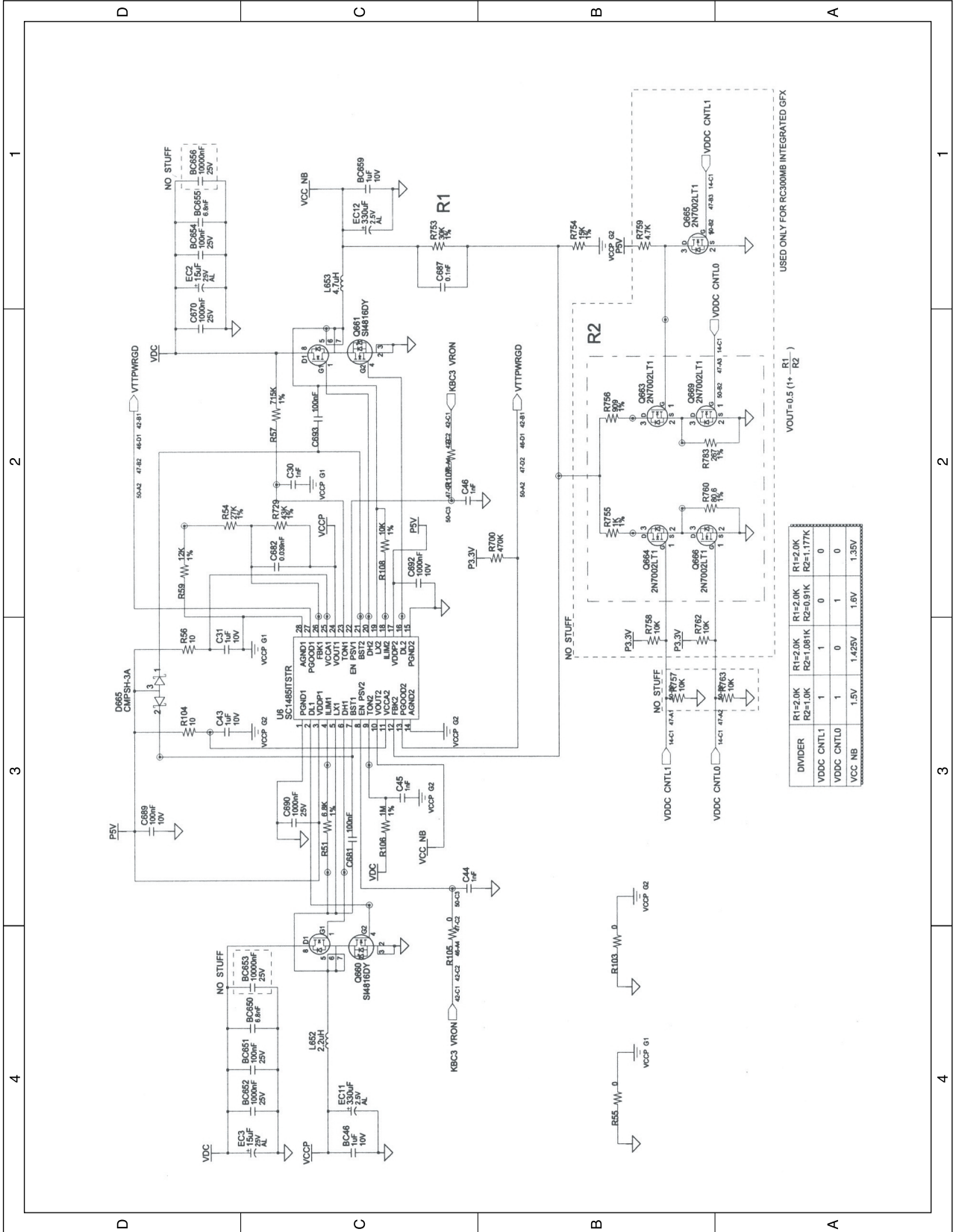
8-1-1(rr) Main Board Schematic Sheet 45 of 51(Switched Power)



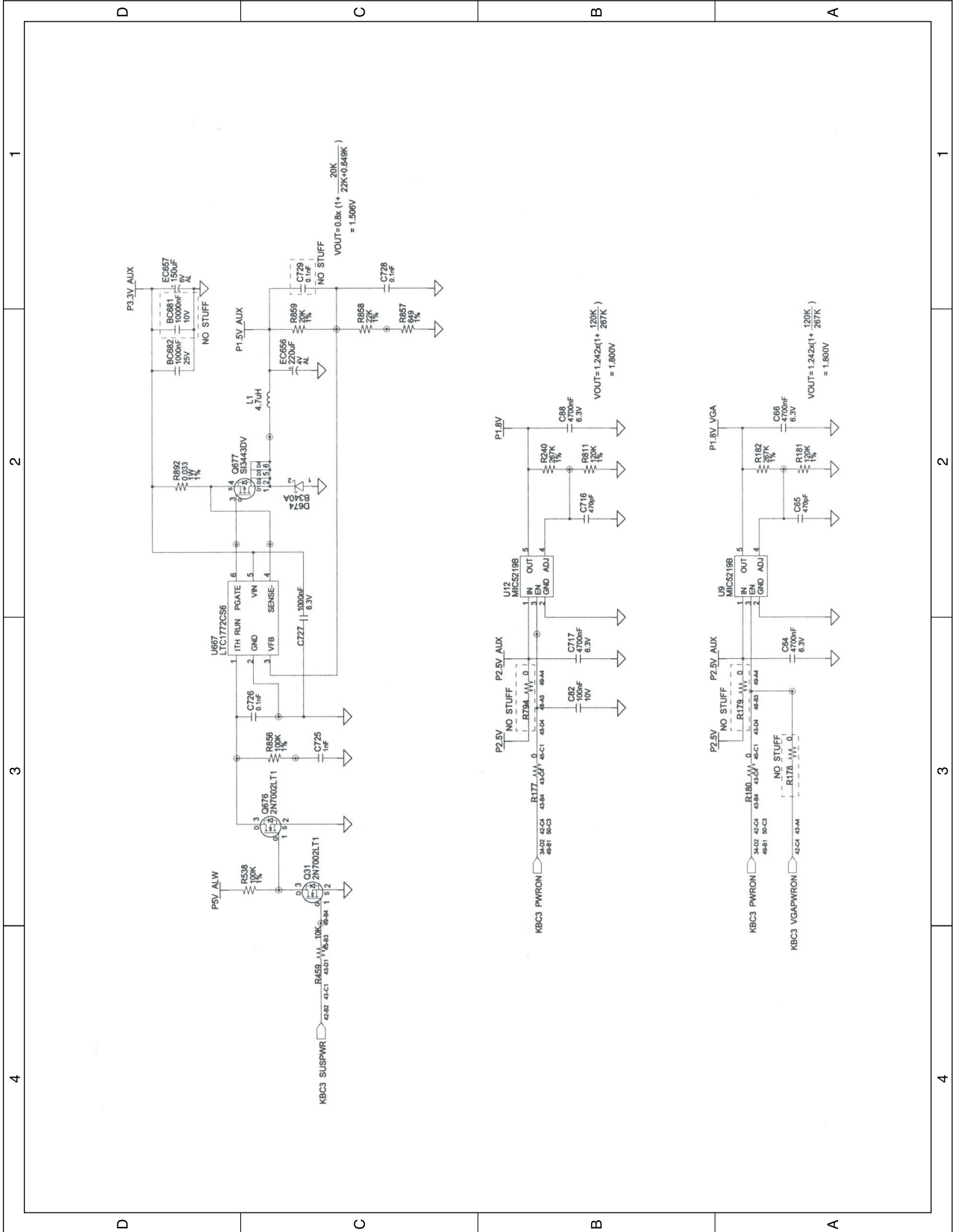
8-1-1(ss) Main Board Schematic Sheet 46 of 51(CPU Power)



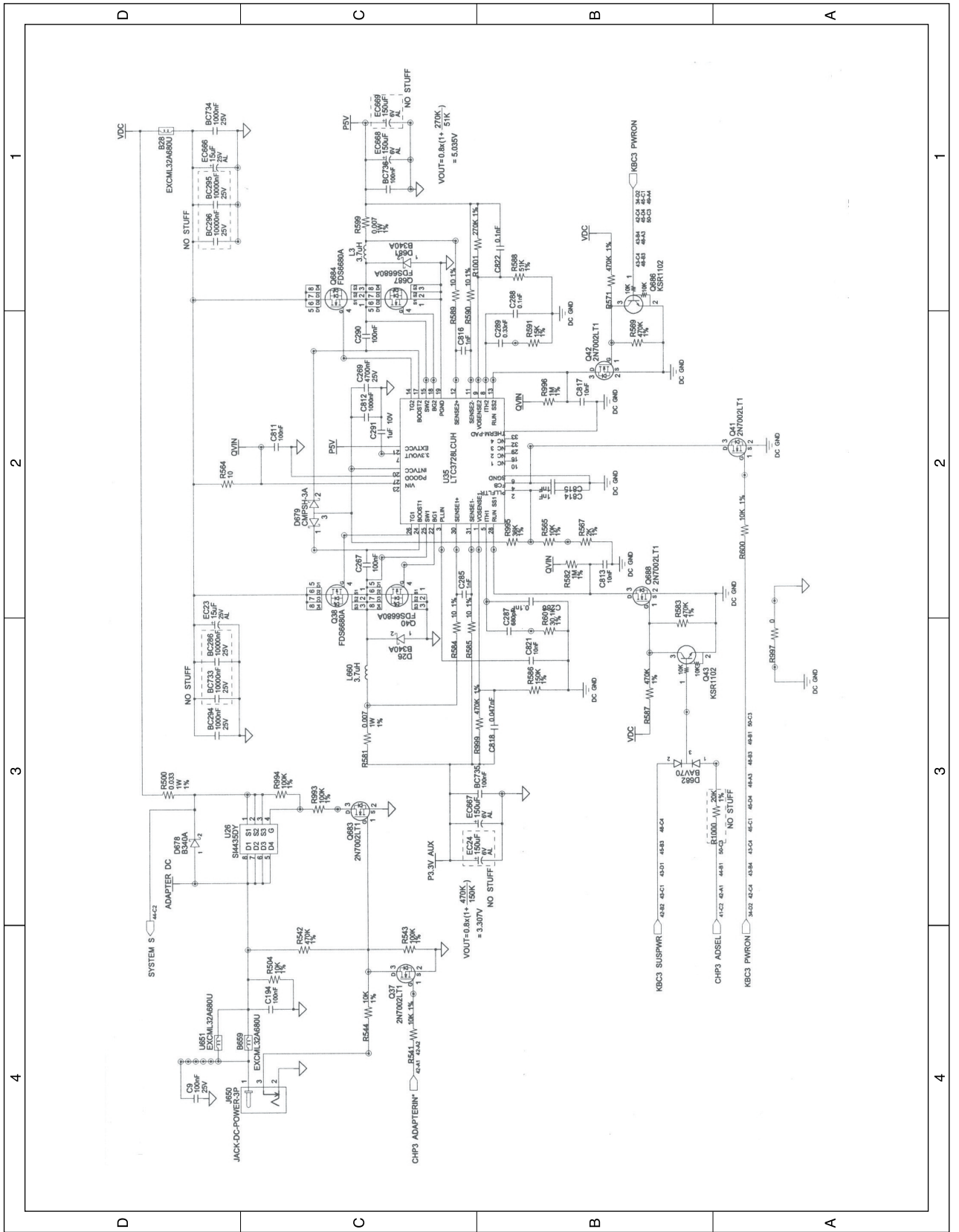
8-1-1(tt) Main Board Schematic Sheet 47 of 51(VCCP & North Bridge Power)



8-1-1(uu) Main Board Schematic Sheet 48 of 51(P1.5V / P1.8V Generation)



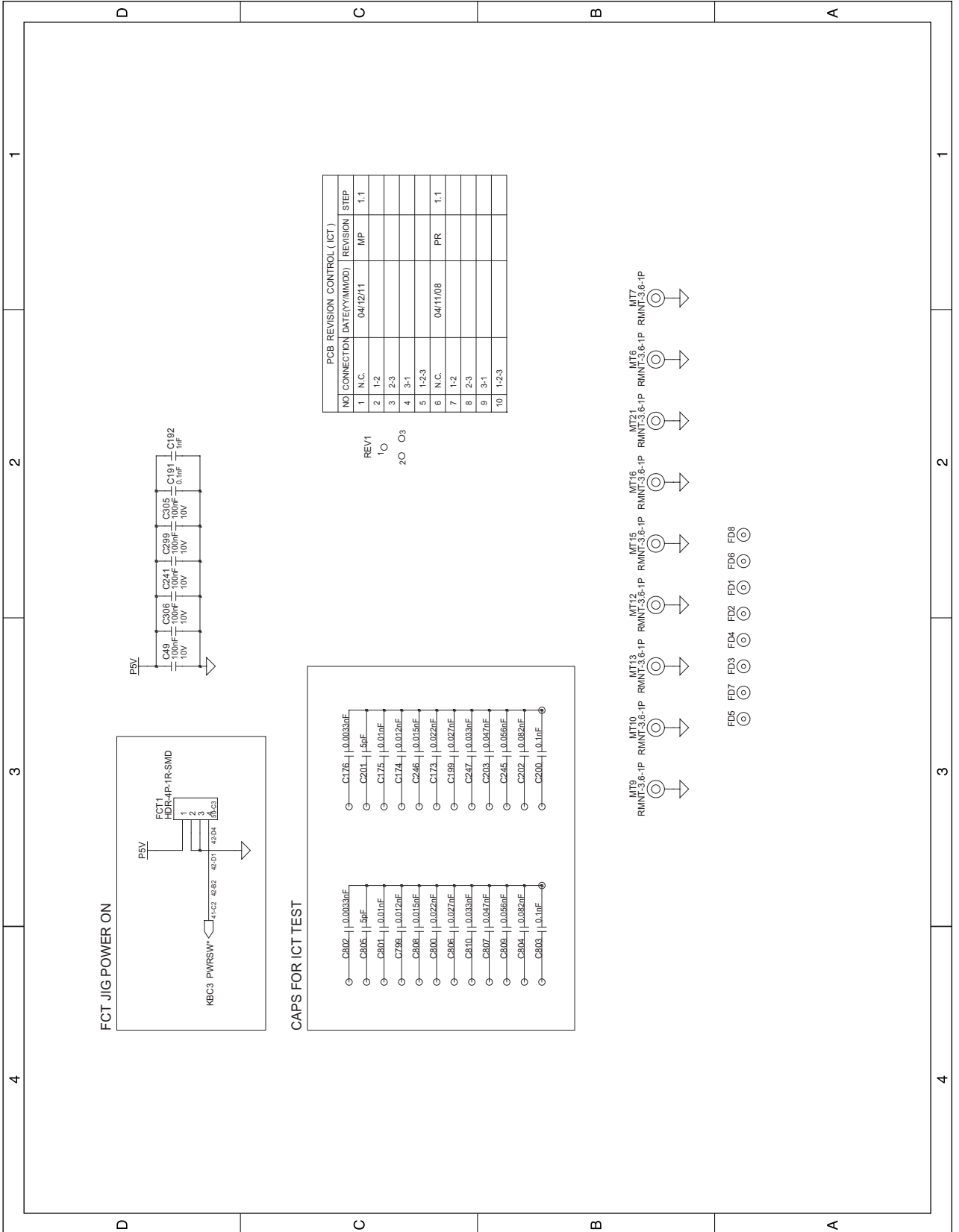
8-1-1(vv) Main Board Schematic Sheet 49 of 51 (3.3V / 5V Generation)







8-1-1(xx) Main Board Schematic Sheet 51 of 51(PCB Suffixs)



## 8-1-2 Signal Location

Signal Name	Sheet Number - Location	Signal Name	Sheet Number - Location
AGP0 VREF	14-A4,14-C2,21-B4,50-D4	AGP1 ADSTB1*	14-B3,21-C4
AGP1 AD(31)	14-D3,21-D4	AGP1 CBE*(2)	14-B3,21-D1
AGP1 AD(2)	14-D3,21-D4	AGP1 CBE*(0)	14-B3,21-D1
AGP1 AD(17)	14-D3,21-D4	AGP1 CBE*(3)	14-B3,21-D1
AGP1 AD(14)	14-D3,21-D4	AGP1 CBE*(1)	14-B3,21-D1
AGP1 AD(21)	14-D3,21-D4	AGP1 CBE*(3:0)	14-B3,21-D1
AGP1 AD(1)	14-D3,21-D4	AGP1 DBI HI	14-B3,22-B1,50-D4
AGP1 AD(12)	14-D3,21-D4	AGP1 DBI LO	14-B3,22-B1,50-D4
AGP1 AD(19)	14-D3,21-D4	AGP1 DEVSEL*	14-B3,21-D4,50-D4
AGP1 AD(8)	14-D3,21-D4	AGP1 FRAME*	14-B3,21-C4,50-D4
AGP1 AD(28)	14-D3,21-D4	AGP1 GNT*	14-B4,21-D4,50-D4
AGP1 AD(4)	14-D3,21-D4	AGP1 IRDY*	14-B3,21-D4,50-D4
AGP1 AD(10)	14-D3,21-D4	AGP1 PAR	14-B3,21-D4,50-D4
AGP1 AD(23)	14-D3,21-D4	AGP1 RBF*	14-B3,21-C4,50-D4
AGP1 AD(13)	14-D3,21-D4	AGP1 REQ*	14-B4,21-D4,50-D4
AGP1 AD(11)	14-D3,21-D4	AGP1 SBA(0)	14-B3,14-C2,21-C4
AGP1 AD(3)	14-D3,21-D4	AGP1 SBA(6)	14-B3,14-D2,21-C4
AGP1 AD(6)	14-D3,21-D4	AGP1 SBA(2)	14-B3,14-D2,21-C4
AGP1 AD(18)	14-D3,21-D4	AGP1 SBA(3)	14-B3,14-D2,21-C4
AGP1 AD(30)	14-D3,21-D4	AGP1 SBA(5)	14-B3,14-D2,21-C4
AGP1 AD(9)	14-D3,21-D4	AGP1 SBA(7)	14-B3,14-D2,21-C4
AGP1 AD(5)	14-D3,21-D4	AGP1 SBA(4)	14-B3,14-D2,21-C4
AGP1 AD(15)	14-D3,21-D4	AGP1 SBA(1)	14-B3,14-C2,21-C4
AGP1 AD(20)	14-D3,21-D4	AGP1 SBA(7:0)	14-B3,21-C4
AGP1 AD(16)	14-D3,21-D4	AGP1 SBSTB	14-C3,14-D2,21-C4
AGP1 AD(29)	14-D3,21-D4	AGP1 SBSTB*	14-C3,21-C4
AGP1 AD(27)	14-D3,21-D4	AGP1 ST(1)	14-B3,21-C4
AGP1 AD(25)	14-D3,21-D4	AGP1 ST(0)	14-B3,21-C4
AGP1 AD(22)	14-D3,21-D4	AGP1 ST(2)	14-B3,21-C4
AGP1 AD(24)	14-D3,21-D4	AGP1 ST(2:0)	14-B3,21-C4
AGP1 AD(7)	14-D3,21-D4	AGP1 STOP*	14-B3,21-D4,50-D4
AGP1 AD(0)	14-D3,21-D4	AGP1 TRDY*	14-B3,21-D4
AGP1 AD(26)	14-D3,21-D4	AGP1 WBF*	14-B3,21-C4,50-D4
AGP1 AD(31:0)	14-D3,21-D4	AGP3 BUSY*	14-D1,21-C4,28-A4,28-C1
AGP1 ADSTB0	14-C3,21-C4	AGP3 DMINUS	21-D1,23-B2
AGP1 ADSTB0*	14-C3,21-C4	AGP3 DPLUS	21-D1,23-B2
AGP1 ADSTB1	14-C3,21-C4	AGP3 GPIO(13)	21-A4,24-D4,50-D4

Signal Name	Sheet Number - Location	Signal Name	Sheet Number - Location
AGP3 GPIO(12)	21-A4,24-D4	ALINK3 AD(20)	14-D4,17-B3,27-D4
AGP3 GPIO(8)	21-A4,24-D4,50-C4	ALINK3 AD(15)	14-D4,27-D4
AGP3 GPIO(6)	21-A4,24-D4,50-C4	ALINK3 AD(8)	14-D4,27-D4
AGP3 GPIO(5)	21-A4,24-D4	ALINK3 AD(29)	14-D4,17-C3,27-D4
AGP3 GPIO(4)	21-A4,24-D4,50-C4	ALINK3 AD(24)	14-D4,17-C3,27-D4
AGP3 GPIO(3)	21-A4,24-D4,50-C4	ALINK3 AD(1)	14-D4,27-D4
AGP3 GPIO(2)	21-A4,24-D4,50-D4	ALINK3 AD(25)	14-D4,17-C3,27-D4
AGP3 GPIO(1)	21-A4,24-D4,50-D4	ALINK3 AD(18)	14-D4,17-B3,27-D4
AGP3 GPIO(0)	21-A4,24-D4,50-D4	ALINK3 AD(2)	14-D4,27-D4
AGP3 GPIO(15)	21-A4,24-D4,43-A1,50-D4	ALINK3 AD(23)	14-D4,17-B3,27-D4
AGP3 GPIO(16)	21-A4,23-D1,24-D4,50-D4	ALINK3 AD(10)	14-D4,27-D4
AGP3 GPIO(11)	21-A4,24-D4	ALINK3 AD(12)	14-D4,27-D4
AGP3 GPIO(9)	21-A4,24-D4,50-C4	ALINK3 AD(21)	14-D4,17-B3,27-D4
AGP3 GPIO(7)	21-A4,24-D4	ALINK3 AD(31)	14-D4,17-D3,27-D4
AGP3 GPIO(10)	21-A4,24-D4	ALINK3 AD(11)	14-D4,27-D4
AGP3 GPIO(14)	21-A4,24-D4	ALINK3 AD(30)	14-D4,17-D3,27-D4
AGP3 GPIO(16:0)	21-A4,24-D4	ALINK3 AD(31:0)	14-D4,27-D4
AGP3 RST*	21-D4,27-C1	ALINK3 CBE*(3)	14-C4,27-C4
AGP3 STP*	14-D1,21-C4,28-A4, 28-C1, 50-C4	ALINK3 CBE*(1)	14-C4,27-C4
ALINK3 ACAT*	14-B4,27-C4	ALINK3 CBE*(2)	14-C4,27-C4
ALINK3 AD(28)	14-D4,17-C3,27-D4	ALINK3 CBE*(0)	14-C4,27-C4
ALINK3 AD(19)	14-D4,17-B3,27-D4	ALINK3 CBE*(3:0)	14-C4,27-C4
ALINK3 AD(27)	14-D4,17-C3,27-D4	ALINK3 DEVSEL*	14-B4,27-C4
ALINK3 AD(26)	14-D4,17-C3,27-D4	ALINK3 END*	14-B4,27-C4
ALINK3 AD(13)	14-D4,27-D4	ALINK3 GNT*	14-B4,27-B4
ALINK3 AD(16)	14-D4,27-D4	ALINK3 INTA*	14-B4,21-C4,27-B4,32-C4, 34-B2
ALINK3 AD(17)	14-D4,17-C3,27-D4	ALINK3 OFF*	14-B4,27-C4
ALINK3 AD(5)	14-D4,27-D4	ALINK3 PAR	14-B4,17-B3,27-C4
ALINK3 AD(7)	14-D4,27-D4	ALINK3 REQ*	14-B4,27-C4
ALINK3 AD(0)	14-D4,27-D4	ALINK3 RST*	12-B4,15-B1,27-C2,27-D4
ALINK3 AD(3)	14-D4,27-D4	ALINK3 STROBE*	14-B4,27-C4
ALINK3 AD(22)	14-D4,17-B3,27-D4	AUD3 LINK BCLK	28-A2,28-B1,37-A2,39-A2, 50-C4
ALINK3 AD(6)	14-D4,27-D4	AUD3 LINK BCLK R	37-A2,37-D4,39-A2
ALINK3 AD(14)	14-D4,27-D4	AUD3 LINK RST*	28-A2,28-B2,28-C1,50-C4
ALINK3 AD(4)	14-D4,27-D4	AUD3 LINK RST R*	28-B1,37-C4,39-B3
ALINK3 AD(9)	14-D4,27-D4	AUD3 LINK SDIO	28-A2,28-B1,37-C4,50-C4

Signal Name	Sheet Number - Location	Signal Name	Sheet Number - Location
AUD3 LINK SDI1	28-A2,28-B1,39-B2,50-C4	CB3 AD(31)	35-D2,35-D3,50-A4
AUD3 LINK SDI2	28-A2,28-B1,50-C4	CB3 AD(8)	35-D2,35-D3,50-A4
AUD3 LINK SDO	28-A2,30-D4,37-D4,39-B3	CB3 AD(7)	35-D2,35-D3,50-A4
AUD3 LINK SPDIFO	28-A2,30-D4,50-C4	CB3 AD(27)	35-D2,35-D3,50-B4
AUD3 LINK SYNC	28-A2,30-D4,37-C4,39-B2, 50-C4	CB3 AD(21)	35-D2,35-D3,50-B4
AUD3 MONO OUT	37-C3,39-B3,50-C4	CB3 AD(29)	35-D2,35-D3,50-B4
AUD3 PCBEEP	37-A3,37-C3	CB3 AD(12)	35-D2,35-D3,50-B4
AUD3 PHONE	37-A3,37-C3,50-C4	CB3 AD(1)	35-D2,35-D3,50-B4
AUD5 CDDCOM	37-B3,37-C3,50-C4	CB3 AD(0)	35-D2,35-D3,50-B4
AUD5 CDDL	37-B3,37-C3,50-C4	CB3 AD(13)	35-D2,35-D3,50-B4
AUD5 CDDR	37-B3,37-C3,50-C4	CB3 AD(23)	35-D2,35-D3,50-B4
AUD5 LINE OUT L	37-C2,37-C3,50-C4	CB3 AD(22)	35-D2,35-D3,50-B4
AUD5 LINE OUT R	37-C3,37-D2,50-C4	CB3 AD(10)	35-D2,35-D3,50-B4
AUD5 MIC 1	37-C3,38-C2,50-C4	CB3 AD(9)	35-D2,35-D3,50-A4
AUD5 MIC 2	37-C3,38-C2	CB3 AD(26)	35-D2,35-D3,50-B4
AUD5 SPKMUTE*	37-C1,37-C1	CB3 AD(17)	35-D2,35-D3,50-B4
AUD5 SPK L+	37-A1,37-C1,50-C4	CB3 AD(16)	35-D2,35-D3,50-B4
AUD5 SPK L-	37-A1,37-C1,50-C4	CB3 AD(4)	35-D2,35-D3,50-A4
AUD5 SPK R+	37-A1,37-C1,50-C4	CB3 AD(3)	35-D2,35-D3,50-B4
AUD5 SPK R-	37-A1,37-C1,50-C4	CB3 AD(15)	35-D2,35-D3,50-B4
AUD5 VREFOUT	37-C3,38-C2	CB3 AD(30)	35-D2,35-D3,50-B4
BAT3 TEMP	42-B2,42-D1,44-C1,50-C4	CB3 AD(5)	35-D2,35-D3,50-A4
BAT3 VOLT	42-B2,44-C1,50-C4	CB3 AD(28)	35-D2,35-D3,50-B4
CB3 3VEN*	35-A4,35-D4,50-C4	CB3 AD(31:0)	35-D2,35-D3
CB3 5VEN*	35-A4,35-D4,50-B4	CB3 AUDIO	35-B1,35-B3,50-A4
CB3 A18	35-C1,35-C4,50-B4	CB3 CBEO*	35-C3,35-D1,50-A4
CB3 A19	35-C1,35-C4,50-B4	CB3 CBE1*	35-C1,35-C3,50-A4
CB3 AD(20)	35-D2,35-D3,50-B4	CB3 CBE2*	35-C1,35-C3,50-A4
CB3 AD(19)	35-D2,35-D3,50-B4	CB3 CBE3*	35-B1,35-C3,50-A4
CB3 AD(18)	35-D2,35-D3,50-B4	CB3 CD1*	35-A2,35-B3,35-D1,50-D3
CB3 AD(6)	35-D2,35-D3,50-A4	CB3 CD2*	35-A2,35-B1,35-B3,50-D3
CB3 AD(25)	35-D2,35-D3,50-B4	CB3 CLK	35-C1,35-C3,50-D3
CB3 AD(24)	35-D2,35-D3,50-B4	CB3 CLKRUN*	35-B1,35-C3,50-D3
CB3 AD(2)	35-D2,35-D3,50-B4	CB3 D14	35-C4,35-D1,50-D3
CB3 AD(11)	35-D2,35-D3,50-B4	CB3 D2	35-B1,35-C4,50-D3
CB3 AD(14)	35-D2,35-D3,50-B4	CB3 DEVSEL*	35-B3,35-C1,50-D3
		CB3 FRAME*	35-B3,35-C1,50-D3

Signal Name	Sheet Number - Location	Signal Name	Sheet Number - Location
CB3 GNT*	35-B3,35-C1,50-D3	CDD5 D(1)	28-B2,39-C4
CB3 INT*	35-B3,35-C1,50-D3	CDD5 D(15:8)	39-C3
CB3 IRDY*	35-B3,35-C1,50-D3	CDD5 D(7:0)	39-C4
CB3 PAR	35-B3,35-C1,50-D3	CDD5 D(15:0)	28-B2
CB3 PERR*	35-B3,35-C1,50-D3	CDD5 DACK*	28-B2,39-C3
CB3 REQ*	35-B1,35-B3,50-D3	CDD5 DREQ	28-B2,39-C3
CB3 RST*	35-A2,35-B1,35-C3,50-D3	CDD5 IOR*	28-B2,39-C3
CB3 SERR*	35-B1,35-B3,50-D3	CDD5 IORDY	28-B2,39-C4
CB3 SPKR	34-B4,37-A4,50-D3	CDD5 IOW*	28-B2,39-C4
CB3 STOP*	35-B3,35-C1,50-D3	CDD5 IRQ	28-B1,28-B2,39-C4
CB3 STSCHG	35-B1,35-B3,50-C3	CDD5 L	37-B4,39-D4
CB3 TRDY*	35-B3,35-C1,50-C3	CDD5 R	37-B4,39-D3
CB3 VPPEN0*	35-A4,35-D4,50-C3	CHARGER VDC	44-B4,44-D1,50-C3
CB3 VPPEN1*	35-A4,35-D4,50-C3	CHP2 SUSSTAT*	12-B4,28-D1,28-D2
CB3 VS1	35-B3,35-C1,50-C3	CHP3 1394 ROMW*	28-A4,34-B2
CB3 VS2	35-B1,35-B3,50-C3	CHP3 ADAPTERIN*	42-A1,42-A2,49-C4
CDD5 A0	28-B2,39-C4	CHP3 ADSEL	41-C2,42-A1,44-B1,49-B4, 50-C3
CDD5 A1	28-B2,39-C4	CHP3 CMOSCLR	27-A1,28-A4
CDD5 A2	28-B2,39-C3	CHP3 DPRSLPVR	27-A4,46-B3,46-C4,46-D4, 50-C3
CDD5 COM	37-B4,39-D3,39-D4	CHP3 NBPWRGD	12-B3,25-B2,45-B1,50-C3
CDD5 CS1*	28-B2,39-C4	CHP3 NBPWRGD	3.3V 45-A2,45-B1
CDD5 CS3*	28-B2,39-C3	CHP3 PATADET	28-C2,39-B4,50-C3
CDD5 D(14)	28-B2,39-C3	CHP3 PCICLKSTOP*	7-C4,27-B4,50-C3
CDD5 D(5)	28-B2,39-C4	CHP3 PME*	28-C1,28-C2,32-C2, 33-B2,34-C4,36-B4,50-C3
CDD5 D(8)	28-B2,39-C3	CHP3 RI*	28-C1,28-D2,50-C3
CDD5 D(0)	28-B2,39-C4	CHP3 RSMRST*	27-A3,28-B4,50-C3
CDD5 D(9)	28-B2,39-C3	CHP3 RTCIRQ*	28-C2,30-D4,50-C3
CDD5 D(6)	28-B2,39-C4	CHP3 SATADET	28-C2,39-B4,50-C3
CDD5 D(12)	28-B2,39-C3	CHP3 SBPWRGD	28-D2,45-A1,50-C3
CDD5 D(13)	28-B2,39-C3	CHP3 SERIRQ	27-A2,27-C1,34-B2,38-B3, 42-C2,50-C3
CDD5 D(2)	28-B2,39-C4	CHP3 SLPS3*	28-D1,28-D2,42-A2
CDD5 D(4)	28-B2,39-C4	CHP3 SLPS5*	28-C1,28-D2,42-A2,50-C3
CDD5 D(15)	28-B2,39-C3	CHP3 SPKR	28-A4,37-A4,50-C3
CDD5 D(7)	28-B1,28-B2,39-C4		
CDD5 D(11)	28-B2,39-C3		
CDD5 D(3)	28-B2,39-C4		
CDD5 D(10)	28-B2,39-C3		

Signal Name	Sheet Number - Location	Signal Name	Sheet Number - Location
CHP3 SUSSTAT*	21-B4,21-C4,28-D1,38-A4, 42-A2,50-C3	FS1	7-A4,7-D1
CLK0 HOST CPU	7-C1,9-D4	FS2	7-A4,7-C1
CLK0 HOST CPU*	7-C1,9-D4	FS4	7-A4,7-C1
CLK0 HOST NB	7-C1,15-C3	FWH1 INIT*	27-B4,31-C3,50-C3
CLK0 HOST NB*	7-C1,15-C3	FWH3 WP TBL *	31-B4,42-B4
CLK1 27M VGA	21-B4,23-D1	GFX3 THERM*	23-B2,42-B4
CLK2 MEM0	13-B3,18-C4	HDD5 A0	28-C2,39-C2,50-C3
CLK2 MEM0*	13-B3,18-C4	HDD5 A1	28-C2,39-C2,50-C3
CLK2 MEM1	13-B3,18-C4	HDD5 A2	28-C2,39-C1,50-C3
CLK2 MEM1*	13-B3,18-C4	HDD5 CS1*	28-C2,39-C2,50-B3
CLK2 MEM2	13-B3,18-C4	HDD5 CS3*	28-C2,39-C1,50-B3
CLK2 MEM2*	13-B3,18-C4	HDD5 D(3)	28-C2,39-C2,50-B3
CLK2 MEM3	13-B3,18-C3	HDD5 D(9)	28-C2,39-D1,50-B3
CLK2 MEM3*	13-B3,18-C3	HDD5 D(12)	28-C2,39-D1,50-B3
CLK2 MEM4	13-B3,18-C3	HDD5 D(11)	28-C2,39-D1,50-B3
CLK2 MEM4*	13-B3,18-C3	HDD5 D(4)	28-C2,39-C2,50-B3
CLK2 MEM5	13-B3,18-C3	HDD5 D(15)	28-C2,39-D1,50-B3
CLK2 MEM5*	13-B3,18-C3	HDD5 D(13)	28-C2,39-D1,50-B3
CLK3 32K SB	28-A4,30-D4	HDD5 D(6)	28-C2,39-C2,50-B3
CLK3 33M CB	27-D2,34-C4,34-C4	HDD5 D(7)	28-B1,28-C2,39-C2,50-B3
CLK3 33M FWH	27-D2,31-B4	HDD5 D(14)	28-C2,39-D1,50-B3
CLK3 33M LAN	27-D2,36-A3,36-B4	HDD5 D(5)	28-C2,39-C2,50-B3
CLK3 33M MICOM	27-D2,42-C2	HDD5 D(0)	28-C2,39-C2,50-B3
CLK3 33M MIN	27-D2,32-B4,32-C4	HDD5 D(10)	28-C2,39-D1,50-B3
CLK3 33M USB	27-D2,33-B4	HDD5 D(1)	28-C2,39-C2,50-B3
CLK3 48M SB USB	7-D1,28-D4	HDD5 D(2)	28-C2,39-C2,50-B3
CLK3 48M USB	7-D1,33-B1	HDD5 D(8)	28-C2,39-D1,50-B3
CLK3 66M AGP	7-C1,21-D4	HDD5 D(7:0)	39-C2
CLK3 66M NB	7-C1,15-B3	HDD5 D(15:8)	39-D1
CLK3 66M SB	7-A4,7-C1,27-D4	HDD5 D(15:0)	28-C2
CLK3 DDR NB	7-C1,15-B3	HDD5 DACK*	28-C2,39-C2,50-B3
CLK3 OSC SB	7-D1,28-A4	HDD5 DREQ*	28-C2,39-C2,50-B3
CLK3 PCLKSIO	27-D2,38-A4	HDD5 IOR*	28-C2,39-C2,50-B3
CLK3 SIO14	7-D1,38-A3	HDD5 IORDY	28-C2,39-C2,50-B3
FAN3 FDBACK*	41-D4,42-B2,50-C3	HDD5 IOW*	28-C2,39-C2,50-B3
FS0	7-A4,7-D1	HDD5 IRQ	28-B1,28-C2,39-C2,50-B3
		HDD5 LED*	39-A4,39-C2,41-A3,50-B3

Signal Name	Sheet Number - Location	Signal Name	Sheet Number - Location
HOST1 A*(24)	8-C4,12-D3	HOST1 CPURST*	8-C3,12-B3
HOST1 A*(15)	8-C4,12-D3	HOST1 D*(3)	8-C2,12-D2
HOST1 A*(5)	8-C4,12-D3	HOST1 D*(35)	8-C1,12-D2
HOST1 A*(6)	8-C4,12-D3	HOST1 D*(2)	8-C2,12-D2
HOST1 A*(3)	8-C4,12-D3	HOST1 D*(21)	8-C2,12-D2
HOST1 A*(12)	8-C4,12-D3	HOST1 D*(32)	8-C1,12-D2
HOST1 A*(19)	8-C4,12-D3	HOST1 D*(27)	8-C2,12-D2
HOST1 A*(20)	8-C4,12-D3	HOST1 D*(26)	8-C2,12-D2
HOST1 A*(9)	8-C4,12-D3	HOST1 D*(11)	8-C2,12-D2
HOST1 A*(14)	8-C4,12-D3	HOST1 D*(57)	8-C1,12-D2
HOST1 A*(7)	8-C4,12-D3	HOST1 D*(53)	8-C1,12-D2
HOST1 A*(16)	8-C4,12-D3	HOST1 D*(38)	8-C1,12-D2
HOST1 A*(18)	8-C4,12-D3	HOST1 D*(25)	8-C2,12-D2
HOST1 A*(29)	8-C4,12-D3	HOST1 D*(24)	8-C2,12-D2
HOST1 A*(8)	8-C4,12-D3	HOST1 D*(28)	8-C2,12-D2
HOST1 A*(11)	8-C4,12-D3	HOST1 D*(59)	8-C1,12-D2
HOST1 A*(27)	8-C4,12-D3	HOST1 D*(46)	8-C1,12-D2
HOST1 A*(31)	8-C4,12-D3	HOST1 D*(6)	8-C2,12-D2
HOST1 A*(28)	8-C4,12-D3	HOST1 D*(42)	8-C1,12-D2
HOST1 A*(17)	8-C4,12-D3	HOST1 D*(41)	8-C1,12-D2
HOST1 A*(4)	8-C4,12-D3	HOST1 D*(40)	8-C1,12-D2
HOST1 A*(26)	8-C4,12-D3	HOST1 D*(16)	8-C2,12-D2
HOST1 A*(10)	8-C4,12-D3	HOST1 D*(55)	8-C1,12-D2
HOST1 A*(23)	8-C4,12-D3	HOST1 D*(54)	8-C1,12-D2
HOST1 A*(22)	8-C4,12-D3	HOST1 D*(39)	8-C1,12-D2
HOST1 A*(13)	8-C4,12-D3	HOST1 D*(31)	8-C2,12-D2
HOST1 A*(25)	8-C4,12-D3	HOST1 D*(10)	8-C2,12-D2
HOST1 A*(30)	8-C4,12-D3	HOST1 D*(19)	8-C2,12-D2
HOST1 A*(21)	8-C4,12-D3	HOST1 D*(50)	8-C1,12-D2
HOST1 A*(31:3)	8-C4,12-D3	HOST1 D*(49)	8-C1,12-D2
HOST1 A20M*	8-B3,10-B3,27-B4	HOST1 D*(34)	8-C1,12-D2
HOST1 ADS*	8-C3,12-C3	HOST1 D*(8)	8-C2,12-D2
HOST1 ADSTB0*	8-C4,12-C3	HOST1 D*(7)	8-C2,12-D2
HOST1 ADSTB1*	8-B4,12-C3	HOST1 D*(36)	8-C1,12-D2
HOST1 BNR*	8-C3,12-C3	HOST1 D*(4)	8-C2,12-D2
HOST1 BPRI*	8-C3,12-C3	HOST1 D*(1)	8-C2,12-D2
HOST1 CPUPWRGD	9-D4,10-B3,27-B4	HOST1 D*(5)	8-C2,12-D2

Signal Name	Sheet Number - Location	Signal Name	Sheet Number - Location
HOST1 D*(17)	8-C2,12-D2	HOST1 DBSY*	8-C3,12-B3
HOST1 D*(58)	8-C1,12-D2	HOST1 DEFER*	8-C3,12-B3
HOST1 D*(37)	8-C1,12-D2	HOST1 DPSLP*	9-D4,11-?3
HOST1 D*(63)	8-C1,12-D2	HOST1 DPWR*	9-D4,12-B3
HOST1 D*(61)	8-C1,12-D2	HOST1 DRDY*	8-C3,12-B3
HOST1 D*(47)	8-C1,12-D2	HOST1 DSTBN0*	8-C2,12-C2
HOST1 D*(15)	8-C2,12-D2	HOST1 DSTBN1*	8-B2,12-C2
HOST1 D*(22)	8-C2,12-D2	HOST1 DSTBN2*	8-C1,12-B2
HOST1 D*(20)	8-C2,12-D2	HOST1 DSTBN3*	8-B1,12-A2
HOST1 D*(62)	8-C1,12-D2	HOST1 DSTBP0*	8-C2,12-C2
HOST1 D*(14)	8-C2,12-D2	HOST1 DSTBP1*	8-B2,12-C2
HOST1 D*(44)	8-C1,12-D2	HOST1 DSTBP2*	8-C1,12-B2
HOST1 D*(0)	8-C2,12-D2	HOST1 DSTBP3*	8-B1,12-A2
HOST1 D*(43)	8-C1,12-D2	HOST1 FERR*	8-B3,11-?4,50-B3
HOST1 D*(51)	8-C1,12-D2	HOST1 GTLREFO	9-A3,9-C2
HOST1 D*(56)	8-C1,12-D2	HOST1 HIT*	8-C3,12-B3,50-B3
HOST1 D*(48)	8-C1,12-D2	HOST1 HITM*	8-C3,12-B3,50-B3
HOST1 D*(45)	8-C1,12-D2	HOST1 IGNNE*	8-B3,10-B3,27-B4,50-A3
HOST1 D*(18)	8-C2,12-D2	HOST1 INIT*	8-C3,10-B3,27-B4,50-A3
HOST1 D*(29)	8-C2,12-D2	HOST1 INTR	8-B3,10-B3,27-B4,50-A3
HOST1 D*(12)	8-C2,12-D2	HOST1 LOCK*	8-C3,12-B3,50-A3
HOST1 D*(60)	8-C1,12-D2	HOST1 NMI	8-B3,10-B3,27-B4,50-A3
HOST1 D*(13)	8-C2,12-D2	HOST1 PSI*	9-B4,46-C4
HOST1 D*(52)	8-C1,12-D2	HOST1 REQ*(4)	8-C4,12-C3
HOST1 D*(23)	8-C2,12-D2	HOST1 REQ*(2)	8-C4,12-C3
HOST1 D*(33)	8-C1,12-D2	HOST1 REQ*(1)	8-C4,12-C3
HOST1 D*(9)	8-C2,12-D2	HOST1 REQ*(0)	8-C4,12-C3
HOST1 D*(30)	8-C2,12-D2	HOST1 REQ*(3)	8-C4,12-C3
HOST1 D*(63:0)	12-D2	HOST1 REQ*(4:0)	8-C4,12-C3
HOST1 D*(63:48)	8-C1	HOST1 RS*(0)	8-B3,12-B3
HOST1 D*(31:16)	8-C2	HOST1 RS*(1)	8-B3,12-B3
HOST1 D*(15:0)	8-C2	HOST1 RS*(2)	8-B3,12-B3
HOST1 D*(47:32)	8-C1	HOST1 RS*(2:0)	8-B3,12-B3
HOST1 DBI0*	8-C2,12-C2	HOST1 SLP*	9-D4,10-B3,27-B4,50-A3
HOST1 DBI1*	8-B2,12-C2	HOST1 SMI*	8-B3,10-B3,27-B4,50-A3
HOST1 DBI2*	8-C1,12-B2	HOST1 STPCLK*	8-B3,10-B3,27-A4,50-A3
HOST1 DBI3*	8-B1,12-A2	HOST1 TCK	9-B2,10-B3,50-A3



Signal Name	Sheet Number - Location
HOST1 TDI	9-B2,10-B3,50-A3
HOST1 TDO	9-B2,10-B3,50-A3
HOST1 TMS	9-B2,10-B3,50-A3
HOST1 TRDY*	8-C3,12-B3,50-A3
HOST1 TRST*	9-B2,10-B3,50-A3
HOST2 THERMDA	9-C4,11-?4,50-D3
HOST2 THERMDC	9-C4,11-?4,50-D3
HOST3 CPUSTOP*	11-?4,15-B1,27-B4,46-C4
HOST3 CPUSTOP NB*	7-C4,15-B1,50-D3
HOST3 FERR*	11-?3,27-A4,50-D3
HOST3 VID(5)	9-C4,46-A2,46-B1,50-D3
HOST3 VID(2)	9-C4,46-B1,50-D3
HOST3 VID(0)	9-C4,46-B1,50-D3
HOST3 VID(3)	9-C4,46-B1,50-D3
HOST3 VID(4)	9-C4,46-B1,50-D3
HOST3 VID(1)	9-C4,46-B1,50-D3
HOST3 VID(5:0)	9-C4
HP5 LINE OUT L	37-C4,38-D2,50-D3
HP5 LINE OUT R	37-C4,38-D2,50-D3
HP DETECT	37-C2,37-C3,38-D2,50-D3
IDE3 RST*	27-B1,39-D2,39-D4
IMVP4 PWRGD	7-C4,28-A4,42-B2,45-B2, 46-D1
KBC3 A20G	28-D2,42-B2,42-C1,50-D3
KBC3 BKLTON	25-B2,42-C2,50-D3
KBC3 BRIT	25-C2,42-B2,50-D3
KBC3 CAPSLED*	41-B2,42-C4,50-D3
KBC3 CHGEN	42-C2,44-B4,50-D3
KBC3 CPURST*	28-D2,42-B2,42-C1,50-C3
KBC3 CPUVRON	42-B1,46-A4,50-C3
KBC3 DELAY PWRGD	42-C2,45-A2,50-C3
KBC3 EXTSMI*	28-D2,38-A3,42-C4,50-C3
KBC3 FANCTRL	41-C3,42-B2,50-C3
KBC3 IMVP4 PWRGD	42-B2,45-C2,50-C3
KBC3 IVTPWRON	25-D3,42-B4
KBC3 LBLEDD*	41-A4,42-C4,50-C3
KBC3 NUMLED*	41-B2,42-C4,50-C3

Signal Name	Sheet Number - Location
KBC3 PWRGD	34-C4,42-C4,50-C3
KBC3 PWRON	34-D2,42-C4,43-B4, 43-C4,45-C1,45-D4, 48-A3,48-B3,49-A4,49-B1, 50-C3
KBC3 PWRON*	45-C2,45-C4,45-D4
KBC3 PWRSW*	41-C2,42-B2,42-D1, 42-D4,50-C3,51-D3
KBC3 PWRSW SB*	28-D2,42-B2,42-C1,50-C3
KBC3 QS*	42-C2,44-B4,50-C3
KBC3 RSMRST*	27-A4,42-C2,50-C3
KBC3 RST*	42-D1,42-D4,45-B4,50-C3
KBC3 RUNSCI*	28-C2,42-C4,50-C3
KBC3 SCL	42-A2,42-C1,44-C1,50-C3
KBC3 SCLED*	41-A2,42-C4,50-C3
KBC3 SDA	42-A2,42-C1,44-C1,50-C3
KBC3 SPKMUTE	37-B2,42-B2,50-C3
KBC3 SUSPWR	42-B2,43-C1,43-D1,45-B3, 48-C4,49-B4
KBC3 THERM*	28-D2,42-A2
KBC3 THERM SCL	11-?3,42-A2,50-C3
KBC3 THERM SDA	11-?3,42-C2,50-C3
KBC3 TPAD*	41-B4,42-C4,50-C3
KBC3 VGAPWRON	42-C4,43-A4,48-A3
KBC3 VRON	42-C1,42-C2,46-A4, 47-C2,47-C4,50-C3
KBC3 WAKESCI*	28-C1,28-C2,42-B2,50-C3
KBC3 WLAN*	32-C4,41-B4,42-A2,50-C3
KBC5 KSI(3)	38-D4,42-B4,50-B3
KBC5 KSI(5)	38-D4,42-B4,50-B3
KBC5 KSI(2)	38-D4,42-B4,50-B3
KBC5 KSI(0)	38-D4,42-B4,50-C3
KBC5 KSI(7)	38-D4,42-B4,50-B3
KBC5 KSI(4)	38-D4,42-B4,50-B3
KBC5 KSI(6)	38-D4,42-B4,50-B3
KBC5 KSI(1)	38-D4,42-B4,50-C3
KBC5 KSI(7:0)	38-D4,42-B4

Signal Name	Sheet Number - Location	Signal Name	Sheet Number - Location
KBC5 KSO(5)	38-D4,42-B4,50-B3		42-C2,50-A3
KBC5 KSO(1)	38-D4,42-B4,50-B3	LPC3 AD(1)	27-B2,31-A3,31-B3,38-B4, 42-C2,50-A3
KBC5 KSO(2)	38-D4,42-B4,50-B3	LPC3 AD(0)	27-B2,31-A3,31-B3,38-B4, 42-C2,50-A3
KBC5 KSO(10)	38-D4,42-B4,50-B3	LPC3 AD(3:0)	27-B2,31-A3,31-B3,38-B4, 42-C2
KBC5 KSO(15)	38-D4,42-B4,50-B3	LPC3 FRAME*	27-B2,31-B3,38-B4,42-C2, 50-A3
KBC5 KSO(11)	38-D4,42-B4,50-B3	LPC3 LDRQ0*	27-B2,38-B3
KBC5 KSO(9)	38-D4,42-B4,50-B3	LPC3 RST*	27-C1,31-B4,42-C2
KBC5 KSO(4)	38-D4,42-B4,50-B3	LPC3 SMI*	28-C1,28-D2,50-A3
KBC5 KSO(13)	38-D4,42-B4,50-B3	LUSB	33-B4,42-D2
KBC5 KSO(6)	38-D4,42-B4,50-B3	LVDS1 L0N	15-C2,21-C1,25-C4
KBC5 KSO(7)	38-D4,42-B4,50-B3	LVDS1 L0P	15-C2,21-C1,25-C4
KBC5 KSO(14)	38-D4,42-B4,50-B3	LVDS1 L1N	15-C2,21-C1,25-C4
KBC5 KSO(0)	38-D4,42-B4,50-B3	LVDS1 L1P	15-C2,21-C1,25-C4
KBC5 KSO(3)	38-D4,42-B4,50-B3	LVDS1 L2N	15-C2,21-B1,25-C4
KBC5 KSO(8)	38-D4,42-B4,50-B3	LVDS1 L2P	15-C2,21-B1,25-C4
KBC5 KSO(12)	38-D4,42-B4,50-B3	LVDS1 LCLKN	15-C2,21-B1,25-C4
KBC5 KSO(15:0)	38-D4,42-B4	LVDS1 LCLKP	15-C2,21-B1,25-C4
KBC5 TCLK	38-B2,42-C4	LVDS1 SSIN	14-B1,14-D1
KBC5 TDATA	38-B2,42-C4	LVDS1 SSOUT	14-B2,14-D1
LAN3 EECK	28-B4,30-B4,50-B3	LVDS1 U0N	15-D2,21-B1,25-D4
LAN3 EEDO	28-B4,30-D4,50-B3	LVDS1 U0P	15-D2,21-B1,25-D4
LAN3 MDCK	28-C4,30-B4,50-B3	LVDS1 U1N	15-D2,21-B1,25-D4
LAN3 PHYPD	28-B4,30-B4,50-B3	LVDS1 U1P	15-D2,21-B1,25-C4
LAN3 TXD0	28-B4,30-D4,50-B3	LVDS1 U2N	15-D2,21-B1,25-C4
LAN3 TXD1	28-B4,30-D4,50-B3	LVDS1 U2P	15-D2,21-B1,25-C4
LAN3 TXD2	28-B4,30-D4,50-A3	LVDS1 UCLKN	15-D2,21-B1,25-C4
LAN3 TXD3	28-B4,30-D4,50-A3	LVDS1 UCLKP	15-C2,21-B1,25-C4
LAN3 TXEN	28-B4,30-D4,50-A3	MDC3 AUDIN	37-A4,39-B2,50-A3
LCD3 BKLTON	25-B1,25-D4,50-A3	MEM2 A(8)	13-D3,18-D4,20-C4
LCD3 BRIT	25-C1,25-D4,50-A3	MEM2 A(2)	13-D3,18-D4,20-C4
LCD3 TYPE	25-C4,42-B4	MEM2 A(11)	13-D3,18-D4,20-C4
LED3 CHARGE	41-C2,42-C2,50-A3	MEM2 A(7)	13-D3,18-D4,20-C4
LFM*	46-A2,46-D4	MEM2 A(5)	13-D3,18-D4,20-C4
LID3 SWITCH*	41-B3,42-B2,50-A3		
LPC3 AD(2)	27-B2,31-A3,31-B3,38-B4, 42-C2,50-A3		
LPC3 AD(3)	27-B2,31-A3,31-B3,38-B4,		

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MEM2 A(1)	13-D3,18-D4,20-C4	MEM2 D(58)	13-D2,18-D2,18-D3,19-D4
MEM2 A(6)	13-D3,18-D4,20-C4	MEM2 D(27)	13-D2,18-D2,18-D3,19-D4
MEM2 A(13)	13-D3,18-D4,20-C4	MEM2 D(13)	13-D2,18-D2,18-D3,19-D4
MEM2 A(9)	13-D3,18-D4,20-C4	MEM2 D(29)	13-D2,18-D2,18-D3,19-D4
MEM2 A(15)	13-D3,18-A2,18-D4,20-C4	MEM2 D(11)	13-D2,18-D2,18-D3,19-D4
MEM2 A(10)	13-D3,18-D4,20-C4	MEM2 D(14)	13-D2,18-D2,18-D3,19-D4
MEM2 A(4)	13-D3,18-D4,20-C4	MEM2 D(43)	13-D2,18-D2,18-D3,19-D4
MEM2 A(14)	13-D3,18-D4,20-C4	MEM2 D(50)	13-D2,18-D2,18-D3,19-D4
MEM2 A(3)	13-D3,18-D4,20-C4	MEM2 D(28)	13-D2,18-D2,18-D3,19-D4
MEM2 A(12)	13-D3,18-D4,20-C4	MEM2 D(38)	13-D2,18-D2,18-D3,19-D4
MEM2 A(0)	13-D3,18-D4,20-C4	MEM2 D(36)	13-D2,18-D2,18-D3,19-D4
MEM2 A(15:0)	13-D3,18-D4,20-C4	MEM2 D(63)	13-D2,18-D2,18-D3,19-D4
MEM2 CAS*	13-C3,18-B4,20-C2	MEM2 D(41)	13-D2,18-D2,18-D3,19-D4
MEM2 CKE0	13-B3,18-C4,20-C2	MEM2 D(1)	13-D2,18-D2,18-D3,19-D4
MEM2 CKE1	13-B3,18-B4,20-C2	MEM2 D(20)	13-D2,18-D2,18-D3,19-D4
MEM2 CKE2	13-B3,18-C3,20-C2	MEM2 D(34)	13-D2,18-D2,18-D3,19-D4
MEM2 CKE3	13-B3,18-B3,20-C2	MEM2 D(49)	13-D2,18-D2,18-D3,19-D4
MEM2 CS0*	13-B3,18-B4,20-C2	MEM2 D(21)	13-D2,18-D2,18-D3,19-D4
MEM2 CS1*	13-B3,18-B4,20-C2	MEM2 D(44)	13-D2,18-D2,18-D3,19-D4
MEM2 CS2*	13-B3,18-B3,20-C2	MEM2 D(32)	13-D2,18-D2,18-D3,19-D4
MEM2 CS3*	13-B3,18-B3,20-B2	MEM2 D(42)	13-D2,18-D2,18-D3,19-D4
MEM2 D(25)	13-D2,18-D2,18-D3,19-D4	MEM2 D(30)	13-D2,18-D2,18-D3,19-D4
MEM2 D(45)	13-D2,18-D2,18-D3,19-D4	MEM2 D(55)	13-D2,18-D2,18-D3,19-D4
MEM2 D(40)	13-D2,18-D2,18-D3,19-D4	MEM2 D(59)	13-D2,18-D2,18-D3,19-D4
MEM2 D(62)	13-D2,18-D2,18-D3,19-D4	MEM2 D(17)	13-D2,18-D2,18-D3,19-D4
MEM2 D(5)	13-D2,18-D2,18-D3,19-D4	MEM2 D(61)	13-D2,18-D2,18-D3,19-D4
MEM2 D(3)	13-D2,18-D2,18-D3,19-D4	MEM2 D(47)	13-D2,18-D2,18-D3,19-D4
MEM2 D(24)	13-D2,18-D2,18-D3,19-D4	MEM2 D(26)	13-D2,18-D2,18-D3,19-D4
MEM2 D(4)	13-D2,18-D2,18-D3,19-D4	MEM2 D(54)	13-D2,18-D2,18-D3,19-D4
MEM2 D(16)	13-D2,18-D2,18-D3,19-D4	MEM2 D(23)	13-D2,18-D2,18-D3,19-D4
MEM2 D(2)	13-D2,18-D2,18-D3,19-D4	MEM2 D(52)	13-D2,18-D2,18-D3,19-D4
MEM2 D(51)	13-D2,18-D2,18-D3,19-D4	MEM2 D(19)	13-D2,18-D2,18-D3,19-D4
MEM2 D(18)	13-D2,18-D2,18-D3,19-D4	MEM2 D(57)	13-D2,18-D2,18-D3,19-D4
MEM2 D(8)	13-D2,18-D2,18-D3,19-D4	MEM2 D(53)	13-D2,18-D2,18-D3,19-D4
MEM2 D(6)	13-D2,18-D2,18-D3,19-D4	MEM2 D(39)	13-D2,18-D2,18-D3,19-D4
MEM2 D(37)	13-D2,18-D2,18-D3,19-D4	MEM2 D(60)	13-D2,18-D2,18-D3,19-D4
MEM2 D(35)	13-D2,18-D2,18-D3,19-D4	MEM2 D(46)	13-D2,18-D2,18-D3,19-D4

Signal Name	Sheet Number - Location	Signal Name	Sheet Number - Location
MEM2 D(9)	13-D2,18-D2,18-D3,19-D4	MEM2 RA(13)	18-D3,20-D3
MEM2 D(22)	13-D2,18-D2,18-D3,19-D4	MEM2 RA(10)	18-D3,20-D3
MEM2 D(7)	13-D2,18-D2,18-D3,19-D4	MEM2 RA(1)	18-D3,20-D3
MEM2 D(56)	13-D2,18-D2,18-D3,19-D4	MEM2 RA(8)	18-D3,20-D3
MEM2 D(48)	13-D2,18-D2,18-D3,19-D4	MEM2 RA(5)	18-D3,20-D3
MEM2 D(15)	13-D2,18-D2,18-D3,19-D4	MEM2 RA(14)	18-D3,20-D3
MEM2 D(33)	13-D2,18-D2,18-D3,19-D4	MEM2 RA(4)	18-D3,20-D3
MEM2 D(31)	13-D2,18-D2,18-D3,19-D4	MEM2 RA(15)	18-A2,18-D3,20-D3
MEM2 D(12)	13-D2,18-D2,18-D3,19-D4	MEM2 RA(11)	18-D3,20-D3
MEM2 D(10)	13-D2,18-D2,18-D3,19-D4	MEM2 RA(15:0)	18-D3,20-D3
MEM2 D(0)	13-D2,18-D2,18-D3,19-D4	MEM2 RAS*	13-C3,18-B4,20-C2
MEM2 D(63:0)	13-D2,18-D2,18-D3,19-D4	MEM2 RCAS*	18-B3,20-D1
MEM2 DM(5)	13-C3,18-B3,18-B4,19-B2	MEM2 RRAS*	18-B3,20-D1
MEM2 DM(0)	13-C3,18-B3,18-B4,19-B2	MEM2 RWE*	18-B3,20-D1
MEM2 DM(1)	13-C3,18-B3,18-B4,19-B2	MEM2 VREF	13-A1,18-B2,18-B2,43-C4
MEM2 DM(6)	13-C3,18-B3,18-B4,19-B2	MEM2 WE*	13-C3,18-B4,20-C2
MEM2 DM(3)	13-C3,18-B3,18-B4,19-B2	NB0 GTLVREF	12-B3,12-C1
MEM2 DM(7)	13-C3,18-B3,18-B4,19-B2	PCI3 ACTREQ*	28-D1,28-D2,50-D2
MEM2 DM(2)	13-C3,18-B3,18-B4,19-B2	PCI3 AD(27)	27-D2,30-B4,32-D4, 33-D4,34-D4,36-D4,50-C2
MEM2 DM(4)	13-C3,18-B3,18-B4,19-B2	PCI3 AD(17)	27-D2,32-D4,33-D4, 34-D4,36-D4,50-D2
MEM2 DM(7:0)	13-C3,18-B3,18-B4,19-B2	PCI3 AD(21)	27-D2,32-D4,33-D4, 34-D4,36-B4,36-D4,50-D2
MEM2 DQS(1)	13-C3,18-B3,18-B4,19-C2	PCI3 AD(28)	27-D2,30-B4,32-D4, 33-D4,34-D4,36-D4,50-C2
MEM2 DQS(5)	13-C3,18-B3,18-B4,19-C2	PCI3 AD(19)	27-D2,32-C2,32-D4, 33-D4,34-D4,36-D4,50-D2
MEM2 DQS(3)	13-C3,18-B3,18-B4,19-C2	PCI3 AD(4)	27-D2,32-D4,33-D4, 34-D4,36-D4,50-C2
MEM2 DQS(6)	13-C3,18-B3,18-B4,19-C2	PCI3 AD(6)	27-D2,32-D4,33-D4, 34-D4,36-D4,50-C2
MEM2 DQS(0)	13-C3,18-B3,18-B4,19-C2	PCI3 AD(22)	27-D2,32-D4,33-B4, 33-D4,34-D4,36-D4,50-D2
MEM2 DQS(4)	13-C3,18-B3,18-B4,19-C2	PCI3 AD(31)	27-D2,30-B4,32-D4, 33-D4,34-D4,36-D4,50-C2
MEM2 DQS(2)	13-C3,18-B3,18-B4,19-C2	PCI3 AD(30)	27-D2,30-B4,32-D4,
MEM2 DQS(7)	13-C3,18-B3,18-B4,19-C2		
MEM2 DQS(7:0)	13-C3,18-B3,18-B4,19-C2		
MEM2 RA(7)	18-D3,20-D3		
MEM2 RA(2)	18-D3,20-D3		
MEM2 RA(0)	18-D3,20-D3		
MEM2 RA(3)	18-D3,20-D3		
MEM2 RA(6)	18-D3,20-D3		
MEM2 RA(12)	18-D3,20-D3		
MEM2 RA(9)	18-D3,20-D3		

Signal Name	Sheet Number - Location	Signal Name	Sheet Number - Location
PCI3 AD(20)	33-D4,34-D4,36-D4,50-C2 27-D2,32-D4,33-D4, 34-B4,34-D4,36-D4,50-D2	PCI3 AD(16)	27-D2,32-D4,33-D4, 34-D4,36-D4,50-D2
PCI3 AD(18)	27-D2,32-D4,33-D4, 34-D4,36-D4,50-D2	PCI3 AD(12)	27-D2,32-D4,33-D4, 34-D4,36-D4,50-D2
PCI3 AD(26)	27-D2,30-B4,32-D4, 33-D4,34-D4,36-D4,50-C2	PCI3 AD(2)	27-D2,32-D4,33-D4, 34-D4,36-D4,50-D2
PCI3 AD(10)	27-D2,32-D4,33-D4, 34-D4,36-D4,50-D2	PCI3 AD(8)	27-D2,32-D4,33-D4, 34-D4,36-D4,50-C2
PCI3 AD(15)	27-D2,32-D4,33-D4, 34-D4,36-D4,50-D2	PCI3 AD(31:0)	27-D2,32-D4,33-D4, 34-D4,36-D4
PCI3 AD(29)	27-D2,30-B4,32-D4, 33-D4,34-D4,36-D4,50-C2	PCI3 CBE*(2)	27-C2,32-C4,33-B4,34-B4, 36-B4,50-C2
PCI3 AD(25)	27-D2,30-B4,32-D4, 33-D4,34-D4,36-D4,50-C2	PCI3 CBE*(0)	27-C2,32-B2,33-B4,34-B4, 36-B4,50-C2
PCI3 AD(7)	27-D2,32-D4,33-D4, 34-D4,36-D4,50-C2	PCI3 CBE*(1)	27-C2,32-B4,33-B4,34-B4, 36-B4,50-C2
PCI3 AD(5)	27-D2,32-D4,33-D4, 34-D4,36-D4,50-C2	PCI3 CBE*(3)	27-C2,32-C4,33-B4,34-B4, 36-B4,50-C2
PCI3 AD(13)	27-D2,32-D4,33-D4, 34-D4,36-D4,50-D2	PCI3 CBE*(3:0)	27-C2,34-B4,36-B4
PCI3 AD(23)	27-D2,32-D4,33-D4, 34-D4,36-D4,50-D2	PCI3 CLKRUN*	27-B2,27-C1,32-B4,33-B2, 34-C4,36-B4,38-A4,42-B2, 50-C2
PCI3 AD(11)	27-D2,32-D4,33-D4, 34-D4,36-D4,50-D2	PCI3 DEVSEL *	27-C1,27-C2,32-B2,33-B2, 34-B4,36-B4,50-C2
PCI3 AD(1)	27-D2,32-D4,33-D4, 34-D4,36-D4,50-D2	PCI3 FRAME*	27-C2,27-D1,32-B2,33-B2, 34-B4,36-B4,50-C2
PCI3 AD(3)	27-D2,32-D4,33-D4, 34-D4,36-D4,50-C2	PCI3 GNT0*	27-B2,27-D1,32-C2,50-C2
PCI3 AD(0)	27-D2,32-D4,33-D4, 34-D4,36-D4,50-D2	PCI3 GNT1*	27-B2,27-D1,34-B4,50-C2
PCI3 AD(14)	27-D2,32-D4,33-D4, 34-D4,36-D4,50-D2	PCI3 GNT2*	27-B2,27-D1,36-B4,50-C2
PCI3 AD(9)	27-D2,32-D4,33-D4, 34-D4,36-D4,50-C2	PCI3 GNT3*	27-B2,33-B2
PCI3 AD(24)	27-D2,32-D4,33-D4, 34-D4,36-D4,50-C2	PCI3 INTB*	27-B4,27-D1,33-B2,34-B2, 50-C2
		PCI3 INTC*	27-B4,27-D1,32-C2,33-B2, 36-B4
		PCI3 INTD*	27-B4,27-D1,33-B2,50-C2
		PCI3 IRDY*	27-B2,27-C1,32-B4,33-B2, 34-B4,36-B4,50-B2

Signal Name	Sheet Number - Location	Signal Name	Sheet Number - Location
PCI3 PAR	27-B2,32-B2,33-B4,34-B4, 36-B4,50-B2	USB3 P1*	33-B4,33-C2,33-D2,40-C4
PCI3 PERR*	27-B2,27-C1,32-B4,33-B2, 34-B4,36-B4,50-B2	USB3 P2	33-C2,33-D2,40-B4
PCI3 REQ0*	27-B2,27-D1,32-C4,50-B2	USB3 P2*	33-B2,33-B4,33-D2,40-B4
PCI3 REQ1*	27-B2,27-D1,34-B4,50-B2	USB3 P3	33-C2,33-D2,40-B2
PCI3 REQ2*	27-B2,27-D1,36-B4,50-B2	USB3 P3*	33-B2,33-B4,33-D2,40-B2
PCI3 REQ3*	27-B2,27-D1,33-B2,50-B2	USB3 P4	33-C2,33-D2
PCI3 REQ4*	27-B2,27-D1,50-B2	USB3 P4*	33-B2,33-B4,33-D2
PCI3 ROMCS*	27-B2,27-C1,50-B2	USB3 SMI	28-C2,28-D1,33-B2,50-C3
PCI3 RST*	27-B1,28-B2,32-C2,33-B2, 34-C4,36-B4,38-B3	VBUSPWR	34-C2,34-D1
PCI3 RSTF*	27-B2,27-D2,50-B2	VDDC CNTLO	14-C1,47-A2,47-A3,50-B2
PCI3 SERR*	27-B2,27-C1,32-B4,33-B2, 34-B4,36-B4,50-B2	VDDC CNTL1	14-C1,47-A1,47-B3,50-B2
PCI3 STOP*	27-B2,27-C1,32-B2,33-B2, 34-B4,36-B4,50-B2	VGA3 BKLTON	14-D1,21-B1,25-B2,50-A2
PCI3 TRDY*	27-B2,27-C1,32-B2,33-B2, 34-B4,36-B4,50-B2	VGA3 BLUE	15-C3,21-A1,26-C4,50-A2
S1+	46-B4,46-C4,46-D1,50-B2	VGA3 C	15-C2,21-B4,25-A4,50-A2
S1-	46-C4,46-D1,50-B2	VGA3 COMP	15-B2,21-B4,25-A4
S2+	46-B1,46-B4,46-B4,50-B2	VGA3 DDCC	15-B2,21-A1,26-A4,50-A2
S2-	46-B1,46-B4,50-B2	VGA3 DDCD	15-B2,21-A1,26-B4,50-A2
SMB3 ALERT*	11-73,28-C1,28-D2,50-B2	VGA3 GREEN	15-C3,21-A1,26-C4,50-A2
SMB3 CLK	7-D4,18-B3,18-B4,23-B1, 28-C2,28-D1	VGA3 HDTVDET*	25-A4,42-C4
SMB3 DATA	7-D4,18-B3,18-B4,23-B1, 28-C2,28-D1	VGA3 HSYNC	15-C3,21-A1,26-C4,50-A2
SYSTEM S	44-C2,49-D4	VGA3 LCDVDDON	14-D1,21-B1,25-C3,50-A2
USB3 P0	33-C2,33-D2,40-C2	VGA3 RED	15-C3,21-A1,26-D4,50-A2
USB3 P0*	33-B4,33-C2,33-D2,40-C2	VGA3 SSIN	21-B4,23-D1
USB3 P1	33-C2,33-D2,40-C4	VGA3 VSYNC	15-C3,21-A1,26-B4,50-A2
		VGA3 Y	15-C2,21-B4,25-B4,50-A2
		VOA-	46-B2,46-C4
		VOS-	46-C1,46-C4,50-A2
		VTPWRGD	42-B1,46-D1,47-B2,47-D2, 50-A2
		XTAL IN	21-B4,23-D3
		XTAL OUT	21-B4,23-D3

## 8-2 SIO SUB BOARD

### 8-2-1 Schematic Diagrams

Table of Contents

- 1. Cover --(1)
- 2. B'd to B'd CONN, Super I/O --(2)
- 3. Serial Port --(3)
- 4. Parallel Port/Test point --(4)

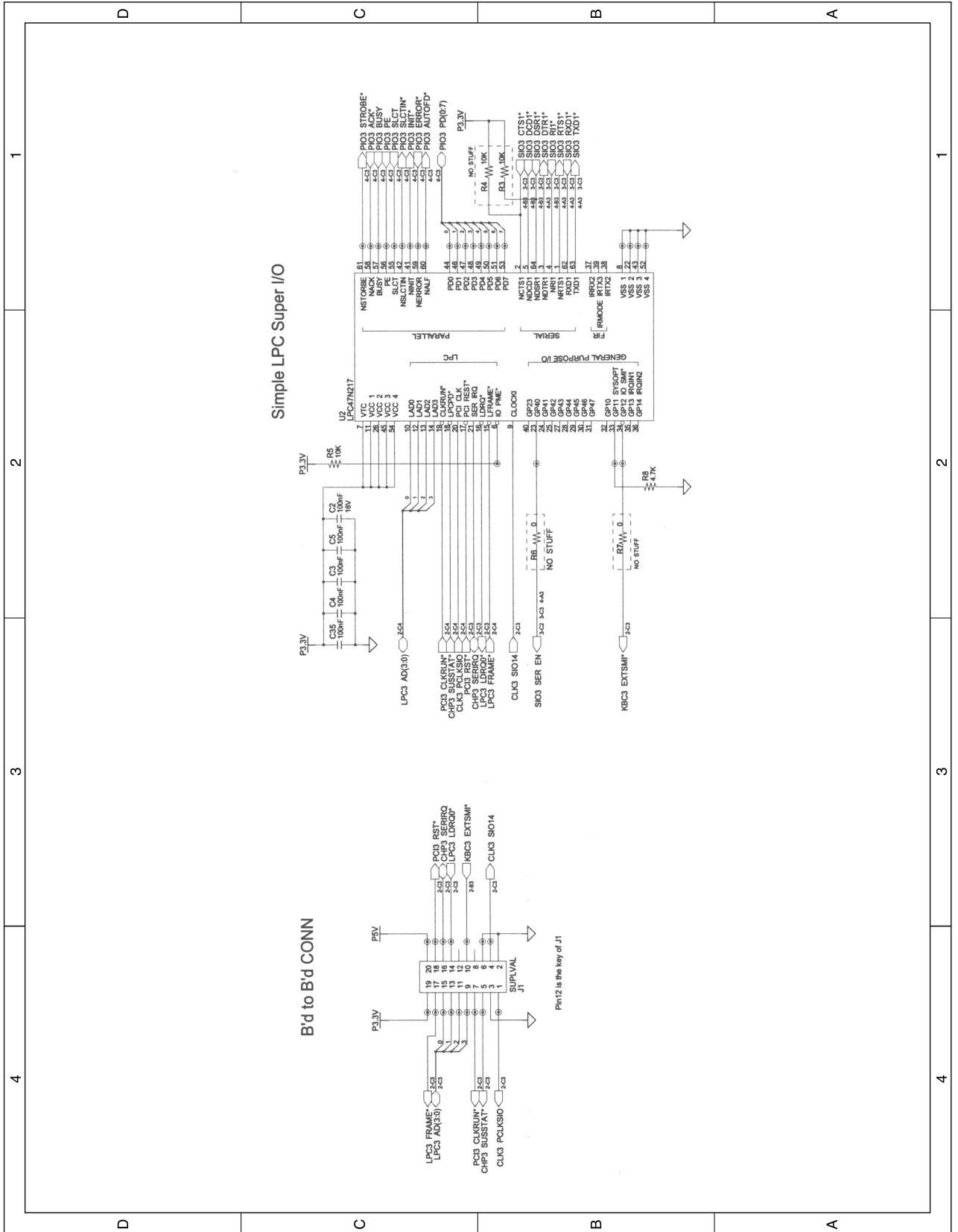
# Oscar-G

CPU : Intel P-M  
 Chip Set : ATI RC300MD + IXP150  
 Remarks : Sub system for Super I/O

Model Name : SIO sub Board  
 PBA Name :  
 PCB Code : BA41-00477A  
 Dev. Step : MP  
 Revision : 1.0  
 T.R. Date : 2004-12-09

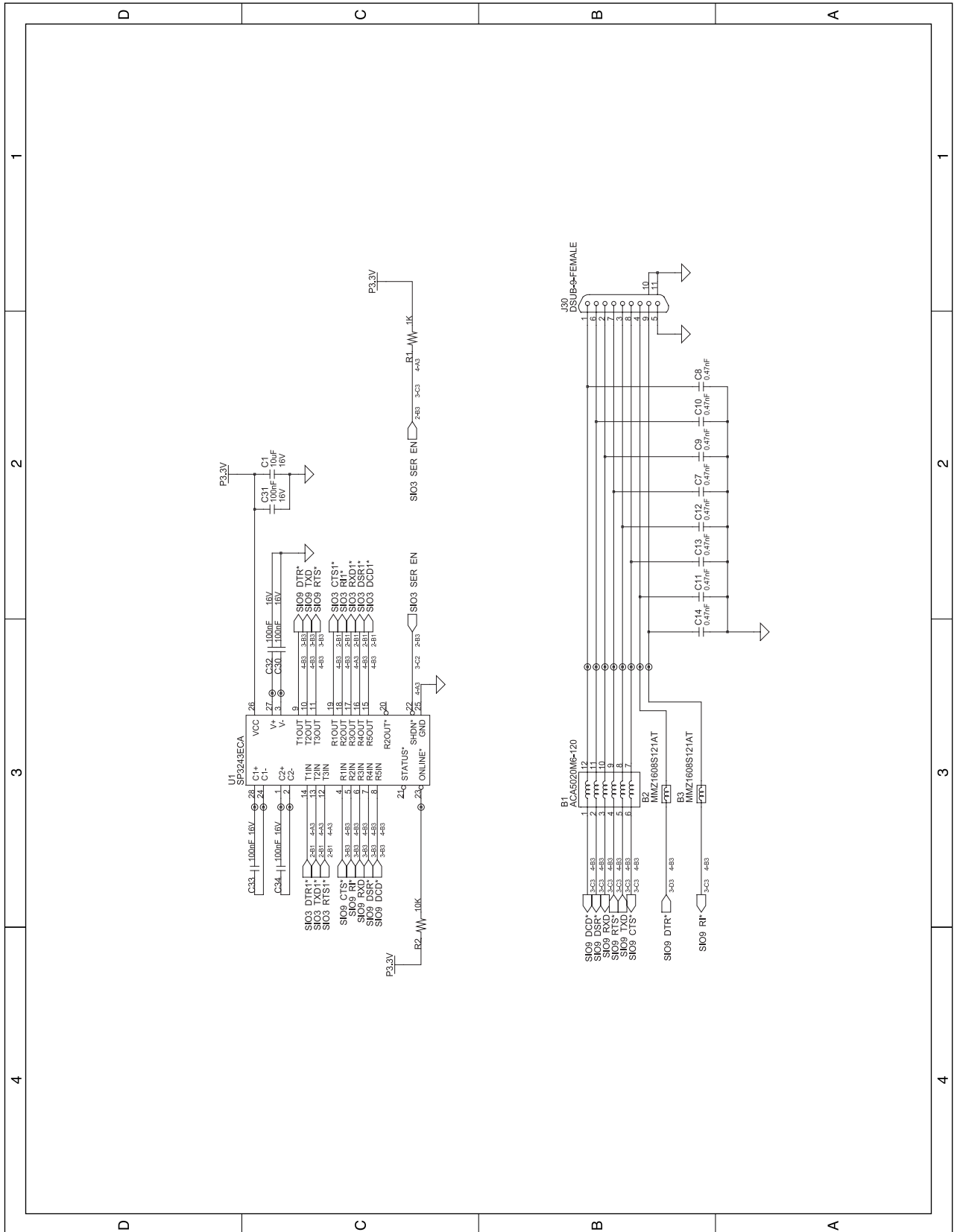
DRAW	CHECK	APPROVAL

8-2-1(a) SIO SUB Board Schematic Sheet 2 of 4(B'd to B'd CONN, Super I/O)

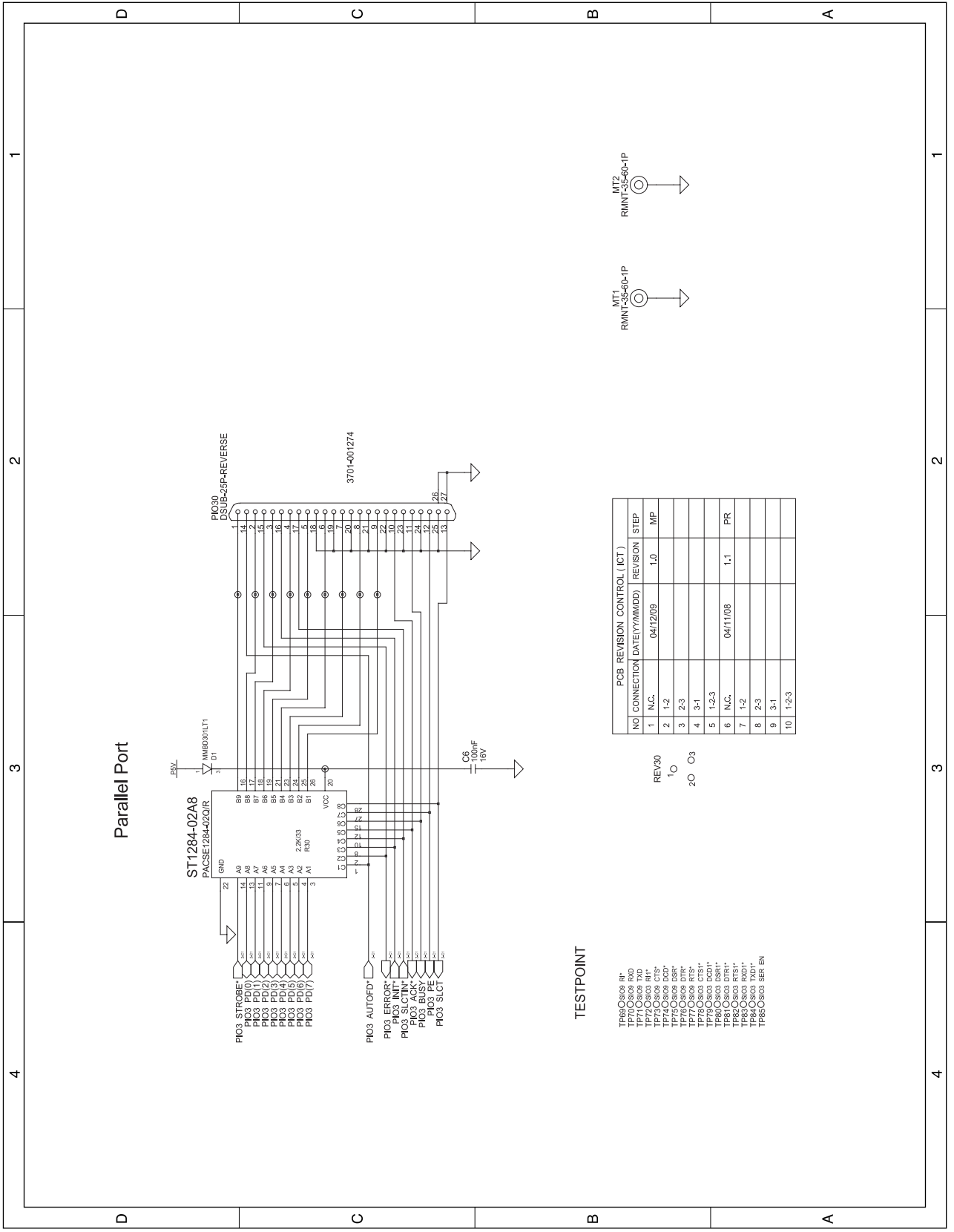




8-2-1(b) SIO SUB Board Schematic Sheet 3 of 4(Serial Port)



8-2-1(c) SIO SUB Board Schematic Sheet 4 of 4(Parallel Port / Test Point)



## 8-2-2 Signal Location

Signal Name	Sheet Number - Location
CHP3 SERIRQ	2-C3,2-C3
CHP3 SUSSTAT*	2-C3,2-C4
CLK3 PCLKSIO	2-C3,2-C4
CLK3 SIO14	2-C3,2-C3
KBC3 EXTSMI*	2-B3,2-C3
LPC3 AD(1)	2-C3,2-C4
LPC3 AD(2)	2-C3,2-C4
LPC3 AD(0)	2-C3,2-C4
LPC3 AD(3)	2-C3,2-C4
LPC3 AD(3:0)	2-C3,2-C4
LPC3 FRAME*	2-C3,2-C4
LPC3 LDRQ0*	2-C3,2-C3
PCI3 CLKRUN*	2-C3,2-C4
PCI3 RST*	2-C3,2-C3
PIO3 ACK*	2-C1,4-C3
PIO3 AUTOFD*	2-C1,4-C3
PIO3 BUSY	2-C1,4-C3
PIO3 ERROR*	2-C1,4-C3
PIO3 INIT*	2-C1,4-C3
PIO3 PD(7)	2-C1,4-C3
PIO3 PD(3)	2-C1,4-C3
PIO3 PD(5)	2-C1,4-C3
PIO3 PD(4)	2-C1,4-C3
PIO3 PD(0)	2-C1,4-C3
PIO3 PD(1)	2-C1,4-C3

Signal Name	Sheet Number - Location
PIO3 PD(2)	2-C1,4-C3
PIO3 PD(6)	2-C1,4-C3
PIO3 PD(0:7)	2-C1
PIO3 PE	2-C1,4-C3
PIO3 SLCT	2-C1,4-C3
PIO3 SLCTIN*	2-C1,4-C3
PIO3 STROBE*	2-C1,4-C3
SIO3 CTS1*	2-B1,3-C3,4-B3
SIO3 DCD1*	2-B1,3-C3,4-B3
SIO3 DSR1*	2-B1,3-C3,4-B3
SIO3 DTR1*	2-B1,3-C3,4-A3
SIO3 RI1*	2-B1,3-C3,4-B3
SIO3 RTS1*	2-B1,3-C3,4-A3
SIO3 RXD1*	2-B1,3-C3,4-A3
SIO3 SER EN	2-B3,3-C2,3-C3,4-A3
SIO3 TXD1*	2-B1,3-C3,4-A3
SIO9 CTS*	3-B3,3-C3,4-B3
SIO9 DCD*	3-B3,3-C3,4-B3
SIO9 DSR*	3-B3,3-C3,4-B3
SIO9 DTR*	3-B3,3-D3,4-B3
SIO9 RI*	3-B3,3-C3,4-B3
SIO9 RTS*	3-B3,3-C3,4-B3
SIO9 RXD	3-B3,3-C3,4-B3
SIO9 TXD	3-B3,3-C3,4-B3