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# TORINO 2

PCB Thickness:1mm

CPU : INTEL MEROM  
 Chip Set : INTEL 965GM & ICH8-M  
 Remarks : w/o INTEL AMT  
 2 SODIMMs

Model Name : TORINO 2  
 PBA Name : MAIN  
 PCB Code : BA41-00727A / 728A  
 Dev. Step : MP  
 Revision : 1.0  
 T.R. Date : 2007.03.09

DRAW	CHECK	APPROVAL
-	-	-

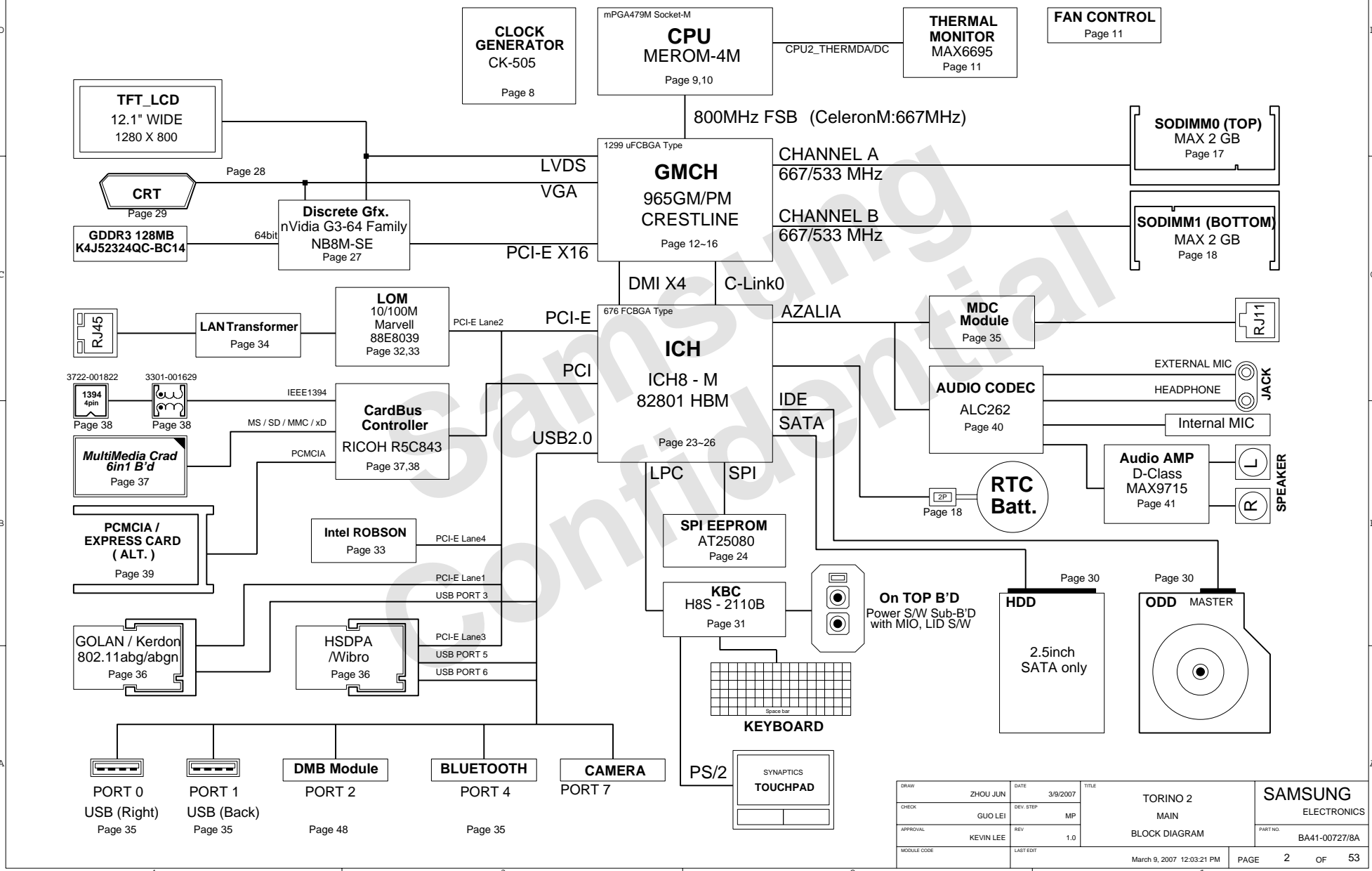
## Table of Contents

Page. 1	---	COVER	
Page. 2	---	OPERATION BLOCK DIAGRAM	] DIAGRAM
Page. 3	---	POWER DIAGRAM	
Page. 4	---	POWER SEQUENCE	
Page. 5	---	POWER RAIL	
Page. 6	---	CLOCK DISTRIBUTION	
Page. 7	---	BOARD INFORMATION	
Page. 8	---	CLOCK GENERATOR (CK-505)	
Page. 9~10	---	MEROM	
Page. 11	---	THERMAL MONITOR	
Page. 12~16	---	CRESTLINE (965GM)	
Page. 17	---	DDR2 SODIMM (TOP)	
Page. 18	---	DDR2 SODIMM (BOTTOM)	
Page. 19	---	DDR2 TERMINATION	
Page. 20~23	---	DISCRETE GFX (NB8M-SE & GDDR3)	
Page. 24~27	---	ICH8-M	
Page. 28	---	LCD (LVDS) CONN.	
Page. 29	---	CRT CONN.	
Page. 30	---	HDD & ODD CONNECTOR	
Page. 31	---	MICOM	
Page. 32	---	LAN CONTROLLER (10/100M)	
Page. 33	---	ROBSON	
Page. 34	---	USB PORT, MDC CONN. & BLUETOOTH	
Page. 35	---	MINI CARD SOCKET (WLAN/HSDPA/WIBRO)	
Page. 36	---	CARDBUS CONTROLLER(1) & 4IN1	
Page. 37	---	CARDBUS CONTROLLER(2) & 1394(4P)	
Page. 38	---	PCMCIA SOCKET	
Page. 39	---	AUDIO CODEC	
Page. 40	---	AUDIO AMP	
Page. 41	---	AUDIO JACK	
Page. 42	---	CHARGER	
Page. 43	---	P3.3V_AUX & P5V_AUX	
Page. 44	---	DDR2 POWER	
Page. 45	---	CPU VRM POWER	
Page. 46	---	P1.25V & P1.05V POWER	
Page. 47	---	GFX CORE & P1.5V POWER	
Page. 48	---	SWITCHED POWER	
Page. 49	---	POWER S/W, DMB, DEBUG & KEYBOARD CONN.	
Page. 50	---	LEDS & TOUCHPAD	
Page. 51	---	MOUNT HOLE	
Page. 52	---	TEST POINTS	
Page. 53	---	REVISION HISTORY	

DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	MP		COVER	
APPROVAL	KEVIN LEE	REV	1.0		CONTENTS	PART NO. BA41-00727/8A
MODULE CODE		LAST ENT		March 9, 2007 12:03:21 PM	PAGE	1 OF 53

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# OPERATION BLOCK DIAGRAM

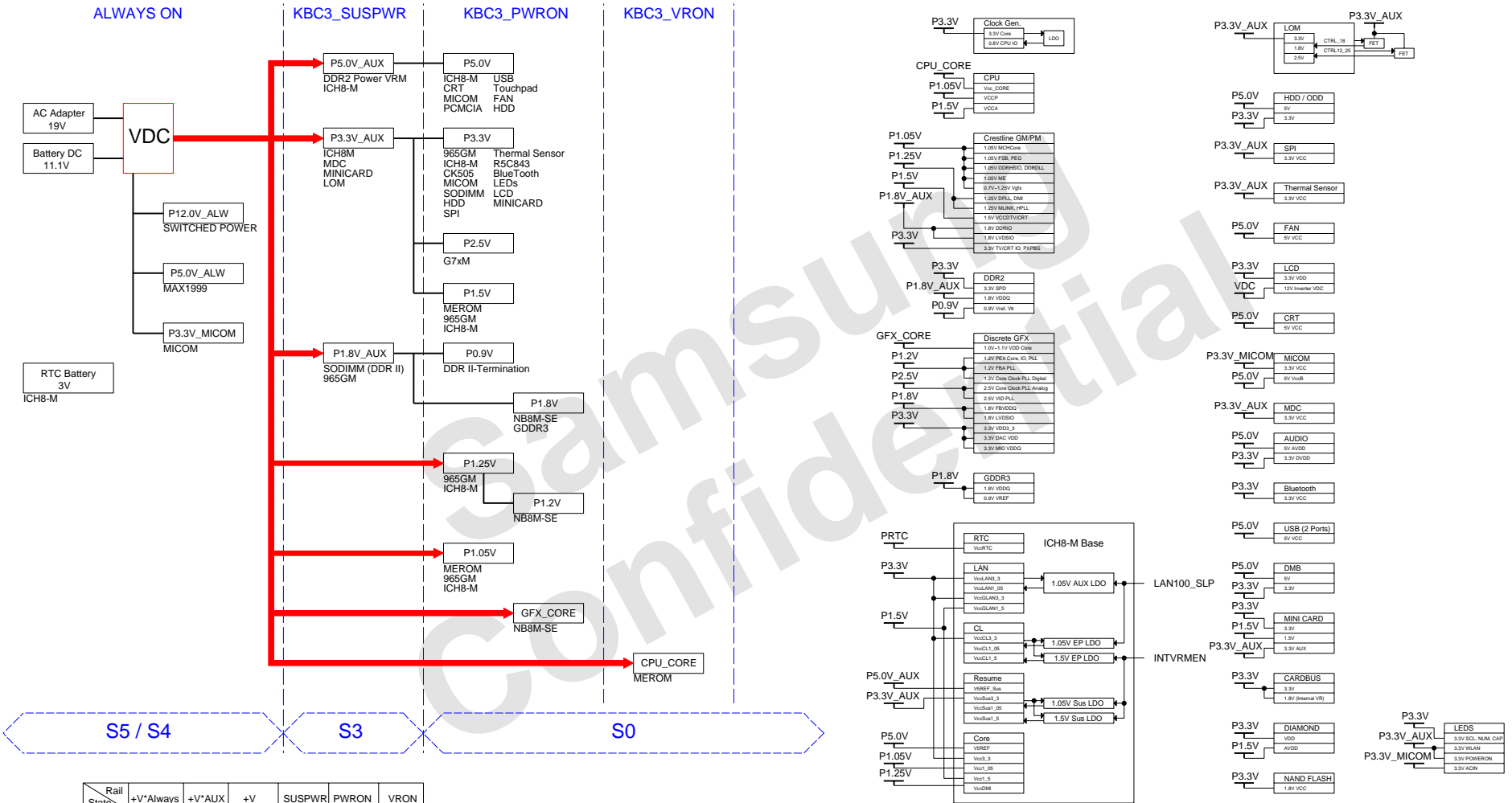


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APPROVAL	KEVIN LEE	REV	1.0		BLOCK DIAGRAM	PART NO. BA41-00727/8A
MODULE CODE		LAST ENT				
				March 9, 2007 12:03:21 PM	PAGE	2 OF 53

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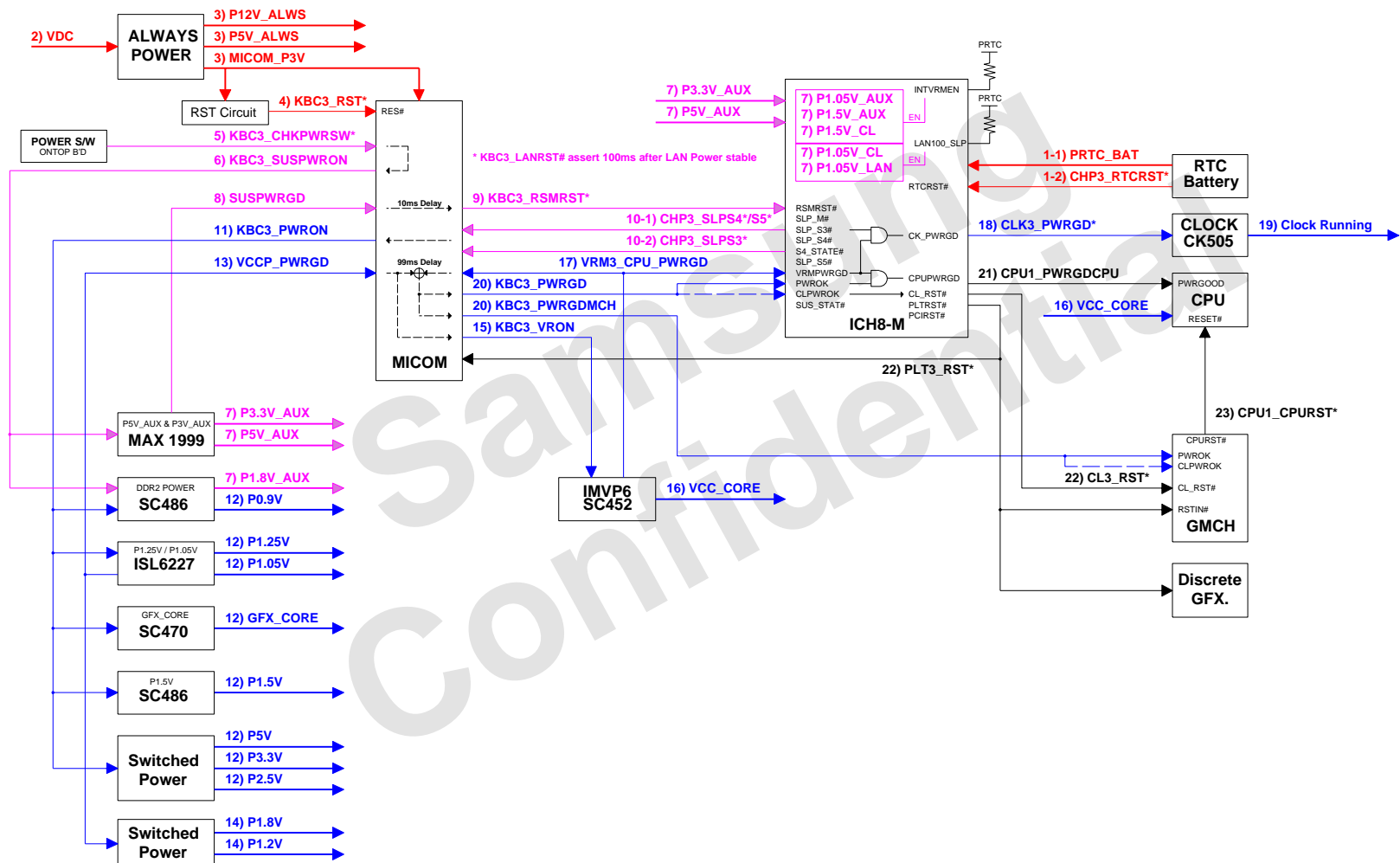
# POWER DIAGRAM



DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2 MAIN POWER DIAGRAM	<b>SAMSUNG</b> ELECTRONICS PART NO. BA41-00727/8A
CHECK	GUO LEI	DEV. STEP	MP			
APPROVAL	KEVIN LEE	REV	1.0			
MODULE CODE		LAST ENT				
				March 9, 2007 12:03:21 PM	PAGE 3 OF 53	

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# POWER SEQUENCE Rev. 0.1

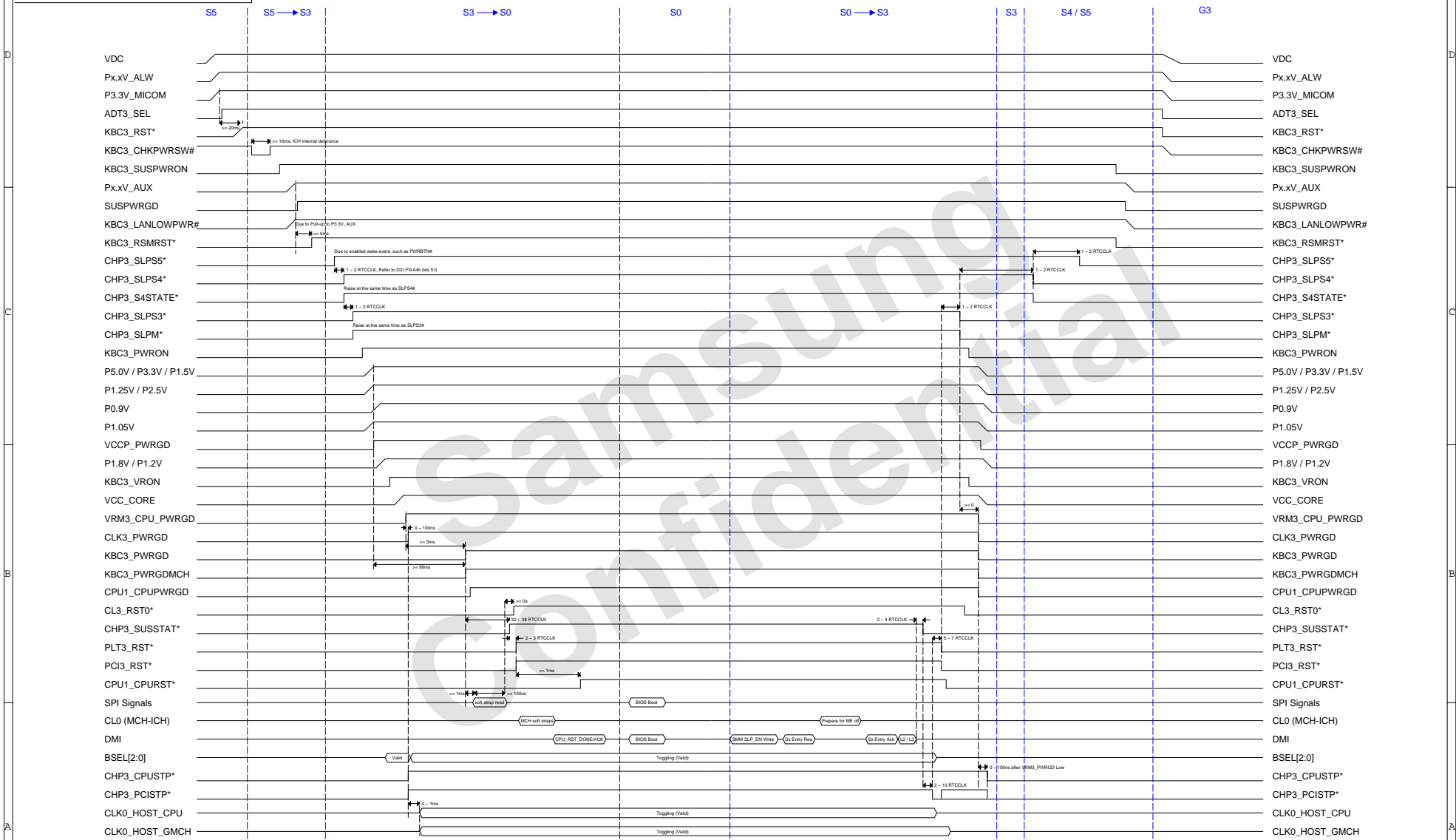


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CHECK	GUO LEI	DEV. STEP	MP			
APPROVAL	KEVIN LEE	REV	1.0			
MODULE CODE		LAST EDIT	March 9, 2007 12:03:21 PM	PAGE	4	OF 53

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**Timing Diagram, no ME**

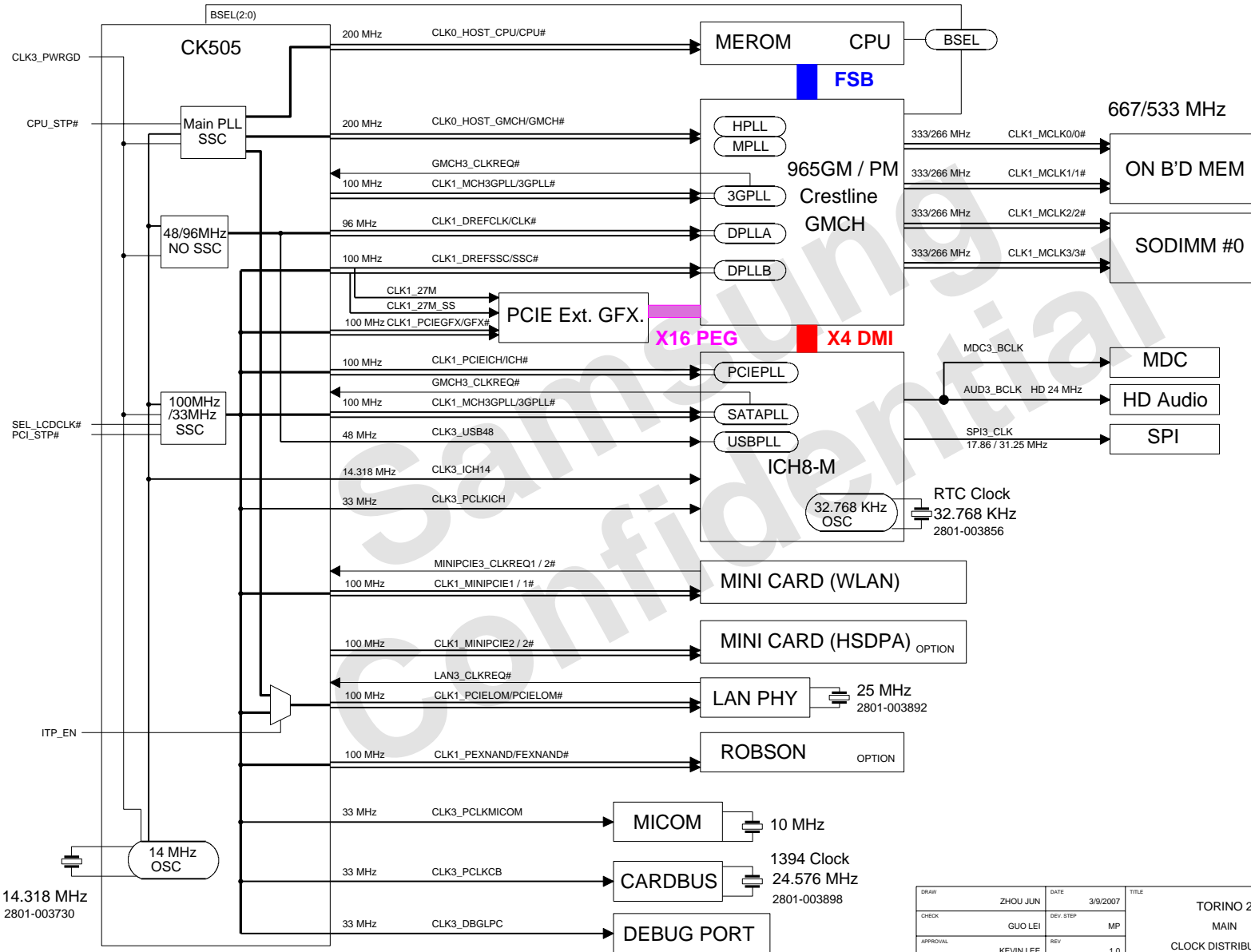
Rev. 0.2 Phil 2006-9-21



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CHECK	GUO LEI	DEV. STEP	MP		MAIN	
APPROVAL	KEVIN LEE	REV	1.0		TIMING DIAGRAM	
MODULE CODE		LAST ENT				
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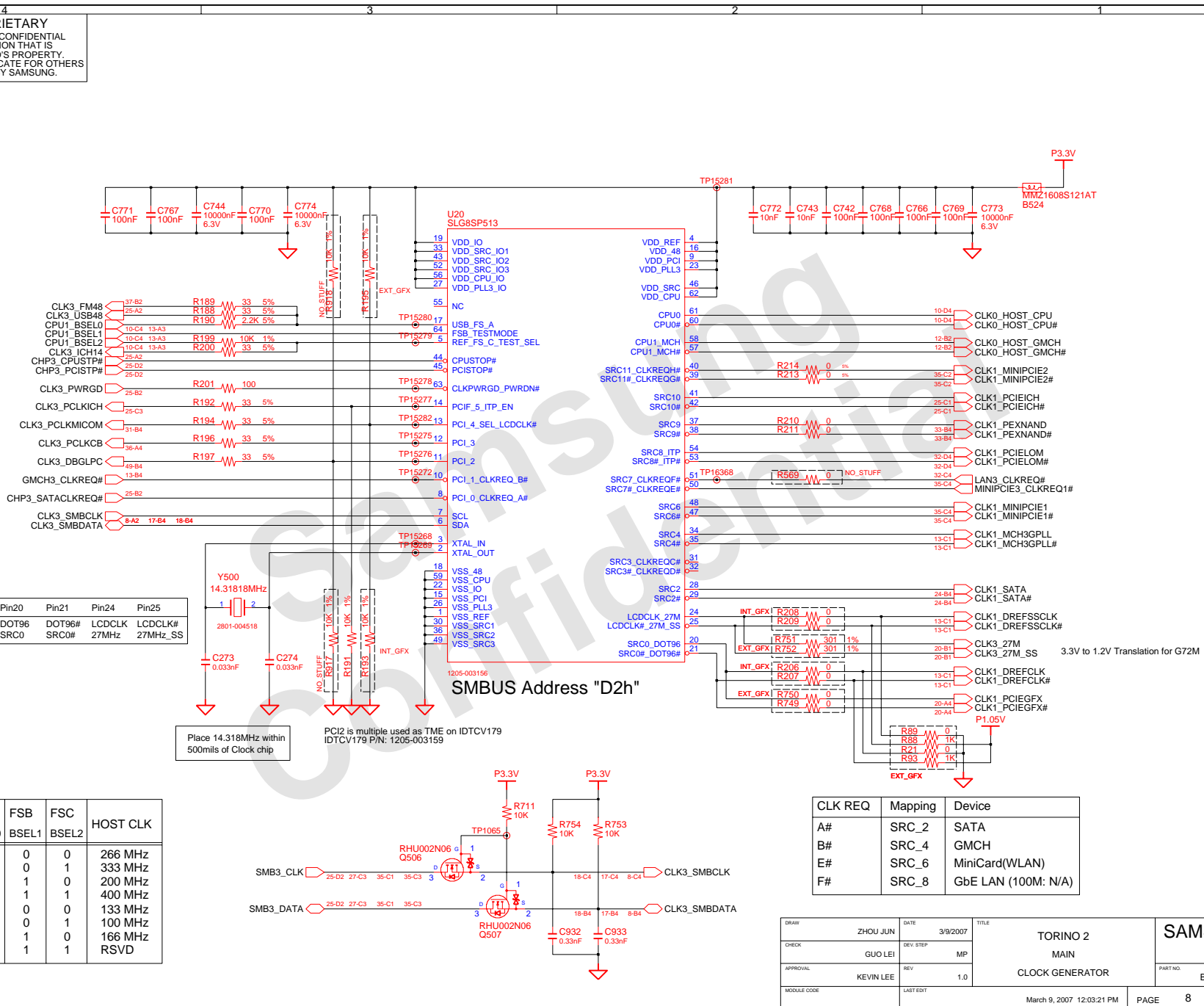
# CLOCK DISTRIBUTION Rev. 0.1



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CHECK	GUO LEI	DEV. STEP	MP	MAIN		
APPROVAL	KEVIN LEE	REV	1.0	CLOCK DISTRIBUTION	PART NO.	BA41-00727/8A
MODULE CODE		LAST EDIT		March 9, 2007 12:03:21 PM	PAGE	6 OF 53



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SEL_LCDCLK#	Pin20	Pin21	Pin24	Pin25
0	DOT96	DOT96#	LCDCLK	LCDCLK#
1	SRC0	SRC0#	27MHz	27MHz_SS

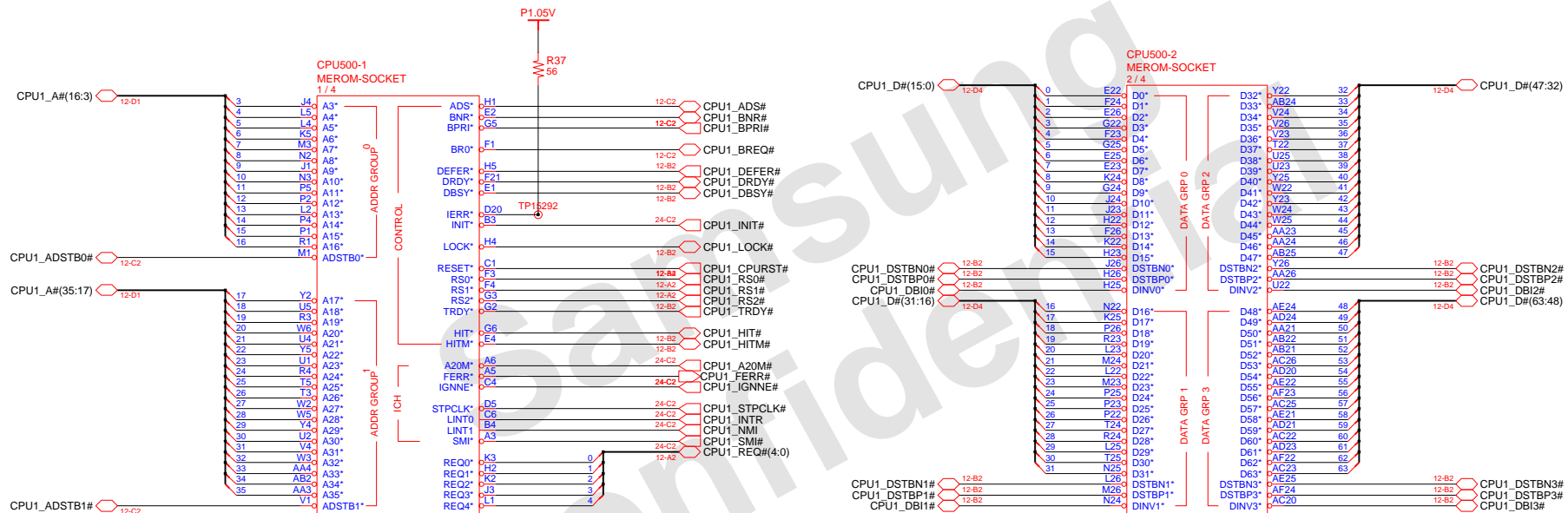
CPU	FSA	FSB	FSC	HOST CLK
	BSEL0	BSEL1	BSEL2	
0	0	0	0	266 MHz
0	0	0	1	333 MHz
0	1	0	0	200 MHz
0	1	1	1	400 MHz
1	0	0	0	133 MHz
1	0	1	1	100 MHz
1	1	0	1	166 MHz
1	1	1	1	RSVD

CLK REQ	Mapping	Device
A#	SRC_2	SATA
B#	SRC_4	GMCH
E#	SRC_6	MiniCard(WLAN)
F#	SRC_8	GbE LAN (100M: N/A)

DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS	
CHECK	GUO LEI	DEV_STEP	MP	MAIN			
APPROVAL	KEVIN LEE	REV	1.0	CLOCK GENERATOR		PART NO. BA41-00727/8A	
MODULE CODE		LAST EDIT					
March 9, 2007 12:03:21 PM						PAGE	8 OF 53



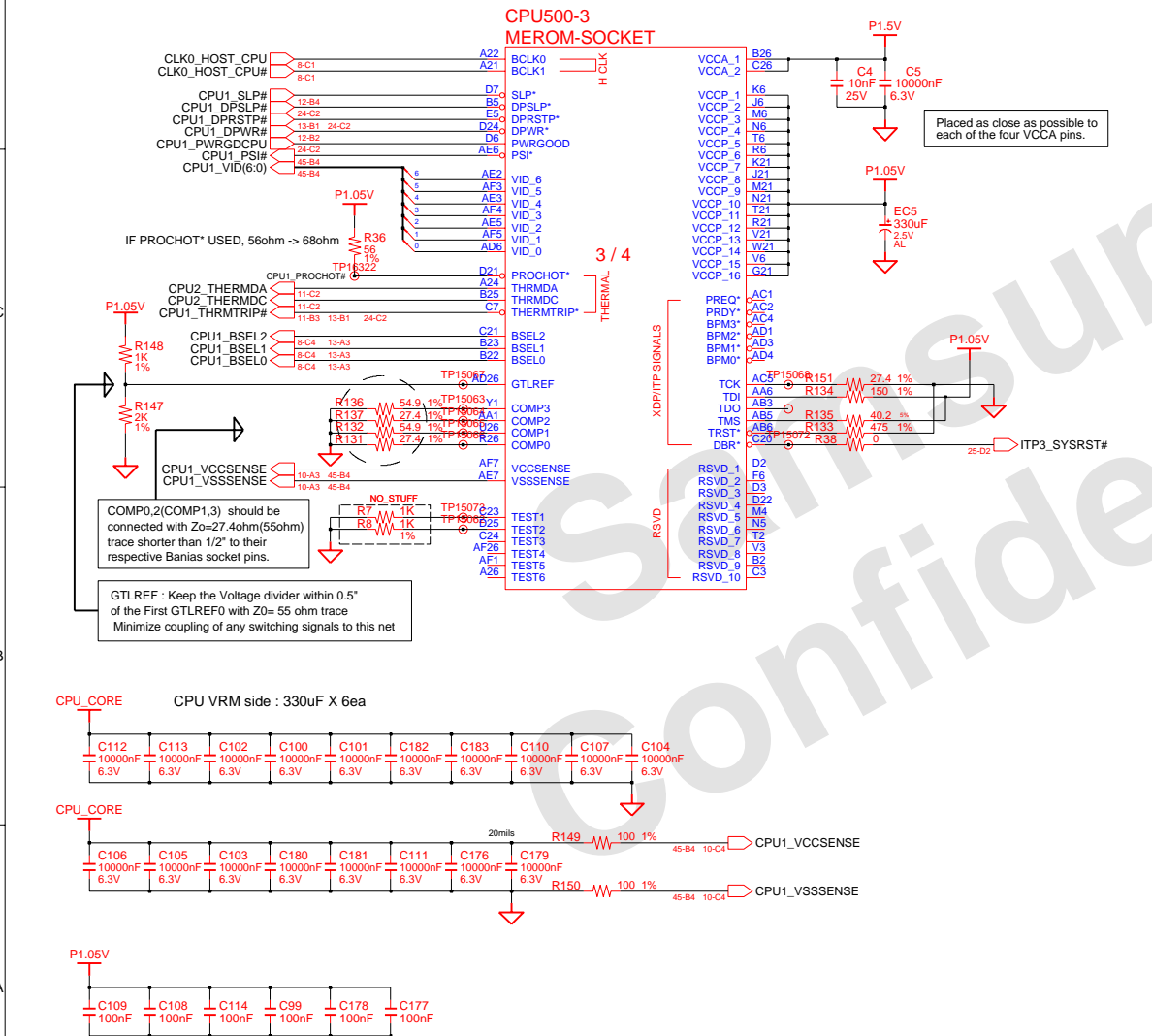
# CPU SOCKET : MEROM



DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2 MAIN MEROM(1)	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV STEP	MP			
APPROVAL	KEVIN LEE	REV	1.0			PART NO. BA41-00727/8A
MODULE CODE		LAST EDITION				March 9, 2007 12:03:21 PM
						PAGE 9 OF 53

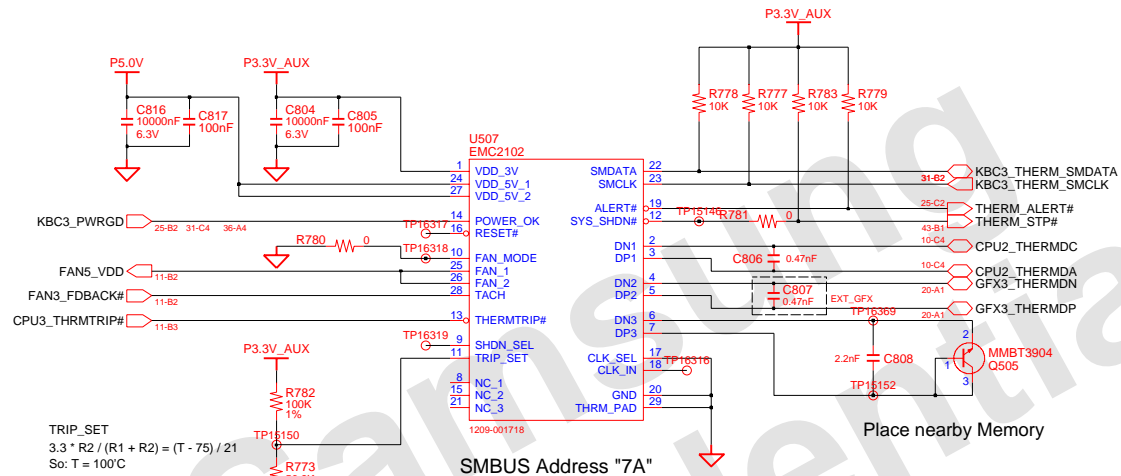
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# CPU SOCKET : MEROM

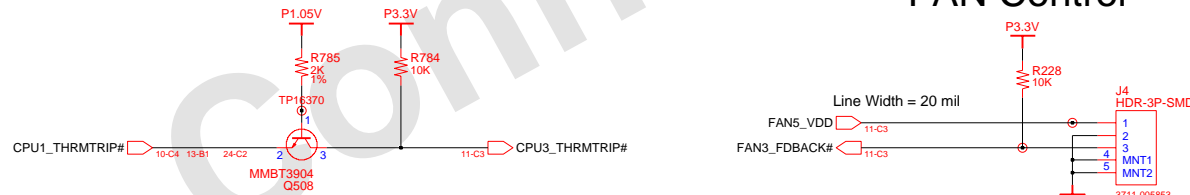


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# Thermal Monitor

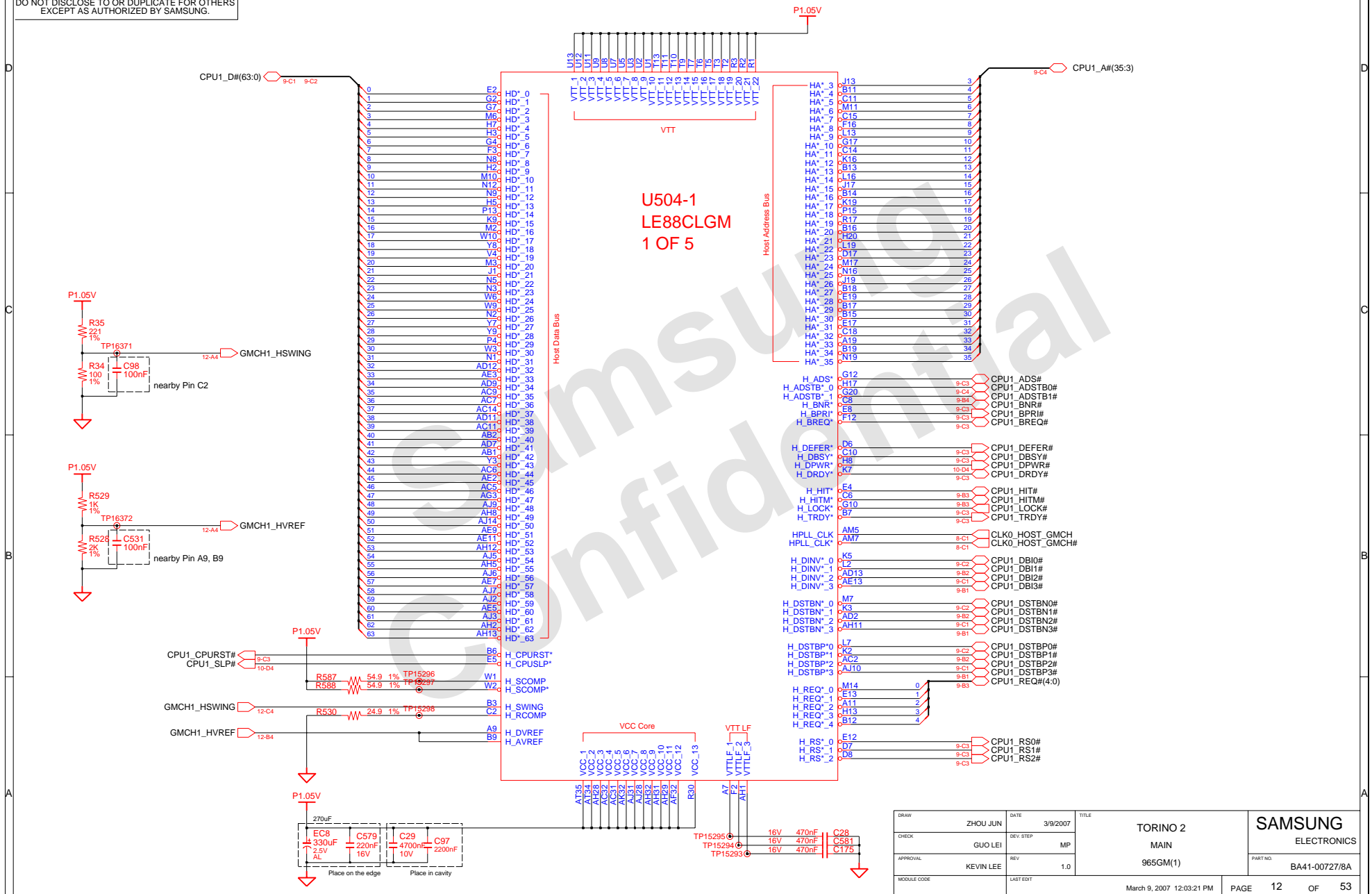


# FAN Control



DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2 MAIN	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	MP	THERMAL MONITOR	PART NO. BA41-00727/8A	
APPROVAL	KEVIN LEE	REV	1.0			
MODULE CODE		LAST EDIT	March 9, 2007 12:03:21 PM	PAGE	11	OF 53

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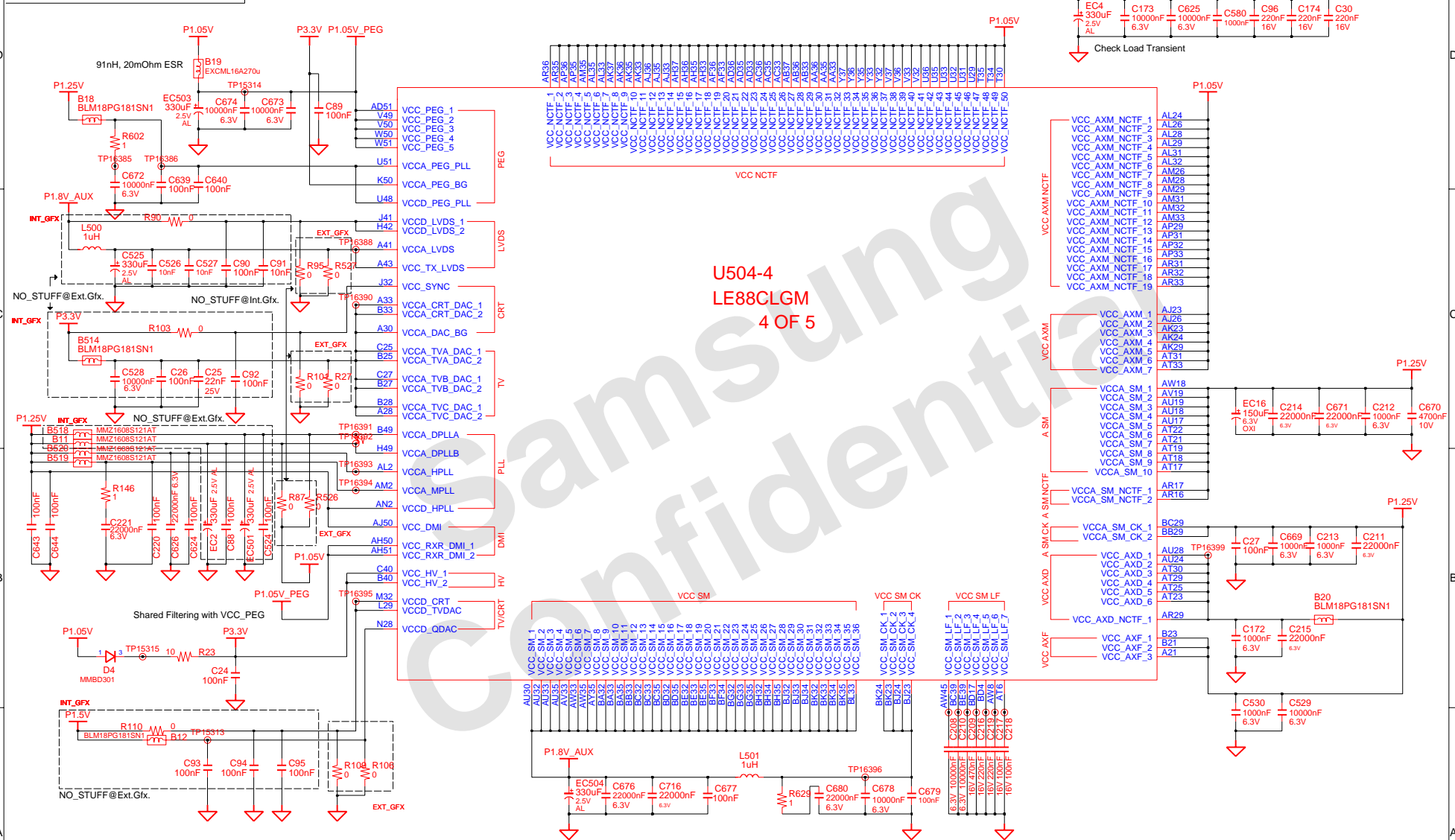


DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV STEP	MP	MAIN	965GM(1)	
APPROVAL	KEVIN LEE	REV	1.0			
MODULE CODE		LAST EDIT				PART NO. BA41-00727/8A
				March 9, 2007 12:03:21 PM	PAGE	12 OF 53



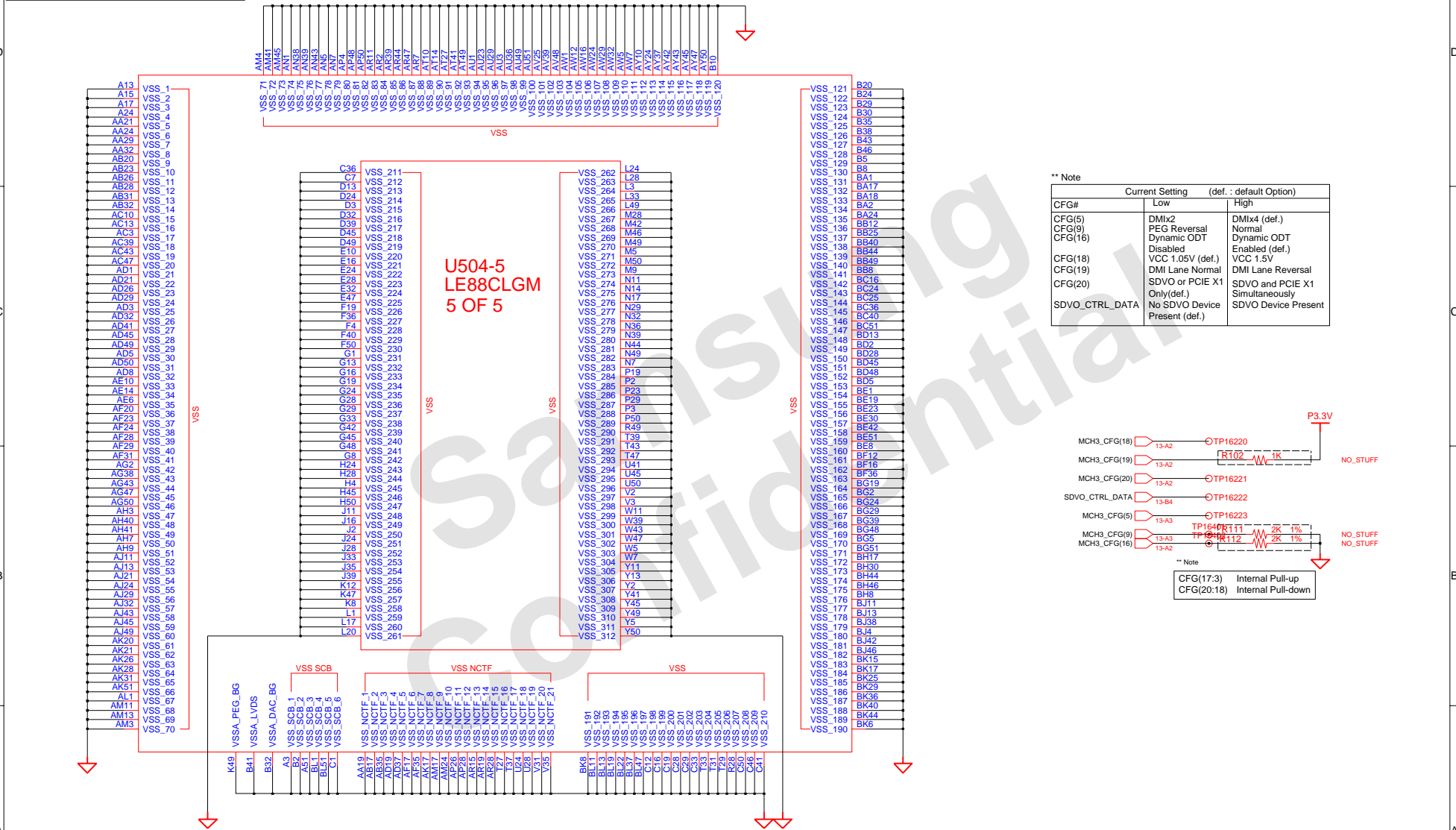


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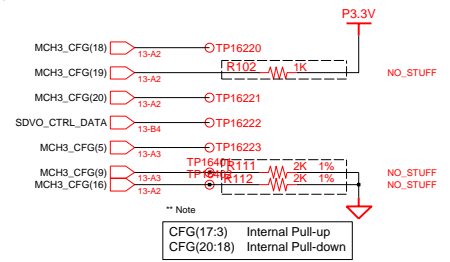


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**\*\* Note**

CFG#	Current Setting (def. : default Option)	
	Low	High
CFG(5)	DMix2	DMix4 (def.)
CFG(9)	PEG Reversal	Normal
CFG(16)	Dynamic ODT	Dynamic ODT
	Disabled	Enabled (def.)
CFG(18)	VCC 1.05V (def.)	VCC 1.5V
CFG(19)	DMI Lane Normal	DMI Lane Reversal
CFG(20)	SDVO or PCIE X1 Only(def.)	SDVO and PCIE X1 Simultaneously
SDVO_CTRL_DATA	No SDVO Device Present (def.)	SDVO Device Present

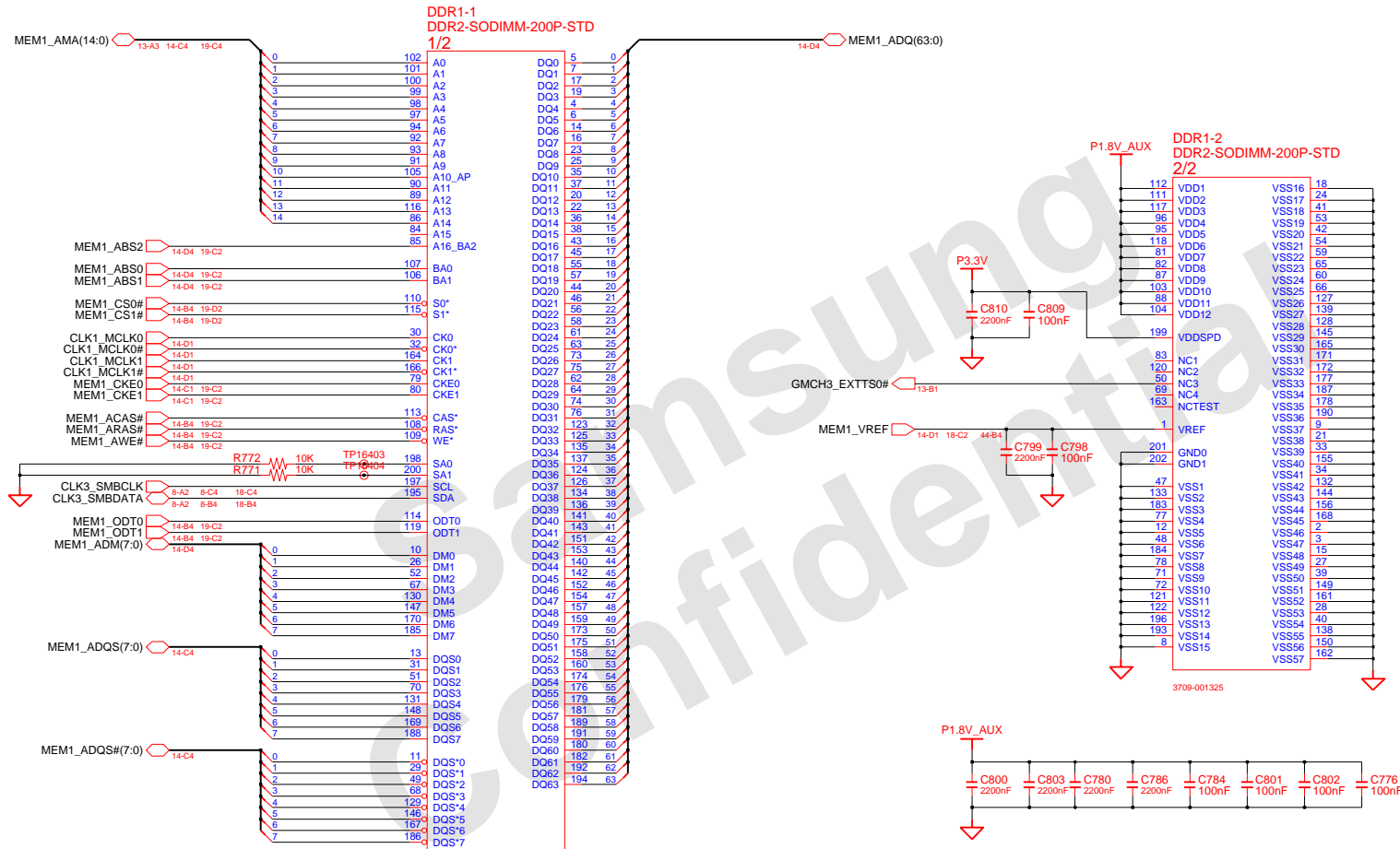


DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	<b>SAMSUNG</b> ELECTRONICS PART NO. BA41-00727/8A
CHECK	GUO LEI	DEV_STEP	MP		MAIN	
APPROVAL	KEVIN LEE	REV	1.0		965GM(5)	
MODULE CODE		LAST EDIT				
				March 9, 2007 12:03:21 PM	PAGE	16 OF 53



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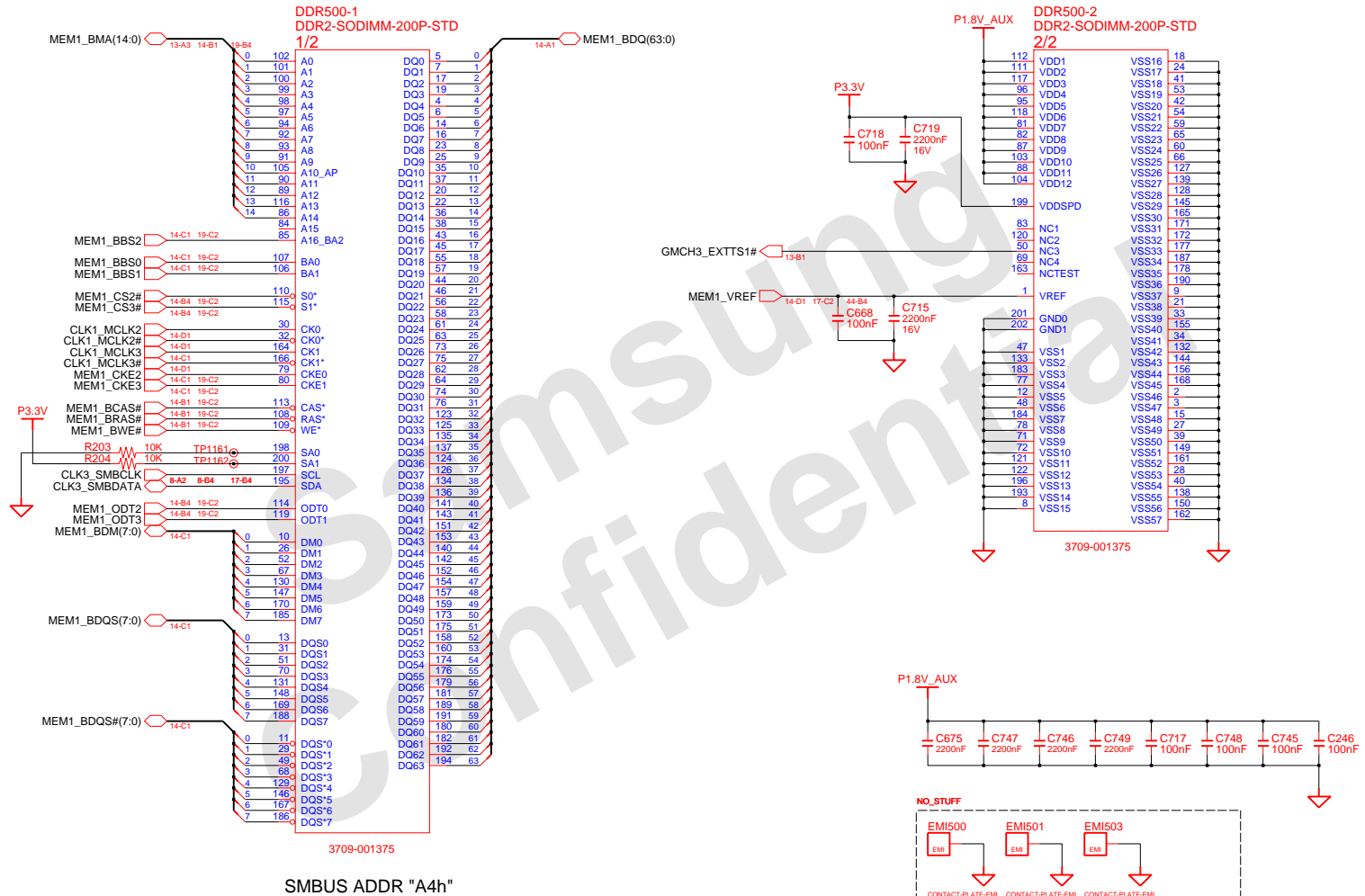
### SODIMM0 (TOP)



DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	MP	MAIN	MAIN	
APPROVAL	KEVIN LEE	REV	1.0	DDR2 SODIMM (TOP)		PART NO. BA41-00727/8A
MODULE CODE		LAST EDIT		March 9, 2007 12:03:21 PM	PAGE	17 OF 53

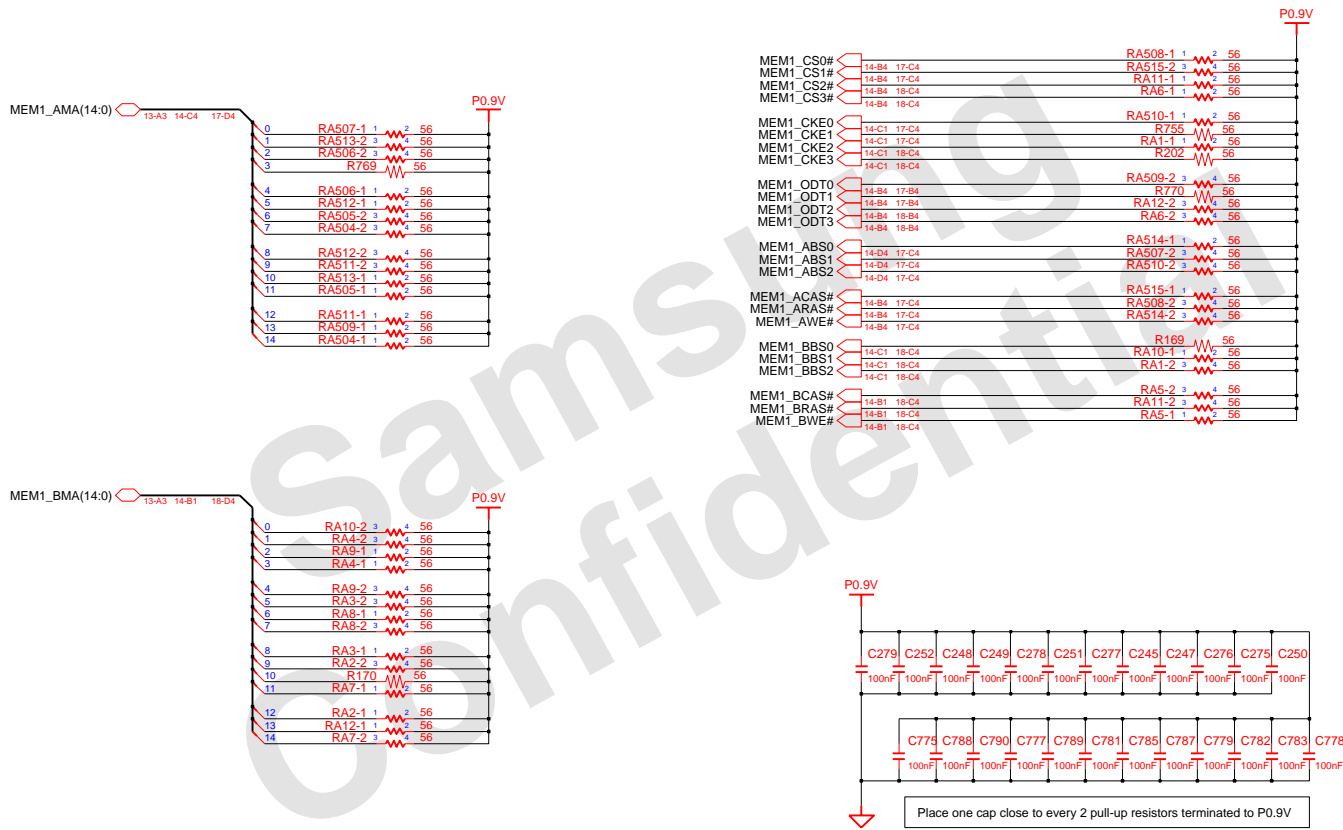
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**SODIMM1 (BOTTOM)**



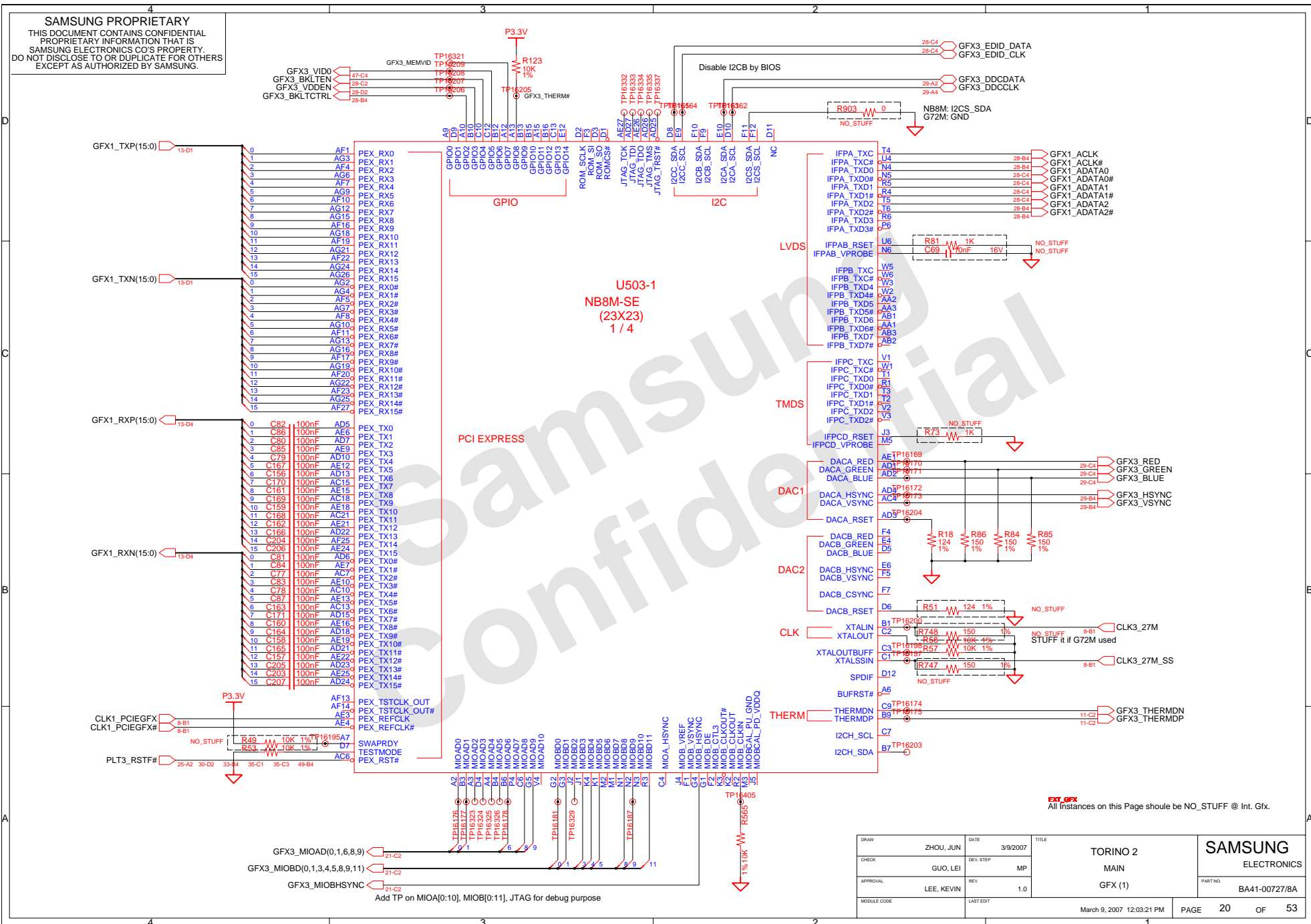
DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	MP		MAIN	
APPROVAL	KEVIN LEE	REV	1.0		DDR2 SODIMM (BOTTOM)	PART NO. BA41-00727/8A
MODULE CODE		LAST EDIT			March 9, 2007 12:03:21 PM	PAGE 18 OF 53

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CHECK	GUO LEI	DEV. STEP	MP	MAIN		
APPROVAL	KEVIN LEE	REV	1.0	DDR2 TERMINATION	PART NO.	BA41-00727/8A
MODULE CODE		LAST EDIT		March 9, 2007 12:03:21 PM	PAGE	19 OF 53

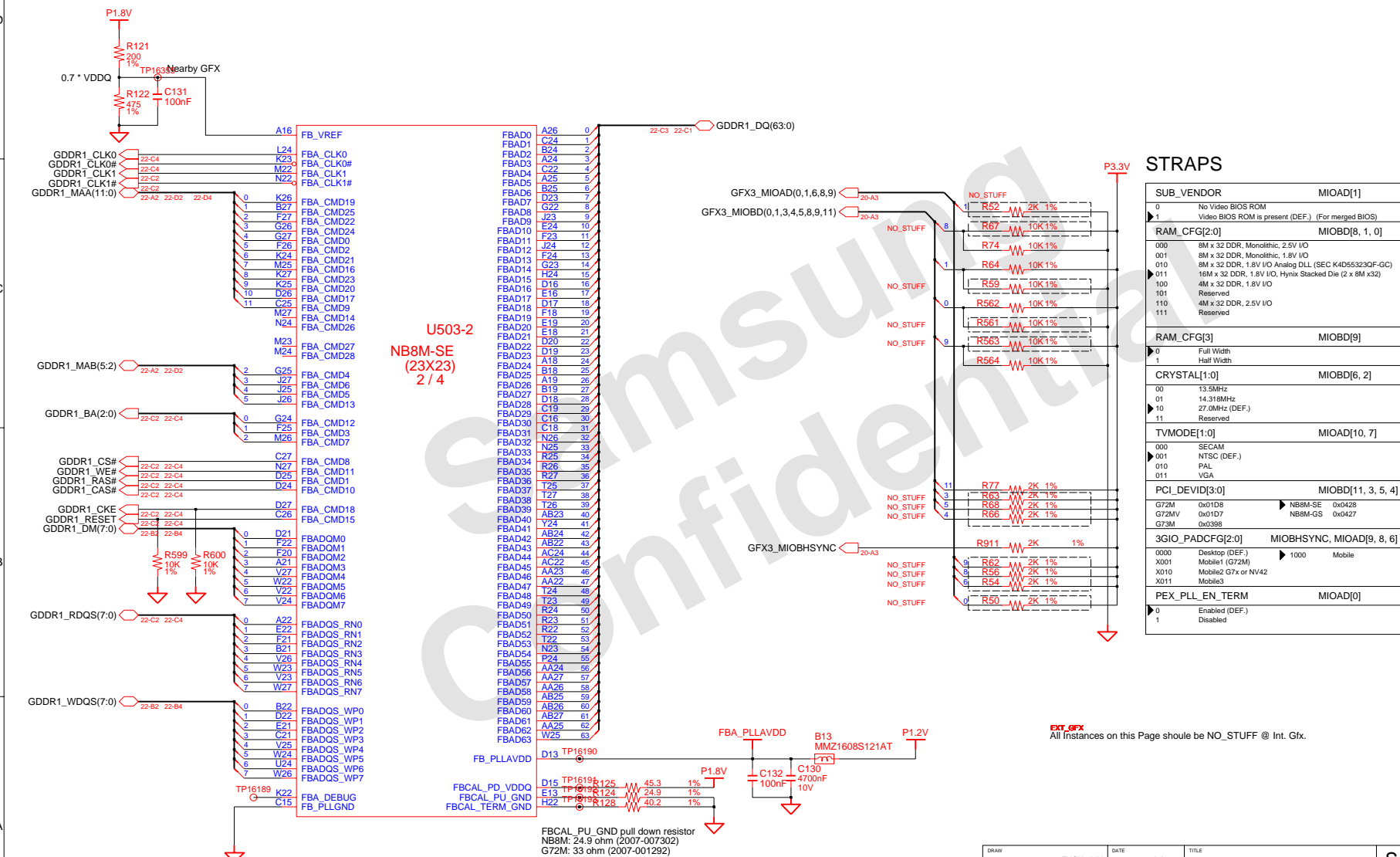
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**EXT\_GFX**  
 All instances on this Page should be NO\_STUFF @ Int. Gfx.

DRAW	ZHOU, JUN	DATE	3/9/2007	TITLE	TORINO 2	<b>SAMSUNG</b> ELECTRONICS PART NO. BA41-00727/8A
CHECK	GUO, LEI	DEV. STEP	MP	MAIN		
APPROVAL	LEE, KEVIN	REV	1.0	GFX (1)		
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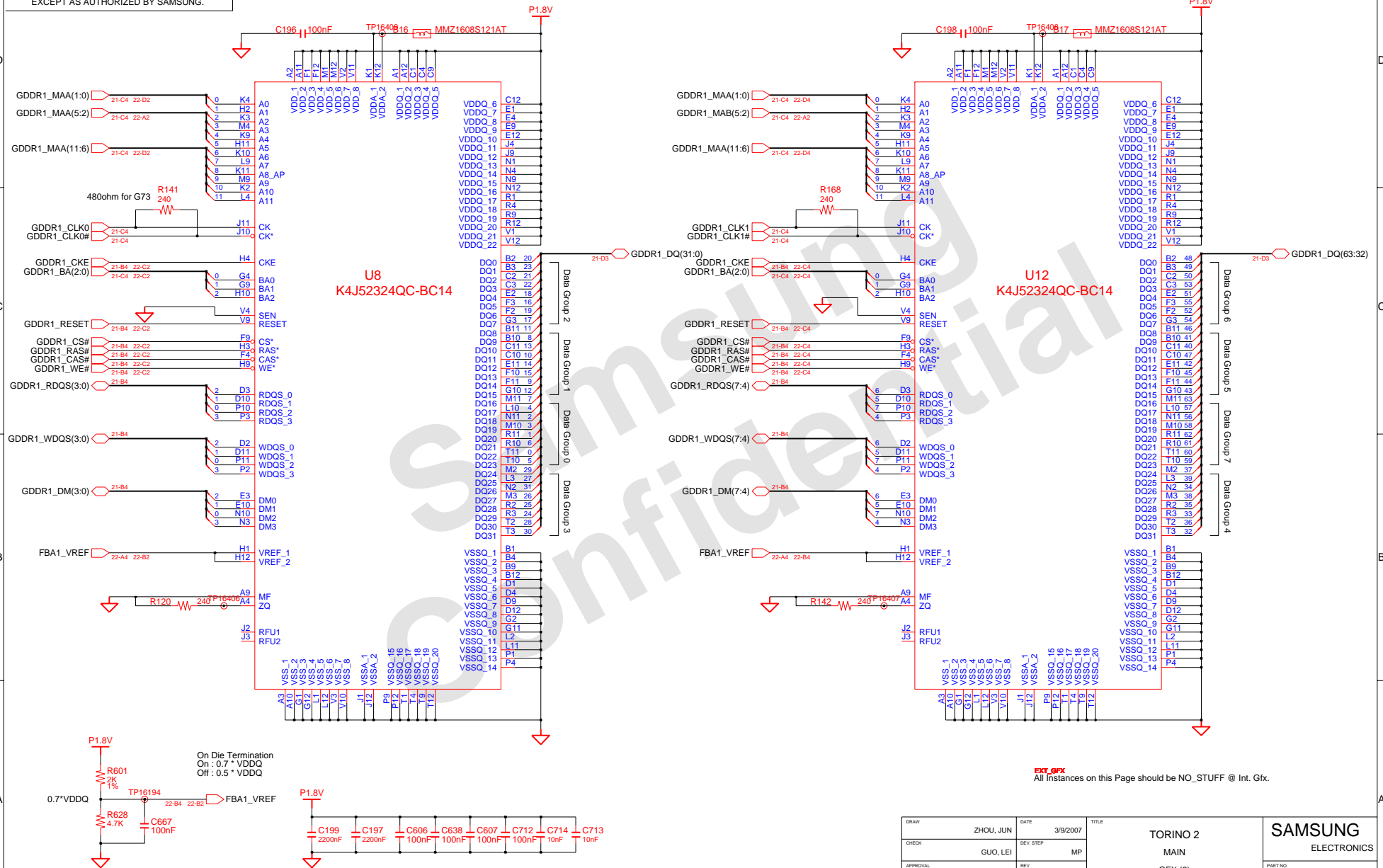
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SUB_VENDOR		MIOAD[1]	
0	No Video BIOS ROM		
1	Video BIOS ROM is present (DEF.)	(For merged BIOS)	
RAM_CFG[2:0]		MIOBD[8, 1, 0]	
000	8M x 32 DDR, Monolithic, 2.5V I/O		
001	8M x 32 DDR, Monolithic, 1.8V I/O		
010	8M x 32 DDR, 1.8V I/O Analog DLL (SEC K4D55320F-GC)		
011	16M x 32 DDR, 1.8V I/O, Hynix Stacked Die (2 x 8M x32)		
100	4M x 32 DDR, 1.8V I/O		
101	Reserved		
110	4M x 32 DDR, 2.5V I/O		
111	Reserved		
RAM_CFG[3]		MIOBD[9]	
0	Full Width		
1	Half Width		
CRYSTAL[1:0]		MIOBD[6, 2]	
00	13.5MHz		
01	14.318MHz		
10	27.0MHz (DEF.)		
11	Reserved		
TMODE[1:0]		MIOAD[10, 7]	
000	SECAM		
001	NTSC (DEF.)		
010	PAL		
011	VGA		
PCI_DEVID[3:0]		MIOBD[11, 3, 5, 4]	
G72M	0x01D8	NB8M-SE	0x0428
G72M	0x01D7	NB8M-GS	0x0427
G73M	0x0398		
3GIO_PADCFG[2:0]		MIOBHSYNC, MIOAD[9, 8, 6]	
0000	Desktop (DEF.)		
X001	Mobile1 (G72M)		
X010	Mobile2 G7x or NV42		
X011	Mobile3		
1000	Mobile		
PEX_PLL_EN_TERM		MIOAD[0]	
0	Enabled (DEF.)		
1	Disabled		

DRAW	ZHOU, JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO, LEI	DEV_STEP	MP	MAIN		
APPROVAL	LEE, KEVIN	REV	1.0	GFX (2)		PART NO. BA41-00727/8A
MODULE CODE		LAST ENT			March 9, 2007 12:03:21 PM	PAGE 21 OF 53

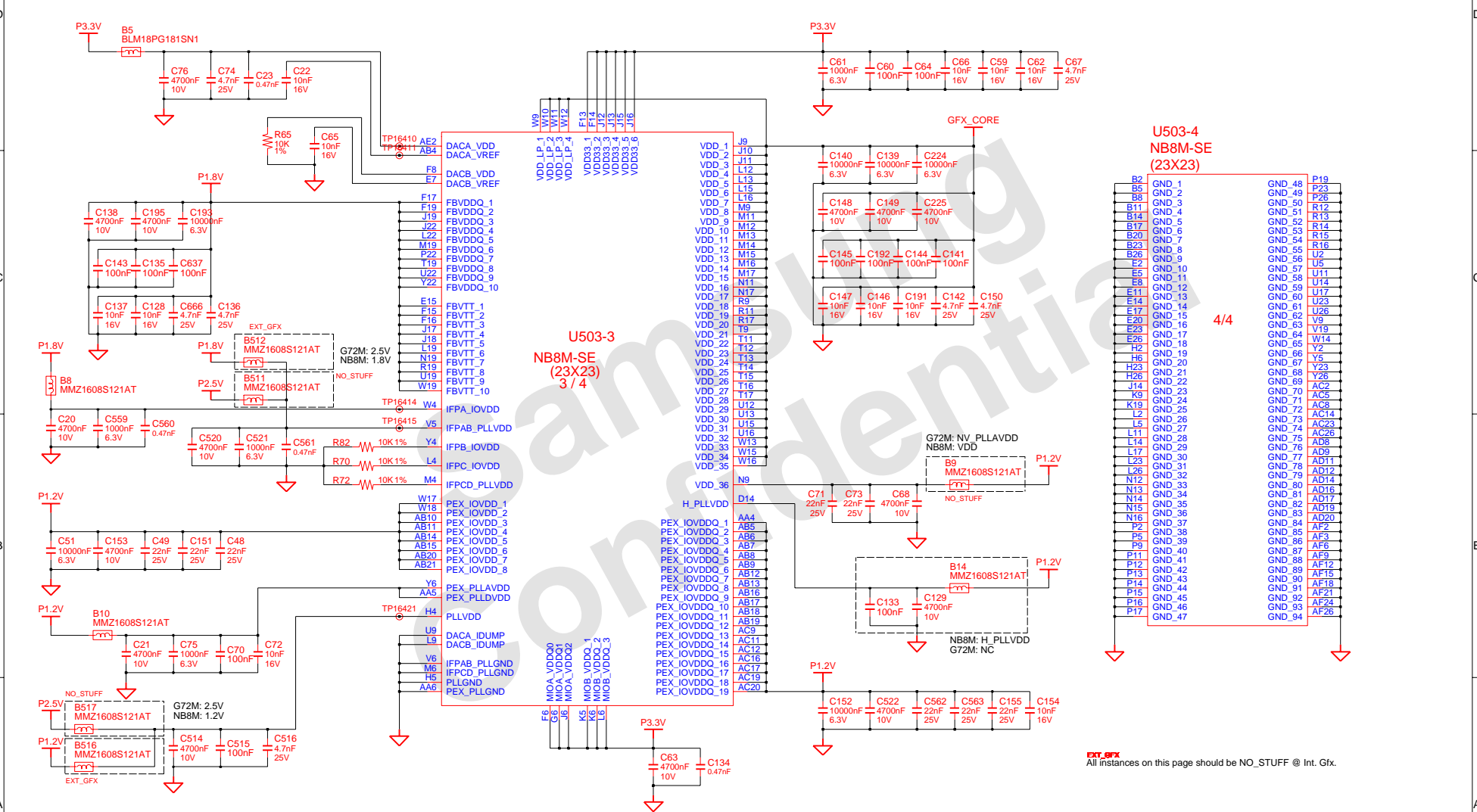
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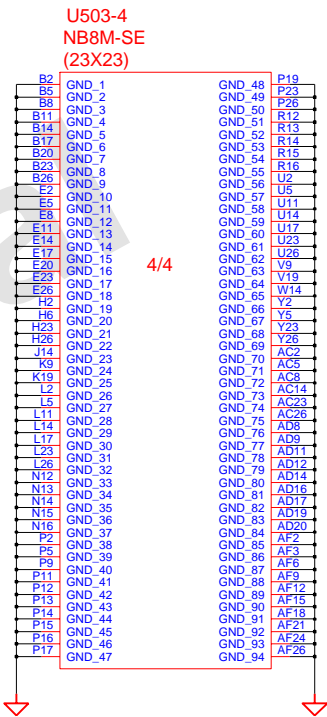
EXT GFX  
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CHECK	GUO, LEI	DEV STEP	MP	MAIN	BA41-00727/8A	
APPROVAL	LEE, KEVIN	REV	1.0	GFX (3)	PART NO.	BA41-00727/8A
MODULE CODE		LAST EDIT				
				March 9, 2007 12:03:21 PM	PAGE	22 OF 53

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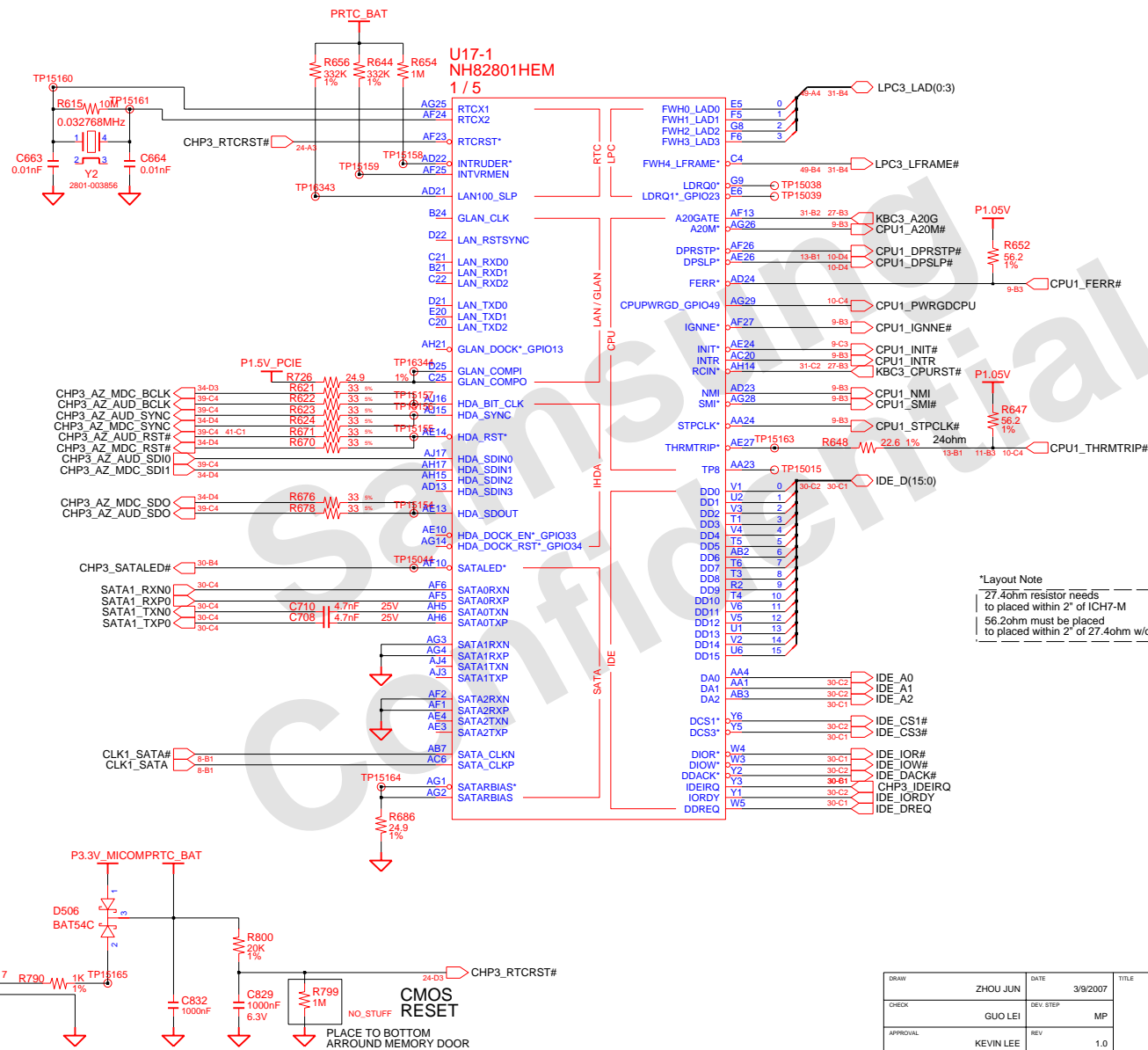
EXT\_GFX  
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DRAW	ZHOU, JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO, LEI	DEV_STEP	MP	MAIN	MAIN	
APPROVAL	LEE, KEVIN	REV	1.0	GFX (4)		PART NO. BA41-00727/8A
MODULE CODE		LAST EDIT			March 9, 2007 12:03:21 PM	PAGE 23 OF 53



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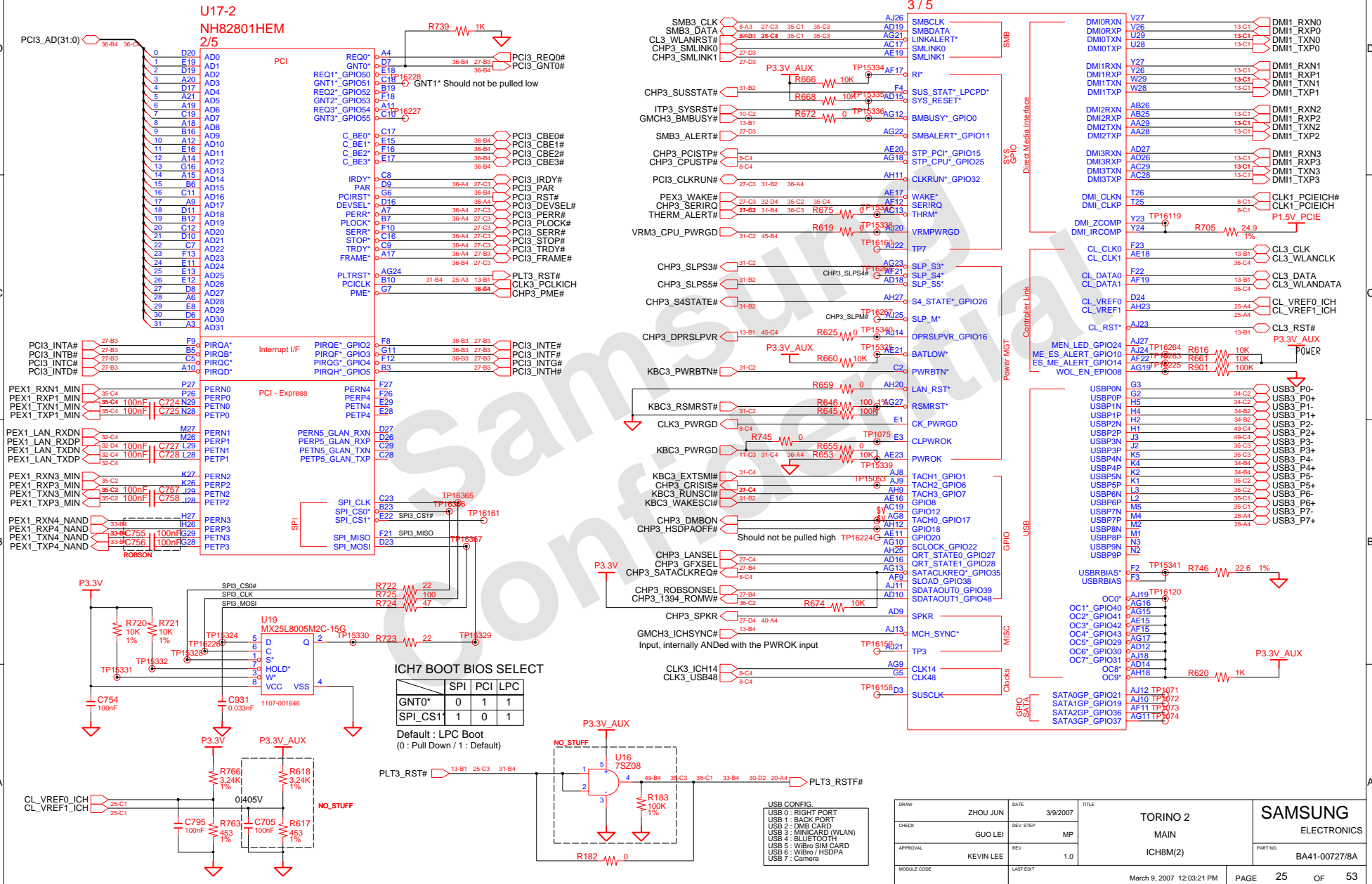
**\*Layout Note**  
 27.4ohm resistor needs  
 to be placed within 2" of ICH7-M  
 56.2ohm must be placed  
 to be placed within 2" of 27.4ohm w/o stub

DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV STEP	MP		MAIN	
APPROVAL	KEVIN LEE	REV	1.0		ICH8M(1)	PART NO. BA41-00727/8A
MODULE CODE		LAST EDIT				
				March 9, 2007 12:03:21 PM	PAGE	24 OF 53

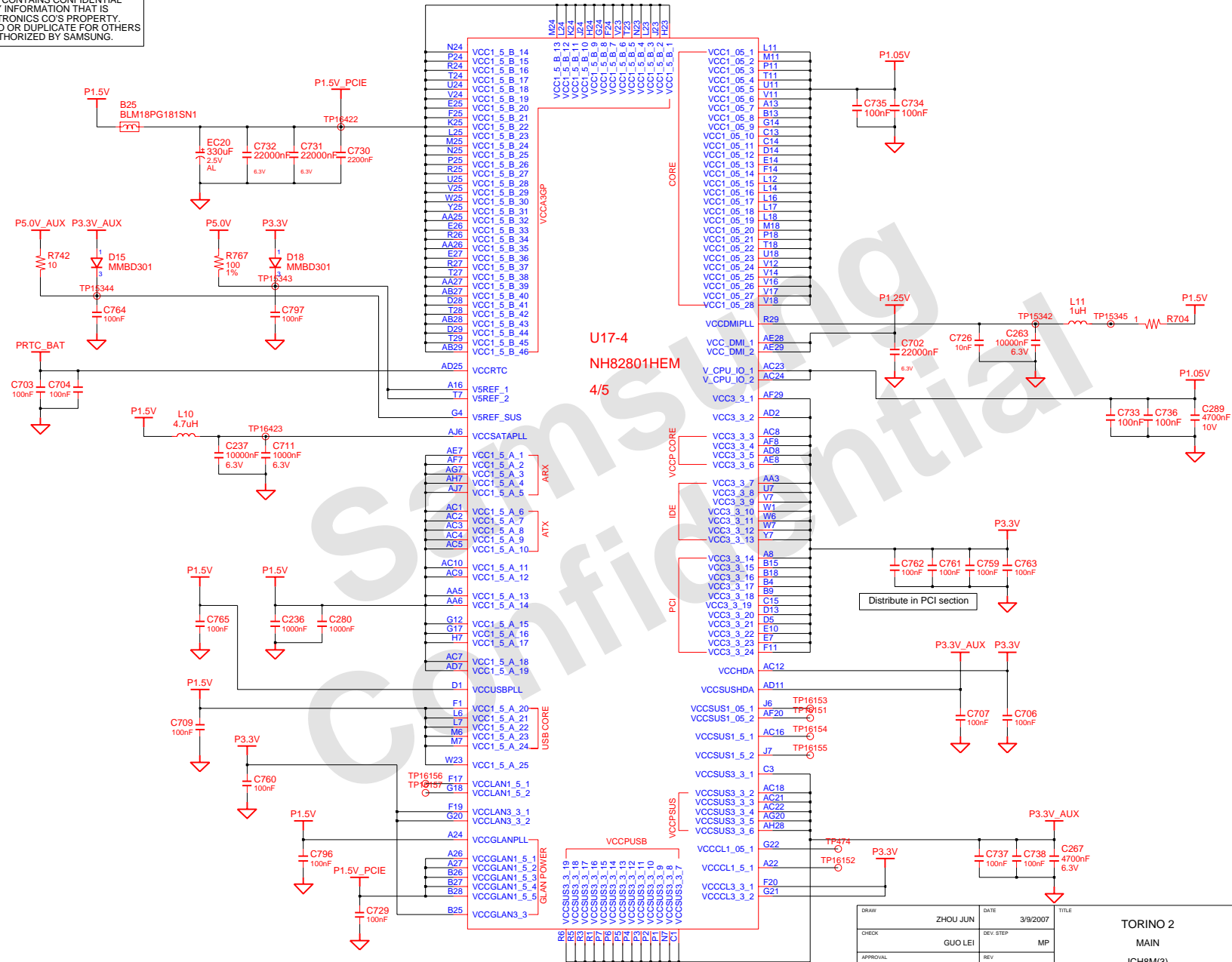


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AC caps : PCIe need to be within 250mils of the driver  
 Resistor for Test : Place Stuffing Option to minimize stubs



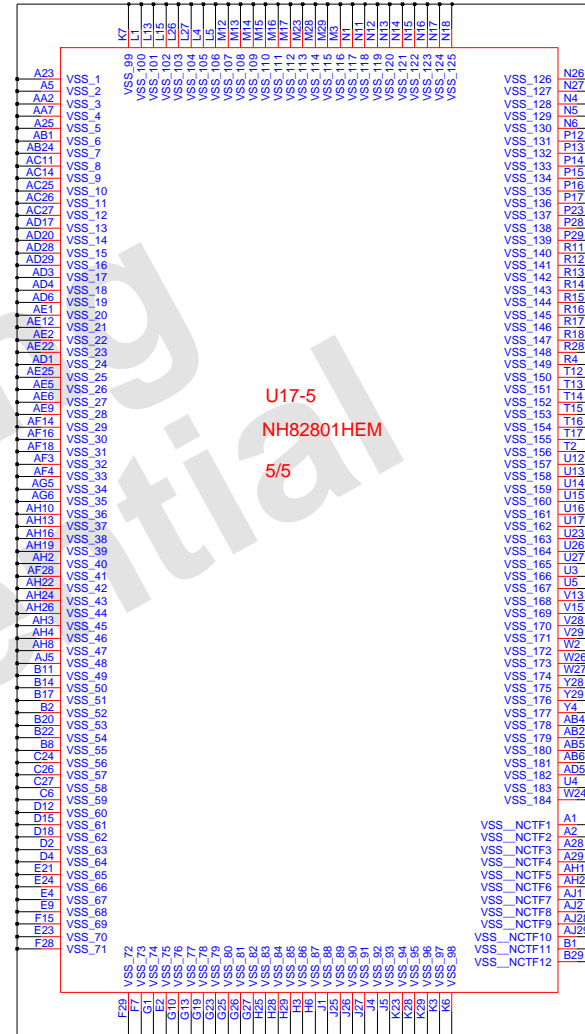
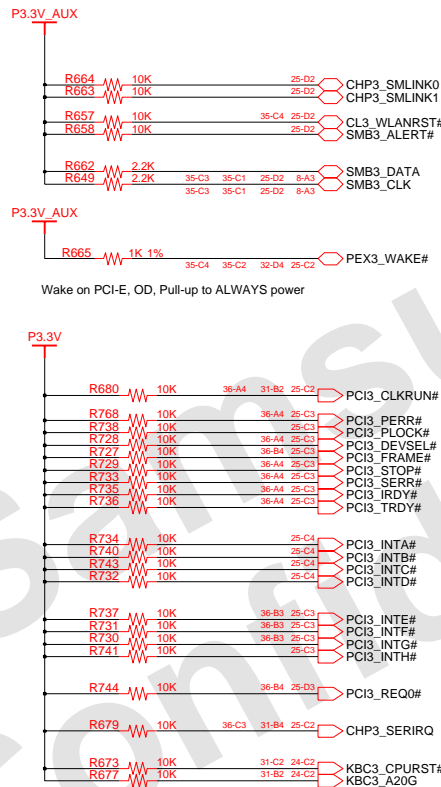
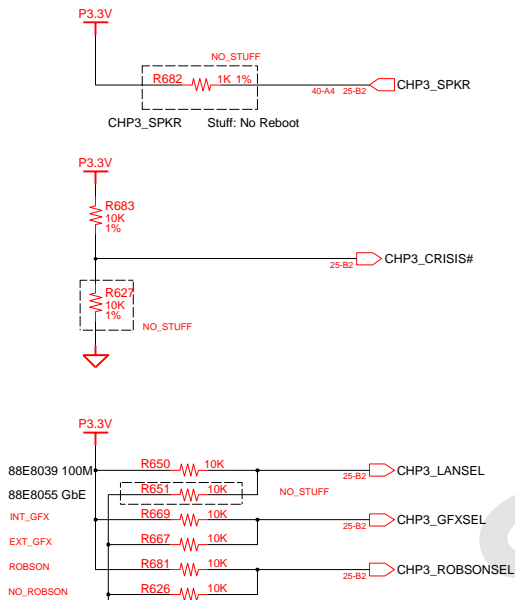
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**U17-4**  
**NH82801HEM**  
**4/5**

DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV STEP	MP	MAIN	ICH8M(3)	
APPROVAL	KEVIN LEE	REV	1.0			PART NO. BA41-00727/8A
MODULE CODE		LAST EDIT				March 9, 2007 12:03:21 PM
				PAGE	26	OF 53

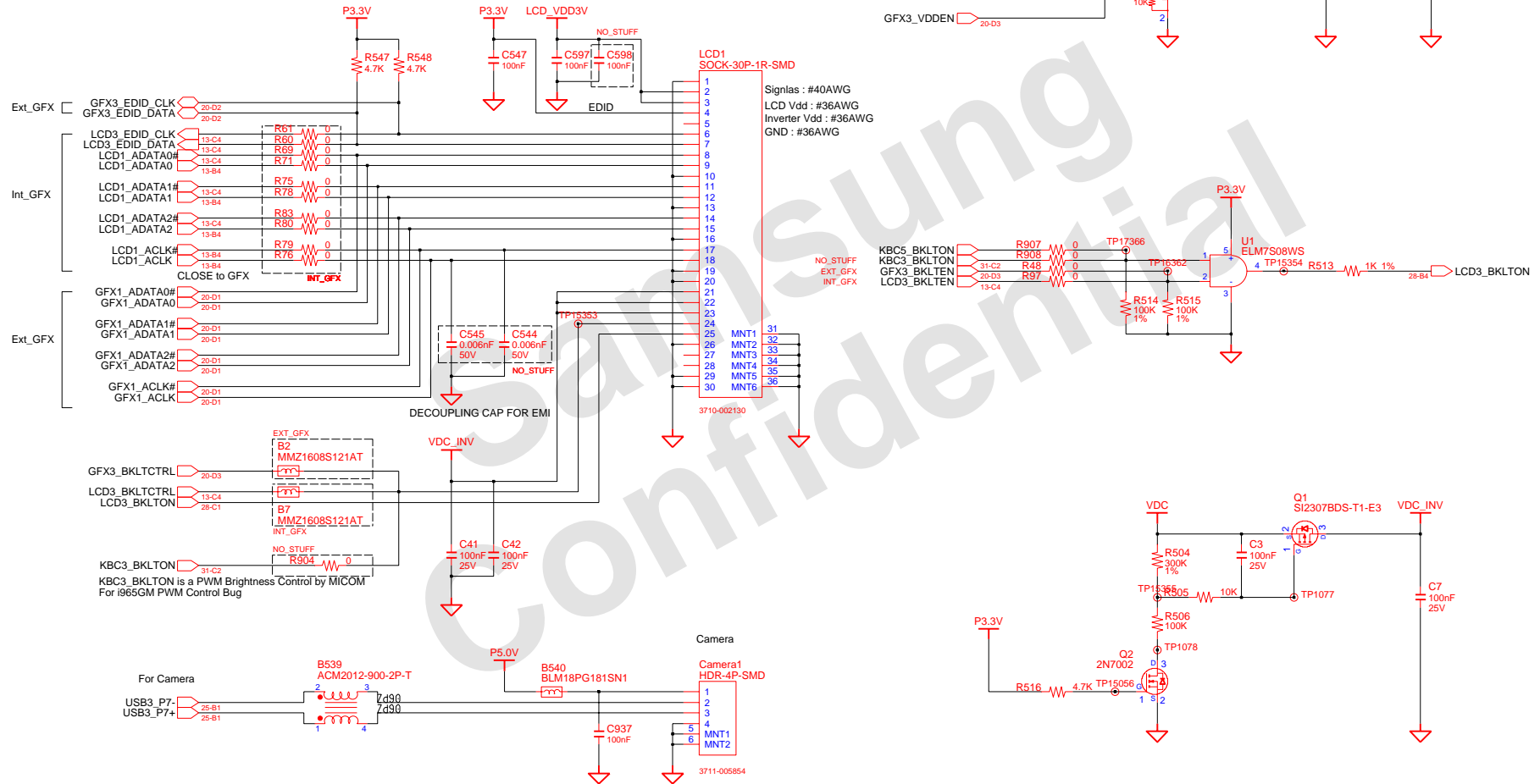
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DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV STEP	MP	MAIN	PART NO.	
APPROVAL	KEVIN LEE	REV	1.0	ICH8M(4)	BA41-00727/8A	
MODULE CODE		LAST EDIT		March 9, 2007 12:03:21 PM	PAGE 27 OF 53	

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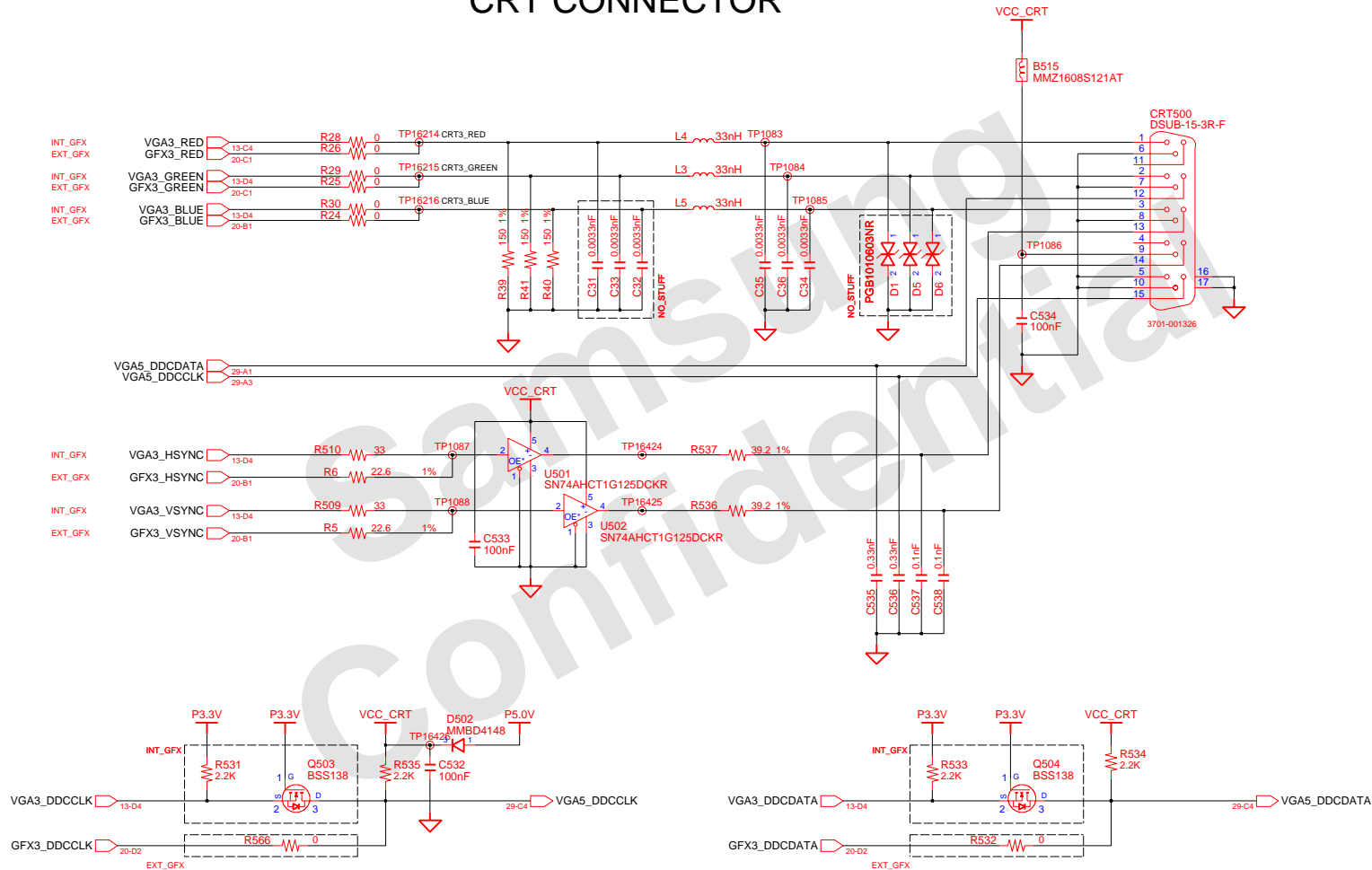
# WXGA LCD CONNECTOR



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CHECK	GUO LEI	DEV. STEP	MP		MAIN		
APPROVAL	KEVIN LEE	REV	1.0		LVDS		
MODULE CODE		LAST ENT					
March 9, 2007 12:03:21 PM						PAGE	28 OF 53

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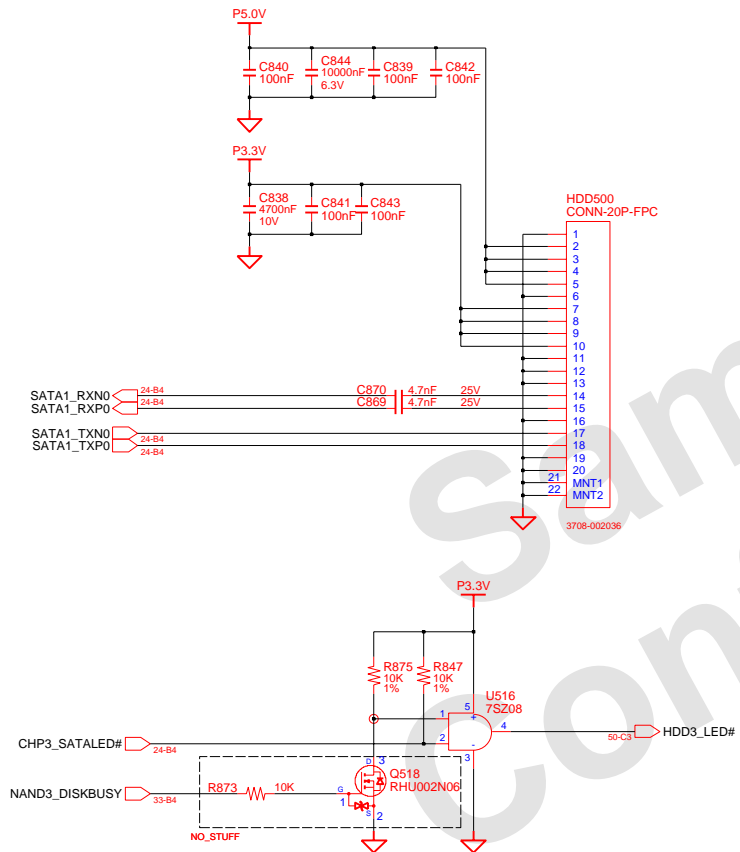
# CRT CONNECTOR



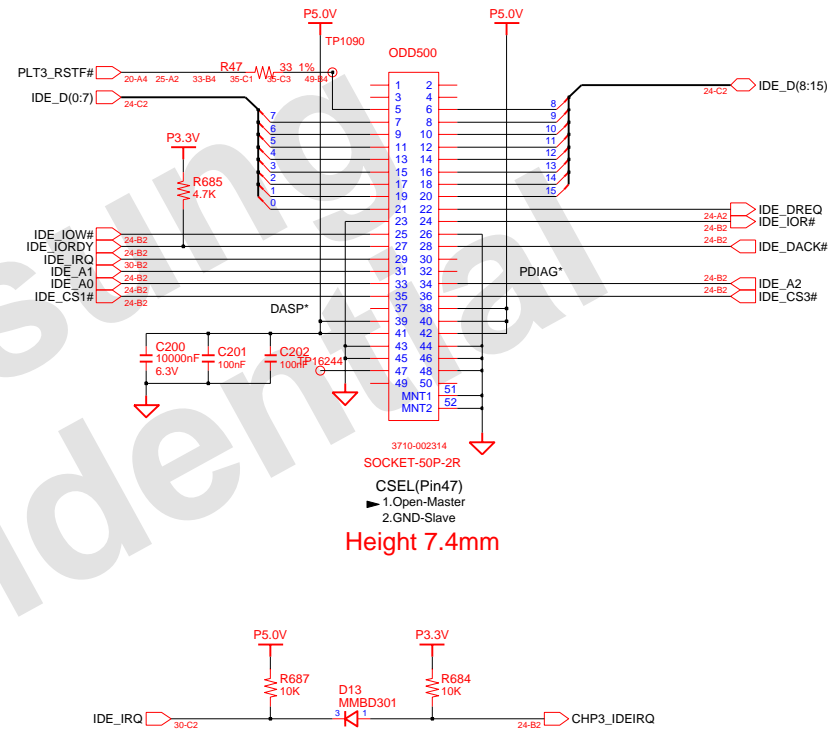
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CHECK	GUO LEI	DEV STEP	MP		MAIN				
APPROVAL	KEVIN LEE	REV	1.0		CRT				
MODULE CODE		LAST ENT							
March 9, 2007 12:03:21 PM						PAGE	29	OF	53

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### Main to HDD

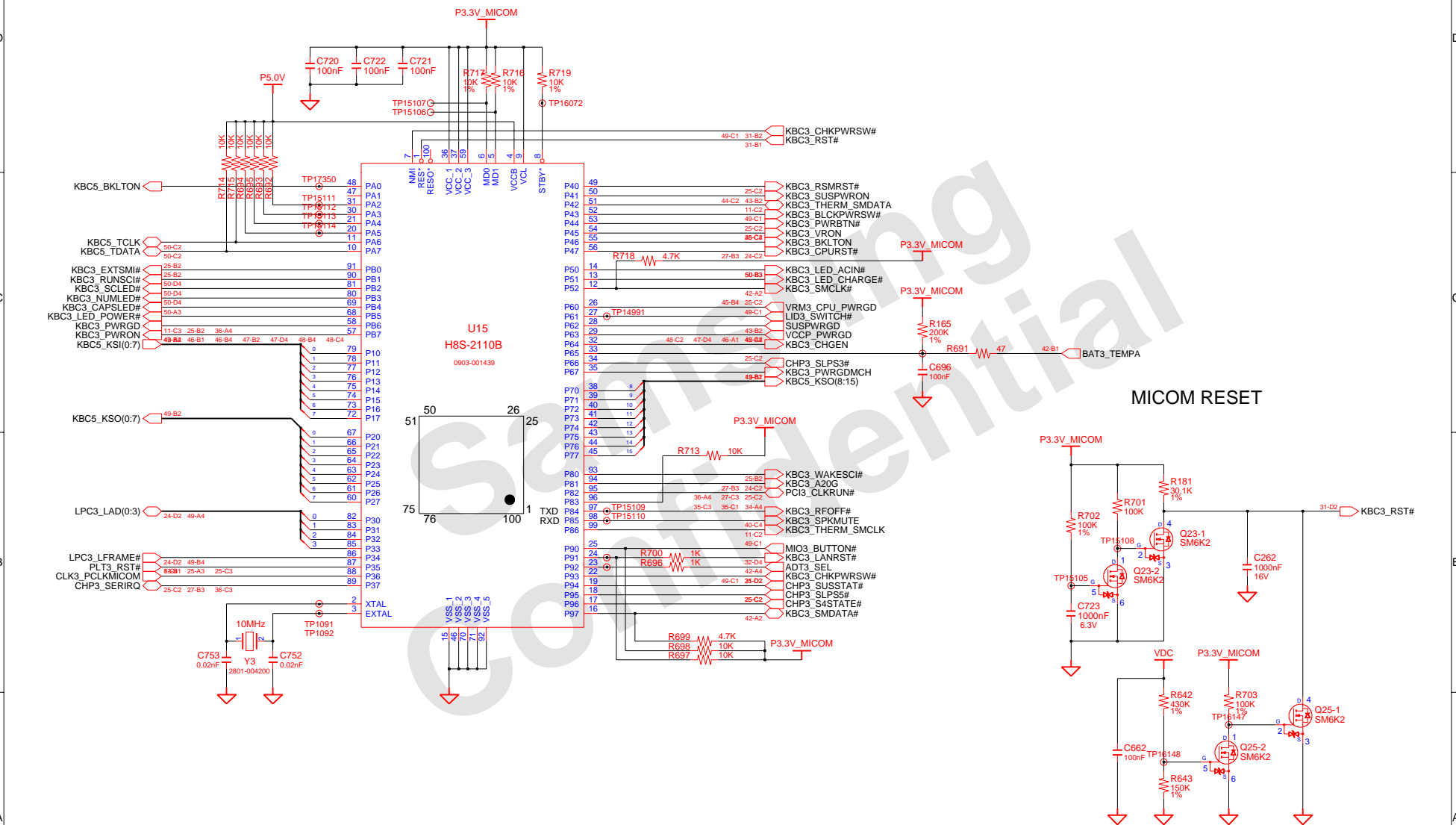


### Main to ODD

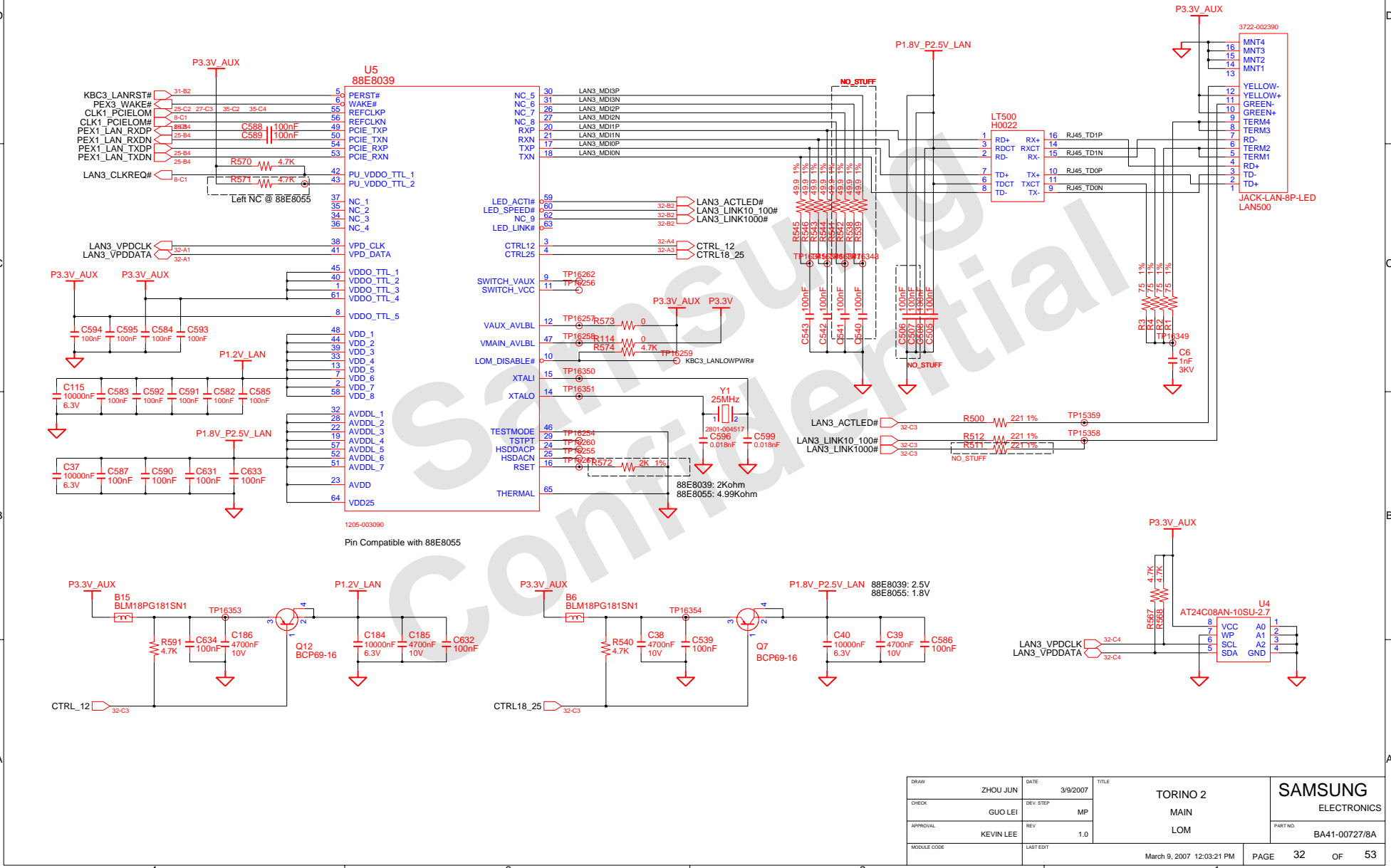


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APPROVAL	KEVIN LEE	REV	1.0	IDE	BA41-00727/8A	
MODULE CODE		LAST EDIT				
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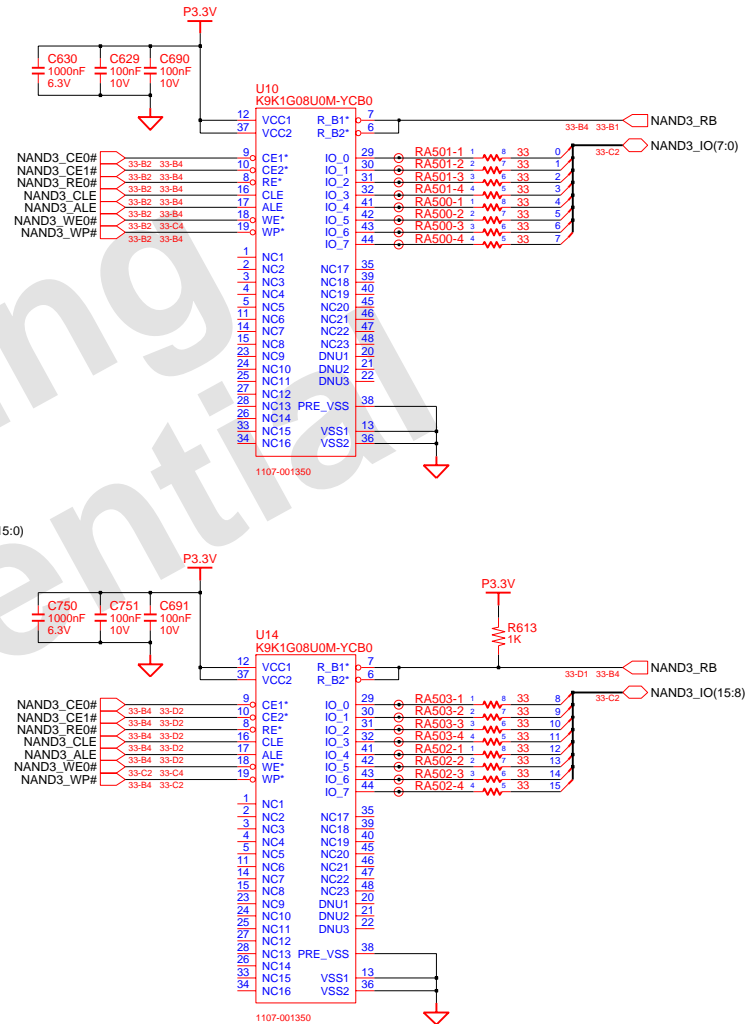
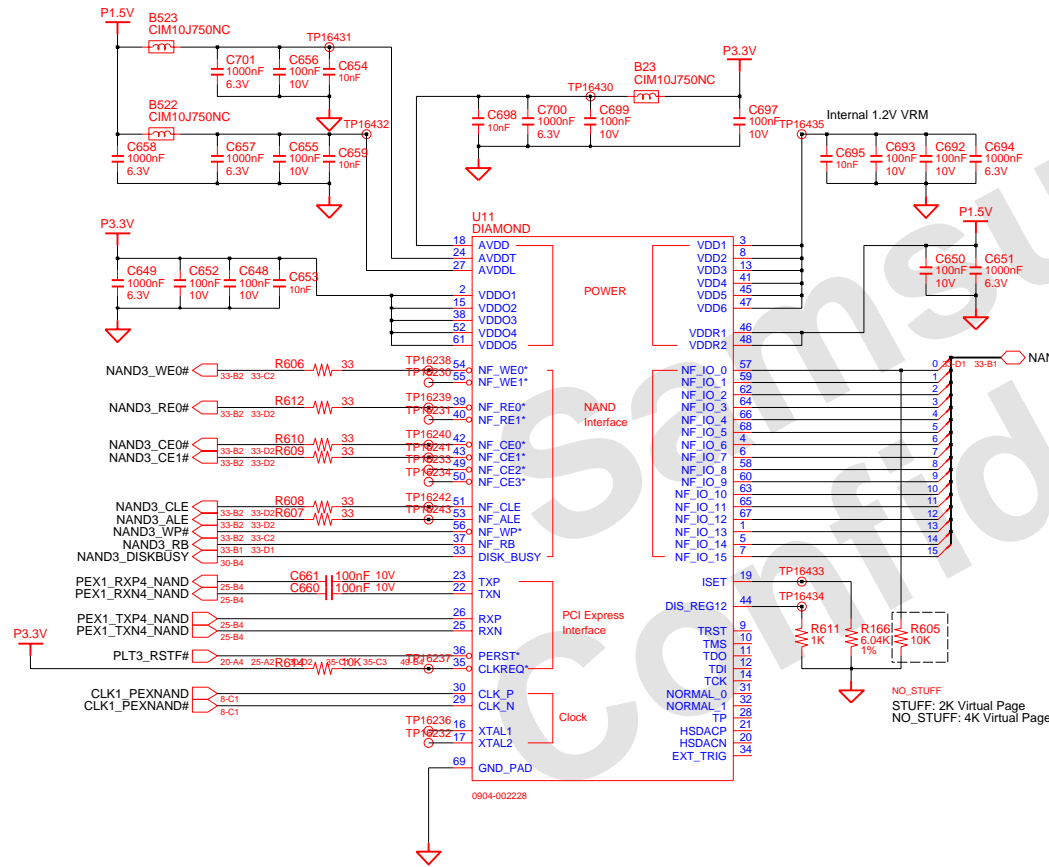


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CHECK	GUO LEI	DEV_STEP	MP	MAIN		
APPROVAL	KEVIN LEE	REV	1.0	LOM		
MODULE CODE		LAST EDIT				PART NO. BA41-00727/8A
March 9, 2007 12:03:21 PM						PAGE 32 OF 53



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### Robson

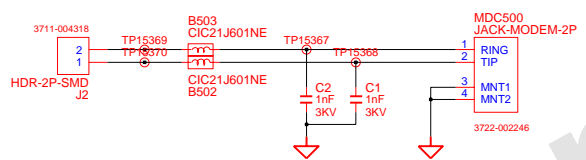
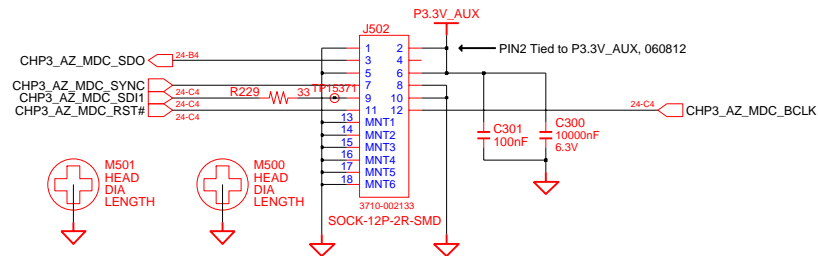


**ROBSON**  
 All instances on this page should be NO\_STUFF if ROBSON is N/A

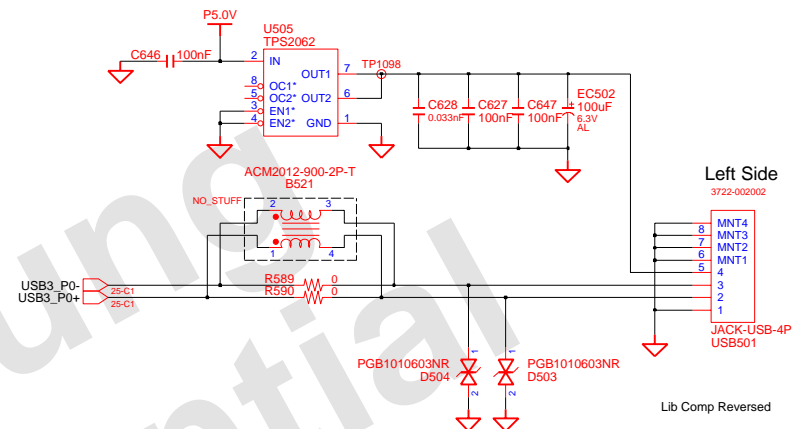
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CHECK	GUO LEI	DEV_STEP	MP		MAIN	
APPROVAL	KEVIN LEE	REV	1.0		ROBSON	
MODULE CODE		LAST EDIT				
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## MDC connector

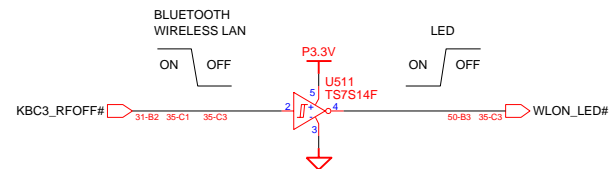
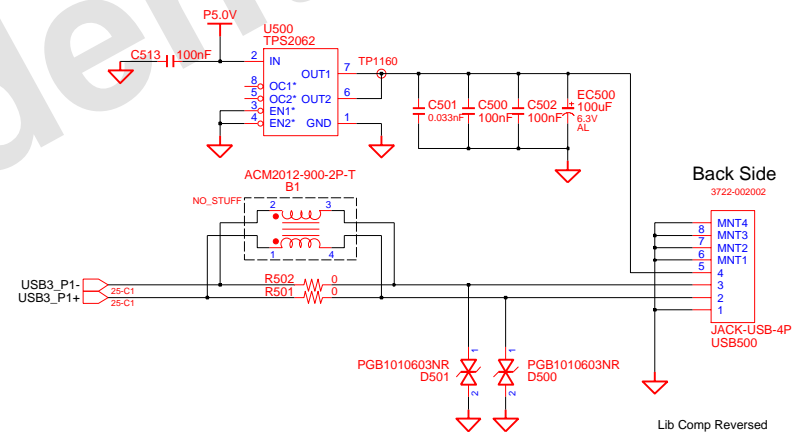
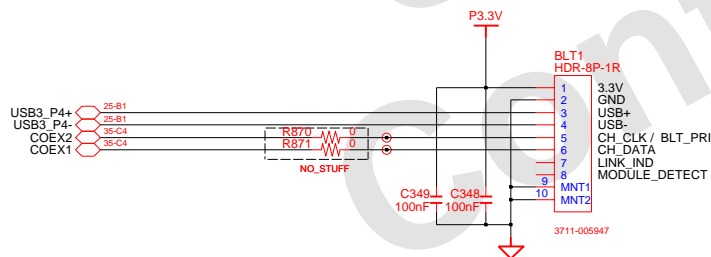


## USB Port



## BLUETOOTH

Bluetooth 2.0 Standard

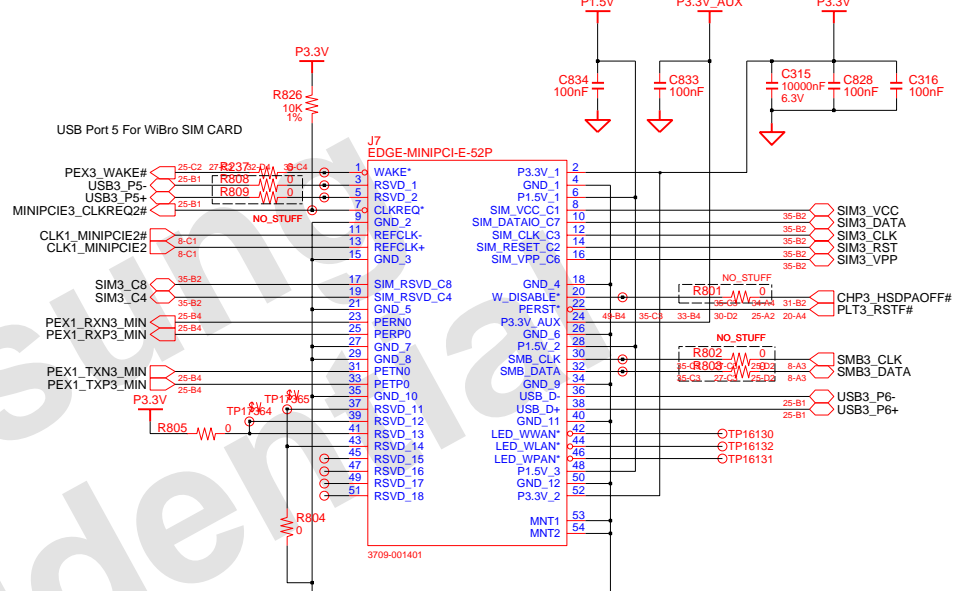
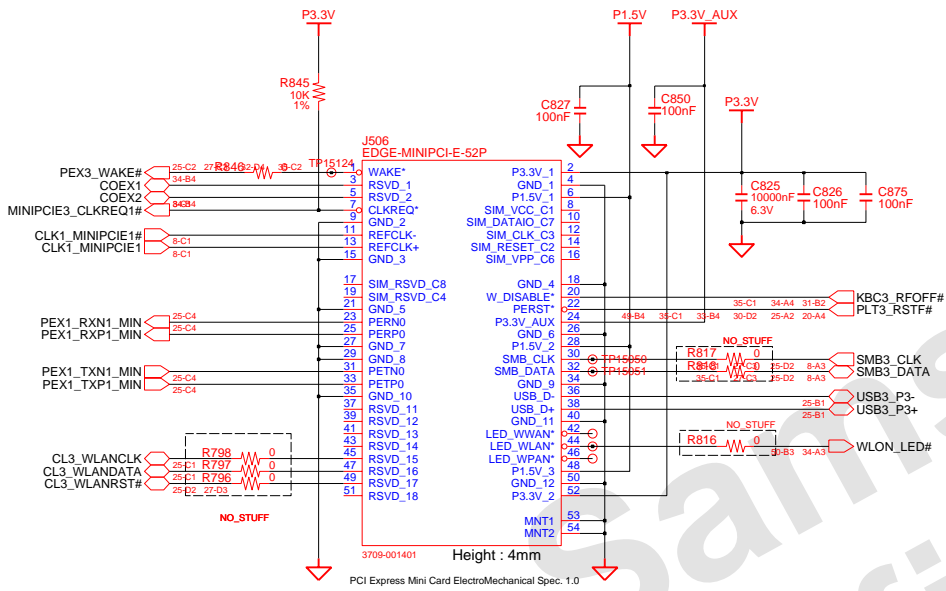


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APPROVAL	KEVIN LEE	REV	1.0		USB, MDC & BLUETOOTH	PART NO. BA41-00727/8A
MODULE CODE		LAST EDIT				
				March 9, 2007 12:03:21 PM	PAGE	34 OF 53

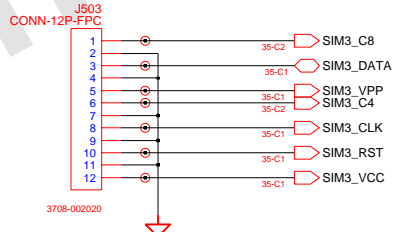
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## Mini PCI-E Card (WLAN)

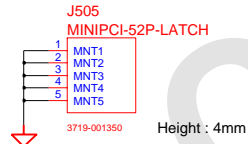
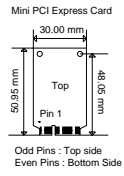
## Mini PCI-E Card (HSDPA)



### SIM Card FFC CONN



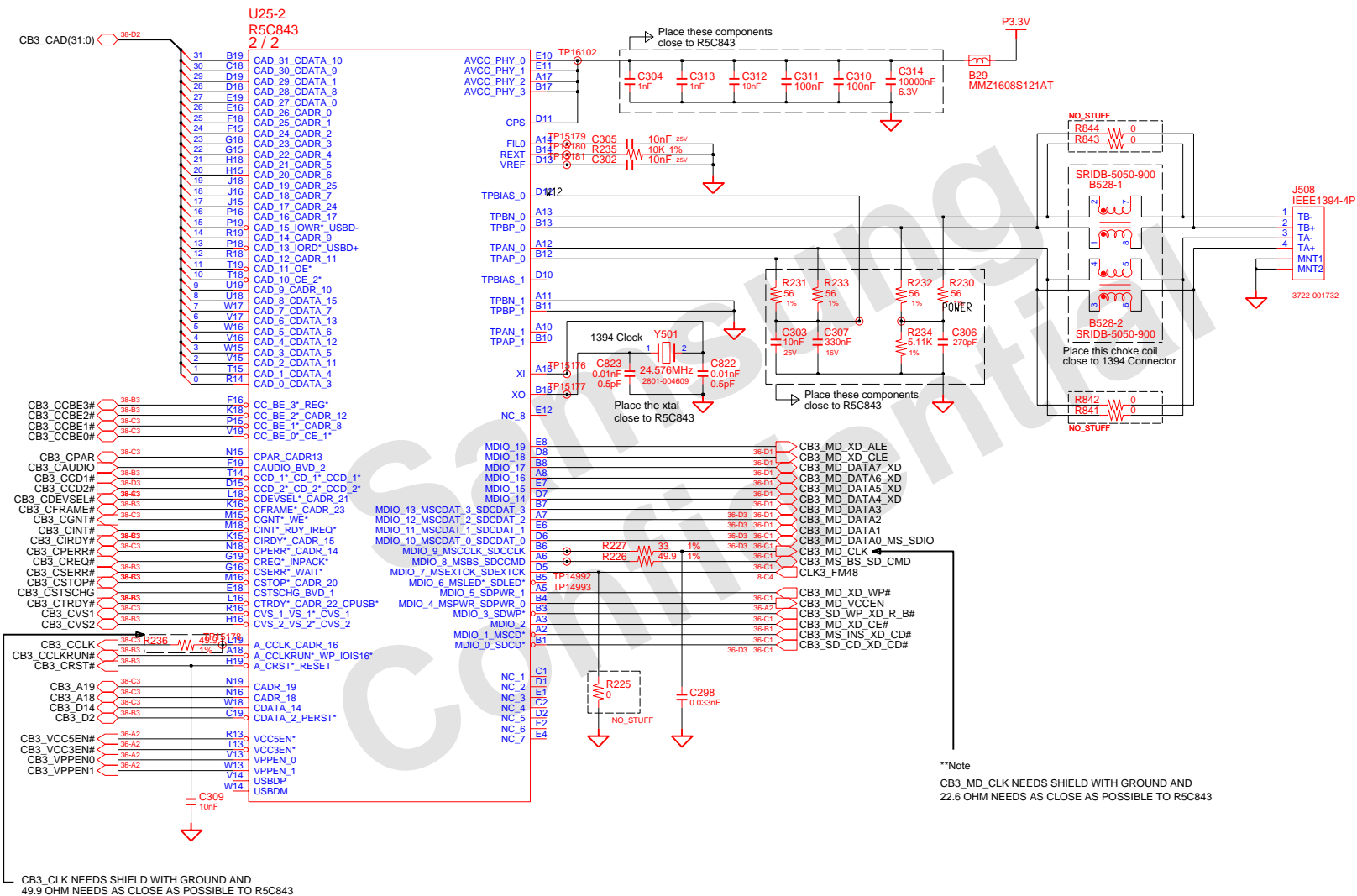
### PEM



DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	MP	MAIN	MAIN CARD	
APPROVAL	KEVIN LEE	REV	1.0			PART NO. BA41-00727/8A
MODULE CODE		LAST EDIT				
March 9, 2007 12:03:21 PM						PAGE 35 OF 53

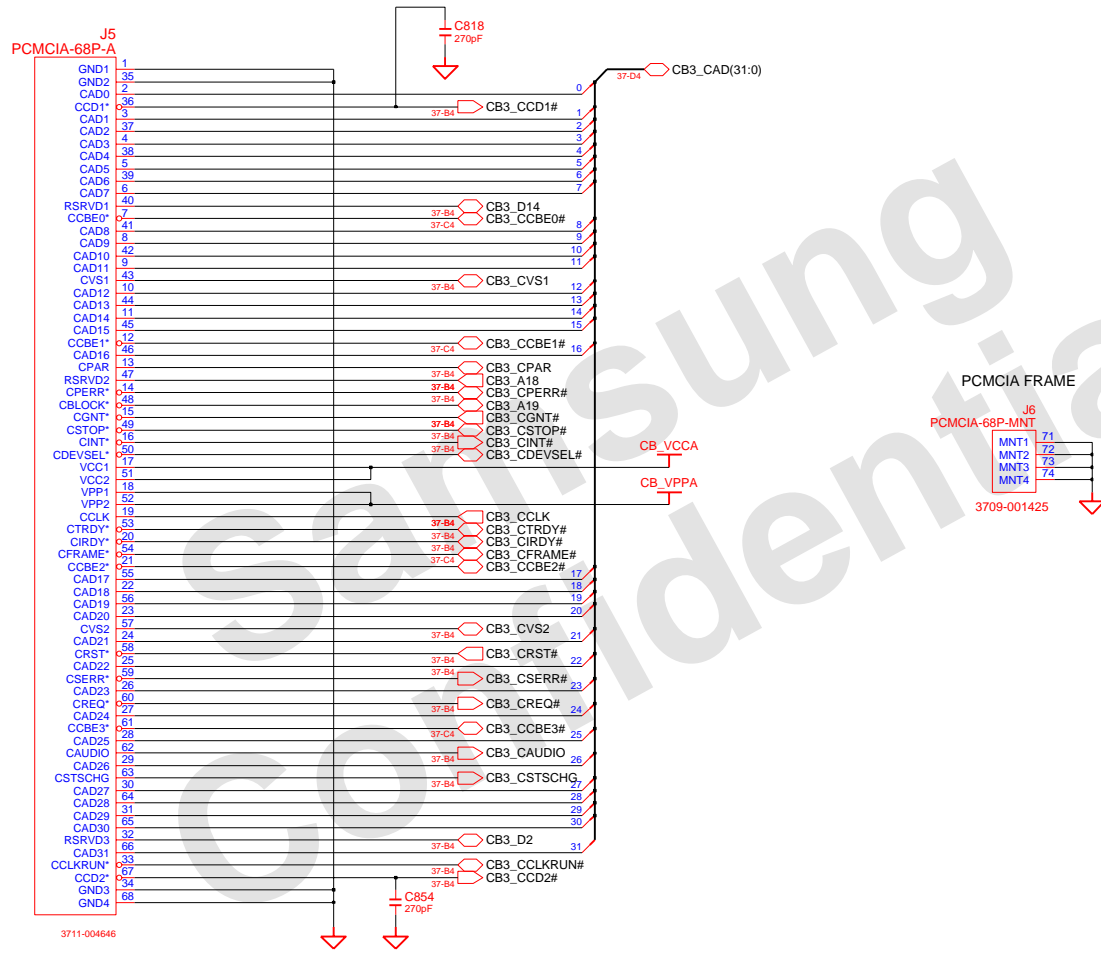


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DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG
CHECK	GUO LEI	DEV STEP	MP		MAIN	ELECTRONICS
APPROVAL	KEVIN LEE	REV	1.0		CARDBUS(2)	PART NO. BA41-00727/8A
MODULE CODE		LAST ENT				
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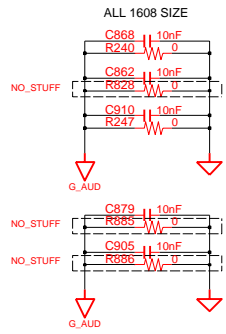
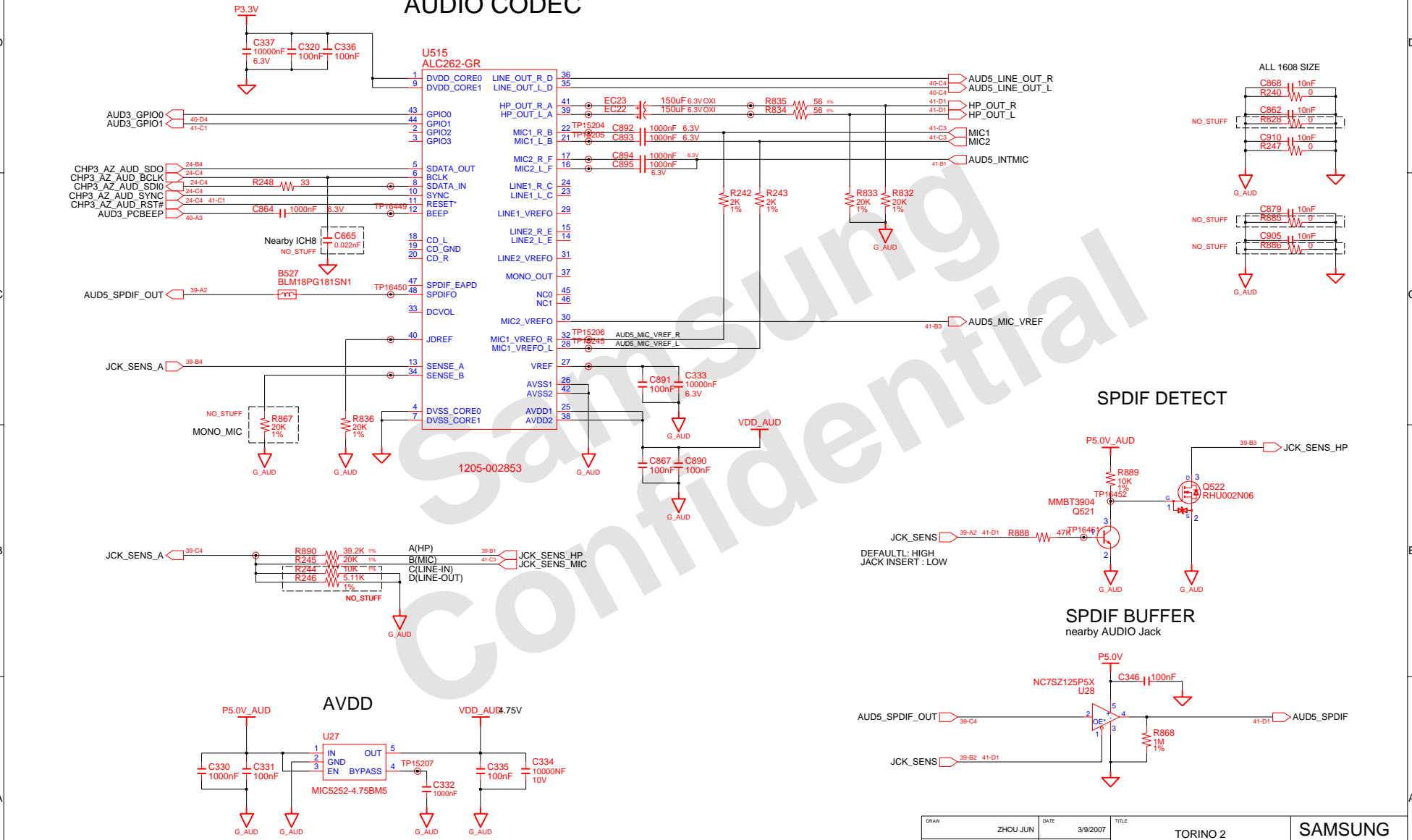
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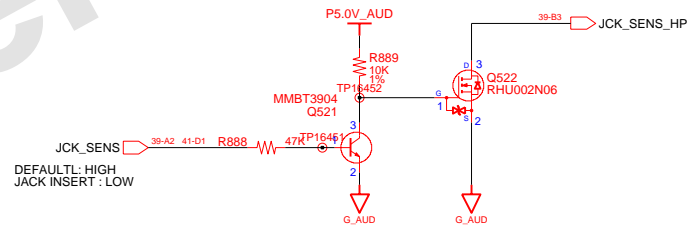
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APPROVAL	KEVIN LEE	REV	1.0	CARDBUS (3)	PART NO.	
MODULE CODE		LAST EDIT	March 9, 2007 12:03:21 PM		BA41-00727/8A	
				PAGE	38	OF 53

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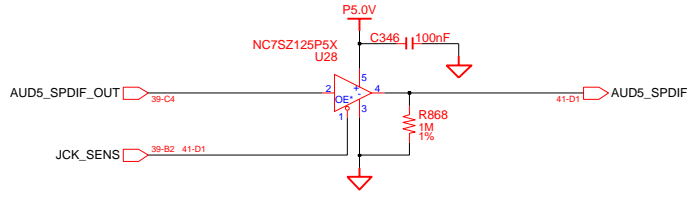
# AUDIO CODEC



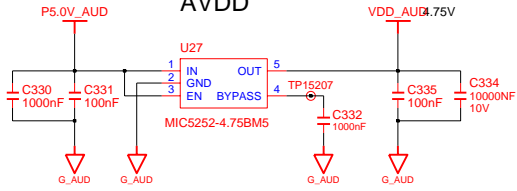
## SPIDF DETECT



## SPIDF BUFFER nearby AUDIO Jack



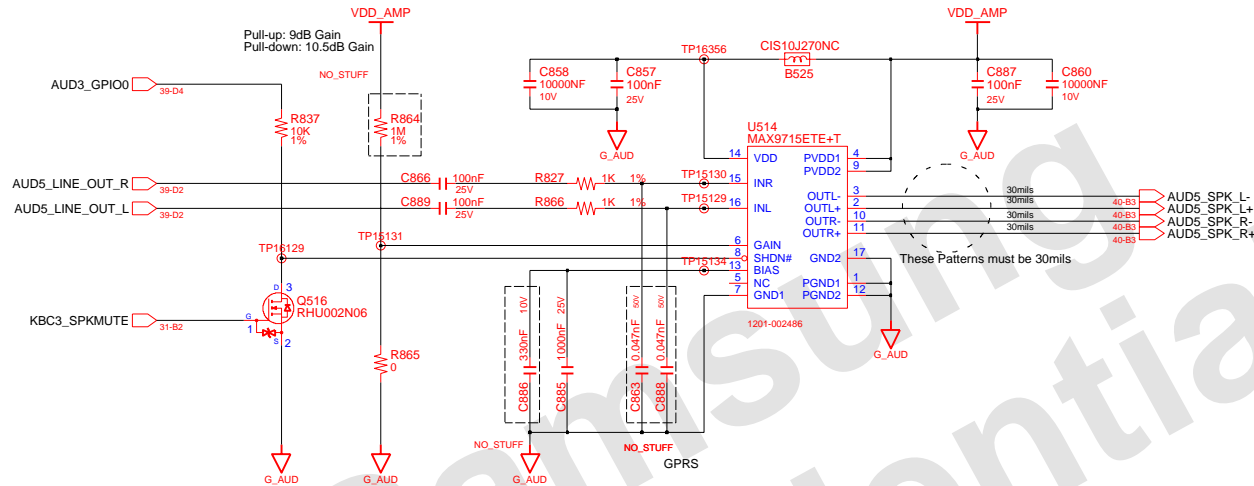
## AVDD



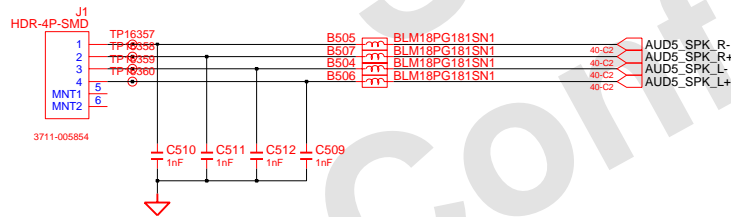
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CHECK	GUO LEI	DEV. STEP	MP	MAIN		
APPROVAL	KEVIN LEE	REV	1.0	AUDIO CODEC		PART NO. BA41-00727/8A
MODULE CODE		LAST EDIT			March 9, 2007 12:03:21 PM	PAGE 39 OF 53

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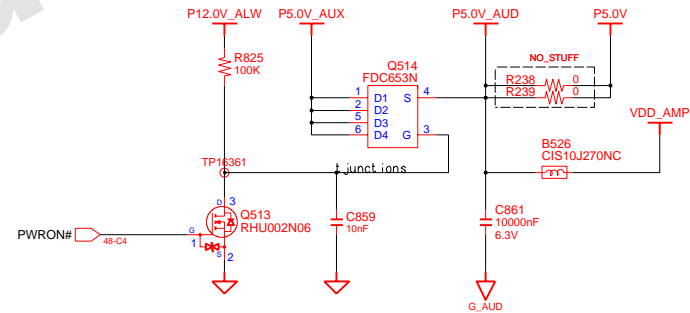
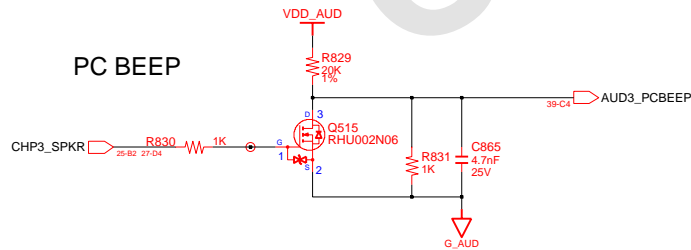
# AUDIO AMP



## INTERNAL STEREO SPEAKERS



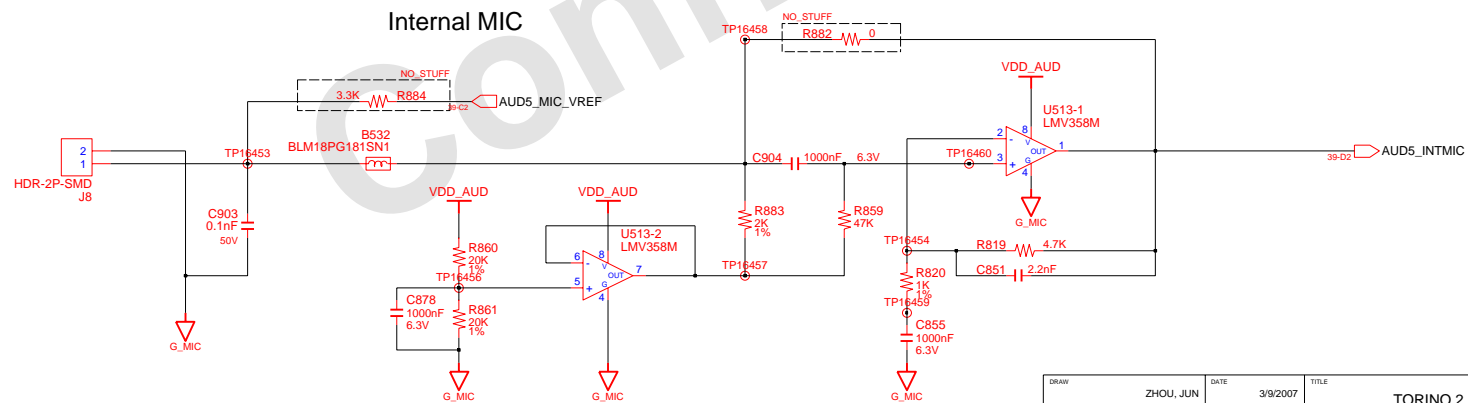
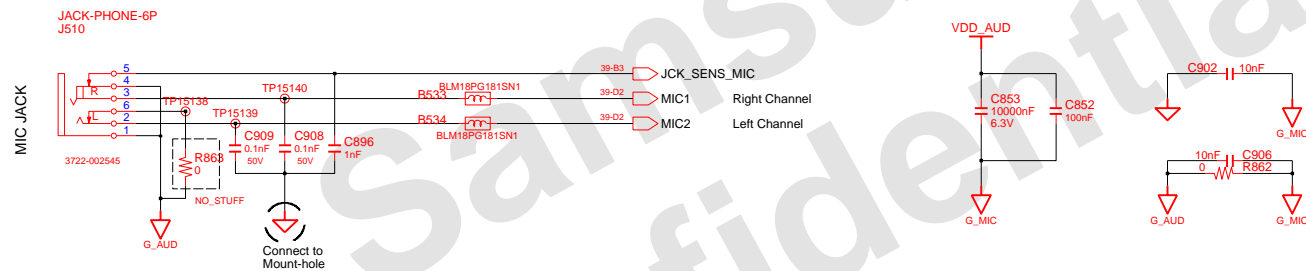
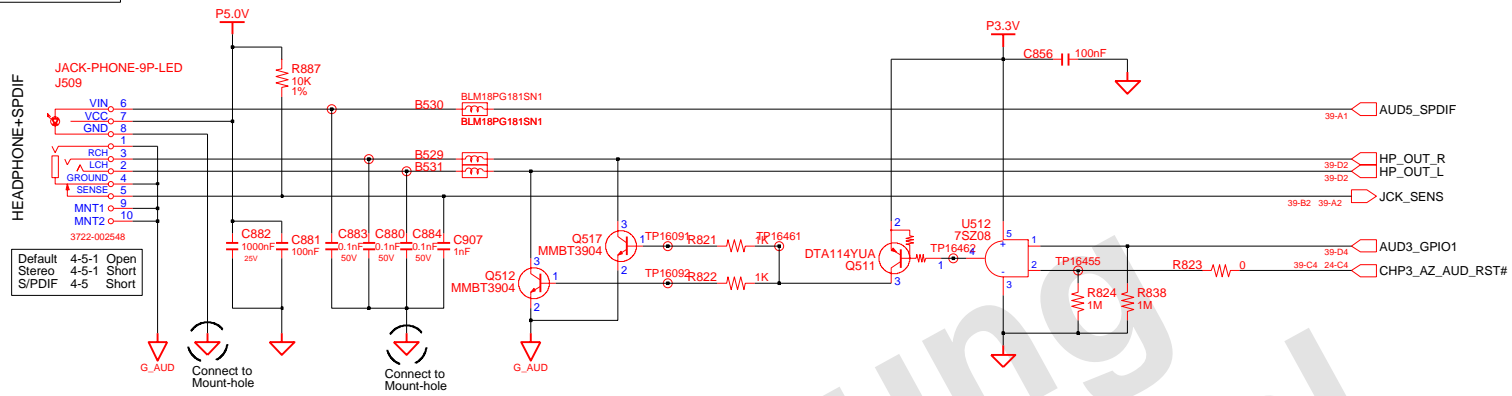
## PC BEEP



DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2 MAIN AUDIO AMPLIFIER	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV STEP	MP			
APPROVAL	KEVIN LEE	REV	1.0			PART NO. BA41-00727/8A
MODULE CODE		LAST EDIT			March 9, 2007 12:03:21 PM	PAGE 40 OF 53



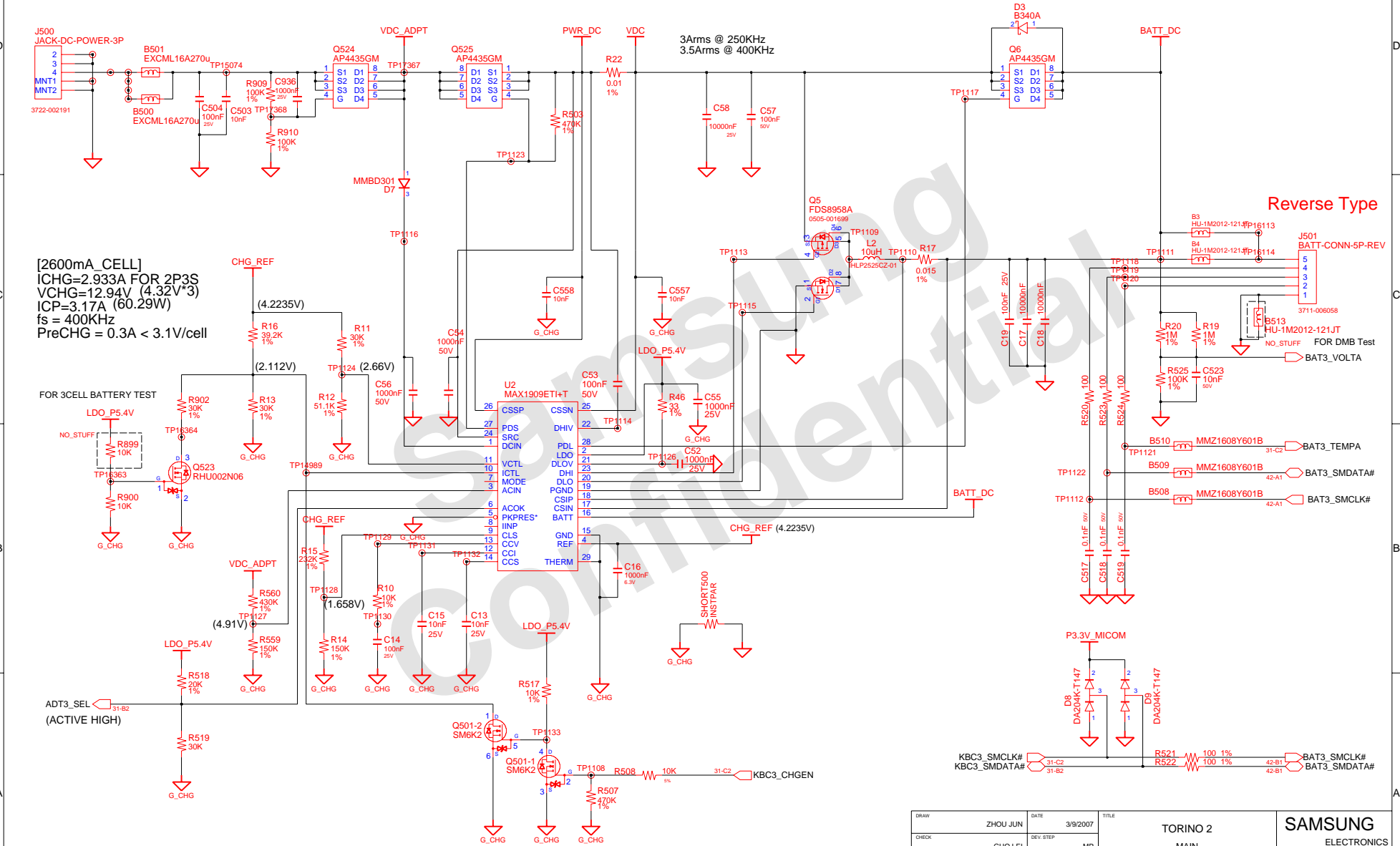
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DRAW	ZHOU, JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO, LEI	DEV. STEP	MP	MAIN	MAIN	
APPROVAL	LEE, KEVIN	REV	1.0	AUDIO JACK	PART NO. BA41-00727/8A	
MODULE CODE		LAST EDIT			March 9, 2007 12:03:21 PM	PAGE 41 OF 53

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# CHARGER & POWER MANAGEMENT

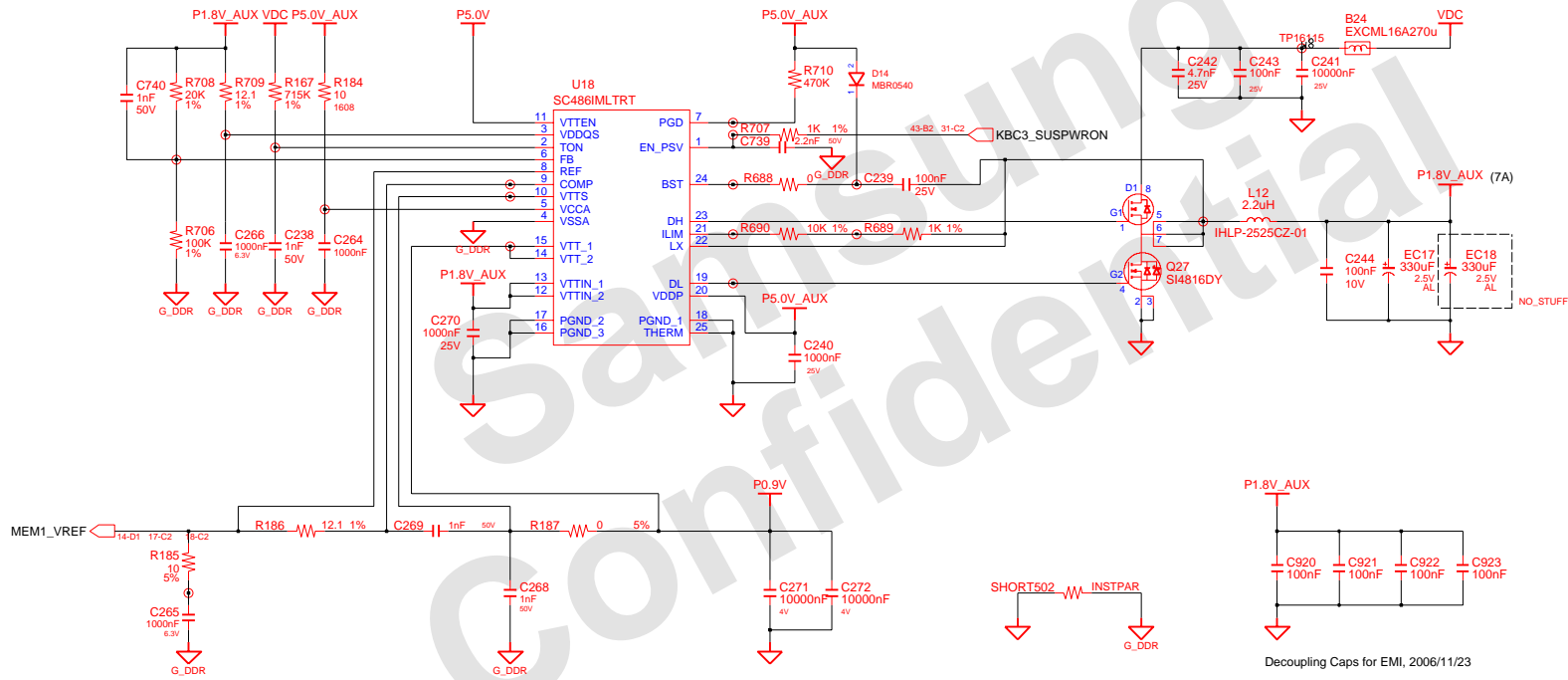


DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2 MAIN VDC & CHARGER	<b>SAMSUNG</b> ELECTRONICS PART NO. BA41-00727/8A
CHECK	GUO LEI	DEV STEP	MP			
APPROVAL	KEVIN LEE	REV	1.0			
MODULE CODE		LAST ENT				
				March 9, 2007 12:03:21 PM	PAGE 42 OF 53	



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# DDR2 POWER

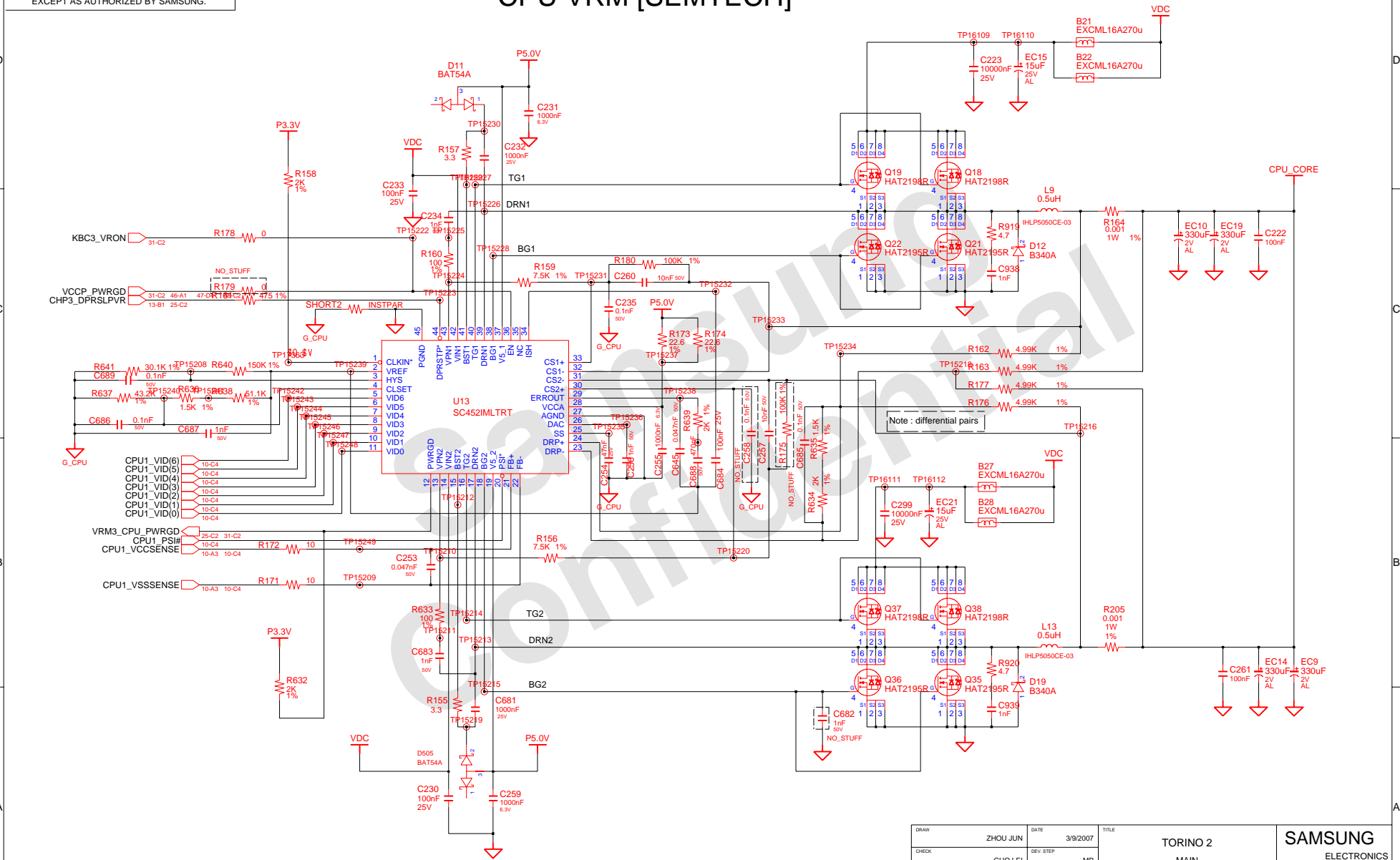


Decoupling Caps for EMI, 2006/11/23

DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	MP	MAIN	PART NO.	
APPROVAL	KEVIN LEE	REV	1.0	DDR2 POWER	BA41-00727/8A	
MODULE CODE		LAST EDIT	March 9, 2007 12:03:21 PM	PAGE	44	OF 53

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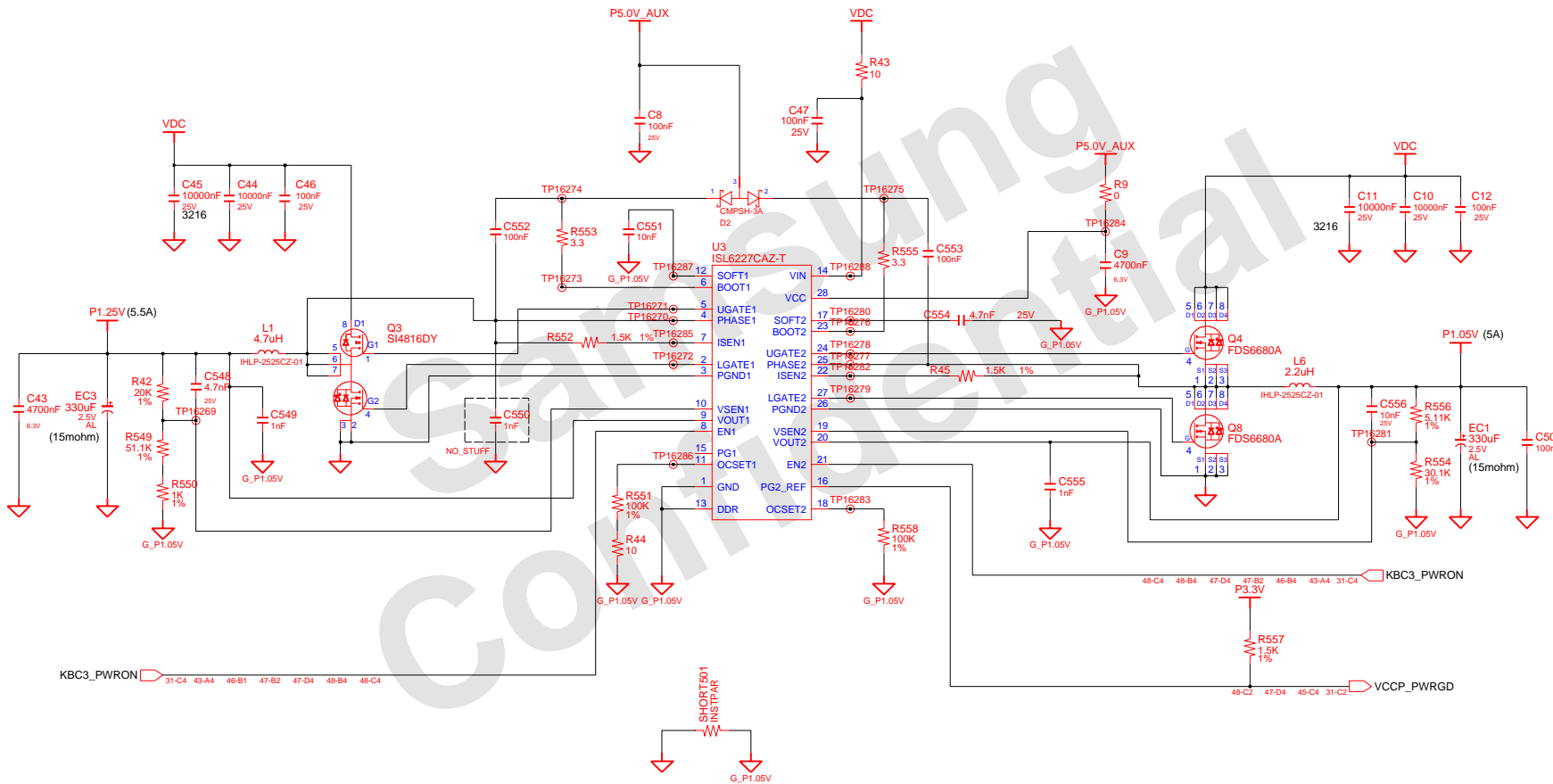
# CPU VRM [SEMTECH]



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CHECK	GUO LEI	DEV. STEP	MP			
APPROVAL	KEVIN LEE	REV	1.0			
MODULE CODE		LAST ENT				
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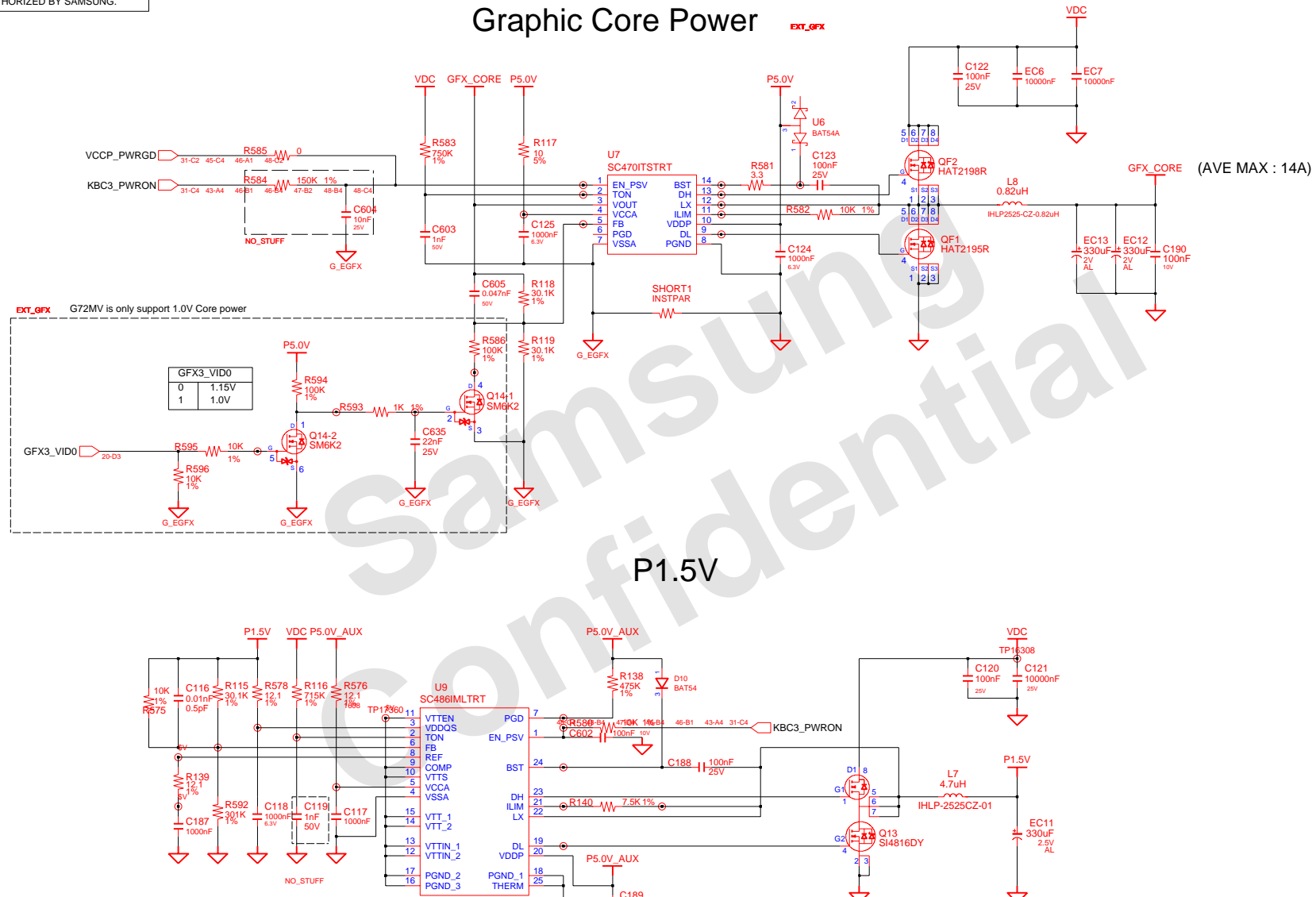
# P1.25V & P1.05V



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CHECK	GUO LEI	DEV. STEP	MP		MAIN	
APPROVAL	KEVIN LEE	REV	1.0		P1.25V & P1.05V	PART NO. BA41-00727/8A
MODULE CODE		LAST EDIT		March 9, 2007 12:03:21 PM	PAGE	46 OF 53

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# Graphic Core Power



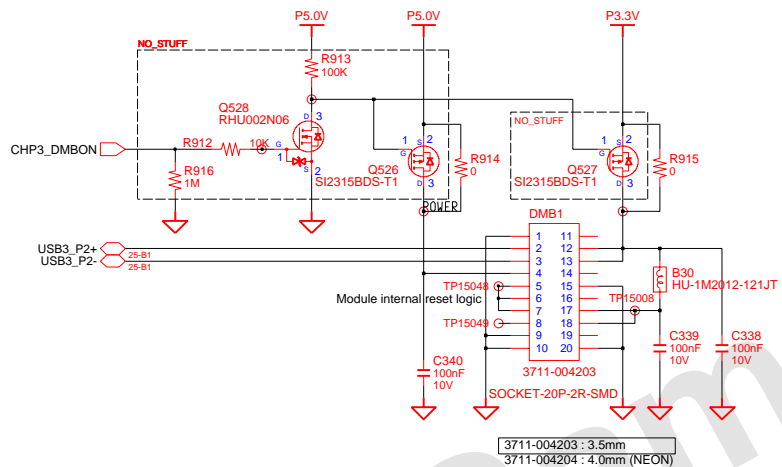
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CHECK	GUO, LEI	DEV. STEP	MP	MAIN		
APPROVAL	LEE, KEVIN	REV	1.0	GFX_CORE & P1.5V	PART NO.	BA41-00727/8A
MODULE CODE		LAST EDIT			PAGE	47 OF 53
				March 9, 2007 12:03:21 PM		



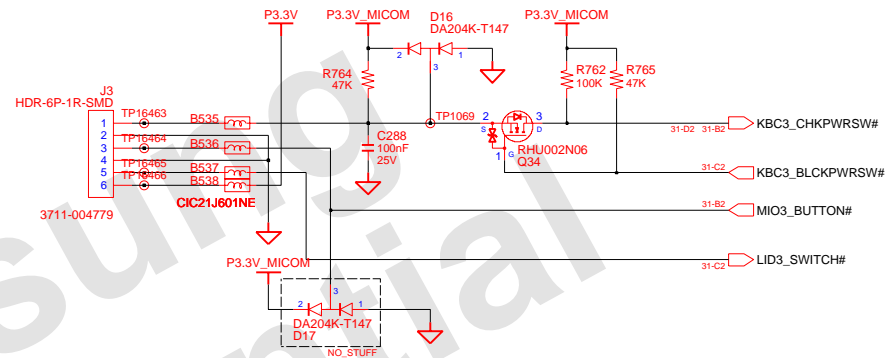


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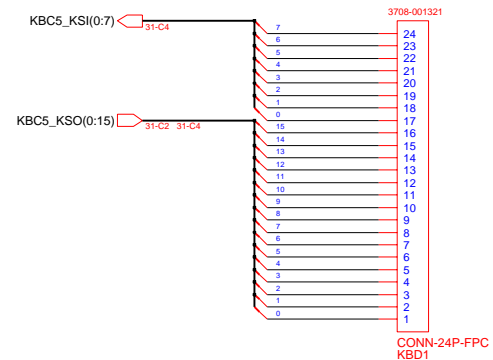
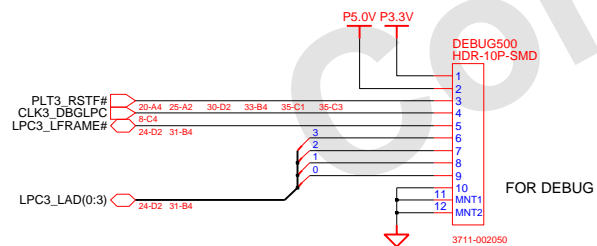
### OPTION: DMB INTERFACE



### Power Switch Connector

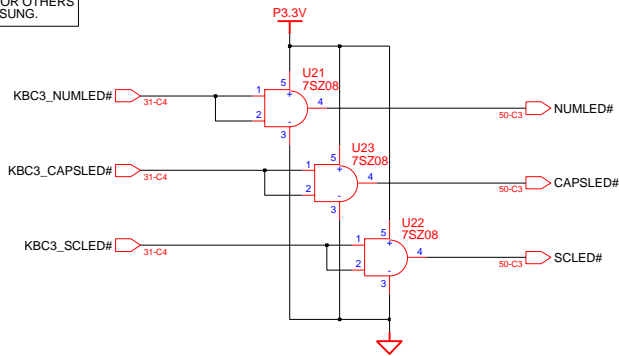


### KEYBOARD

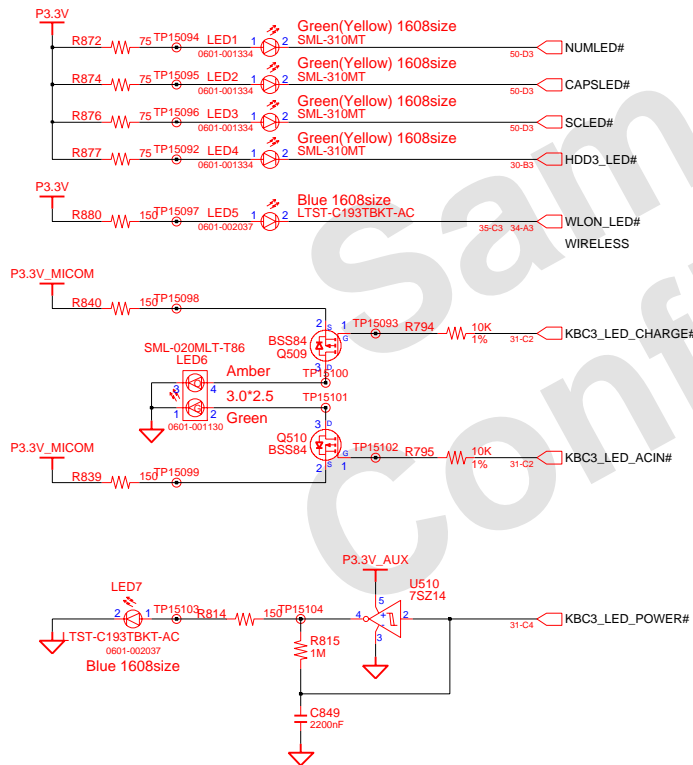


DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV STEP	MP	MAIN		
APPROVAL	KEVIN LEE	REV	1.0	DMB & KBD & DEBUG & POWER S/W		PART NO. BA41-00727/8A
MODULE CODE		LAST EDIT		March 9, 2007 12:03:21 PM	PAGE	49 OF 53

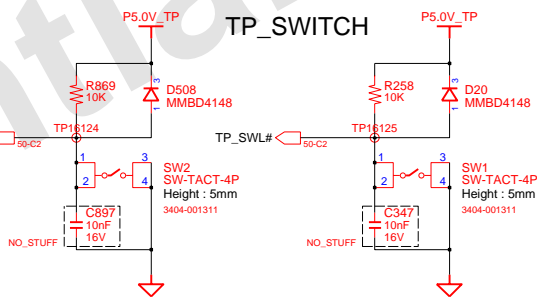
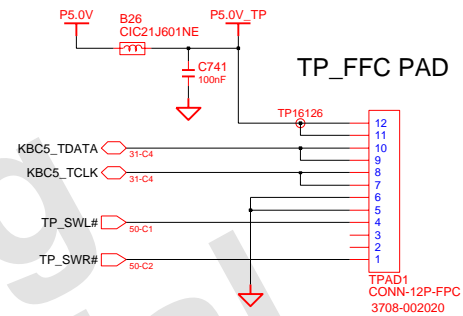
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### LEDS



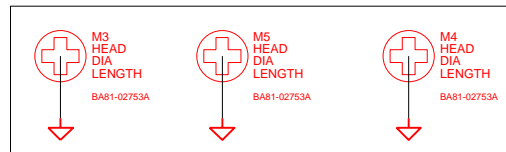
### TOUCH PAD



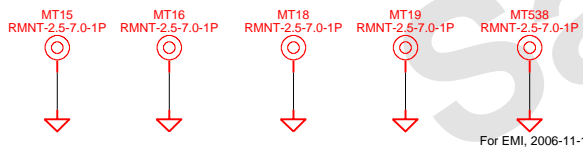
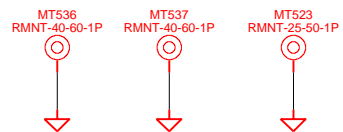
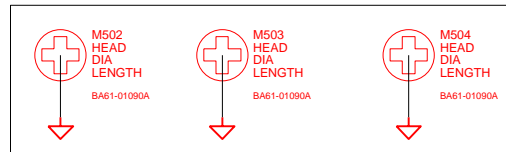
DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	MP	MAIN		
APPROVAL	KEVIN LEE	REV	1.0	TOUCH PAD & LEDS	PART NO.	BA41-00727/8A
MODULE CODE		LAST EDIT		March 9, 2007 12:03:21 PM	PAGE	50 OF 53

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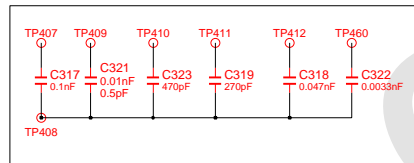
**DMB-NUT**



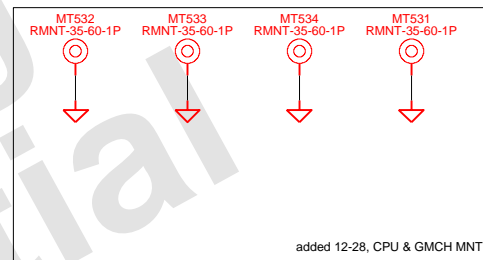
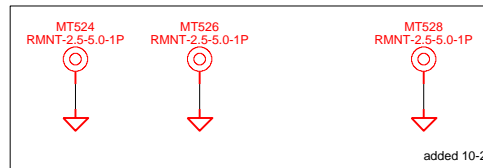
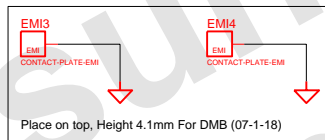
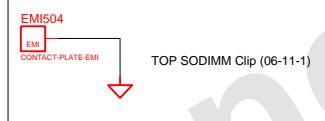
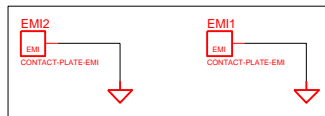
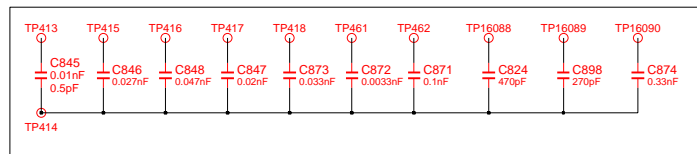
NUT for RHE, Locate on TOP of PCB



**1 - 499 SIDE**



**500 - SIDE**



PCB REVISION CONTROL ( ICT )				
NO	CONNECTION	DATE(Y/M/M/DD)	REVISION	STEP
1	N.C.			
2	1-2			
3	2-3			
4	3-1			
5	1-2-3			
6	N.C.			
7	1-2			
8	2-3			
9	3-1			
10	1-2-3			

REV1  
 1 O  
 2 O O3

DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2 MAIN MOUNT HOLE	<b>SAMSUNG</b> ELECTRONICS PART NO. BA41-00727/8A
CHECK	GUO LEI	DEV. STEP	MP			
APPROVAL	KEVIN LEE	REV	1.0			
MODULE CODE		LAST EDIT	March 9, 2007 12:03:21 PM	PAGE	51 OF 53	

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TP17080 ADT3\_SEL  
 TP17081 AUD3\_GP00  
 TP17082 AUD3\_GP01  
 TP17083 AUD3\_PCBEPP  
 TP17084 AUD5\_INTMIC  
 TP17085 AUD5\_LINE\_OUT\_L  
 TP17086 AUD5\_LINE\_OUT\_R  
 TP17087 AUD5\_MIC\_VREF  
 TP17088 AUD5\_SPDIF  
 TP17089 AUD5\_SPODF\_OUT  
 TP17090 AUD5\_SPK\_L+  
 TP17091 AUD5\_SPK\_L-  
 TP17092 AUD5\_SPK\_R+  
 TP17093 AUD5\_SPK\_R-  
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 TP17097 BAT3\_VOLTA  
 TP17098 CAPSLED#  
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 TP17078 CB3\_CDEVSEGL#  
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 TP17028 CB3\_CVSI2  
 TP17029 CB3\_D14  
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 TP17031 CB3\_MD\_CLK

TP17032 CB3\_MD\_DATA4\_XD  
 TP17033 CB3\_MD\_DATA5\_XD  
 TP17034 CB3\_MD\_DATA6\_XD  
 TP17035 CB3\_MD\_DATA7\_XD  
 TP17036 CB3\_MD\_VCCEN  
 TP17037 CB3\_MD\_XD\_ALE  
 TP17038 CB3\_MD\_XD\_CE#  
 TP17039 CB3\_MD\_XD\_CLE  
 TP17040 CB3\_MD\_XD\_WP#  
 TP17041 CB3\_MS\_BS\_SD\_CMD  
 TP17042 CB3\_MS\_MS\_XD\_CDM  
 TP17043 CB3\_SD\_CD\_CD#  
 TP17044 CB3\_SD\_DATA0\_MS\_SDI0  
 TP17045 CB3\_SD\_DATA1#  
 TP17046 CB3\_SD\_DATA2  
 TP17047 CB3\_SD\_DATA3  
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 TP16935 CLK3\_SMBCLK  
 TP16936 CLK3\_SMBDATA  
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 TP17346 OSMB3\_CLK  
 TP17347 OSMB3\_DATA  
 TP17348 OTHERM\_ALERT#  
 TP17349 OTHERM\_STP#  
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 TP17184 VGA3\_BLUE  
 TP17185 VGA3\_DDCCLK  
 TP17186 VGA3\_DDCDATA  
 TP17187 VGA3\_GREEN  
 TP17188 VGA3\_HSYNC  
 TP17189 VGA3\_RED  
 TP17190 VGA3\_VSYNC  
 TP17191 VGA5\_DDCCLK  
 TP17192 VGA5\_DDCDATA  
 TP17193 VPM3\_CPU\_PWRGD  
 TP17194 WLAN\_LED#  
 TP17195 QCB\_MD\_VCC  
 TP17196 QCB\_VCCA  
 TP17203 QCB\_VPPA  
 TP17207 QCG\_REF  
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 TP17220 QGFX\_CORE  
 TP17221 QG\_OH\_CG  
 TP17222 QG\_CPU  
 TP17225 QG\_DDR  
 TP17227 QG\_EGFX

TP17229 QG\_MIC  
 TP17231 QG\_P1\_05V  
 TP17233 QG\_P3\_3V  
 TP17235 QG\_LCD\_VDD3V  
 TP17236 QG\_LDO\_P5\_4V  
 TP17237 QG\_P1\_05V  
 TP17238 QG\_P1\_05V  
 TP17239 QG\_P1\_05V  
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 TP17268 QG\_P1\_8V\_P2\_5V\_LAN  
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 TP17274 QG\_P2\_5V  
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 TP17354 QG\_P3\_3V  
 TP17355 QG\_P3\_3V  
 TP17356 QG\_P3\_3V  
 TP17357 QG\_P3\_3V  
 TP17358 QG\_P3\_3V\_AUX  
 TP17278 QG\_P3\_3V\_AUX  
 TP17279 QG\_P3\_3V\_AUX  
 TP17280 QG\_P3\_3V\_AUX  
 TP17281 QG\_P3\_3V\_MICOM  
 TP17282 QG\_P3\_3V\_MICOM  
 TP17283 QG\_P3\_3V\_MICOM  
 TP17284 QG\_P3\_3V\_MICOM  
 TP17285 QG\_P5\_0V  
 TP17286 QG\_P5\_0V  
 TP17287 QG\_P5\_0V  
 TP17288 QG\_P5\_0V  
 TP17289 QG\_P5\_0V\_ALW  
 TP17290 QG\_P5\_0V\_ALW  
 TP17291 QG\_P5\_0V\_AUD  
 TP17292 QG\_P5\_0V\_AUD  
 TP17293 QG\_P5\_0V\_AUD  
 TP17294 QG\_P5\_0V\_AUD  
 TP17295 QG\_P5\_0V\_AUD  
 TP17296 QG\_PRTC\_BAT  
 TP17300 QG\_PWR\_DC  
 TP17301 QG\_PWR\_DC  
 TP17302 QG\_PWR\_DC  
 TP17303 QG\_PWR\_DC  
 TP17304 QG\_VDD\_INV  
 TP17305 QG\_VDD\_INV  
 TP17306 QG\_VDD\_INV  
 TP17307 QG\_VDD\_INV  
 TP17308 QG\_VDD\_AMP  
 TP17309 QG\_VDD\_AUD  
 TP17376 QG\_GROUND  
 TP17377 QG\_GROUND  
 TP17378 QG\_GROUND  
 TP17379 QG\_GROUND  
 TP17380 QG\_GROUND

DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2 MAIN TEST POINT	<b>SAMSUNG</b> ELECTRONICS PART NO. BA41-00727/8A
CHECK	GUO LEI	DEV STEP	MP			
APPROVAL	KEVIN LEE	REV	1.0			
MODULE CODE		LAST ENT				
			March 9, 2007 12:03:21 PM	PAGE	52 OF 53	

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REVISION HISTORY

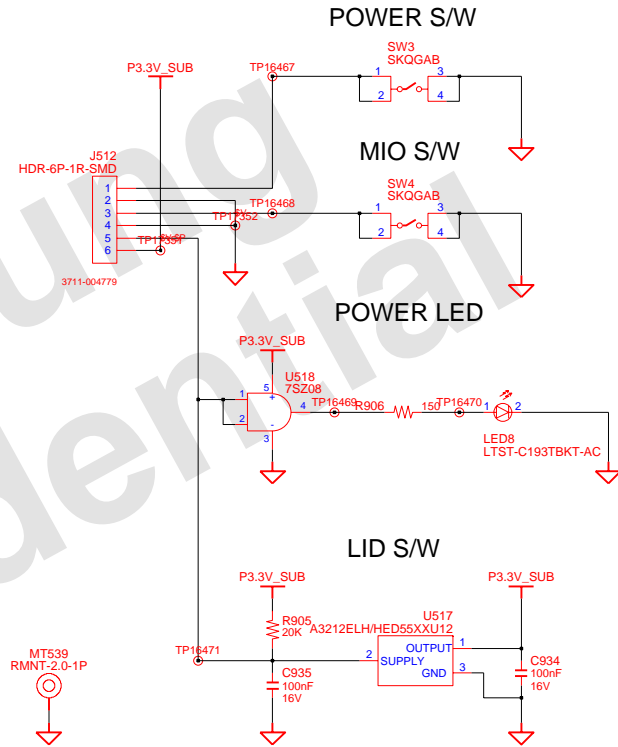
REVISION	DATE	NO.#	CONTENTS
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NOTES

Note1. Schematic Revision: MP 1.0.0  
 Development Stage  
 PCB Version  
 Schematic Version

Note2. PCB SEC Code  
 Vendor: SGCE P/N: BA41-??????  
 Vendor: TPT P/N: BA41-??????

ON TOP BOARD



DRAW	ZHOU JUN	DATE	3/9/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV STEP	MP	MAIN	MAIN	
APPROVAL	KEVIN LEE	REV	1.0	REVISION HISTORY	REVISION HISTORY	PART NO. BA41-00727/8A
MODULE CODE		LAST EDIT	March 9, 2007 12:03:21 PM	PAGE	53	OF 53