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TORINO 2

PCB Thickness:1mm

CPU :INTEL MEROM
Chip Set :INTEL 965GM & ICH8-M
Remarks :w/o INTEL AMT
2 SODIMMs

Model Name : TORINO 2
PBA Name : MAIN
PCB Code : BA41-00727A / 728A
Dev. Step : MP
Revision : 1.1
T.R. Date : 2007.04.10

DRAW	CHECK	APPROVAL
-	-	-

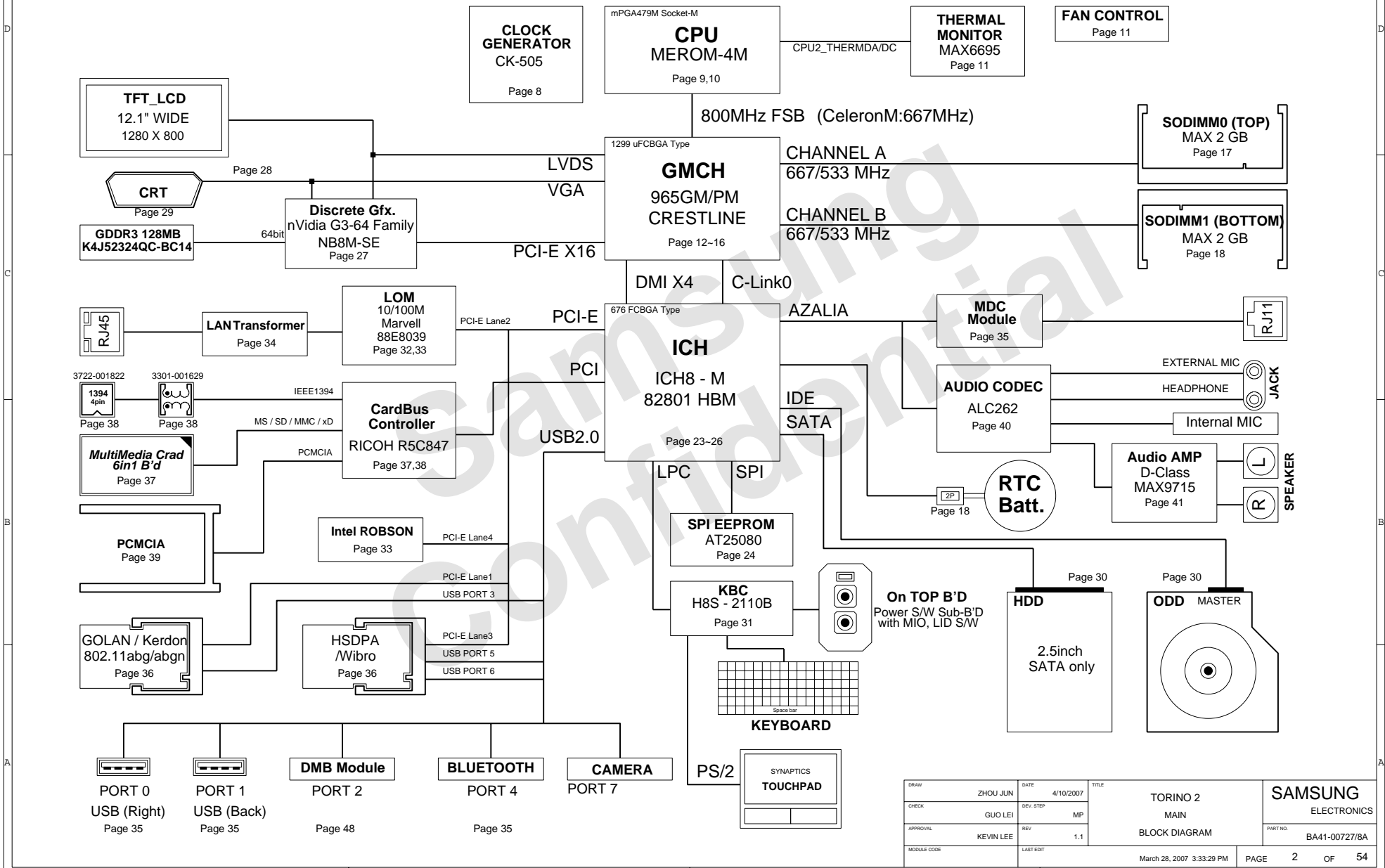
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DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	MP		COVER	
APPROVAL	KEVIN LEE	REV	1.1		CONTENTS	PART NO. BA41-00727/8A
MODULE CODE		LAST ENT			March 28, 2007 3:33:29 PM	PAGE 1 OF 54

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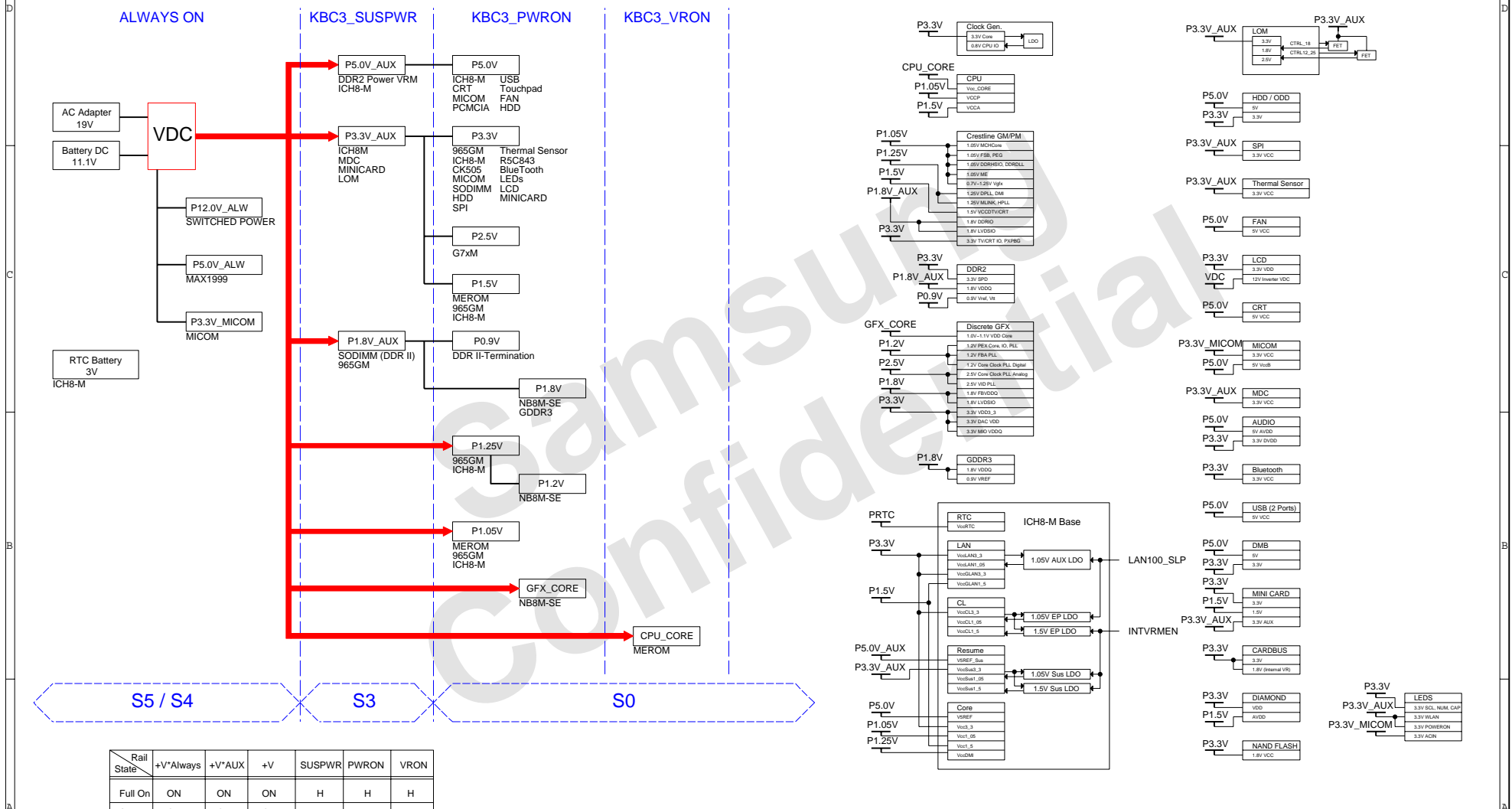
OPERATION BLOCK DIAGRAM



DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
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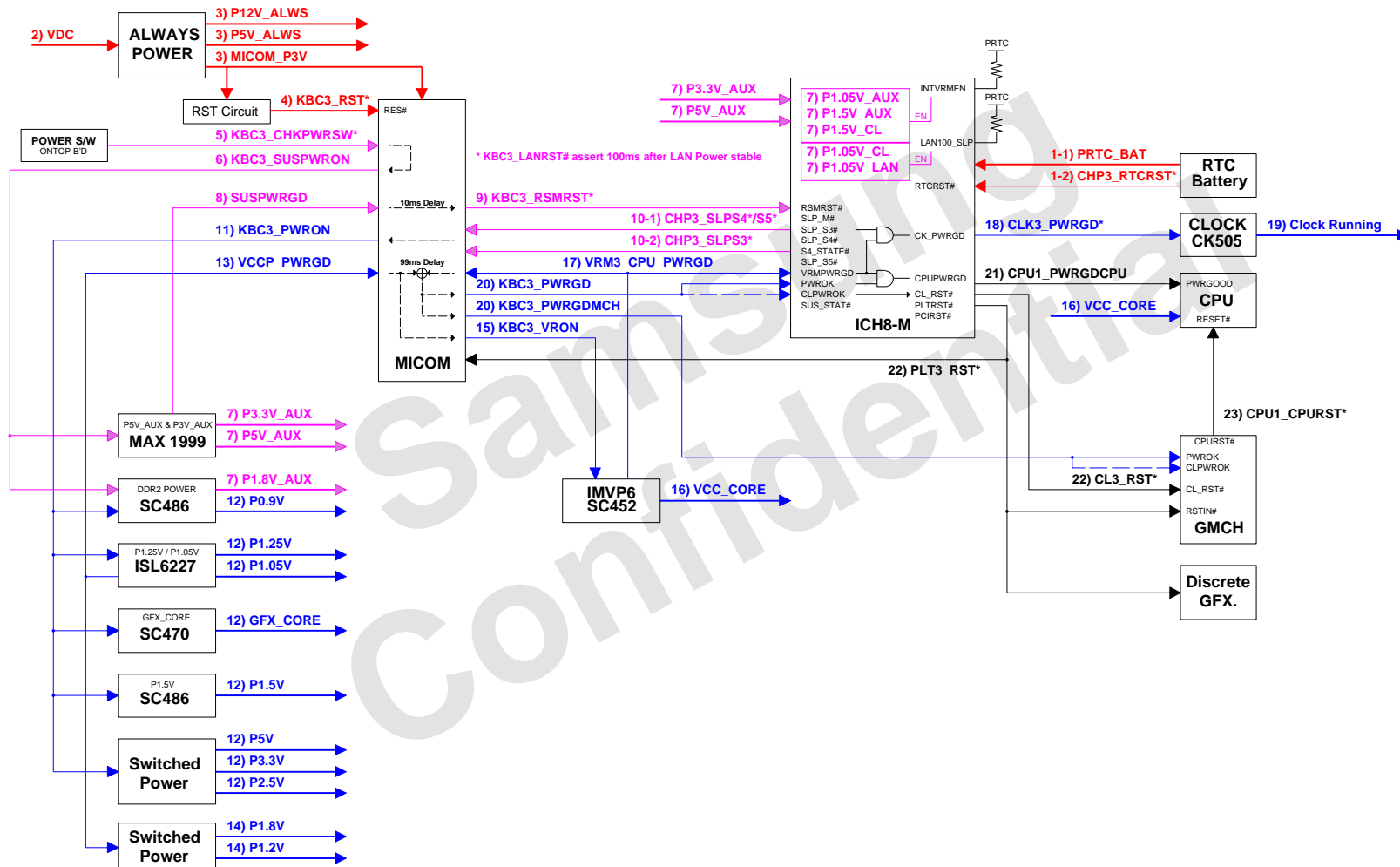
POWER DIAGRAM



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CHECK	GUO LEI	DEV. STEP	MP			
APPROVAL	KEVIN LEE	REV	1.1			
MODULE CODE		LAST ENT				

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POWER SEQUENCE Rev. 0.1



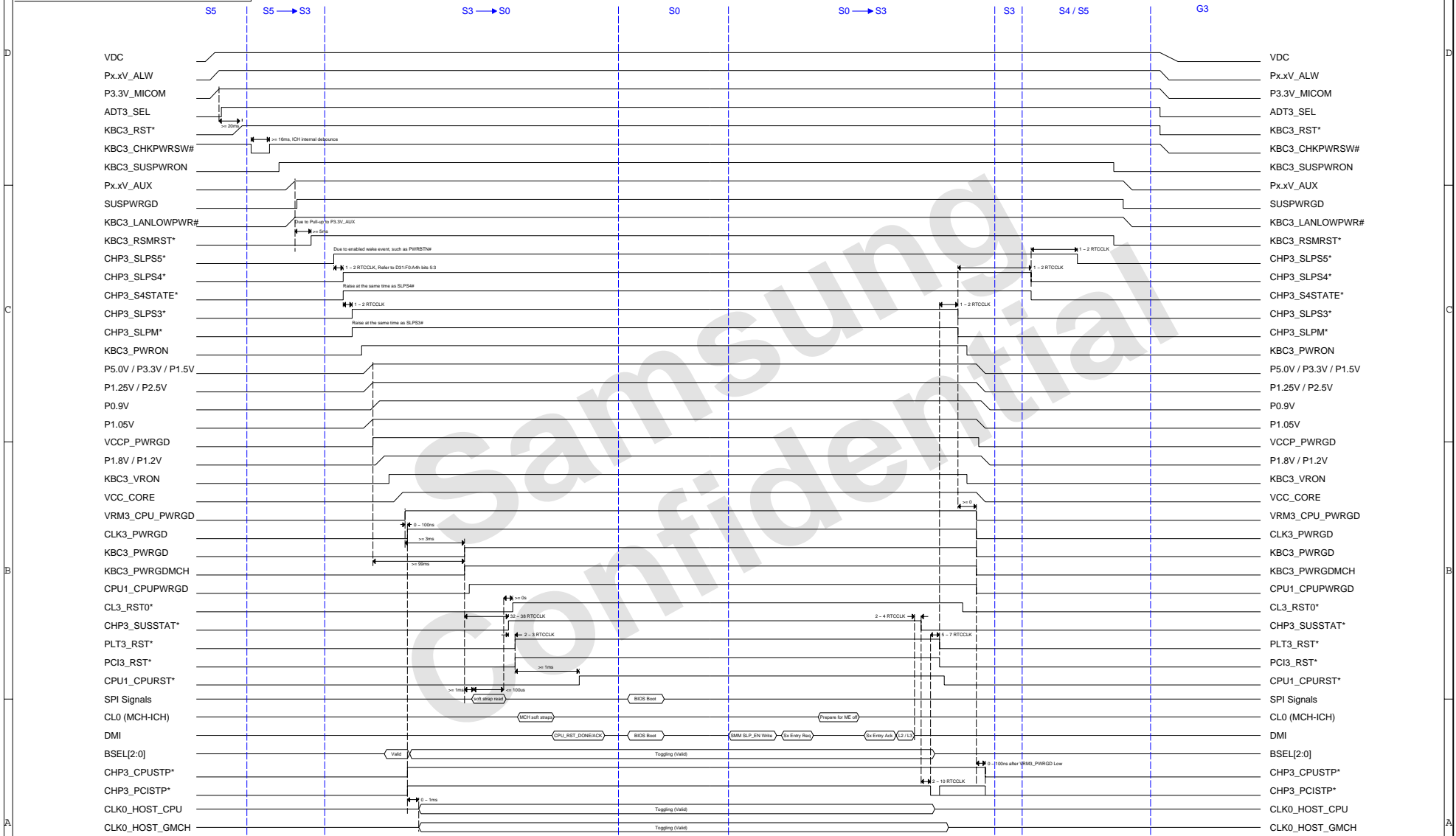
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CHECK	GUO LEI	DEV. STEP	MP		PART NO. BA41-00727/8A	
APPROVAL	KEVIN LEE	REV	1.1			
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Timing Diagram, no ME

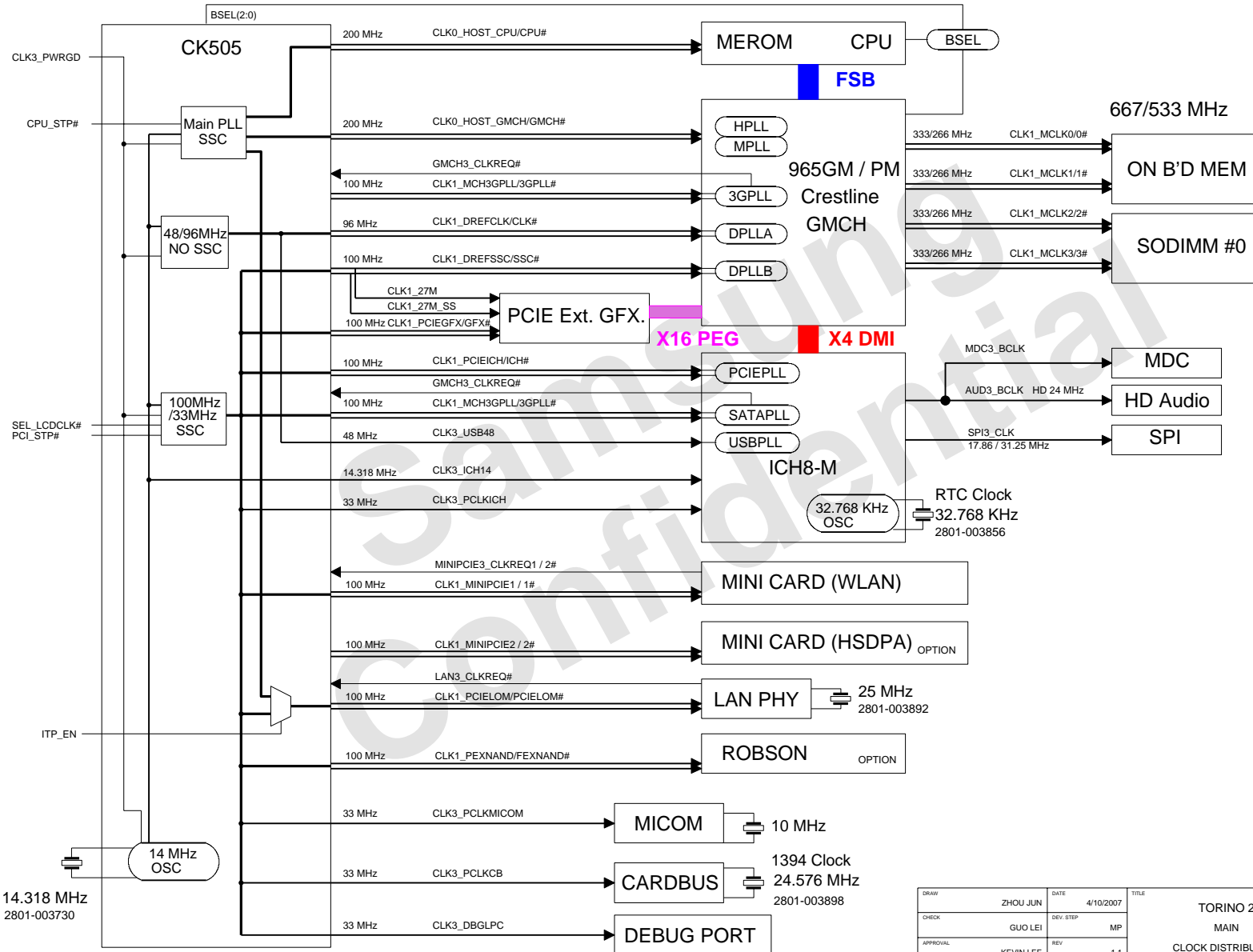
Rev. 0.2 Phil 2006-9-21



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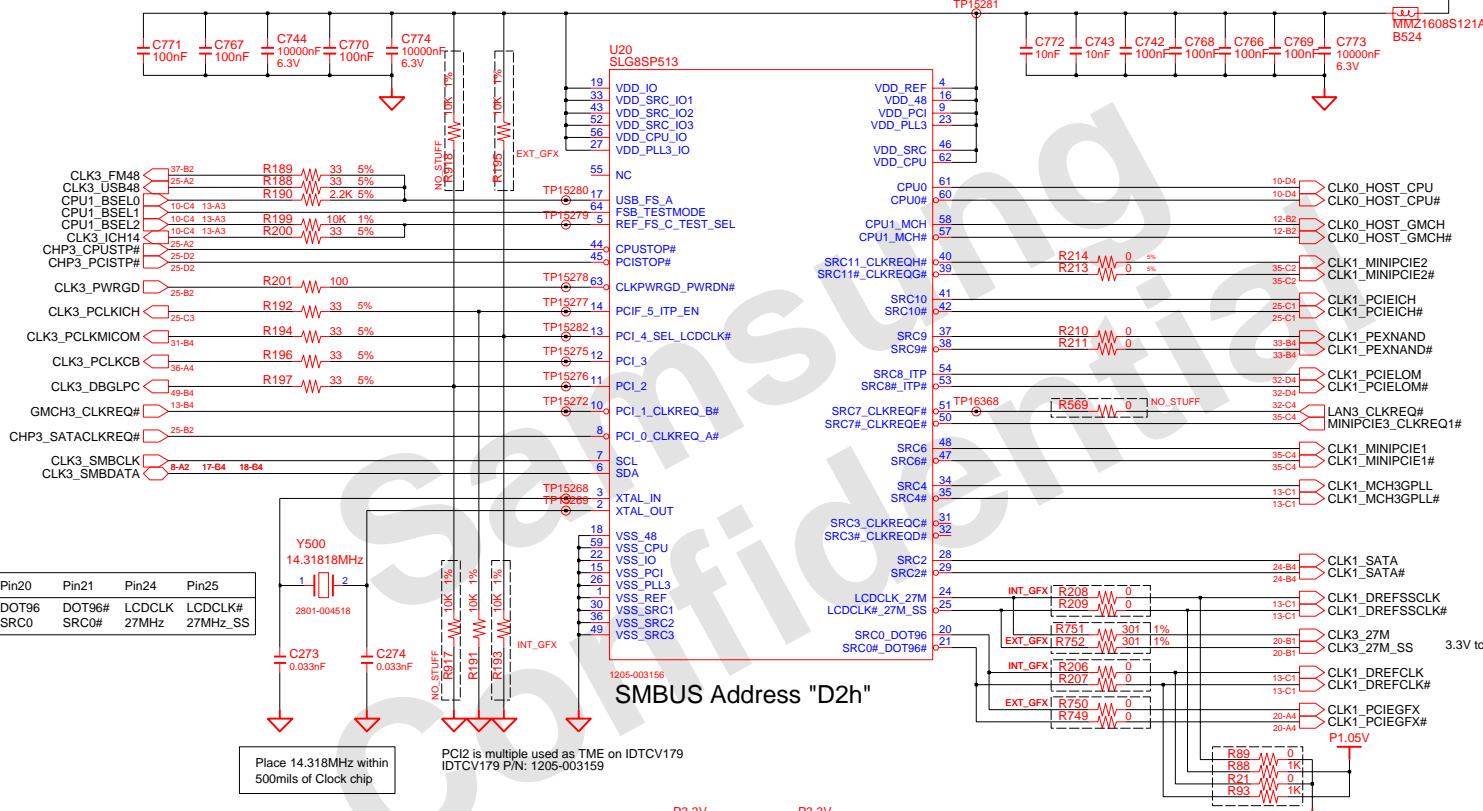
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CLOCK DISTRIBUTION Rev. 0.1



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CHECK	GUO LEI	DEV. STEP	MP	MAIN		
APPROVAL	KEVIN LEE	REV	1.1	CLOCK DISTRIBUTION	PART NO.	BA41-00727/8A
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SEL_LCDCLK#	Pin20	Pin21	Pin24	Pin25
0	DOT96	DOT96#	LCDCLK	LCDCLK#
1	SRC0	SRC0#	27MHz	27MHz_SS

Place 14.318MHz within 500mils of Clock chip

PCI2 is multiple used as TME on IDTCV179 IDTCV179 P/N: 1205-003159

SMBUS Address "D2h"

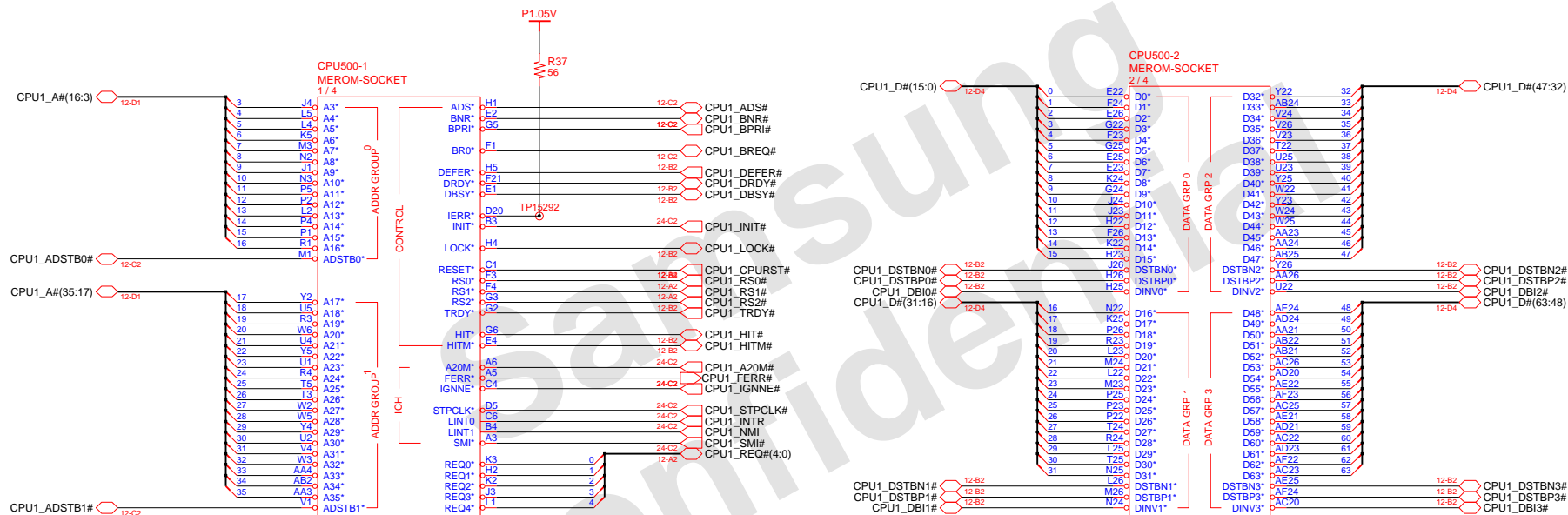
3.3V to 1.2V Translation for G72M

CPU	FSA	FSB	FSC	HOST CLK
	BSEL0	BSEL1	BSEL2	
0	0	0	0	266 MHz
0	0	0	1	333 MHz
0	1	0	0	200 MHz
0	1	1	1	400 MHz
1	1	0	0	133 MHz
1	1	0	1	100 MHz
1	1	1	0	166 MHz
1	1	1	1	RSVD

CLK REQ	Mapping	Device
A#	SRC_2	SATA
B#	SRC_4	GMCH
E#	SRC_6	MiniCard(WLAN)
F#	SRC_8	GbE LAN (100M: N/A)

DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV_STEP	MP	MAIN		
APPROVAL	KEVIN LEE	REV	1,1	CLOCK GENERATOR		PART NO. BA41-00727/8A
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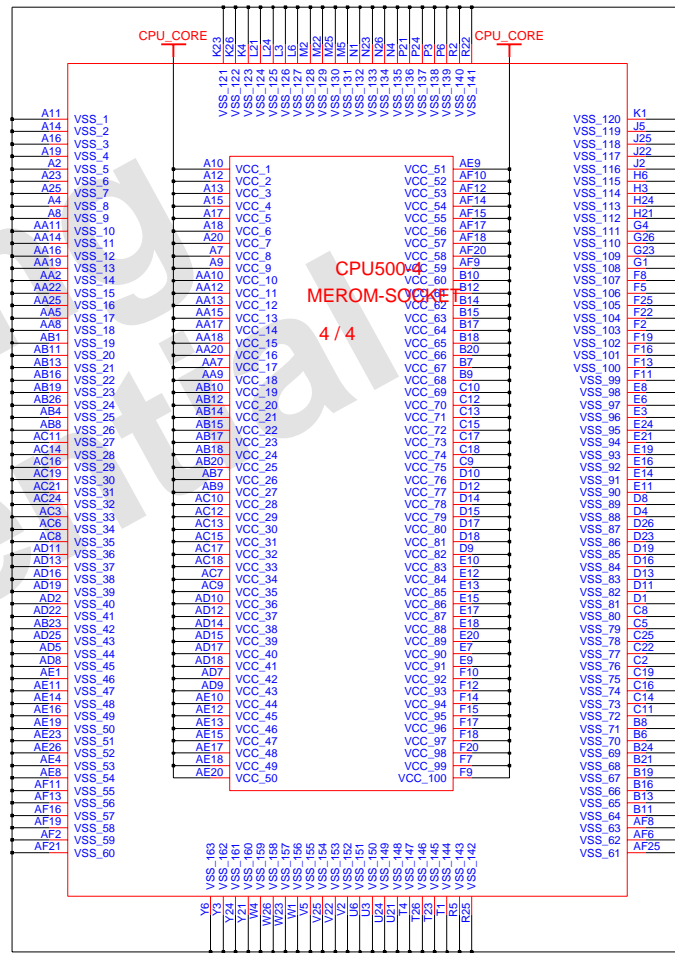
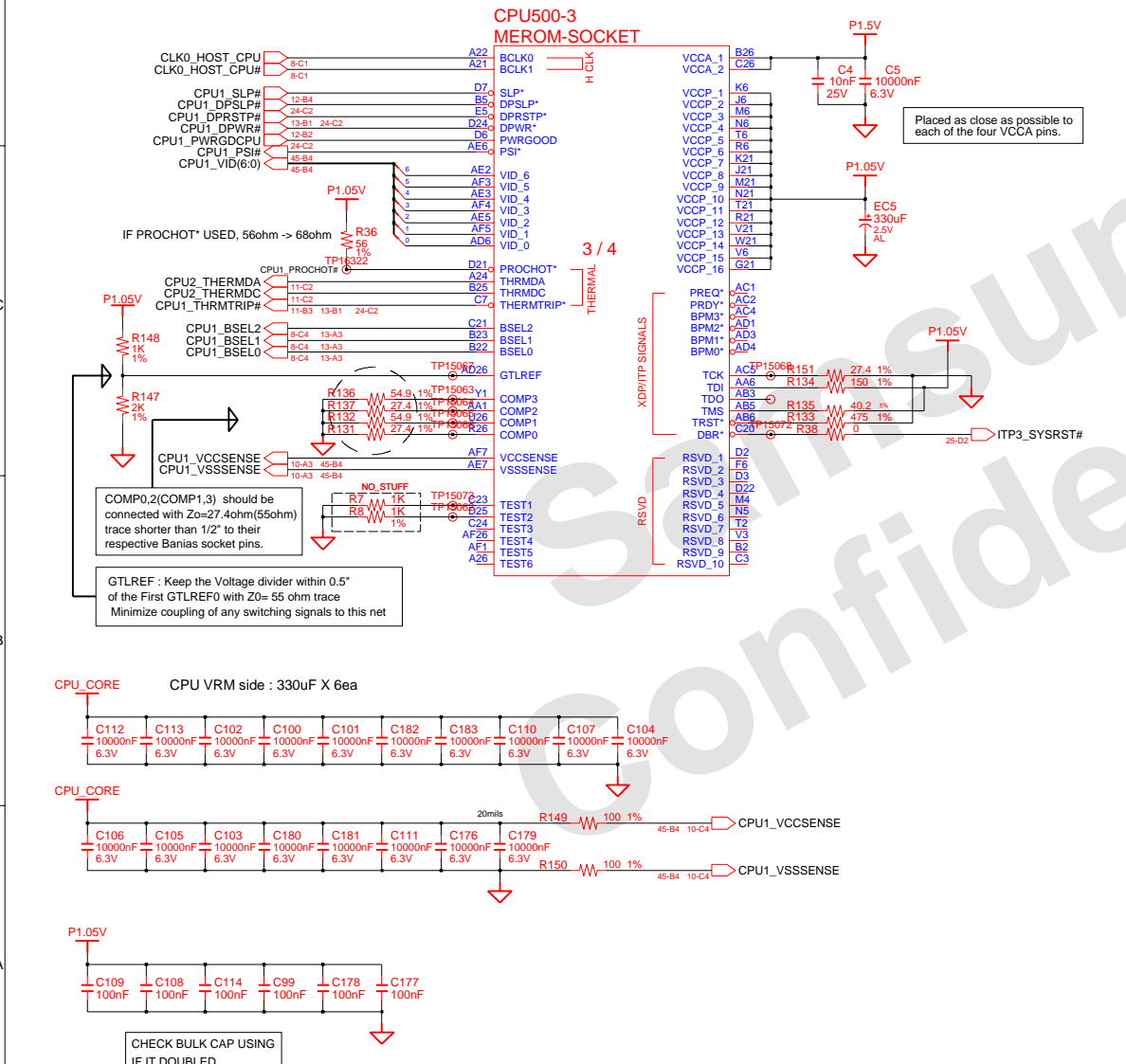
CPU SOCKET : MEROM



DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2 MAIN MEROM(1)	SAMSUNG ELECTRONICS PART NO. BA41-00727/8A
CHECK	GUO LEI	DEV. STEP	MP			
APPROVAL	KEVIN LEE	REV	1.1			
MODULE CODE		LAST TEST				
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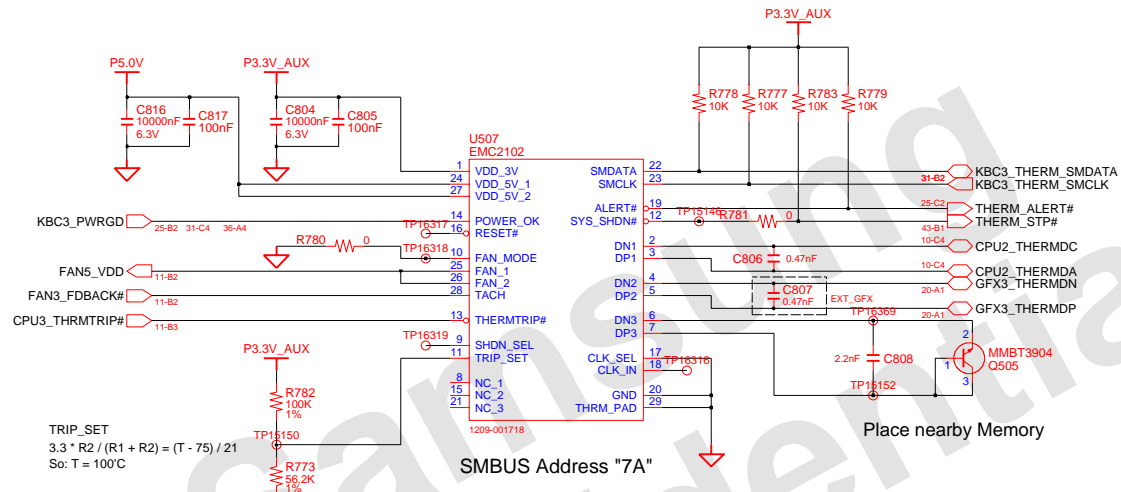
CPU SOCKET : MEROM



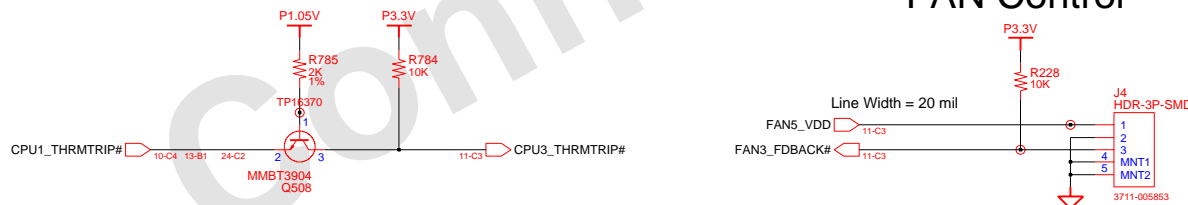
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APPROVAL	KEVIN LEE	REV	1.1	MEROM(2)	PAGE	10 OF 54
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Thermal Monitor

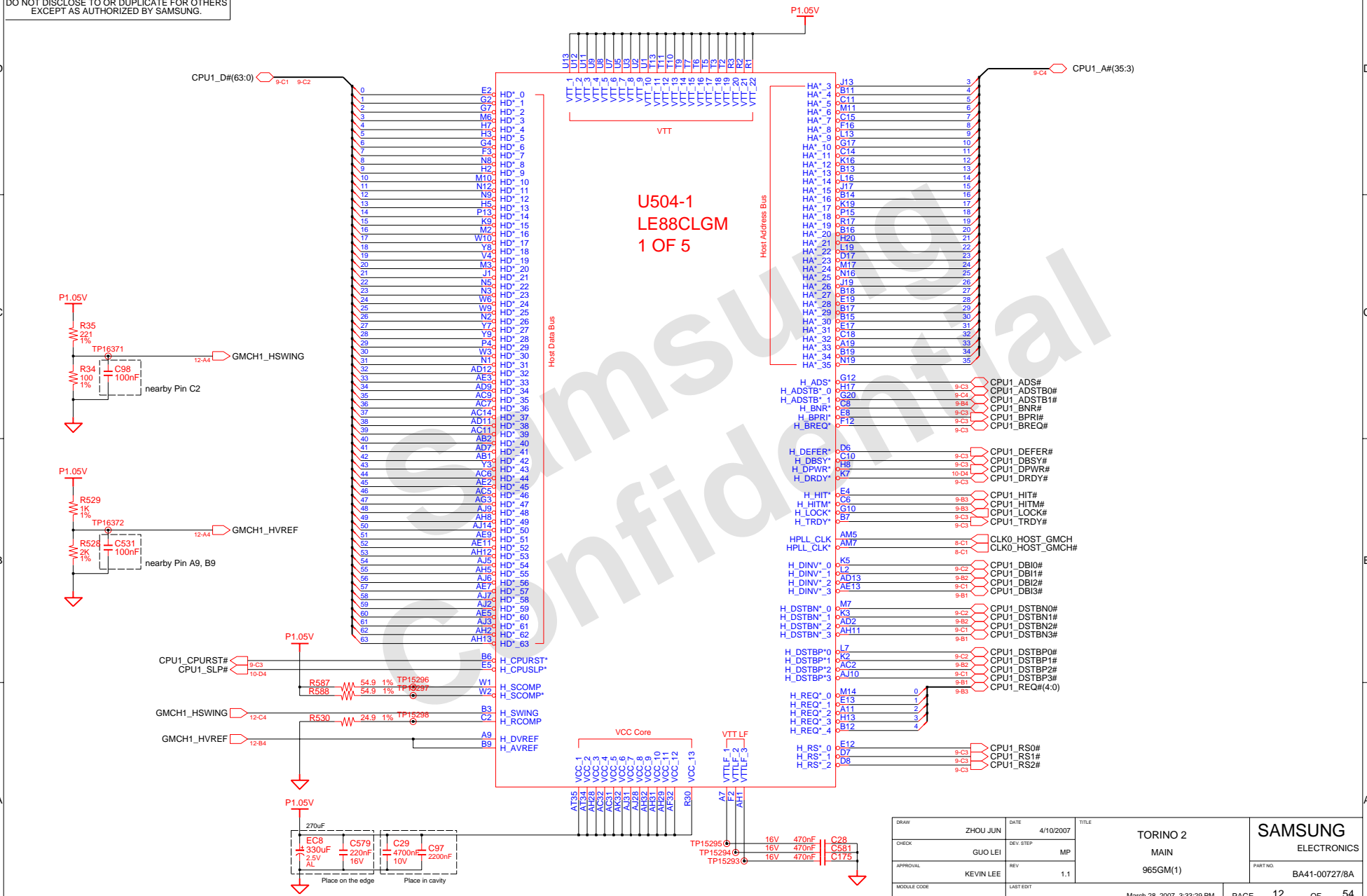


FAN Control



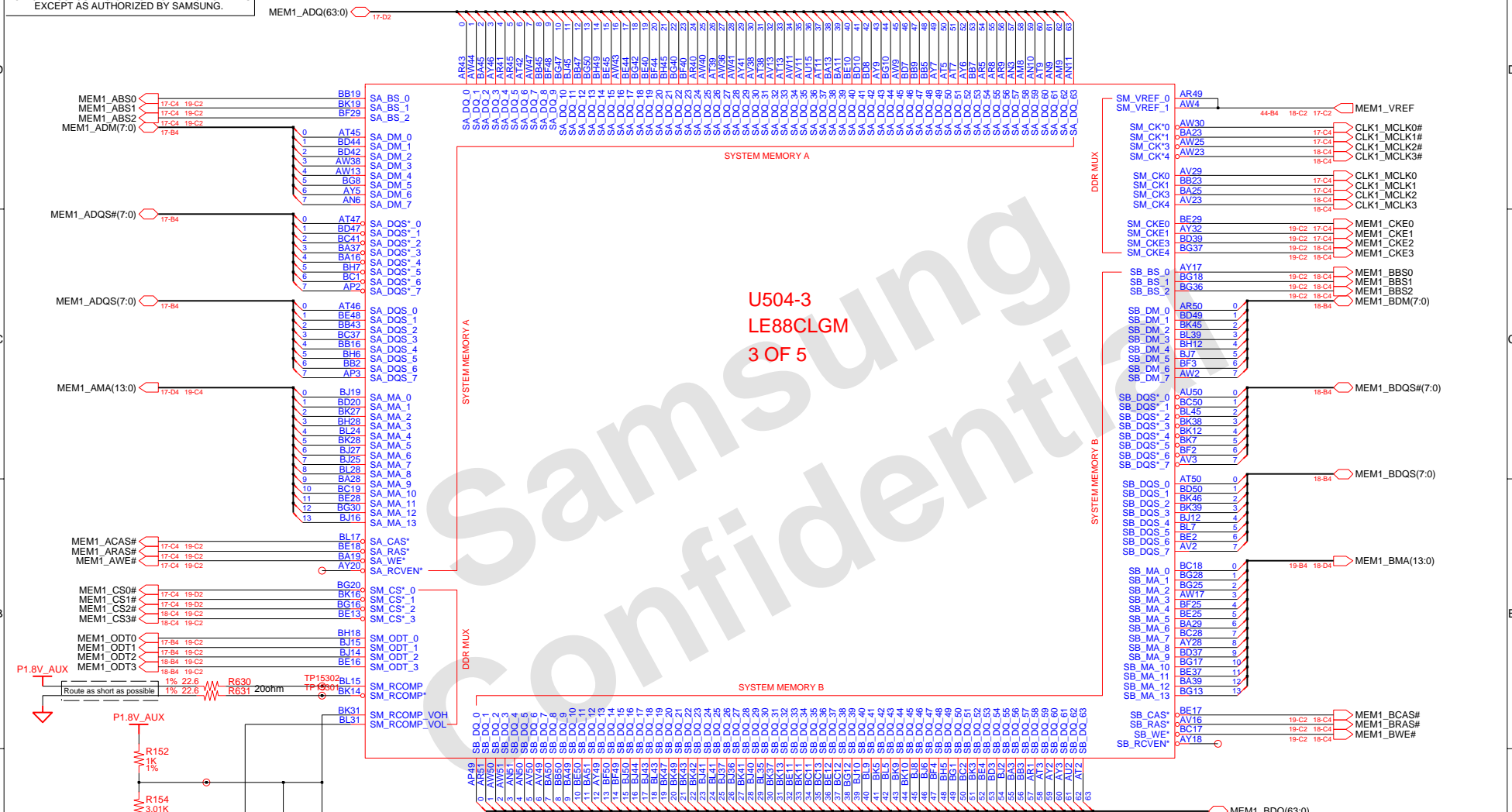
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APPROVAL	KEVIN LEE	REV	1,1			
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CHECK	GUO LEI	DEV_STEP	MP		MAIN	
APPROVAL	KEVIN LEE	REV	1.1		965GM(1)	
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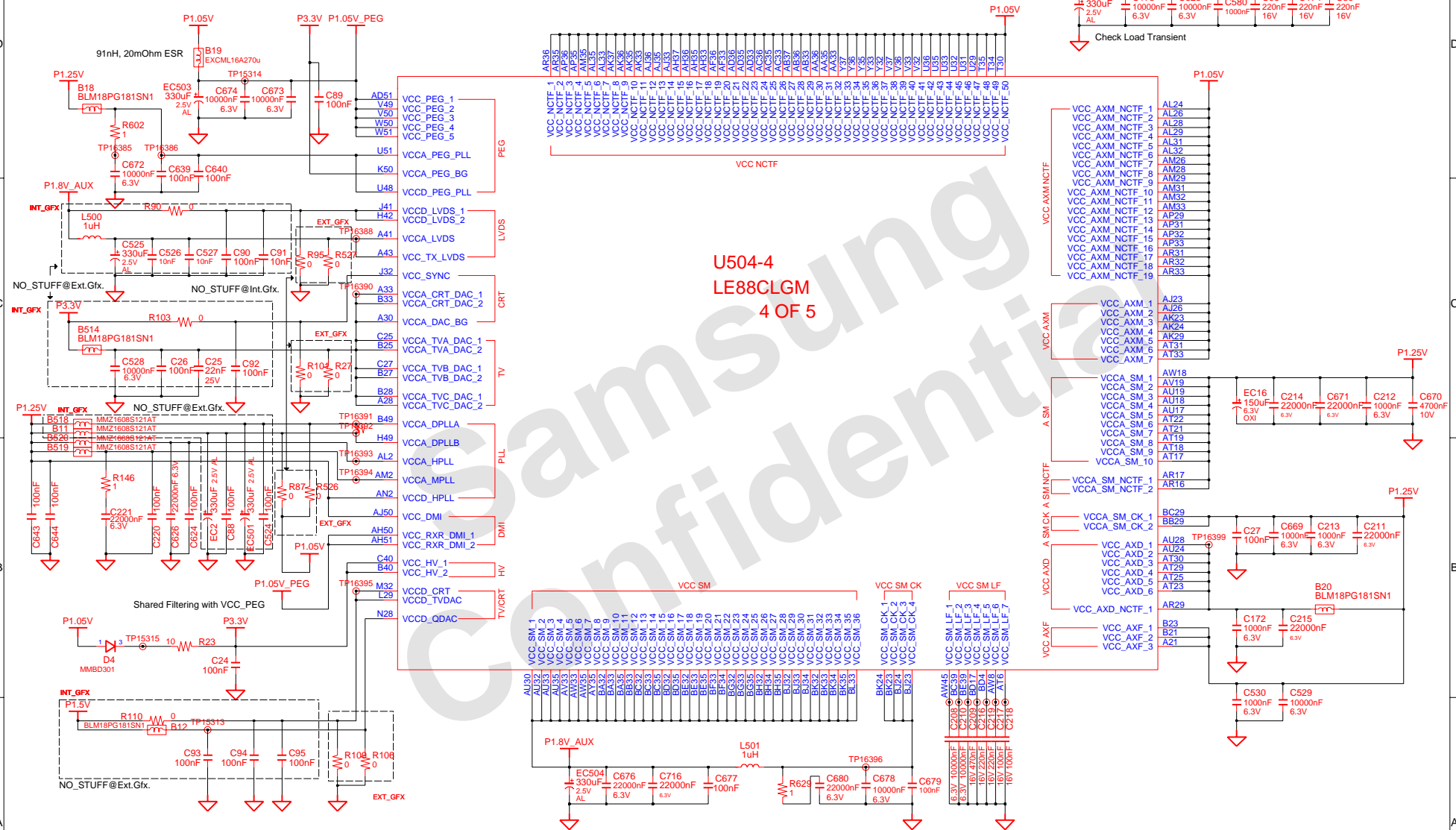


**U504-3
 LE88CLGM
 3 OF 5**

Dual Channel	Ch. A (So-DIMM A)	Ch. B (So-DIMM B)
SM_CK(2:0)	SA_CK(2:0)	N/A
SM_CK(2:0)*	SA_CK(2:0)*	N/A
SM_CK(5:3)	N/A	SB_CK(2:0)
SM_CK(5:3)*	N/A	SB_CK(2:0)*
SM_CS(1:0)*	SA_CS(1:0)*	N/A
SM_CKE(1:0)	SA_CKE(1:0)	N/A
SM_ODT(1:0)	SA_ODT(1:0)	N/A
SM_CS(3:2)	N/A	SB_CS(3:2)*
SM_SKE(3:2)	N/A	SB_CKE(3:2)
SM_ODT(3:2)	N/A	SB_ODT(3:2)

DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV_STEP	MP		MAIN	
APPROVAL	KEVIN LEE	REV	1.1		965GM(3)	
MODULE CODE		LAST EDIT				PART NO. BA41-00727/8A
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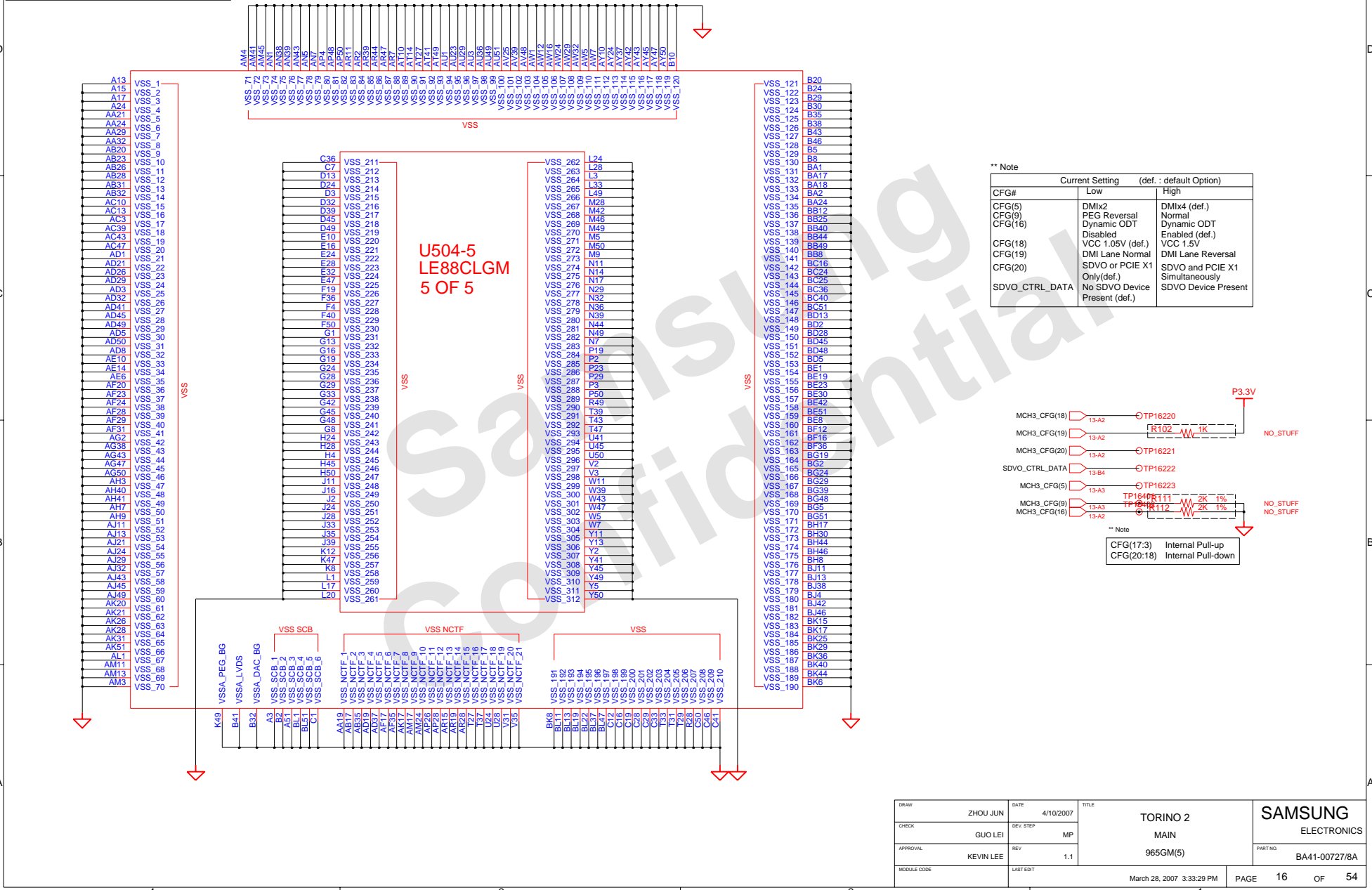


U504-4
 LE88CLGM
 4 OF 5

DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
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APPROVAL	KEVIN LEE	REV	1.1		965GM(4)	
MODULE CODE		LAST ENT				

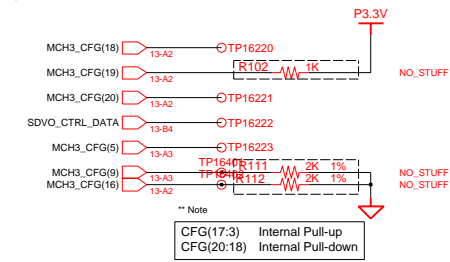
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**** Note**

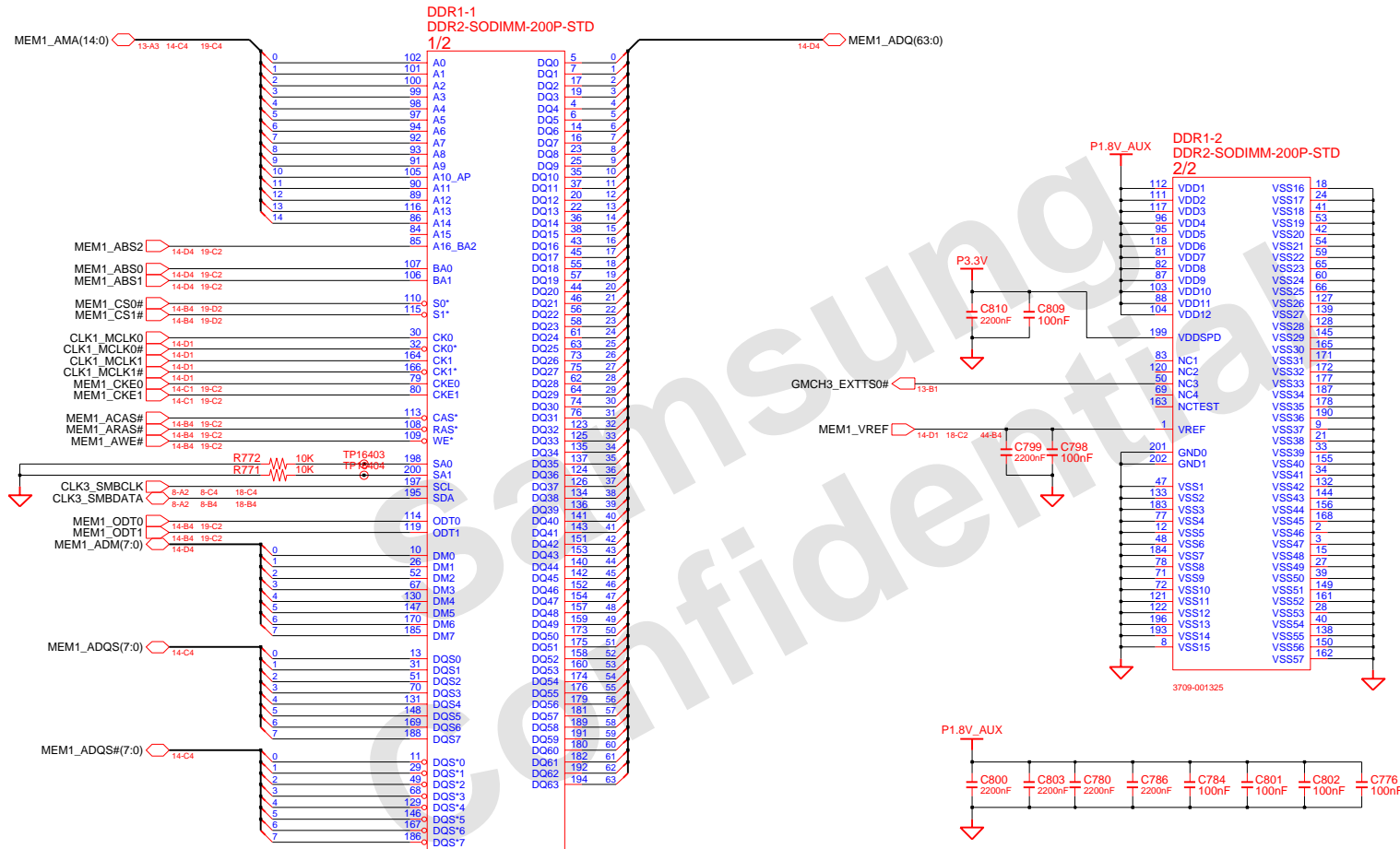
CFG#	Current Setting (def. : default Option)	
	Low	High
CFG(5)	DMix2	DMix4 (def.)
CFG(9)	PEG Reversal	Normal
CFG(16)	Dynamic ODT	Dynamic ODT
	Disabled	Enabled (def.)
CFG(18)	VCC 1.05V (def.)	VCC 1.5V
CFG(19)	DMI Lane Normal	DMI Lane Reversal
CFG(20)	SDVO or PCIe X1 Only(def.)	SDVO and PCIe X1 Simultaneously
SDVO_CTRL_DATA	No SDVO Device Present (def.)	SDVO Device Present



DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS PART NO. BA41-00727/8A
CHECK	GUO LEI	DEV_STEP	MP		MAIN	
APPROVAL	KEVIN LEE	REV	1,1		965GM(5)	
MODULE CODE		LAST ENT				
				March 28, 2007 3:33:29 PM	PAGE	16 OF 54

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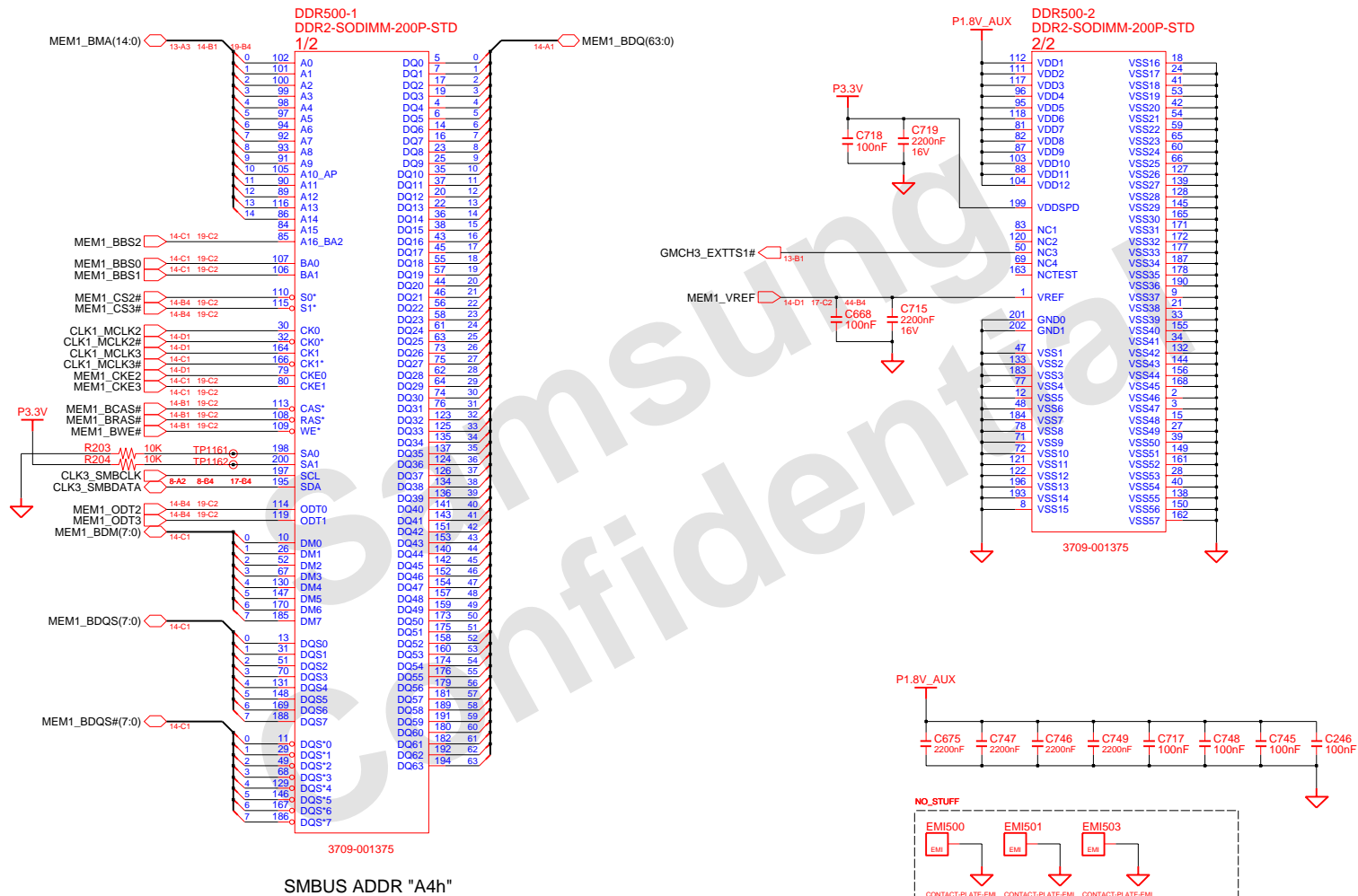
SODIMM0 (TOP)



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CHECK	GUO LEI	DEV. STEP	MP		MAIN	
APPROVAL	KEVIN LEE	REV	1.1		DDR2 SODIMM (TOP)	PART NO. BA41-00727/8A
MODULE CODE		LAST EDIT		March 28, 2007 3:33:29 PM	PAGE	17 OF 54

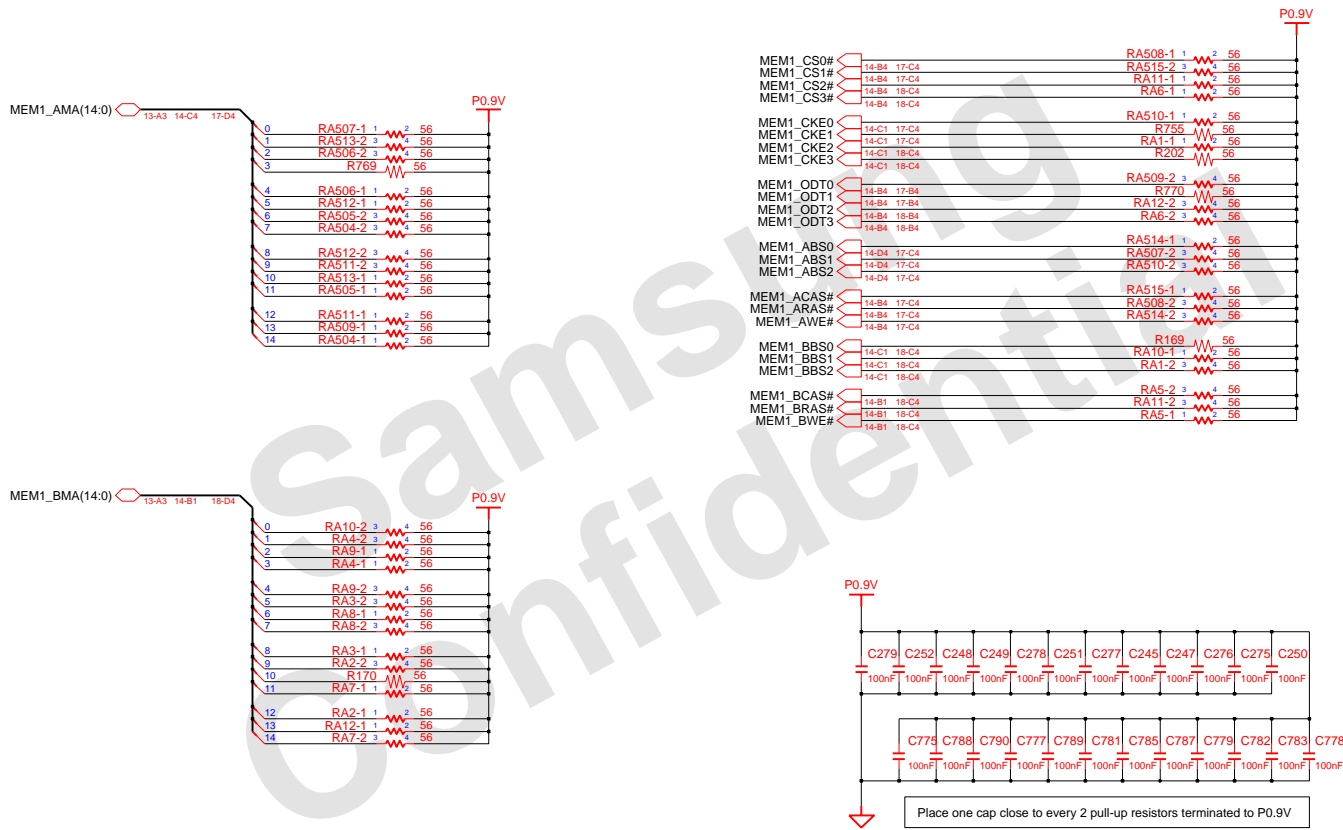
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SODIMM1 (BOTTOM)



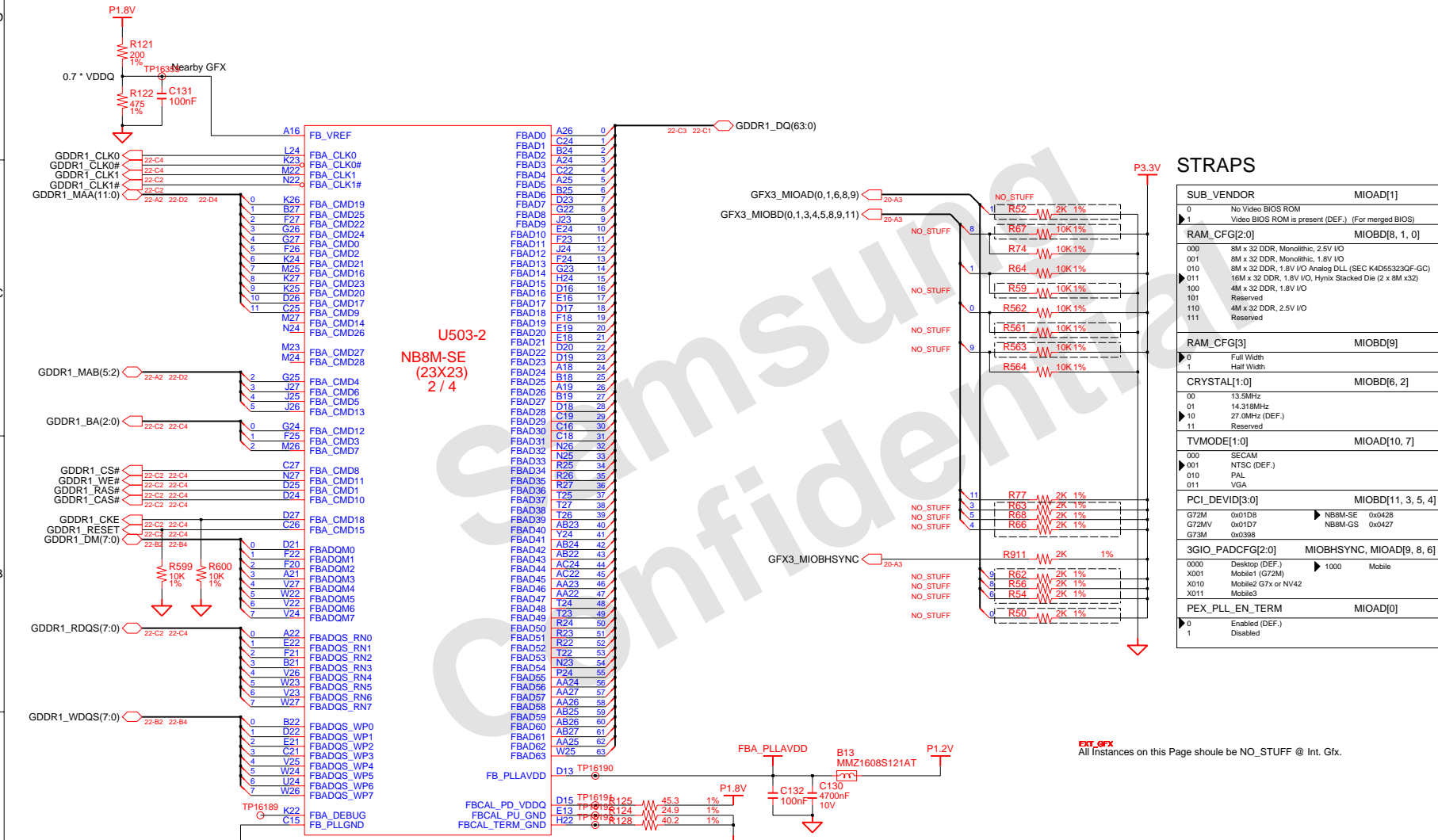
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APPROVAL	KEVIN LEE	REV	1.1		DDR2 SODIMM (BOTTOM)	PART NO. BA41-00727/8A
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CHECK	GUO LEI	DEV. STEP	MP	MAIN		
APPROVAL	KEVIN LEE	REV	1,1	DDR2 TERMINATION	PART NO.	BA41-00727/8A
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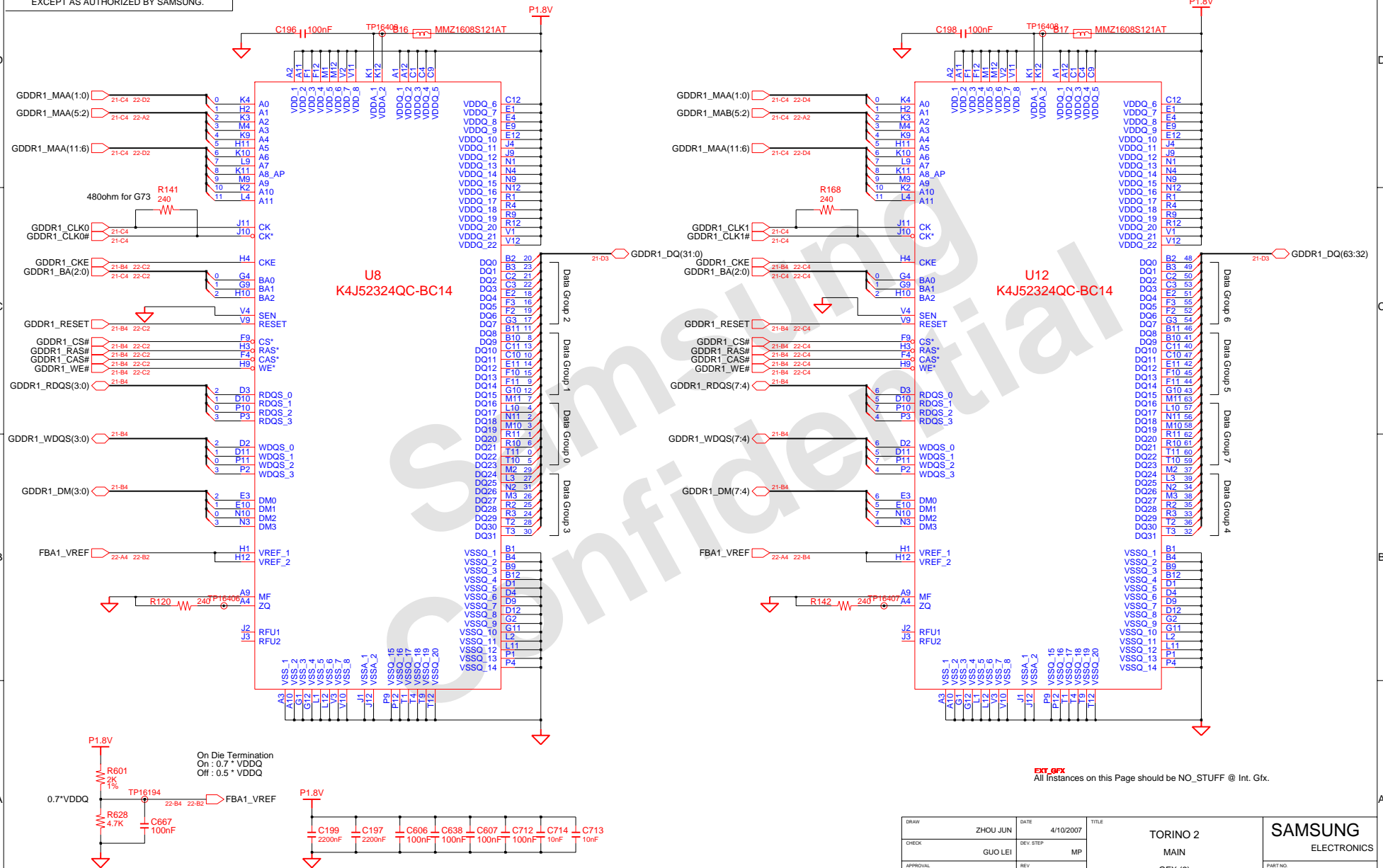
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FBCAL_PU_GND pull down resistor
 NB8M: 24.9 ohm (2007-007302)
 G72M: 33 ohm (2007-001292)

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CHECK	GUO LEI	DEV_STEP	MP	MAIN		
APPROVAL	KEVIN LEE	REV	1.1	GFX (2)		
MODULE CODE		LAST ENT				
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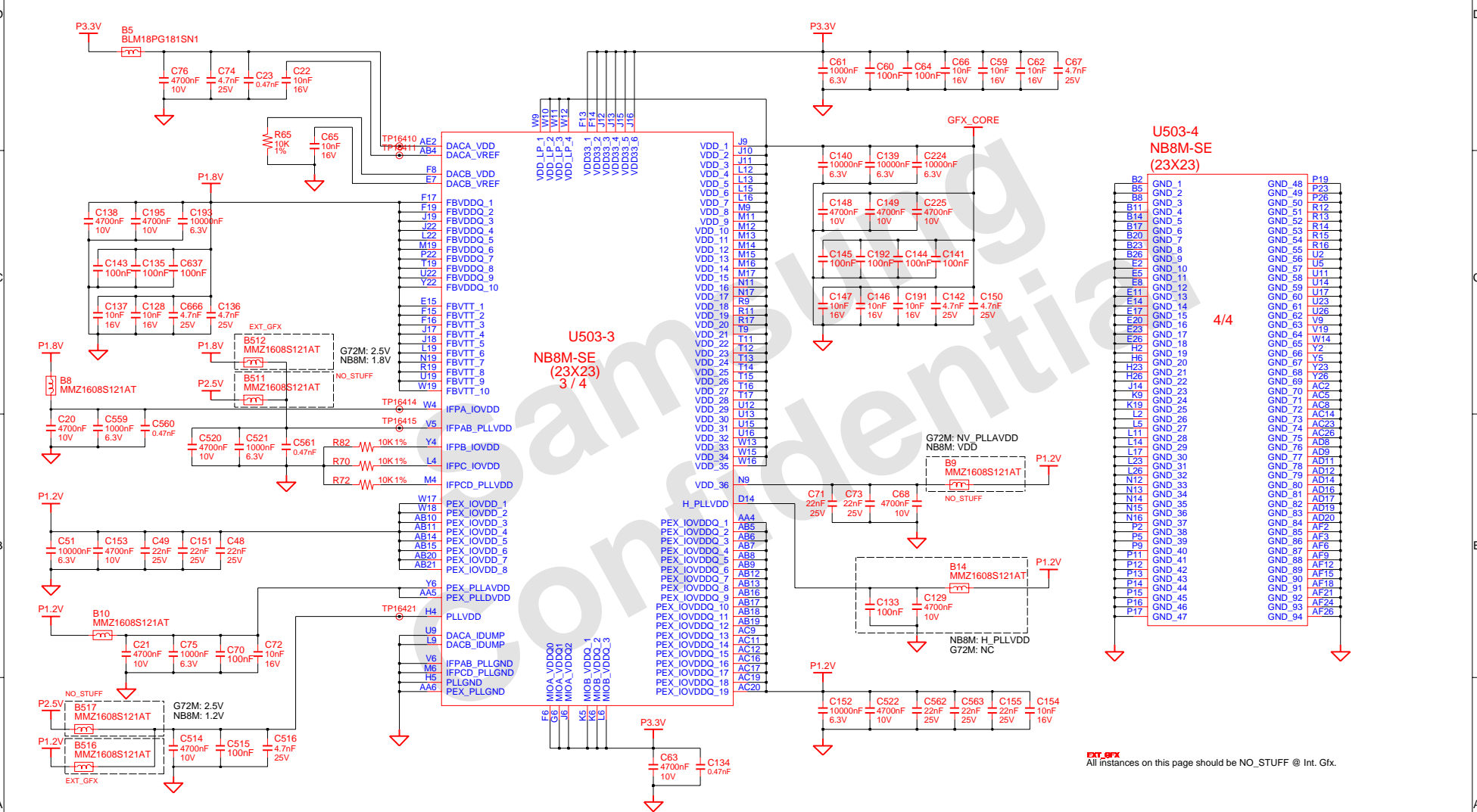
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EXT GFX
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CHECK	GUO LEI	DEV STEP	MP	MAIN	MP	
APPROVAL	KEVIN LEE	REV	1.1	GFX (3)	PART NO.	BA41-00727/8A
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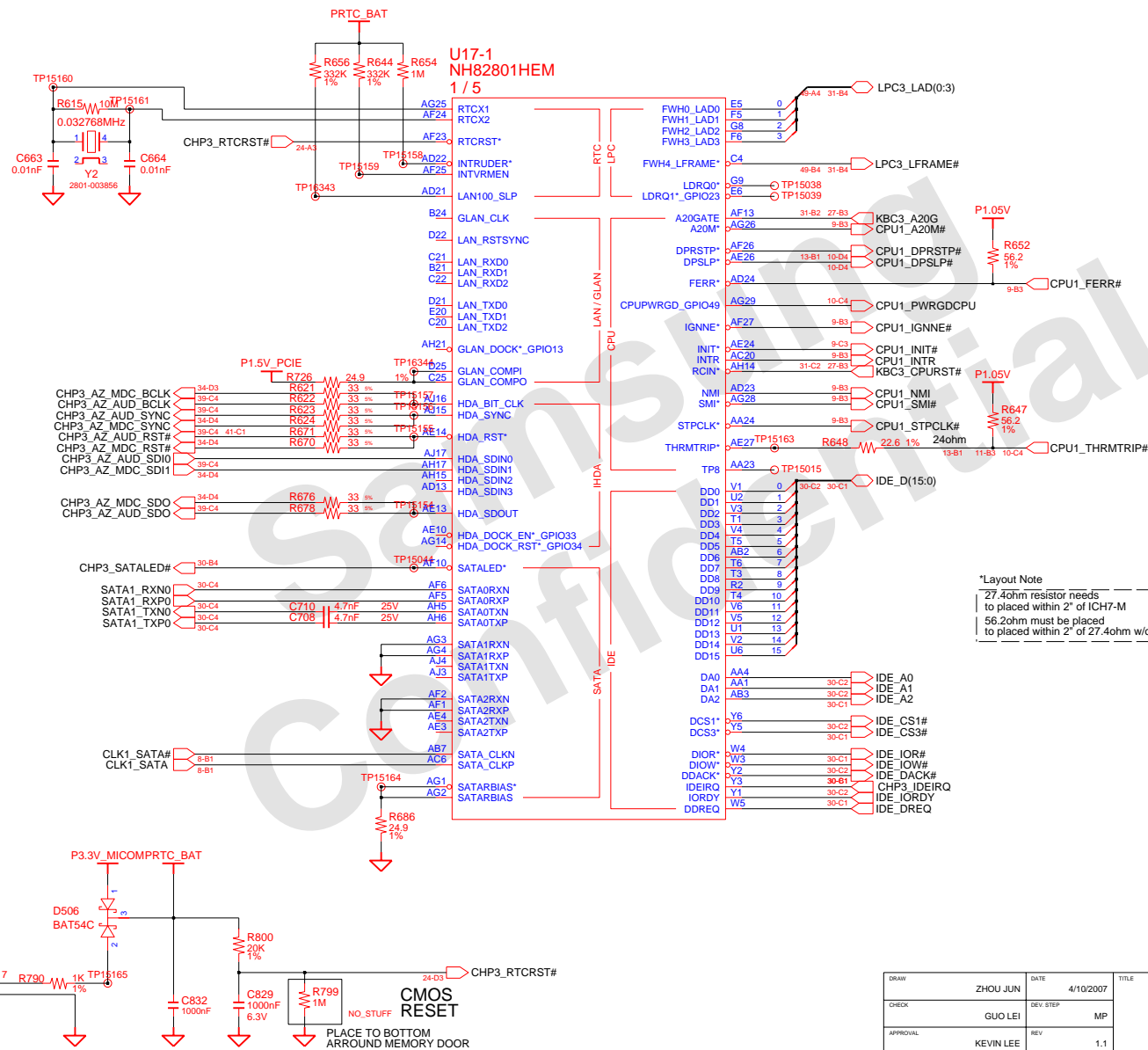
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CHECK	GUO LEI	DEV_STEP	MP	MAIN	MAIN	
APPROVAL	KEVIN LEE	REV	1.1	GFX (4)		PART NO. BA41-00727/8A
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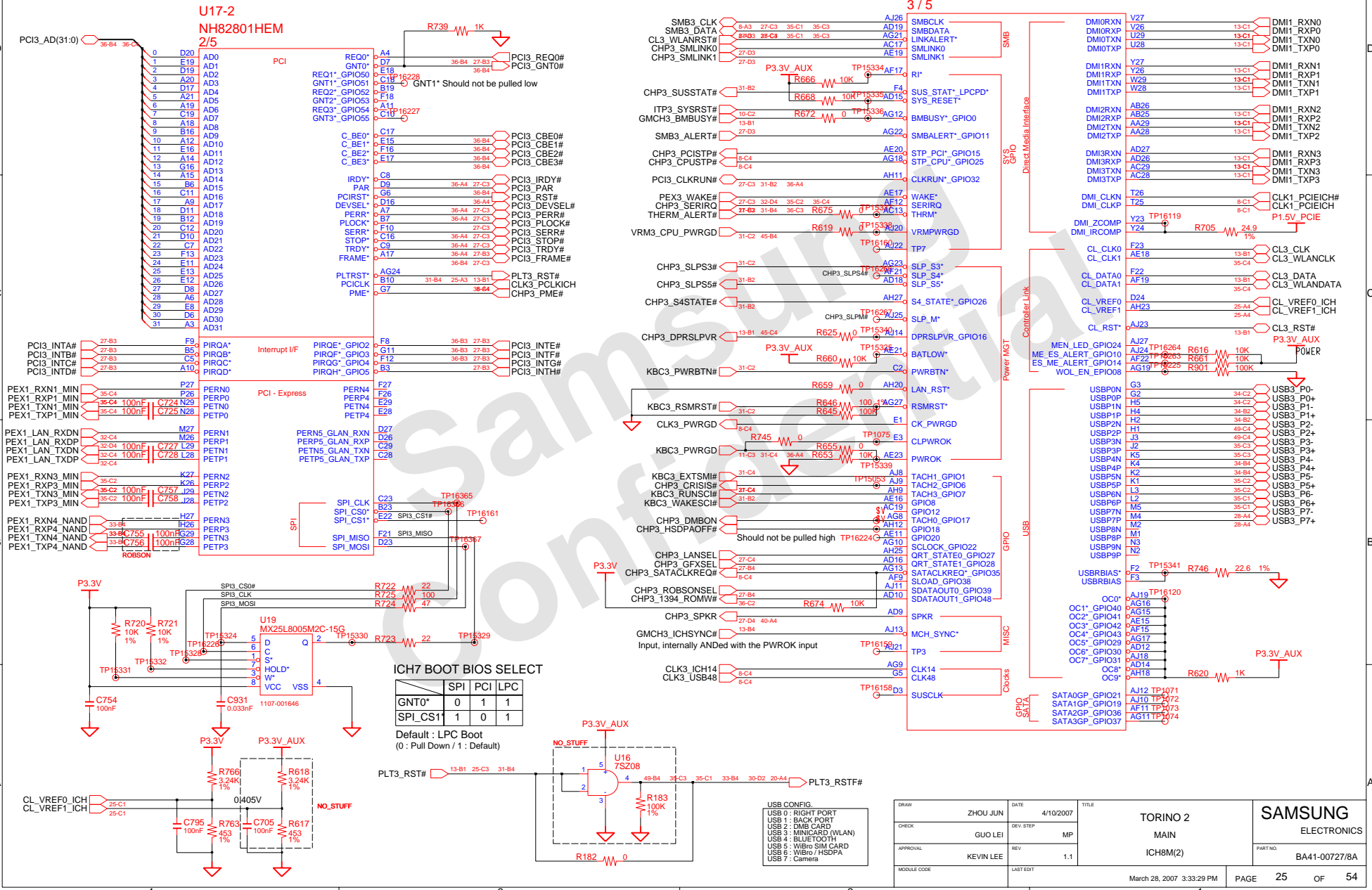


***Layout Note**
 27.4ohm resistor needs
 to be placed within 2" of ICH7-M
 56.2ohm must be placed
 to be placed within 2" of 27.4ohm w/o stub

DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV_STEP	MP	MAIN	PART NO.	
APPROVAL	KEVIN LEE	REV	1,1	ICH8M(1)	BA41-00727/8A	
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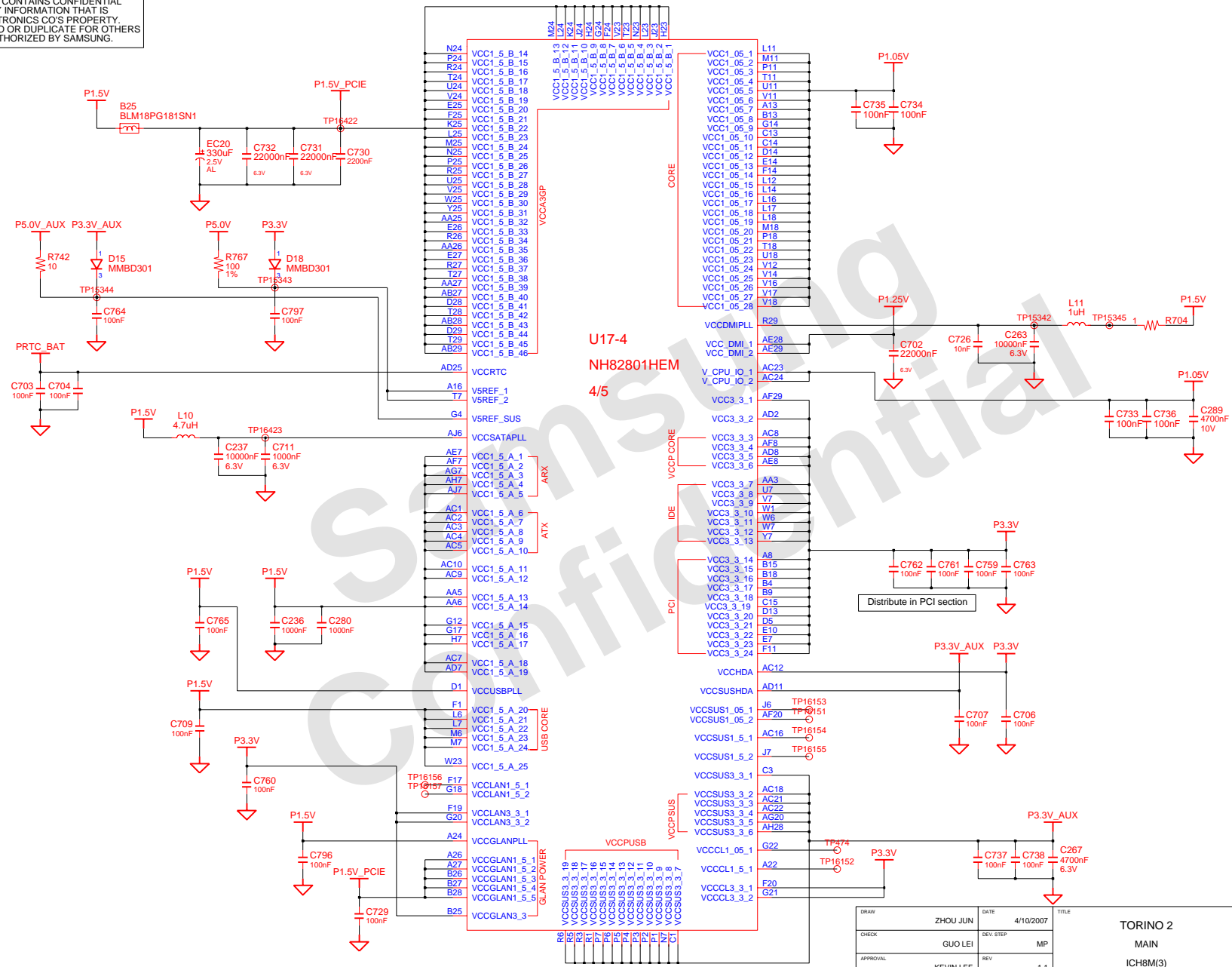
AC caps : PCIe need to be within 250mils of the driver
 Resistor for Test : Place Stuffing Option to minimize stubs



DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2
CHECK	GUO LEI	DEV STEP	MP		MAIN
APPROVAL	KEVIN LEE	REV	1.1		ICH8M(2)
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ELECTRONICS
PART NO. BA41-00727/8A

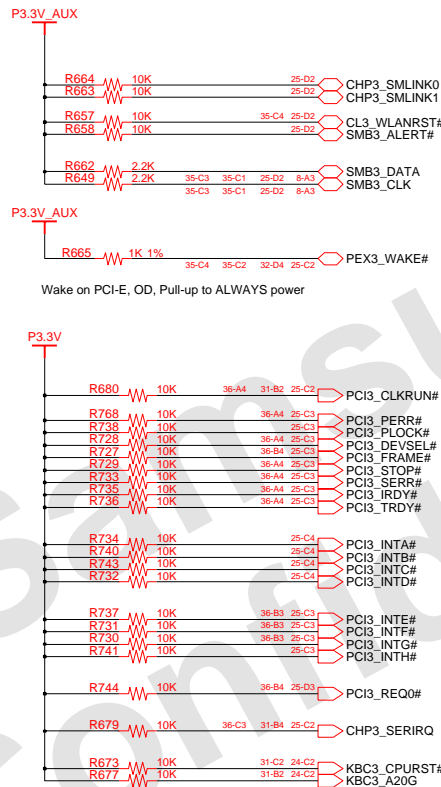
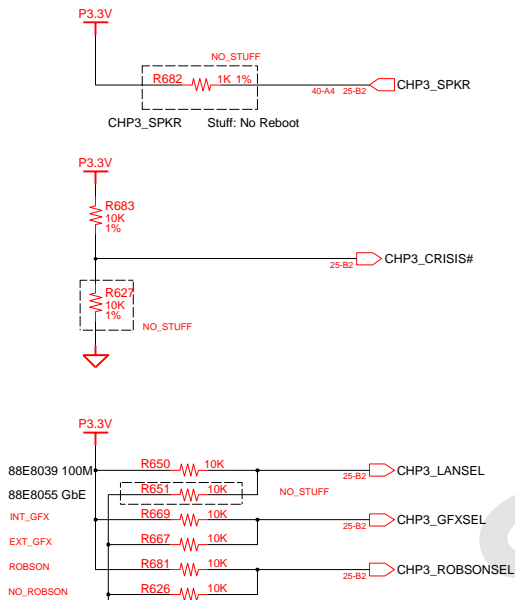
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U17-4
NH82801HEM
4/5

DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV STEP	MP	MAIN	ICH8M(3)	
APPROVAL	KEVIN LEE	REV	1.1			PART NO. BA41-00727/8A
MODULE CODE		LAST EDIT				
March 28, 2007 3:33:29 PM				PAGE	26	OF 54

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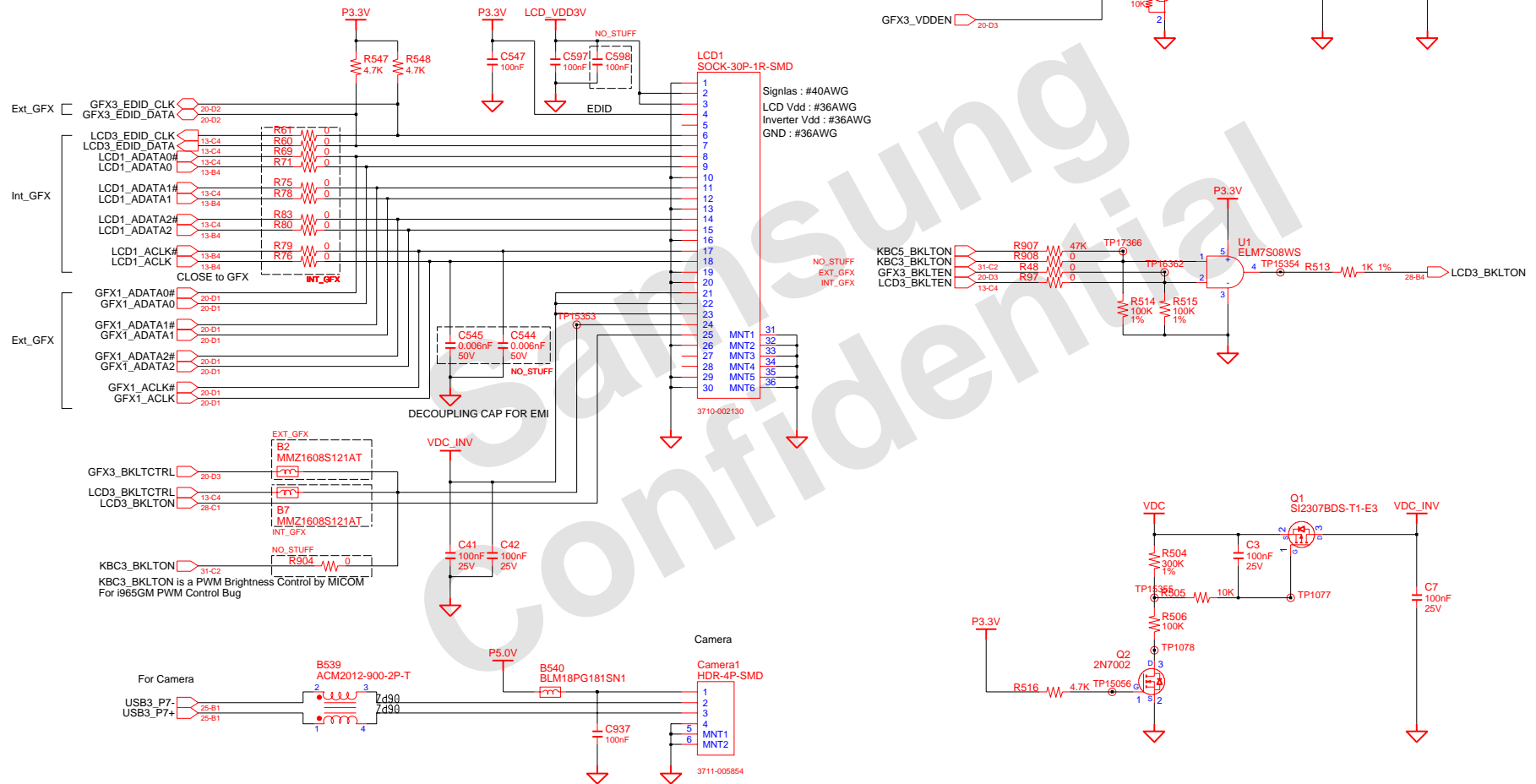
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A5	VSS_2	VSS_127	N27
AA2	VSS_3	VSS_128	N4
AA7	VSS_4	VSS_129	N5
A6	VSS_5	VSS_130	N6
AB1	VSS_6	VSS_131	P12
AB24	VSS_7	VSS_132	P13
AC11	VSS_8	VSS_133	P14
AC14	VSS_9	VSS_134	P15
AC25	VSS_10	VSS_135	P16
AC26	VSS_11	VSS_136	P17
AC27	VSS_12	VSS_137	P23
AD17	VSS_13	VSS_138	P28
AD20	VSS_14	VSS_139	P29
AD28	VSS_15	VSS_140	R11
AD29	VSS_16	VSS_141	R12
AD3	VSS_17	VSS_142	R13
AD4	VSS_18	VSS_143	R14
AD6	VSS_19	VSS_144	R15
AE1	VSS_20	VSS_145	R16
AE12	VSS_21	VSS_146	R17
AE2	VSS_22	VSS_147	R18
AE22	VSS_23	VSS_148	R4
AD1	VSS_24	VSS_149	R28
AE25	VSS_25	VSS_150	T12
AE5	VSS_26	VSS_151	T13
AE6	VSS_27	VSS_152	T14
AE9	VSS_28	VSS_153	T15
AF14	VSS_29	VSS_154	T16
AF16	VSS_30	VSS_155	T17
AF18	VSS_31	VSS_156	T2
AF3	VSS_32	VSS_157	U13
AF4	VSS_33	VSS_158	U14
AG5	VSS_34	VSS_159	U15
AG6	VSS_35	VSS_160	U16
AH10	VSS_36	VSS_161	U17
AH13	VSS_37	VSS_162	U23
AH16	VSS_38	VSS_163	U26
AH19	VSS_39	VSS_164	U27
AH2	VSS_40	VSS_165	U3
AF26	VSS_41	VSS_166	U5
AH22	VSS_42	VSS_167	V13
AH24	VSS_43	VSS_168	V15
AH26	VSS_44	VSS_169	V28
AH3	VSS_45	VSS_170	V29
AH4	VSS_46	VSS_171	W2
AH8	VSS_47	VSS_172	W26
AJ5	VSS_48	VSS_173	W27
B11	VSS_49	VSS_174	Y28
B14	VSS_50	VSS_175	Y29
B17	VSS_51	VSS_176	Y4
B2	VSS_52	VSS_177	AB4
B20	VSS_53	VSS_178	AB23
B22	VSS_54	VSS_179	AB5
C24	VSS_55	VSS_180	AB6
C26	VSS_56	VSS_181	AD5
C27	VSS_57	VSS_182	U4
C6	VSS_58	VSS_183	W24
D12	VSS_59	VSS_184	
D15	VSS_60	VSS_NCTF1	A1
D18	VSS_61	VSS_NCTF2	A2
D2	VSS_62	VSS_NCTF3	A28
D4	VSS_63	VSS_NCTF4	A29
E21	VSS_64	VSS_NCTF5	AH1
E24	VSS_65	VSS_NCTF6	AH29
E25	VSS_66	VSS_NCTF7	AJ1
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F15	VSS_69	VSS_NCTF10	AJ29
E23	VSS_70	VSS_NCTF11	B1
F28	VSS_71	VSS_NCTF12	B29
F29	VSS_72		
F7	VSS_73		
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G13	VSS_76		
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G23	VSS_78		
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K23	VSS_94		
K28	VSS_95		
K39	VSS_96		
K8	VSS_97		
K8	VSS_98		
L6	VSS_99		

U17-5
 NH82801HEM
 5/5

DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS PART NO. BA41-00727/8A
CHECK	GUO LEI	DEV STEP	MP		MAIN	
APPROVAL	KEVIN LEE	REV	1,1		ICH8M(4)	
MODULE CODE		LAST ENT				
				March 28, 2007 3:33:29 PM	PAGE	27 OF 54

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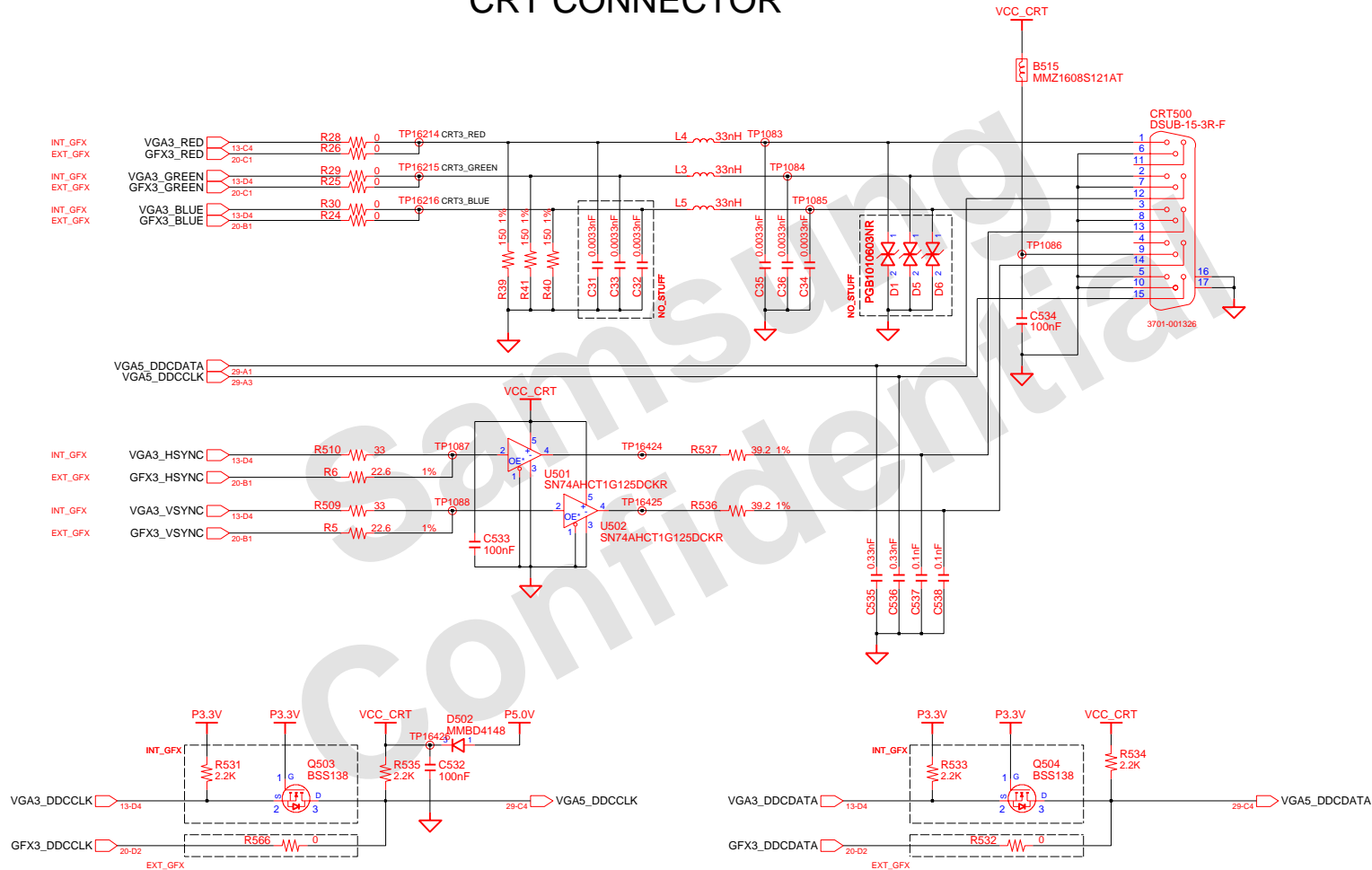
WXGA LCD CONNECTOR



DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	MP	MAIN	PART NO.	
APPROVAL	KEVIN LEE	REV	1.1	LVDS	BA41-00727/8A	
MODULE CODE		LAST ENT			March 28, 2007 3:33:29 PM	PAGE 28 OF 54

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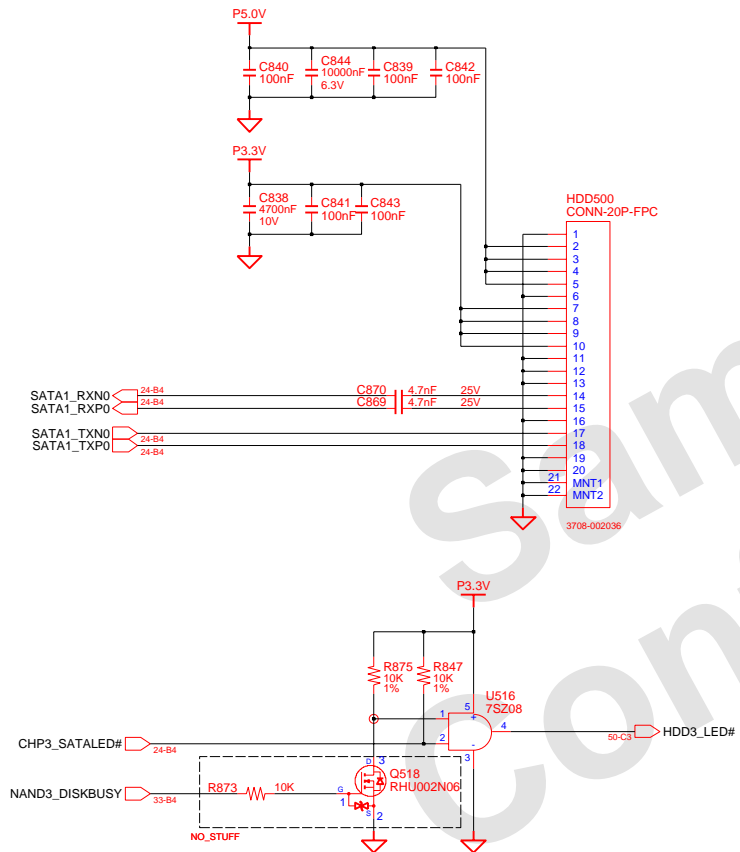
CRT CONNECTOR



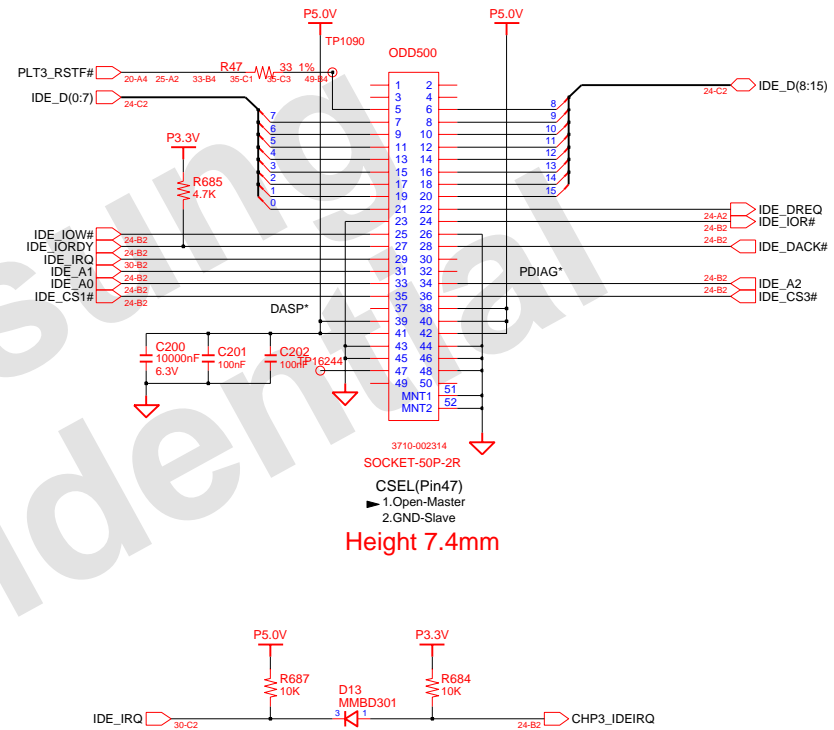
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APPROVAL	KEVIN LEE	REV	1,1	CRT	PART NO.	
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Main to HDD

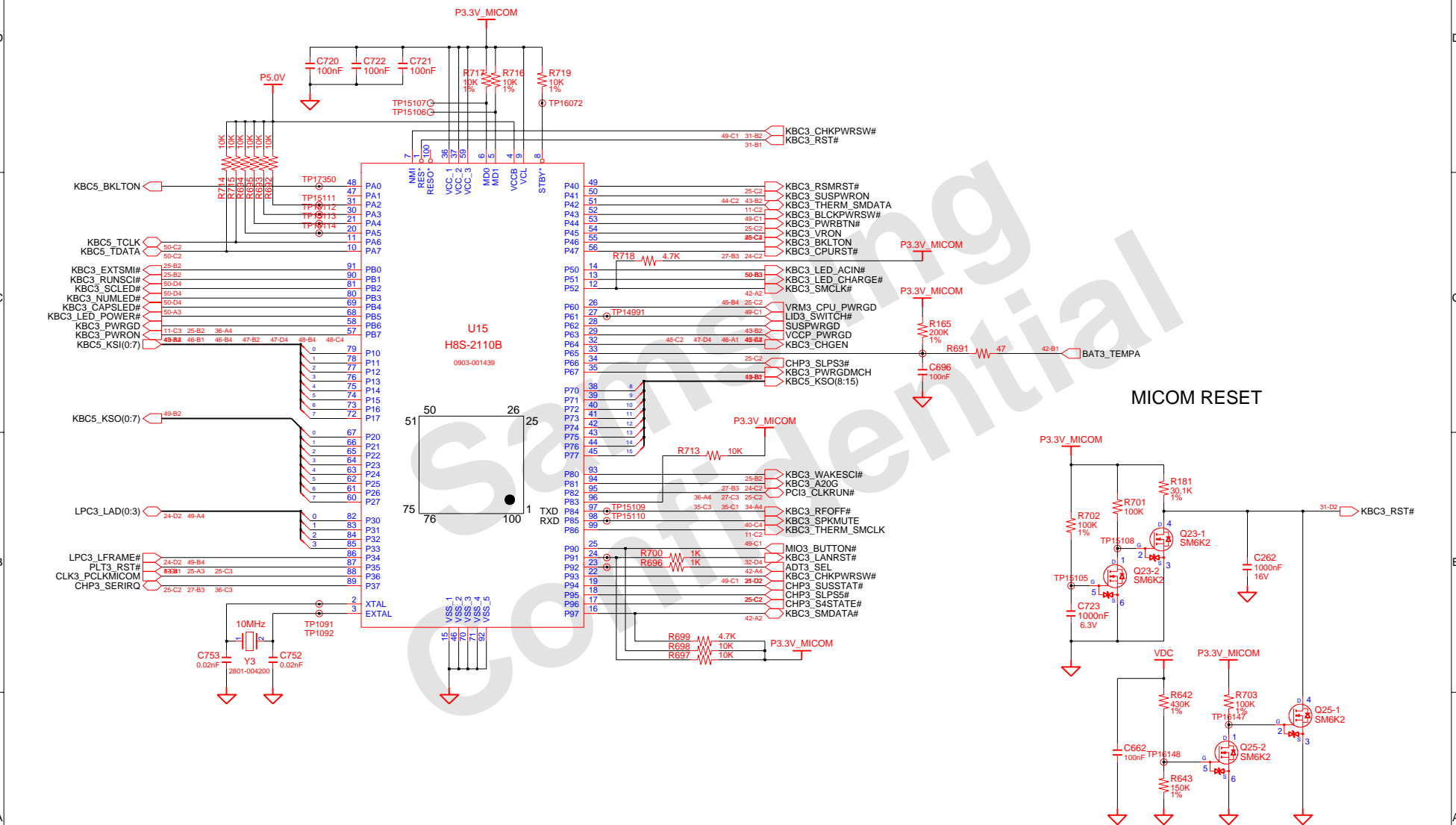


Main to ODD



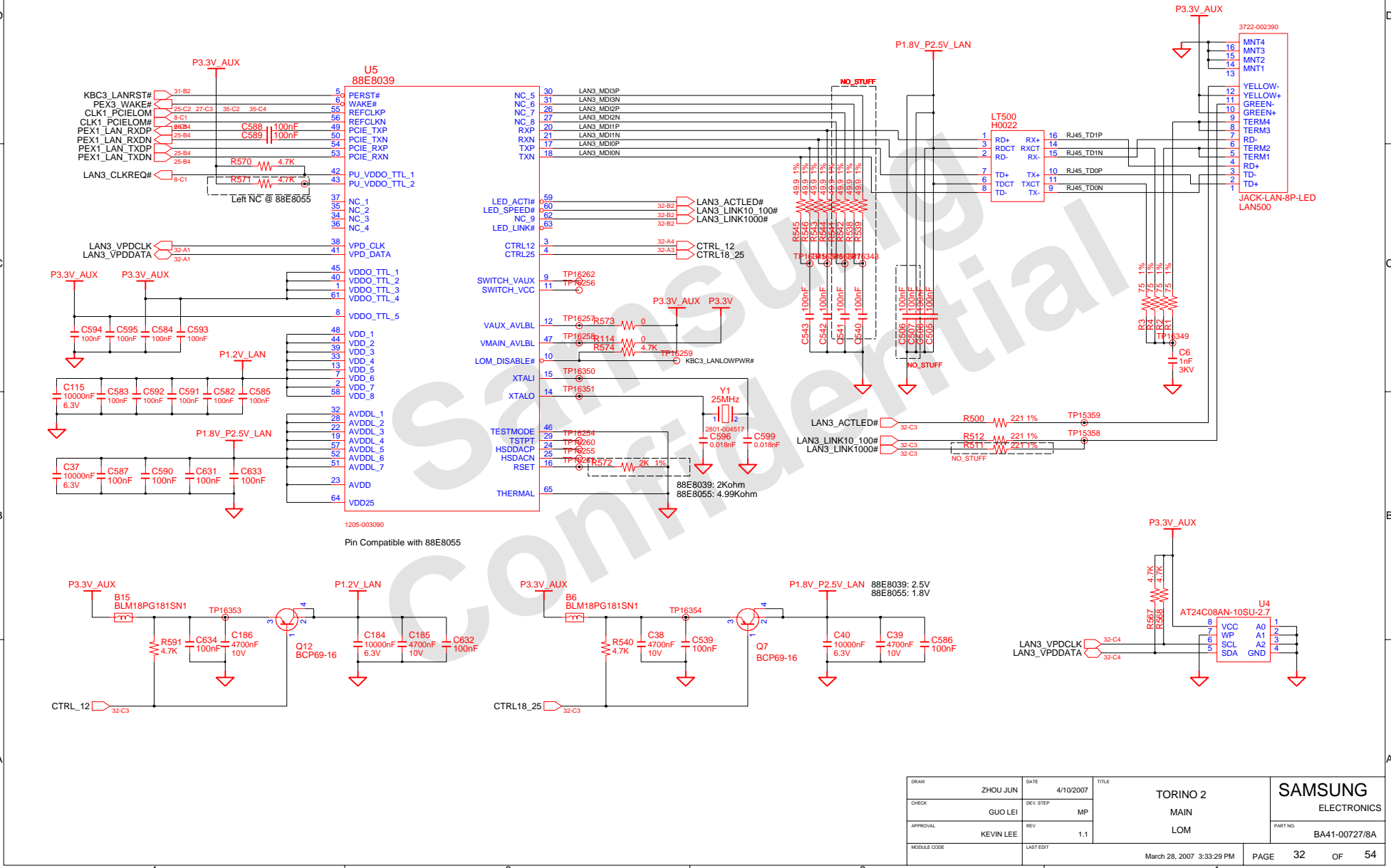
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APPROVAL	KEVIN LEE	REV	1,1	IDE	BA41-00727/8A	
MODULE CODE		LAST EDIT	March 28, 2007 3:33:29 PM	PAGE	30	OF 54

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DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV STEP	MP	MAIN	MICOM	
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MODULE CODE		LAST ENT			March 28, 2007 3:33:29 PM	PAGE 31 OF 54

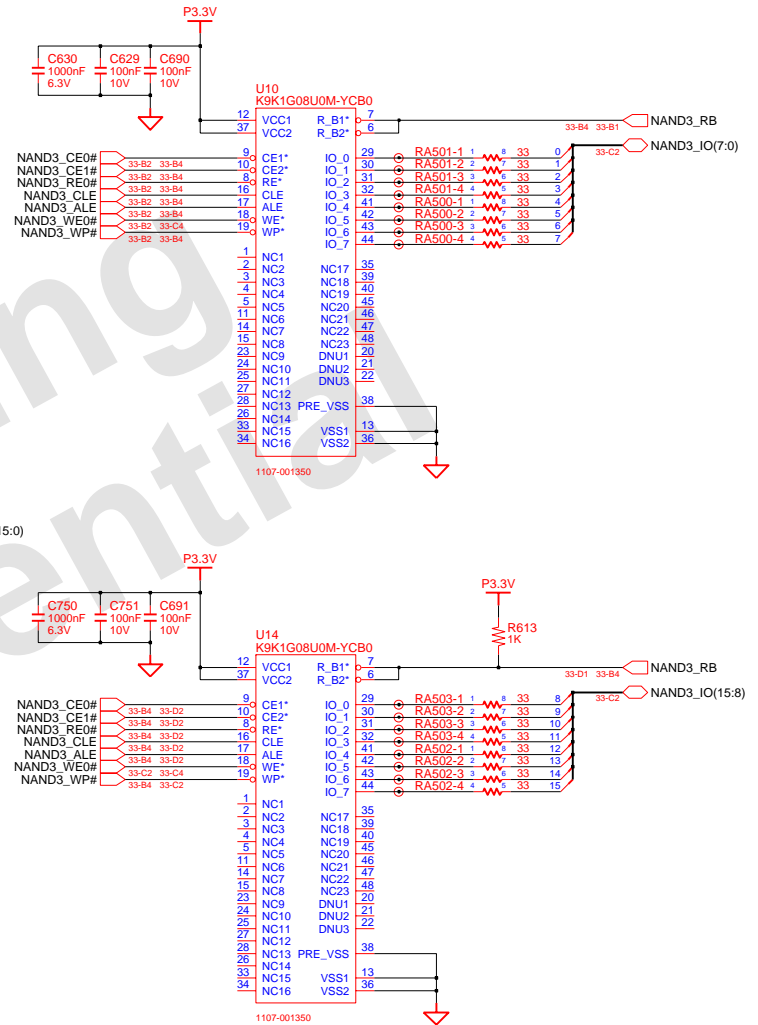
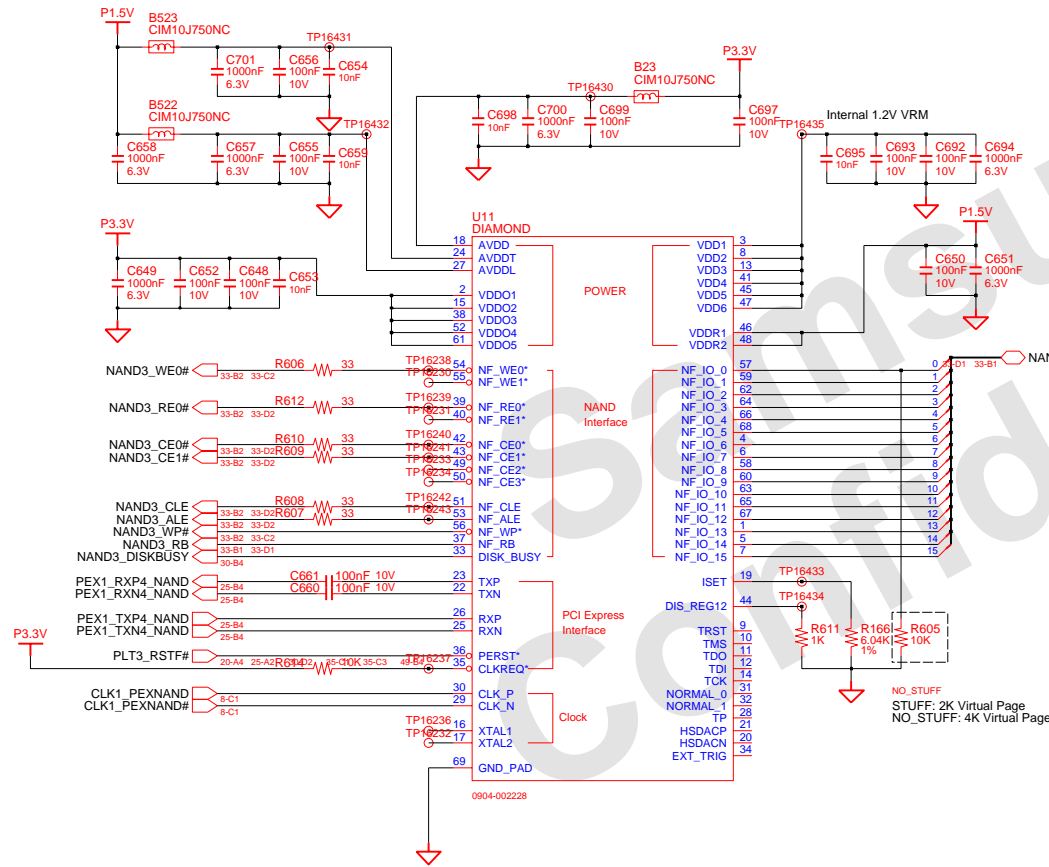
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CHECK	GUO LEI	DEV_STEP	MP	MAIN		
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MODULE CODE		LAST EDIT				PART NO. BA41-00727/8A
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Robson

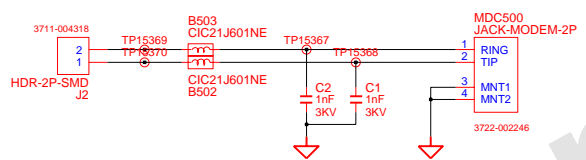
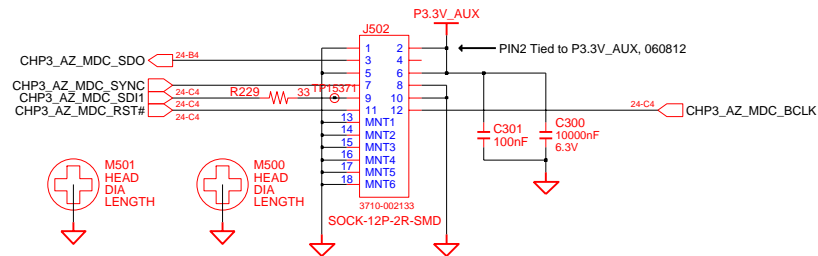


ROBSON
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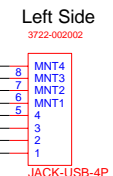
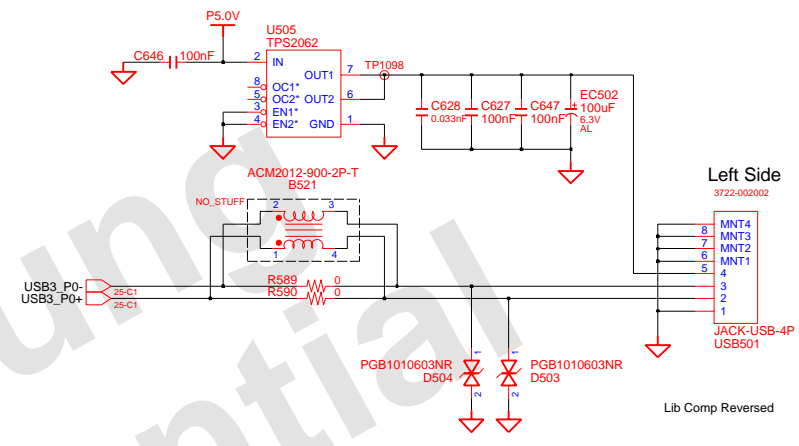
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APPROVAL	KEVIN LEE	REV	1.1			
MODULE CODE		LAST EDIT				
March 28, 2007 3:33:29 PM						PAGE 33 OF 54

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MDC connector

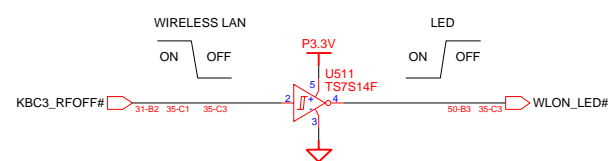
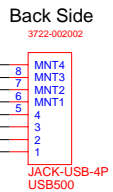
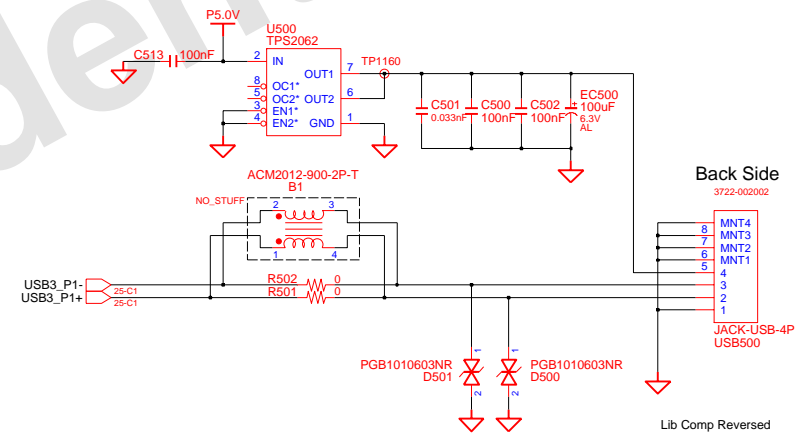
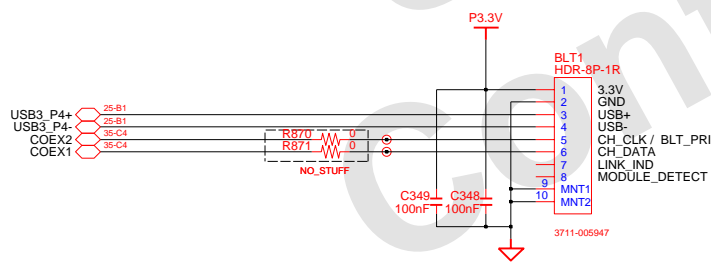


USB Port



BLUETOOTH

Bluetooth 2.0 Standard

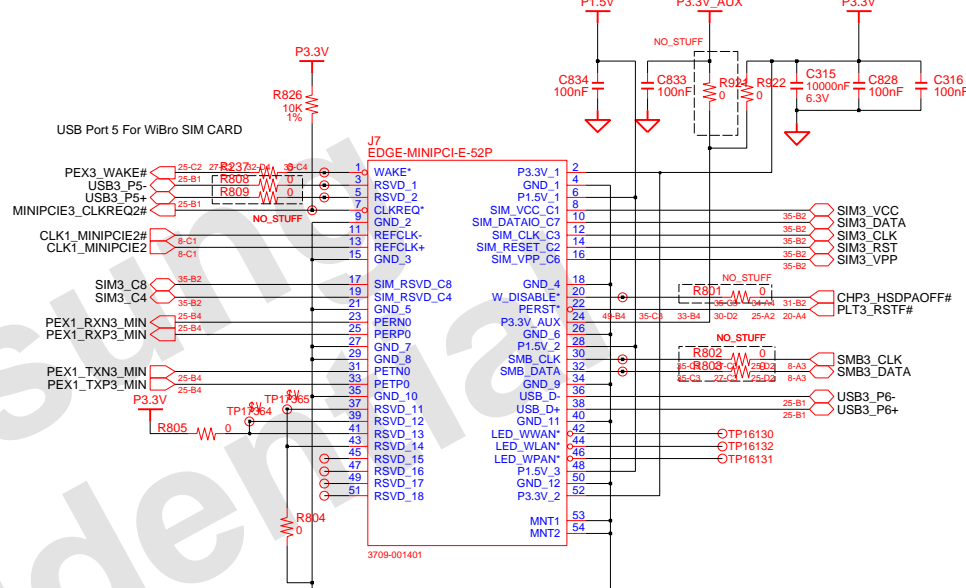
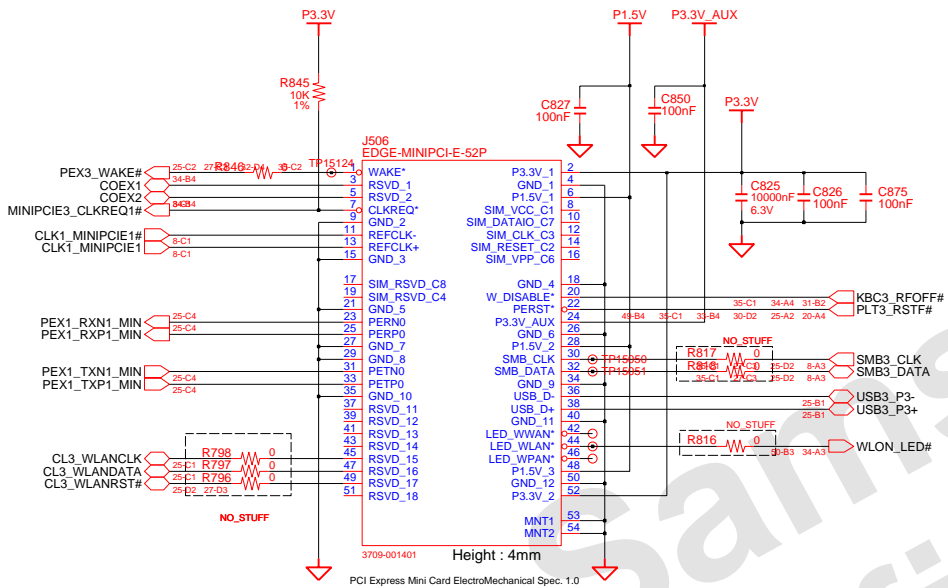


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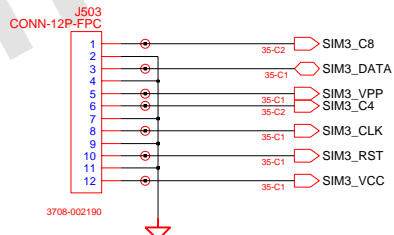
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Mini PCI-E Card (WLAN)

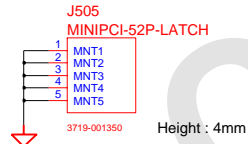
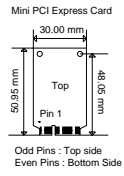
Mini PCI-E Card (HSDPA)



SIM Card FFC CONN

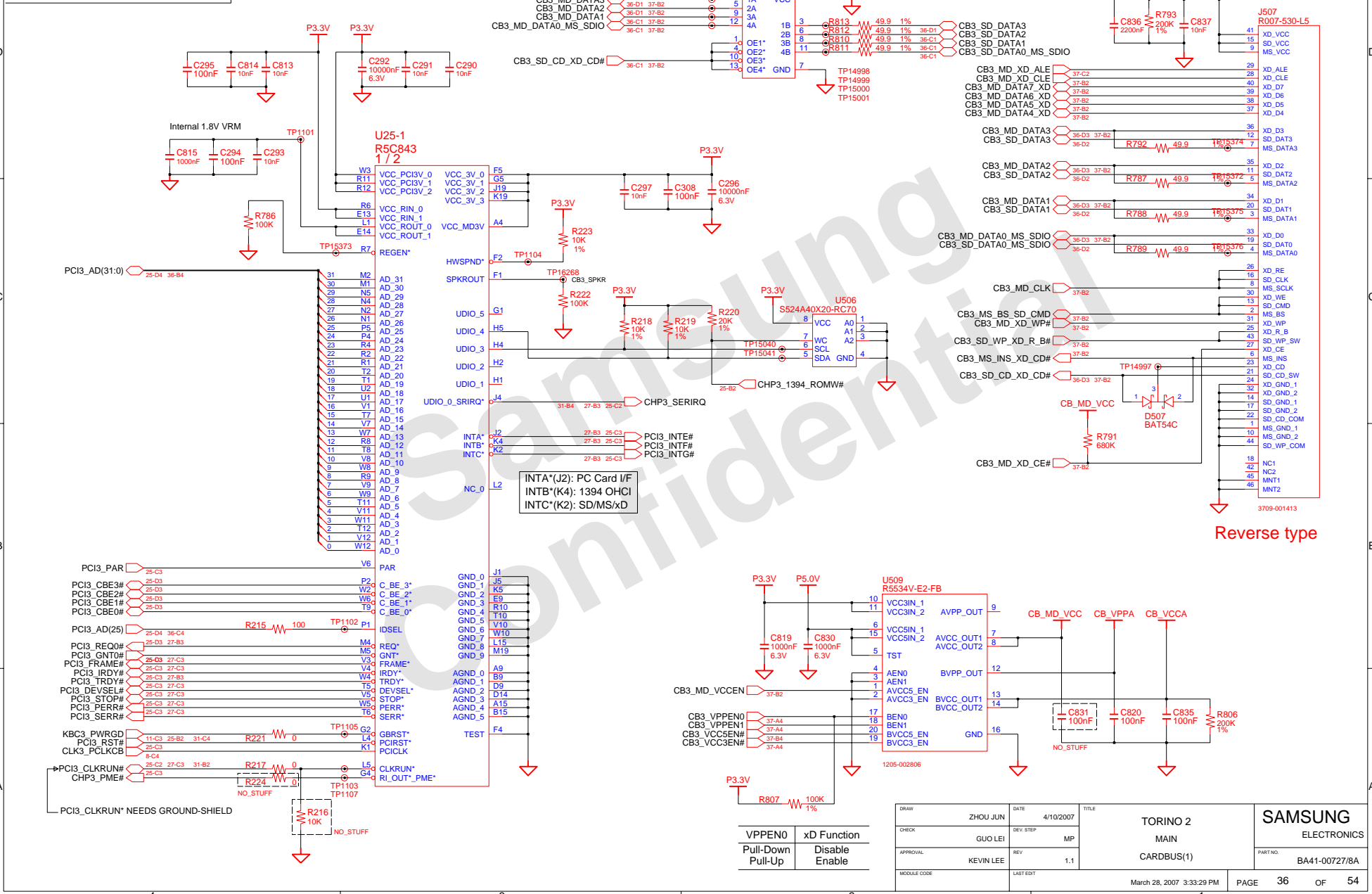


PEM



DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
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APPROVAL	KEVIN LEE	REV	1.1	MINI CARD	MINI CARD	PART NO. BA41-00727/8A
MODULE CODE		LAST EDIT				
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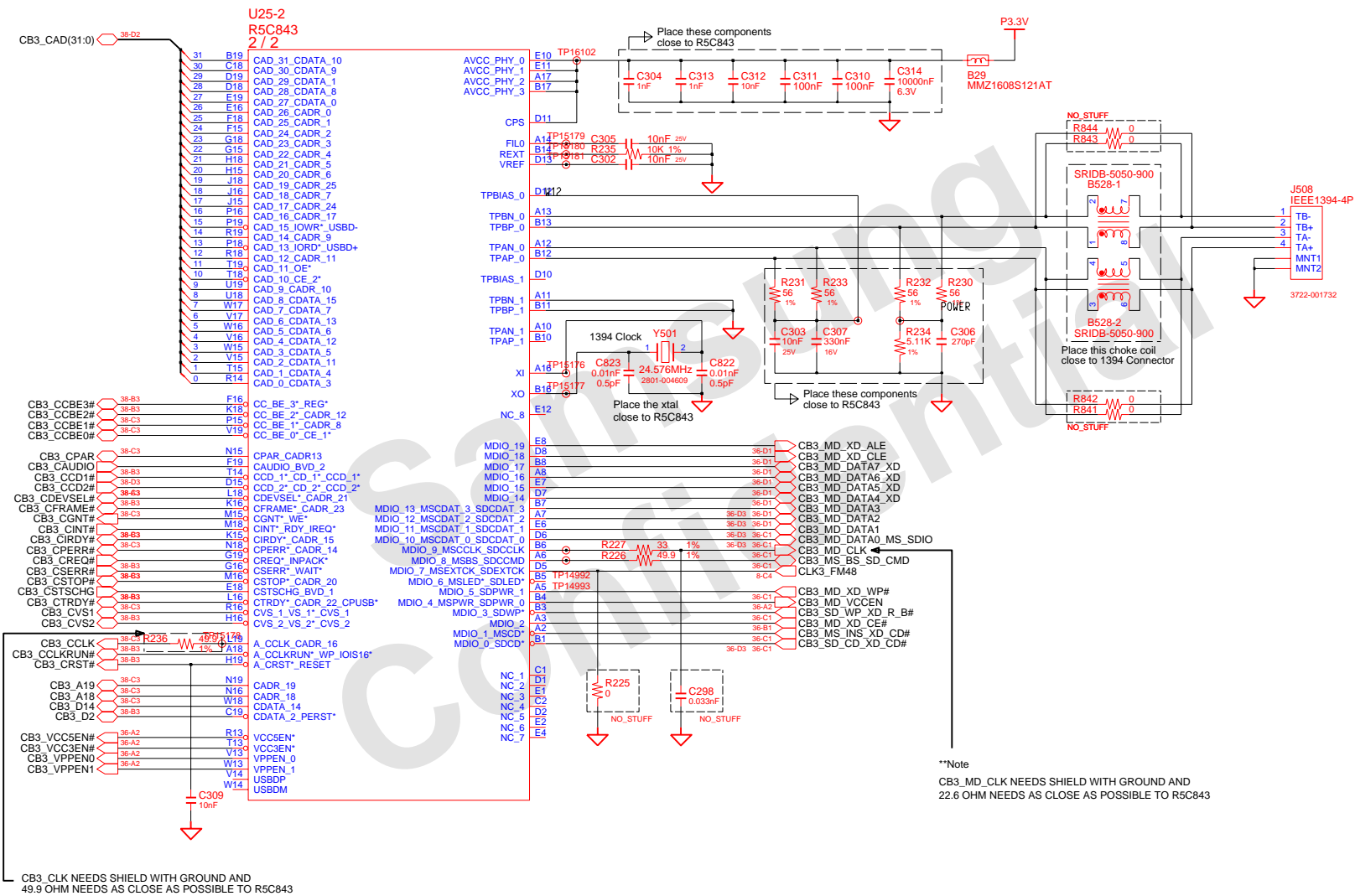


INTA*(J2): PC Card I/F
 INTB*(K4): 1394 OHCI
 INTC*(K2): SD/MS/xD

Reverse type

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Pull-Down	Disable																																					
Pull-Up	Enable																																					
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PART NO.	BA41-00727/8A																																					
March 28, 2007 3:33:29 PM				PAGE	36	OF	54																															

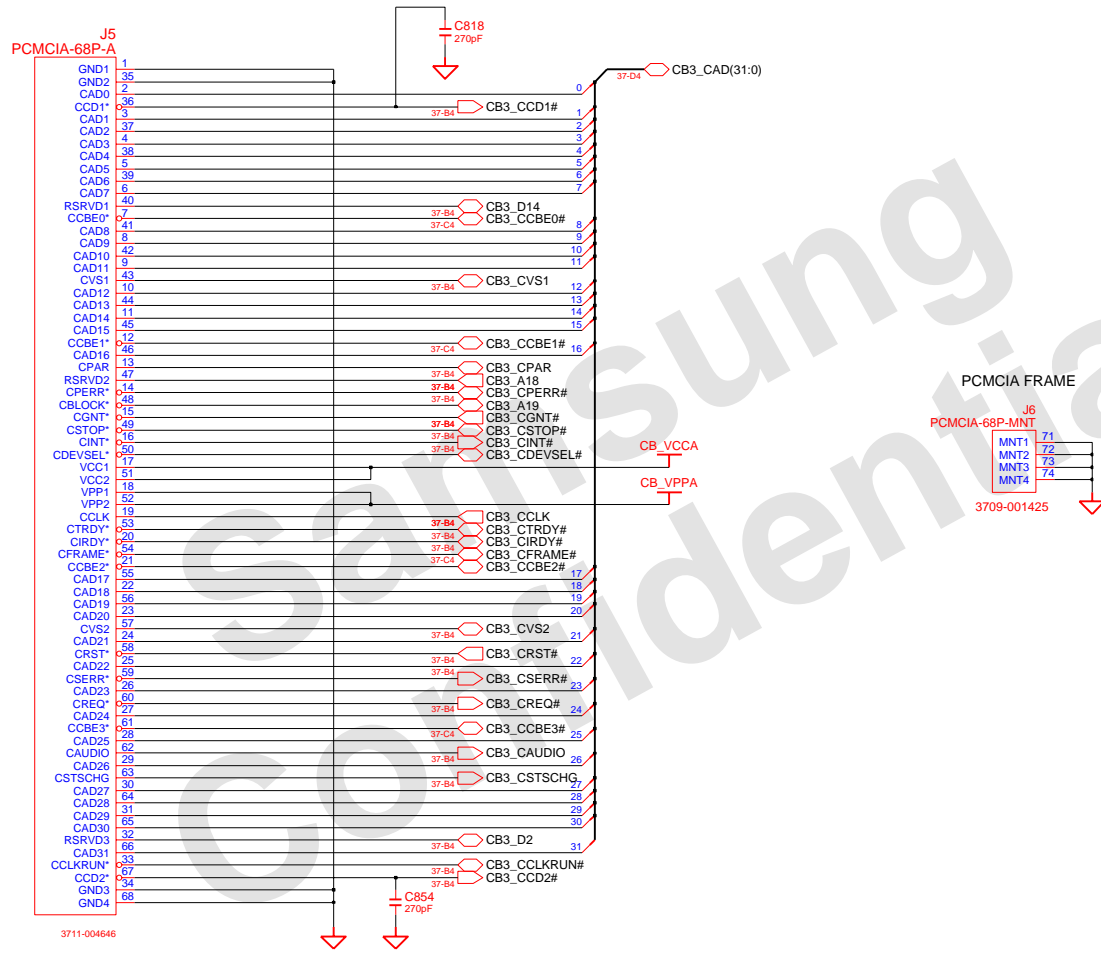
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**Note
 CB3_MD_CLK NEEDS SHIELD WITH GROUND AND
 22.6 OHM NEEDS AS CLOSE AS POSSIBLE TO R5C843

DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2 MAIN CARDBUS(2)	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV_STEP	MP			
APPROVAL	KEVIN LEE	REV	1.1			PART NO. BA41-00727/8A
MODULE CODE		LAST ENT				
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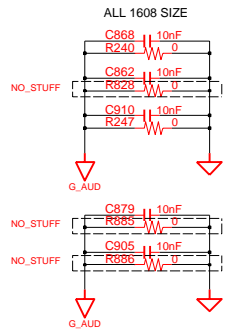
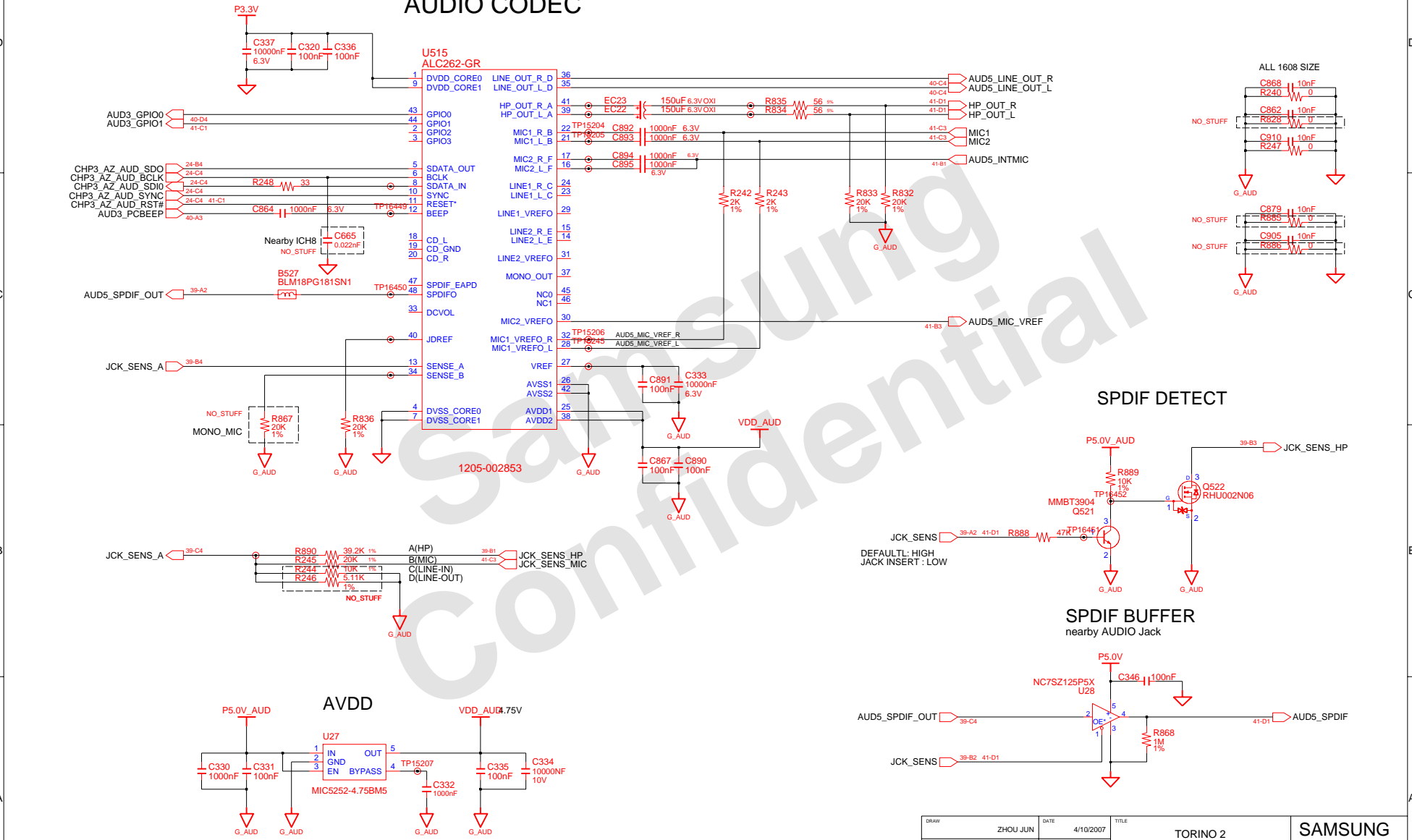
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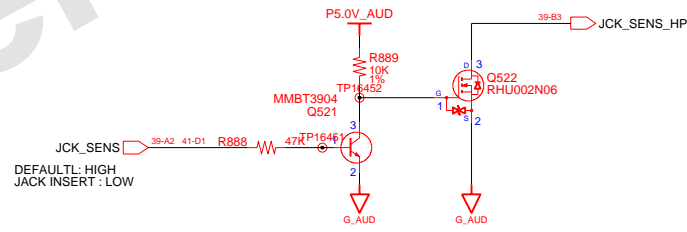
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APPROVAL	KEVIN LEE	REV	1,1	CARBUS (3)	PART NO.	BA41-00727/8A
MODULE CODE		LAST EDIT		March 28, 2007 3:33:29 PM	PAGE	38 OF 54

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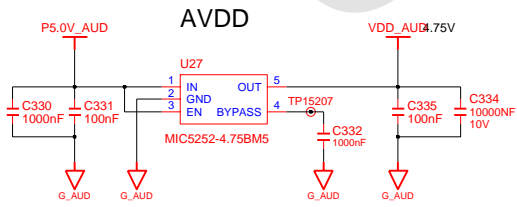
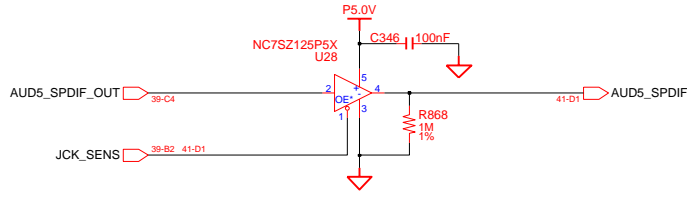
AUDIO CODEC



SPIDF DETECT



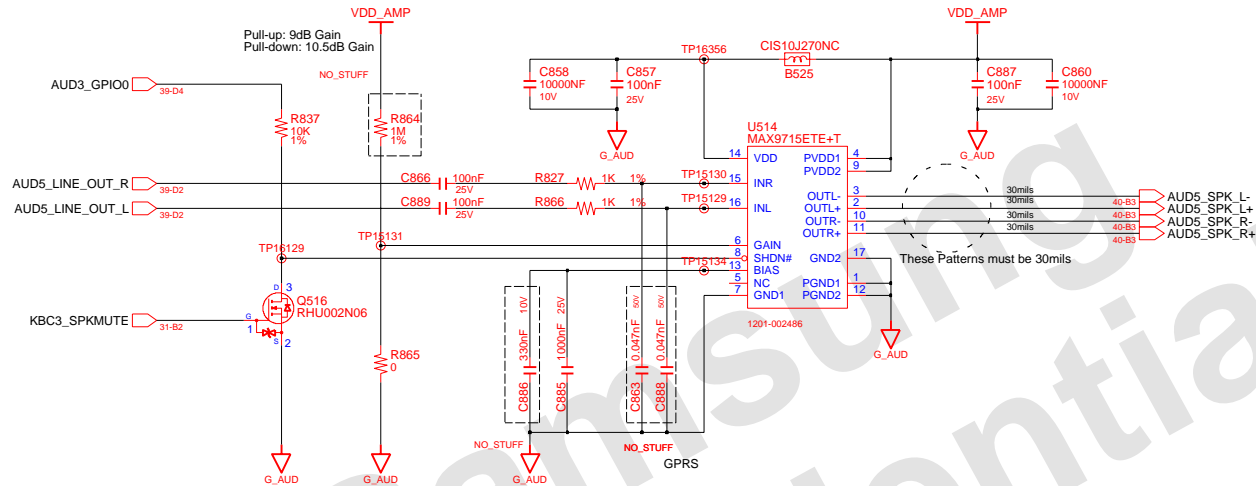
SPIDF BUFFER nearby AUDIO Jack



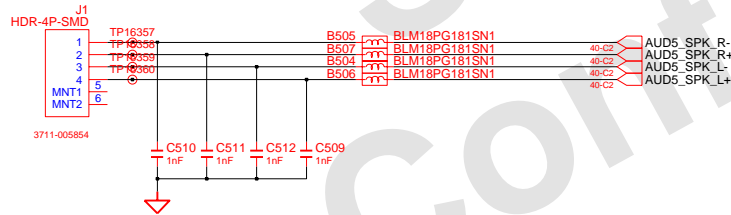
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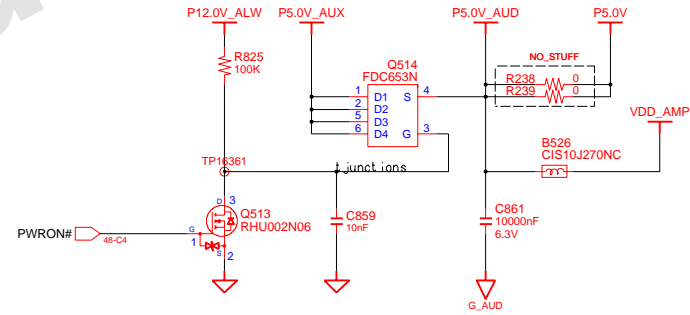
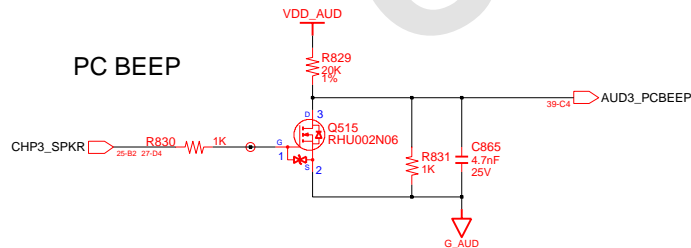
AUDIO AMP



INTERNAL STEREO SPEAKERS

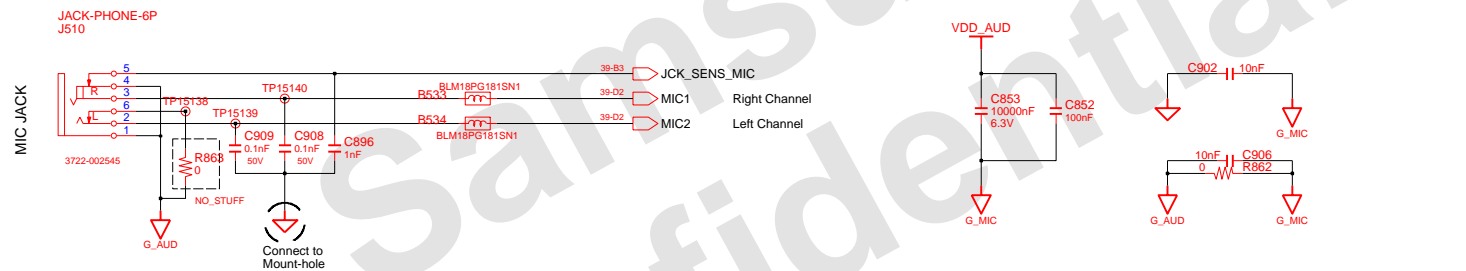
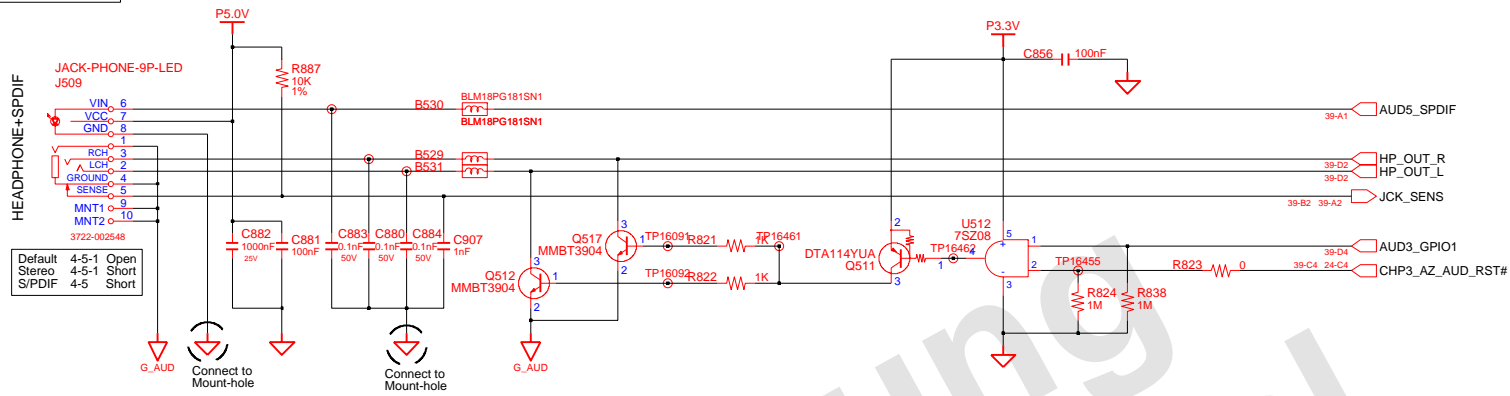


PC BEEP

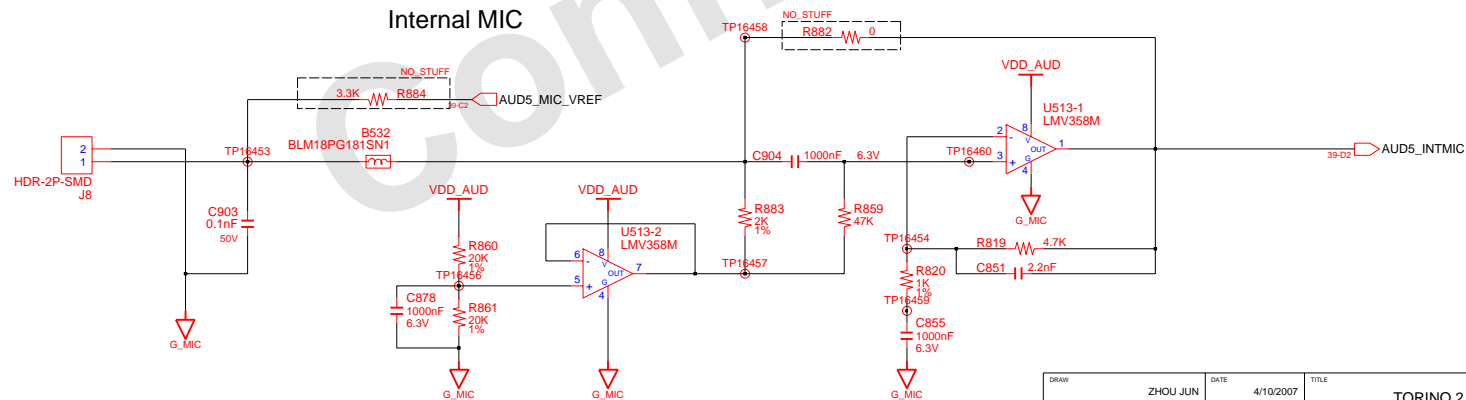


DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2	SAMSUNG
CHECK	GUO LEI	DEV. STEP	MP		MAIN	ELECTRONICS
APPROVAL	KEVIN LEE	REV	1,1		AUDIO AMPLIFIER	PART NO.
MODULE CODE		LAST ENT				BA41-00727/8A
				March 28, 2007 3:33:29 PM	PAGE	40 OF 54

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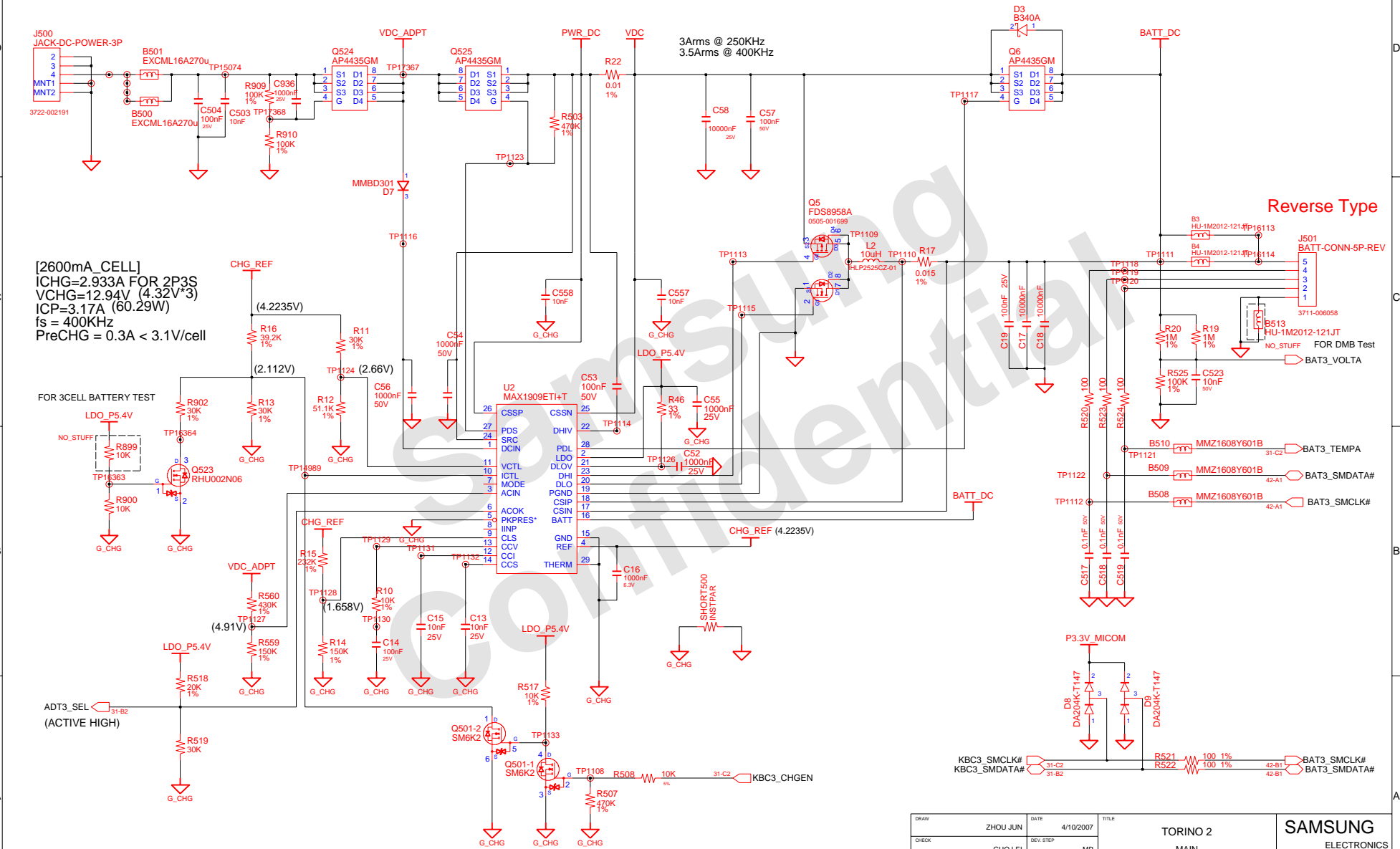
Internal MIC



DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	MP	MAIN	MAIN	
APPROVAL	KEVIN LEE	REV	1,1	AUDIO JACK	PART NO. BA41-00727/8A	
MODULE CODE		LAST EDIT			March 28, 2007 3:33:29 PM	PAGE 41 OF 54

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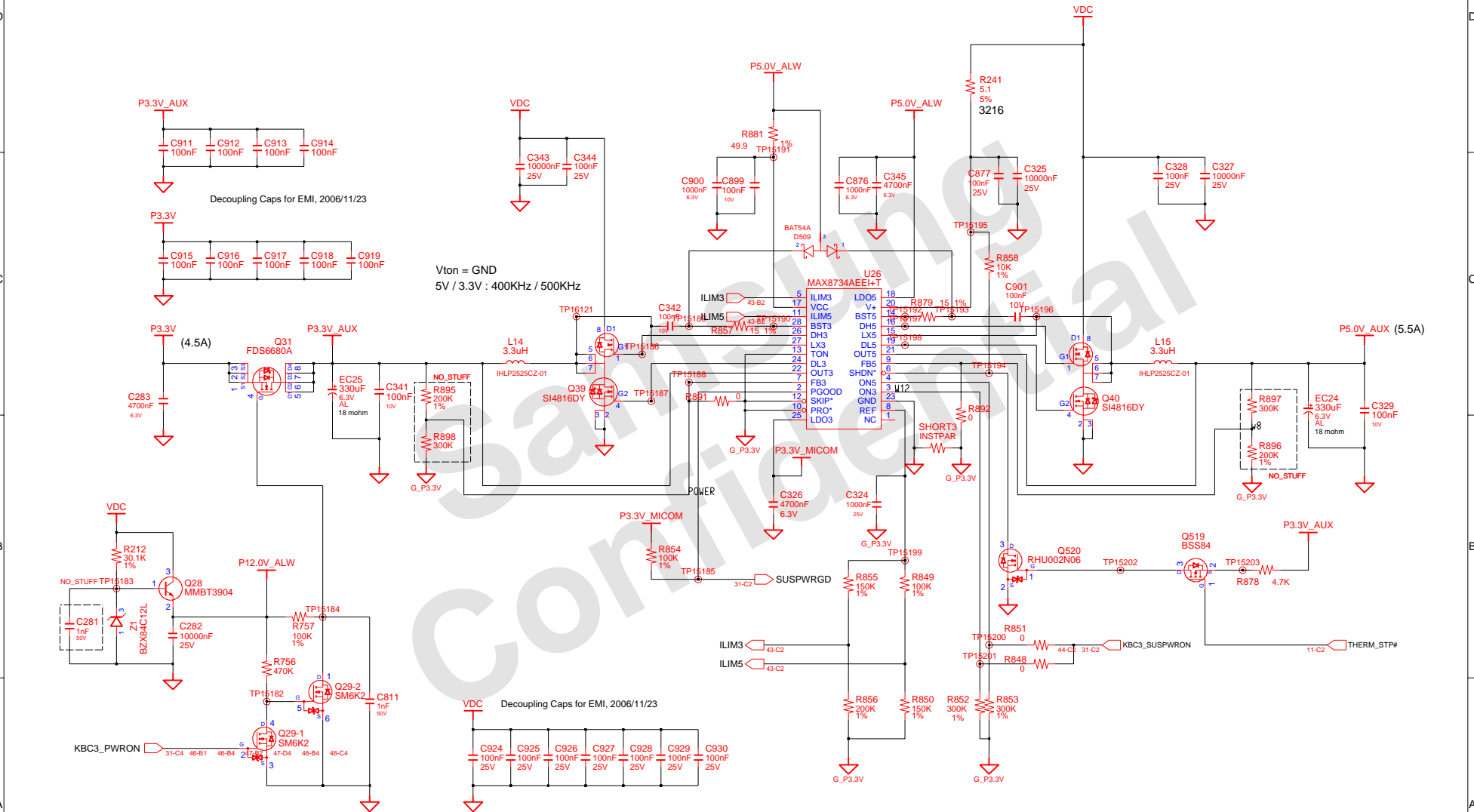
CHARGER & POWER MANAGEMENT



DRAW	DATE	TITLE	SAMSUNG	
ZHOU JUN	4/10/2007	TORINO 2	ELECTRONICS	
CHECK	DEV_STEP	MAIN	PART NO. BA41-00727/8A	
APPROVAL	REV	VDC & CHARGER	PAGE 42 OF 54	
MODULE CODE	LAST ENT	March 28, 2007 3:33:29 PM	D:\users\mentor\Torino2\MP12_MP1.1_0410	

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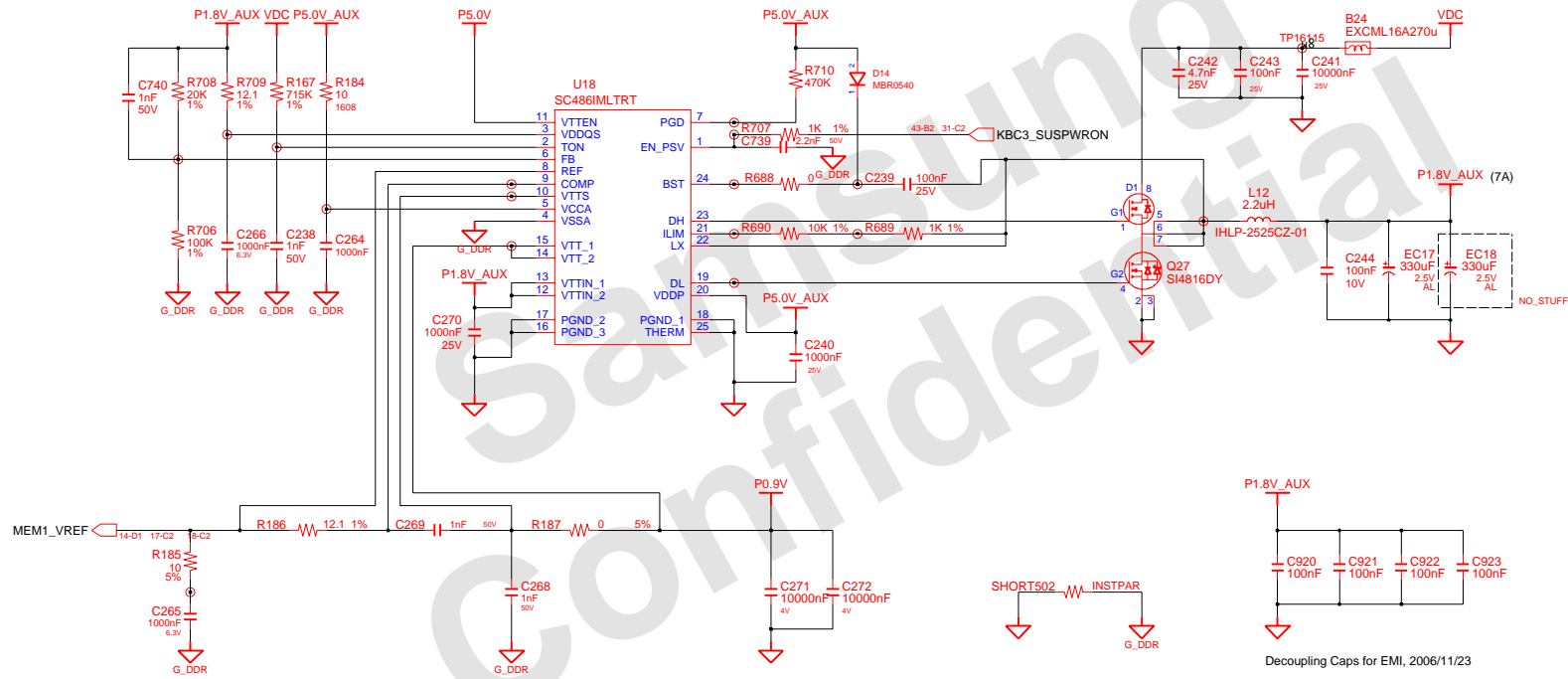
P5V_AUX & P3.3V_AUX



DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	MP	MAIN	MAIN	
APPROVAL	KEVIN LEE	REV	1,1	P5V_AUX & P3.3V_LAN(AUX)		PART NO. BA41-00727/8A
MODULE CODE		LAST EDIT		March 28, 2007 3:33:29 PM	PAGE	43 OF 54

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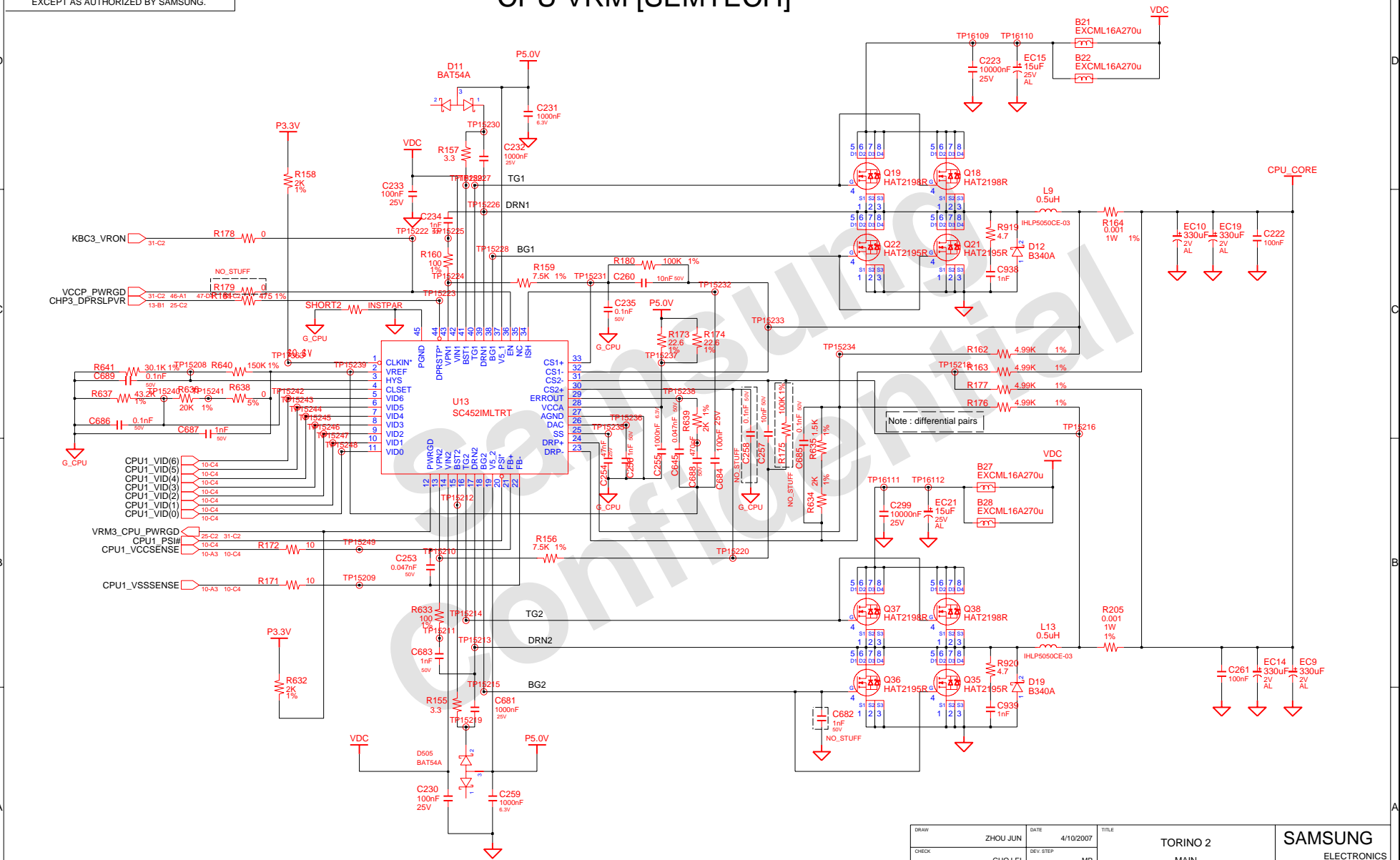
DDR2 POWER



DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2 MAIN DDR2 POWER	SAMSUNG ELECTRONICS PART NO. BA41-00727/8A
CHECK	GUO LEI	DEV. STEP	MP			
APPROVAL	KEVIN LEE	REV	1,1			
MODULE CODE		LAST EDIT				
			March 28, 2007 3:33:29 PM	PAGE	44	OF 54

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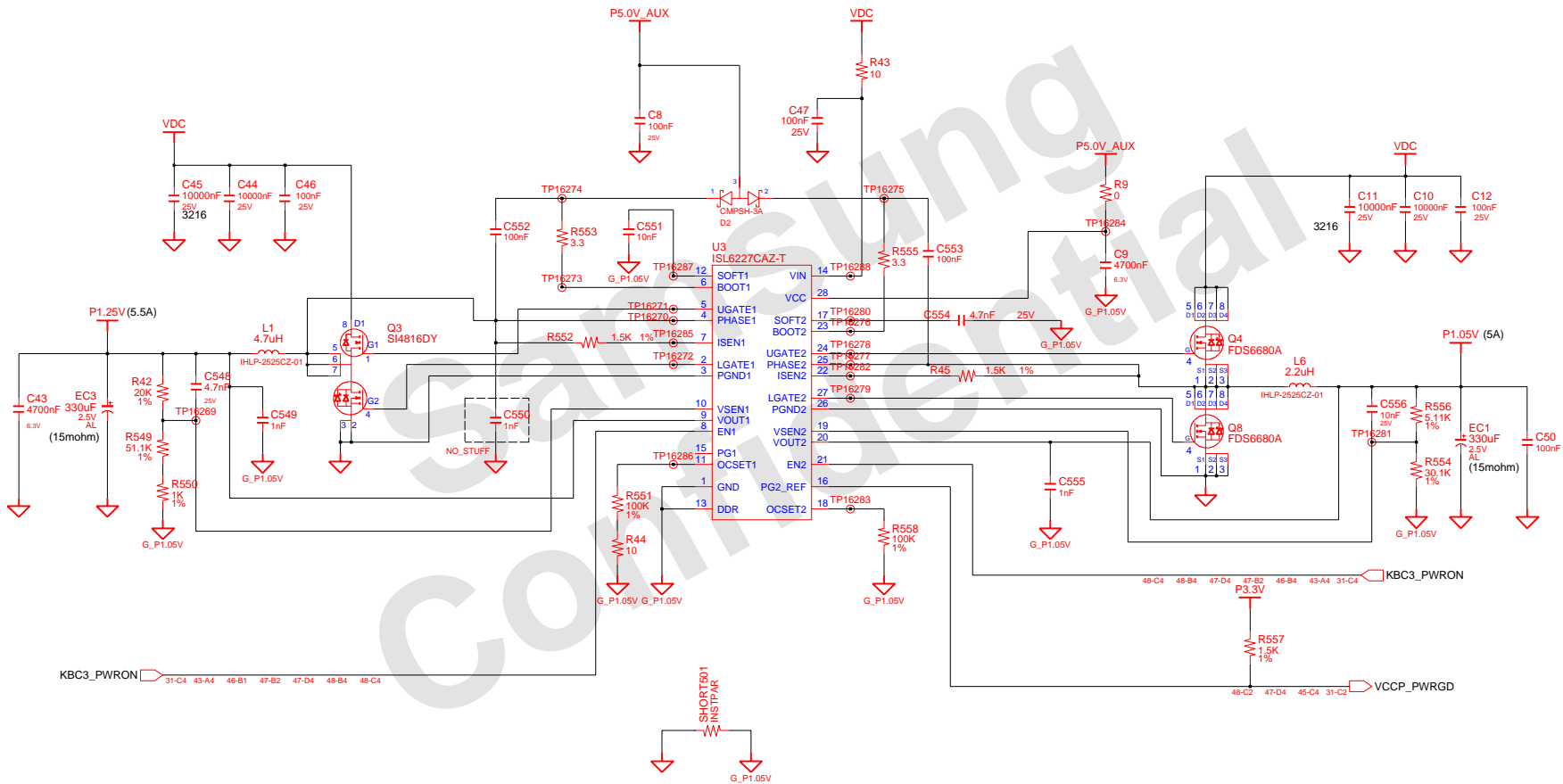
CPU VRM [SEMTECH]



DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2 MAIN CPU VRM	SAMSUNG ELECTRONICS PART NO. BA41-00727/8A
CHECK	GUO LEI	DEV. STEP	MP			
APPROVAL	KEVIN LEE	REV	1.1			
MODULE CODE		LAST ENT				
March 28, 2007 3:33:29 PM				PAGE	45 OF 54	

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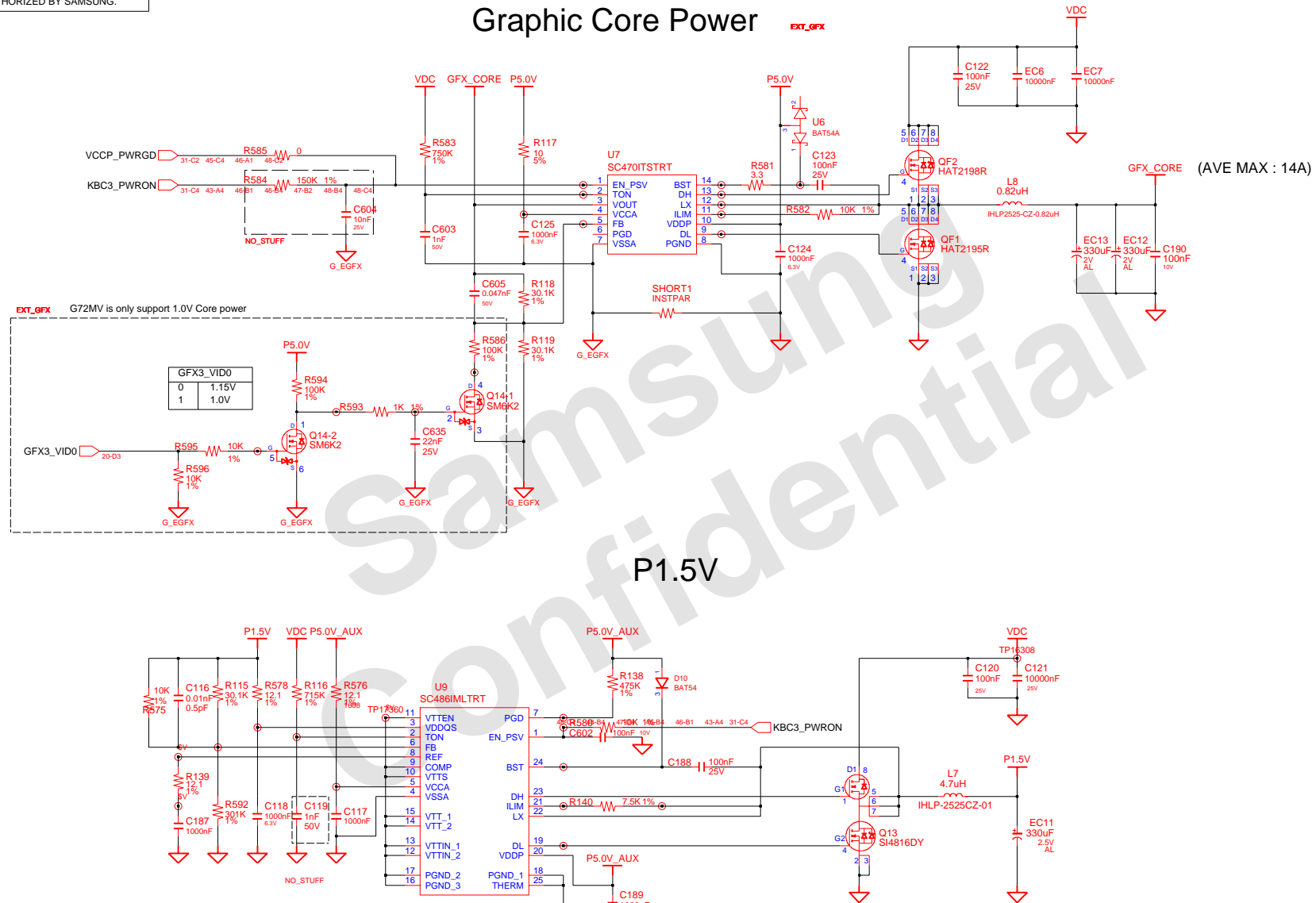
P1.25V & P1.05V



DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	MP		MAIN	
APPROVAL	KEVIN LEE	REV	1,1		P1.25V & P1.05V	PART NO. BA41-00727/8A
MODULE CODE		LAST EDIT		March 28, 2007 3:33:29 PM	PAGE	46 OF 54

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Graphic Core Power

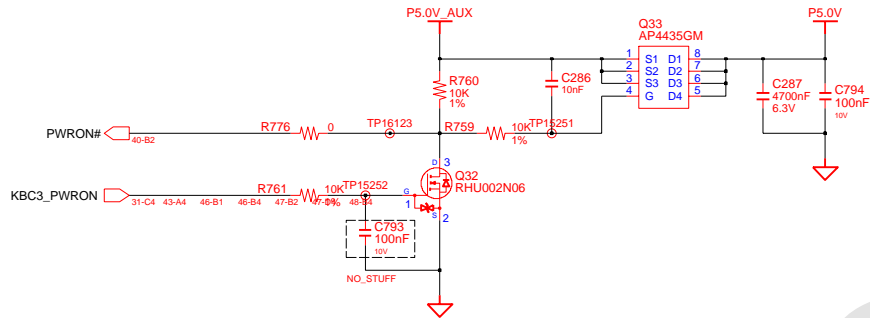


P1.5V

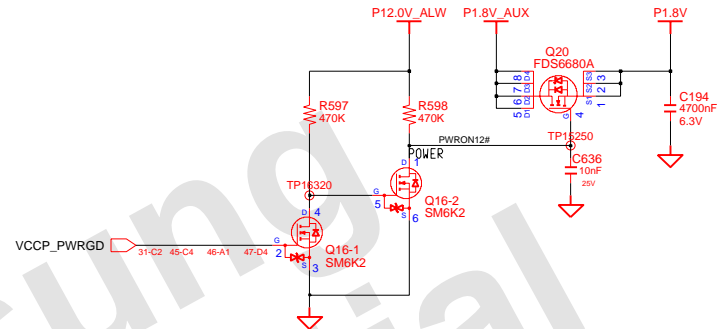
DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2 MAIN GFX_CORE & P1.5V	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	MP		PART NO. BA41-00727/8A	
APPROVAL	KEVIN LEE	REV	1,1			
MODULE CODE		LAST ENT				
				March 28, 2007 3:33:29 PM	PAGE	47 OF 54

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Switched Power On (P5.0V)

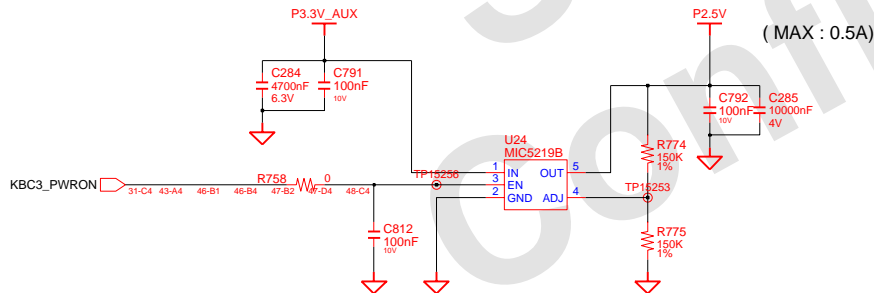


Switched Power On (P1.8V)



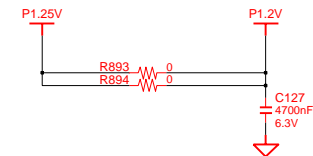
Switched Power On (P2.5V)

NO_STUFF
 It's only used for G72M



Switched Power On (P1.2V)

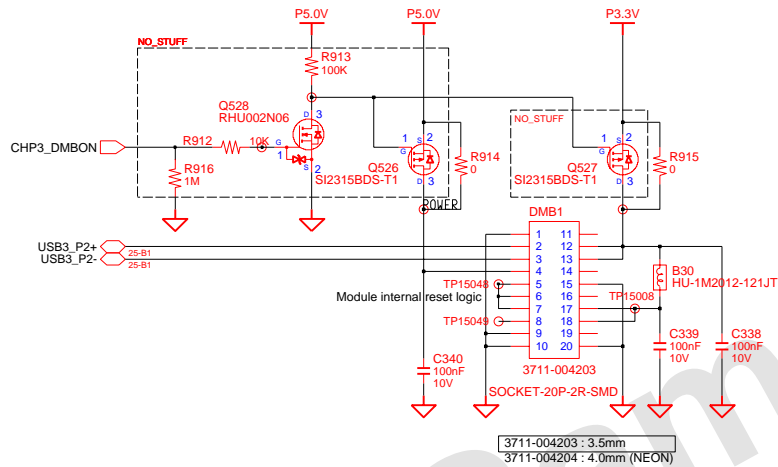
EXT_GFX



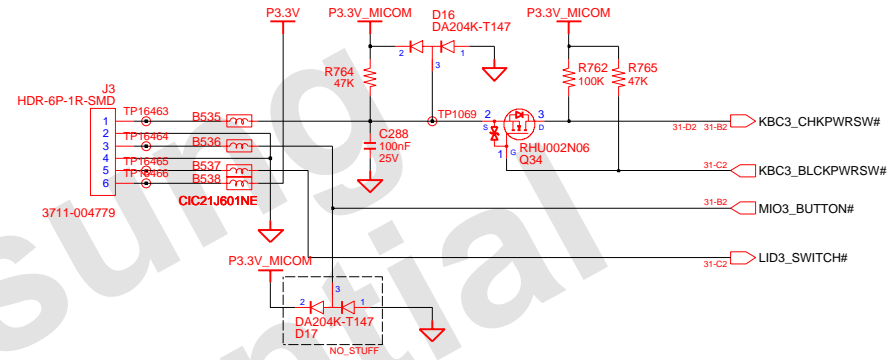
DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2 MAIN SWITCHED POWER	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	MP			
APPROVAL	KEVIN LEE	REV	1,1			PART NO. BA41-00727/8A
MODULE CODE		LAST EDIT	March 28, 2007 3:33:29 PM	PAGE	48	OF 54

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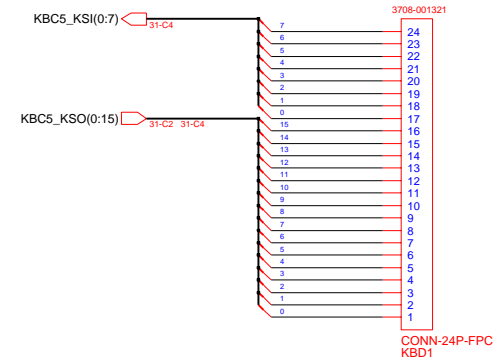
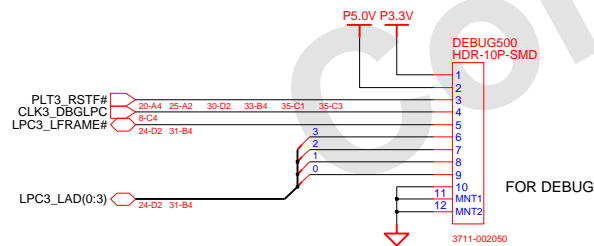
OPTION: DMB INTERFACE



Power Switch Connector

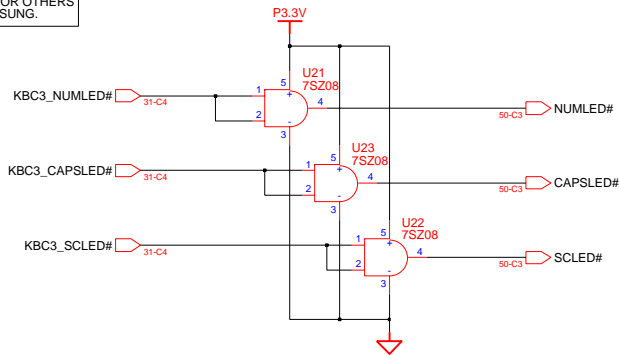


KEYBOARD

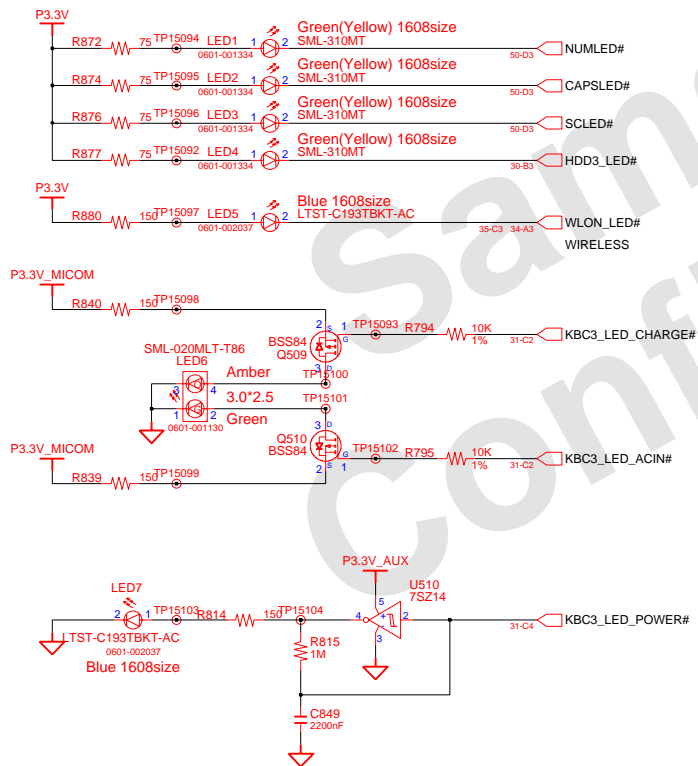


DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV STEP	MP	MAIN		
APPROVAL	KEVIN LEE	REV	1,1	DMB & KBD & DEBUG & POWER S/W	PART NO.	BA41-00727/8A
MODULE CODE		LAST EDIT		March 28, 2007 3:33:29 PM	PAGE	49 OF 54

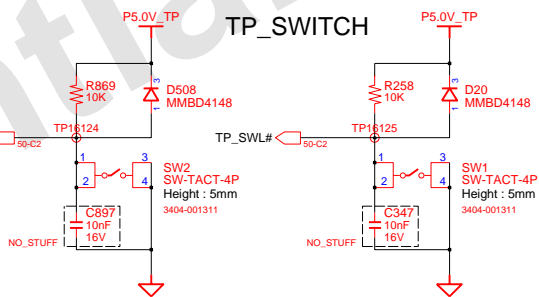
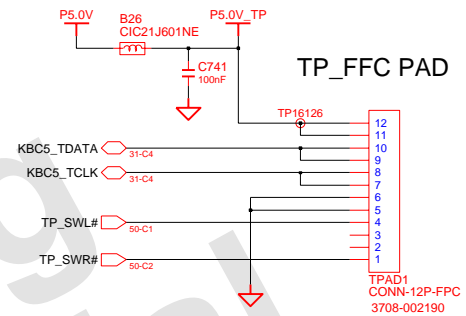
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LEDS



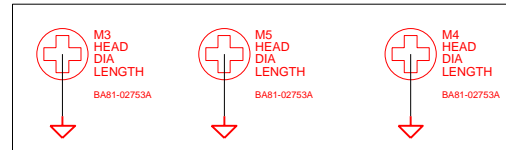
TOUCH PAD



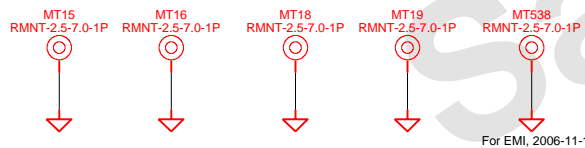
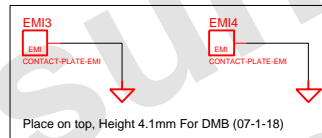
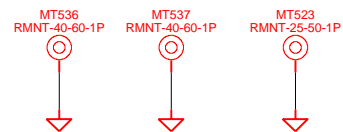
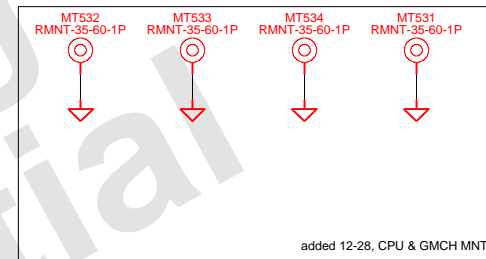
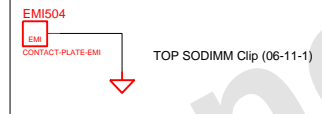
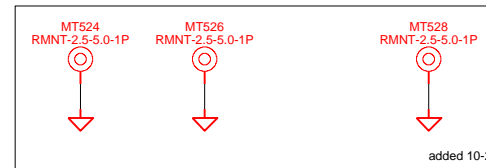
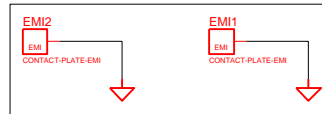
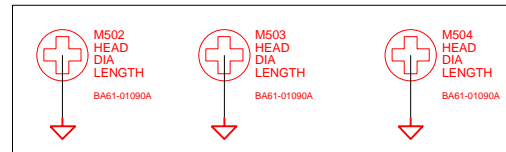
DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	MP	MAIN	MAIN	
APPROVAL	KEVIN LEE	REV	1,1	TOUCH PAD & LEDS	PART NO.	BA41-00727/8A
MODULE CODE		LAST EDIT			PAGE	50 OF 54
				March 28, 2007 3:33:29 PM		

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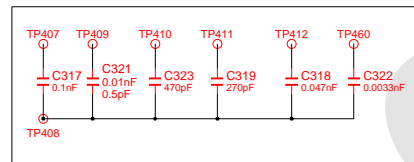
DMB-NUT



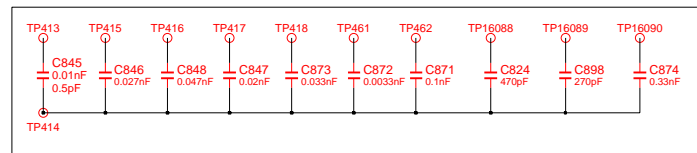
NUT for RHE, Locate on TOP of PCB



1 - 499 SIDE



500 - SIDE



REV1
 1 O
 2 O O3

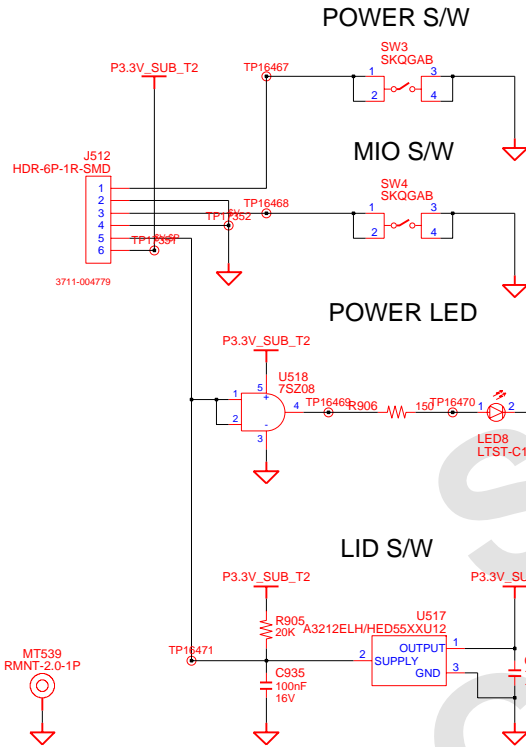
PCB REVISION CONTROL (ICT)				
NO	CONNECTION	DATE(Y/M/M/DD)	REVISION	STEP
1	N.C.			
2	1-2			
3	2-3			
4	3-1			
5	1-2-3			
6	N.C.			
7	1-2			
8	2-3			
9	3-1			
10	1-2-3			

DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2 MAIN MOUNT HOLE	SAMSUNG ELECTRONICS PART NO. BA41-00727/8A
CHECK	GUO LEI	DEV. STEP	MP			
APPROVAL	KEVIN LEE	REV	1,1			
MODULE CODE		LAST EDIT				
			March 28, 2007 3:33:29 PM	PAGE	51	OF 54

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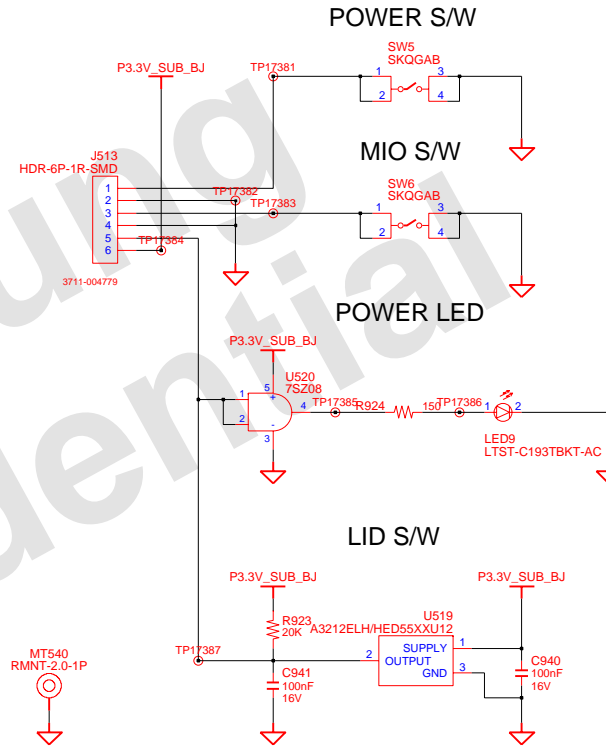
TORINO2 ON TOP BOARD

T2_SUB



BEIJING ON TOP BOARD

BJ_SUB



Drill size: 2.2mm
 PAD size: TOP 6.0mm
 Bottom 5.0mm

DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2 MAIN ON TOP BOARD	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	MP			PART NO. BA41-00727/8A
APPROVAL	KEVIN LEE	REV	1,1			
MODULE CODE		LAST EDIT	March 28, 2007 3:33:29 PM	PAGE	53	OF 54

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REVISION HISTORY

REVISION	DATE	NO.#	CONTENTS
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NOTES

Note1. Schematic Revision: MP 1 . 0 . 0
 Development Stage
 PCB Version
 Schematic Version

Note2. PCB SEC Code
 Vendor: SGCE P/N: BA41-??????
 Vendor: TPT P/N: BA41-??????

DRAW	ZHOU JUN	DATE	4/10/2007	TITLE	TORINO 2	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV STEP	MP	MAIN		
APPROVAL	KEVIN LEE	REV	1,1	REVISION HISTORY		PART NO. BA41-00727/8A
MODULE CODE		LAST EDIT	March 28, 2007 3:33:29 PM	PAGE	54	OF 54